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☑ Claim(s) : paged ..9... to paged 10....
☐ Drawing(s) : paged ..... to paged .....
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FLAGLESS SEMICONDUCTOR PACKAGE

Background of the Invention

This invention relates, in general, to molded semiconductor packages, and more particularly to a molded semiconductor package having a flagless leadframe.

Molded semiconductor packages generally comprise a semiconductor die mounted on the flag of a metal leadframe wherein the semiconductor die is electrically connected to the leads of the metal leadframe by wire bonds. The semiconductor die, the wire bonds and the leadframe, excepting the leads, are then encapsulated in plastic. There are many problems inherent with this type of package. These problems include the package being relatively thick, asymmetric, and having a maximum number of material interfaces. An asymmetric package creates increased bonding stresses that act on the die while material interfaces cause a molded semiconductor package to be more susceptible to failure during vapor phase, solder dip, infra red and high stress testing.

A prominent test for molded semiconductor packages is to saturate them with moisture or water vapor, followed within a short time by immersion in either a solvent vapor or a liquid solder at or above the soldering temperature. Water absorption in some currently used plastic molded packages has been measured by a wight gain of 0.4%. Upon sudden heating, this water will vaporize and rapidly build up pressure. This results in packages that may crack, and in extreme cases, literally blow apart. This test is particular destructive if there is condensed water in package voids that generally occur at the material interfaces. Even if the molding is perfect, voids may still be produced as a result of mechanical stresses in the

package due to thermal expansion differences between the molding compound, mounting flag and silicon die.

Cracks in the molding compound have occurred between the die and flag, die and molding compound and the flag and molding compound. It is also common for voids to occur at the die bond areas. Cracks and separations in the molded semiconductor packages are especially detrimental in that they cause damage to the wire bonds as well as to the die. It is common for large amounts of stress to act upon the semiconductor die, especially at the sides and corners as a crack propagates throughout the plastic molding compound. Commonly, a separation of the passivation layer from the remainder of the die results and causes damage to the metal circuitry of the semiconductor die. Of course, this results in failure of the semiconductor device. Therefore, it would be highly desirable to have a molded semiconductor package that is thin, symmetrical so as to eliminate various bonding stresses and has a minimum number of material interfaces so as to reduce the number of voids contained therein.

Another problem commonly encountered with the use of molded semiconductor packages is that of heat dissipation. It is desirable for heat to flow out of the package through the metal leads because only a minimal amount of heat will actually flow out through the plastic encapsulation itself. Heat often encounters difficulty in flowing from the die to the leads and is essentially trapped within the plastic package. This heat is liable to cause temperature cycling damage due to the thermal expansion coefficient mismatch of the various materials as well as inhibiting device performance. Therefore, an improved thermal path would also be highly desirable.

Summary of the Invention

Accordingly, it is an object of the present invention to provide a molded semiconductor package having a flagless

leadframe that allows for a thin package without wafer thinning.

It is an additional object of the present invention to provide a molded semiconductor package having a flagless leadframe that has a minimum number of material interfaces.

Yet a further object of the present invention is to provide a molded semiconductor package having a flagless leadframe that attempts to reduce the failure rate due to moisture saturation.

An object of a preferred embodiment of the present invention is to provide a molded semiconductor package having a flagless leadframe that is less susceptible to thermal cycling stress damage.

The foregoing and other objects and advantages are achieved in the present invention by one embodiment in which, as a part thereof, makes use of a flagless leadframe. A semiconductor die is mounted either within a die opening of the leadframe and essentially coplanar with it or on the leadframe itself, directly above a die opening. This allows for thinner, more symmetrical packages having a minimum number of material interfaces. It is also possible to include guard rings for protection of the semiconductor die as well as heat spreaders to enhance the dissipation of heat to the leads.

Brief Description of the Drawings

FIG. 1 is an enlarged top-view of a portion of a first embodiment of the present invention;

FIG. 2 is an enlarged cross-sectional view of a portion of the first embodiment of the present invention taken from line 2-2 of FIG. 1;

FIG. 3 is an enlarged top-view of a portion of a second embodiment of the present invention;

FIG. 4 is an enlarged cross-sectional view of a portion of the second embodiment of the present invention taken from line 4-4 of FIG. 3;

FIG. 5 is an enlarged top-view of a portion of a third embodiment of the present invention;

FIG. 6 is an enlarged cross-sectional view of a portion of the third embodiment of the present invention taken from line 6-6 of FIG. 5;

FIG. 7 is an enlarged top view of a portion of a fourth embodiment of the present invention;

FIG. 8 is an enlarged cross-sectional view of a portion of the fourth embodiment of the present invention taken from line 8-8 of FIG. 7;

FIG. 9 is an enlarged cross-sectional view of a portion of an embodiment of the present invention including a heat spreader;

FIG. 10 is an enlarged cross-sectional view of a portion of an embodiment of the present invention including a heat spreader; and

FIG. 11 is an enlarged cross-sectional view of a portion of an embodiment of the present invention including a heat spreader with an exposed heat dissipation path.

Detailed Description of the Invention

FIG. 1 is an enlarged top view and FIG. 2 is an enlarged cross-sectional view taken from line 2-2 of FIG. 1 of portions of a molded semiconductor package having a flagless leadframe 22a. A semiconductor die 10a includes a first surface 12a, a second surface 14a, sides 16a and corners 18a. Semiconductor die 10a is disposed in die opening 20a of leadframe 22a which is larger than the periphery of semiconductor die 10a in this embodiment. Leadframe 22a further includes corner leads 24a which serve

to bond semiconductor die 10a to leadframe 22a and electrical bonding leads 28a which serve as electrical contacts to the semiconductor package. In this embodiment, an organic adhesive 26a is employed to bond die bonding leads 24a to semiconductor die 10a at corners 18a although an organic adhesive could be used. Leads other than corner leads 24a may be used to bond semiconductor die 10a to leadframe 22a. For example, certain leads 28a could be bonded to semiconductor die 10a at its sides 16a, in which case corner leads 24a could be used to make electrical contacts. Electrical bonding leads 28a are selectively wire bonded to semiconductor die 10a to form the electrical contacts. One skilled in the art will recognize that used electrical bonding leads 28a may be included in leadframe 22a to enhance heat dissipation.

An encapsulation 30a is shown disposed about semiconductor die 10a as well as portions of corner leads 24a and electrical bonding leads 28a. One skilled in the art will understand that the wire bonds (not shown) are also encapsulated by encapsulation 30a. It should be noted that electrical bonding leads 28a extend from encapsulation 30a to serve as external contacts to the molded package. Encapsulation 30a is comprised of molded plastic in this embodiment, however, it should be understood that many well known encapsulates may be employed.

It should be noted that the semiconductor package illustrated by FIGS. 1 and 2 is substantially symmetrical. This is because leads 24a and 28a are essentially coplanar with semiconductor die 10a. Because the package is symmetrical, some stresses are reduced and semiconductor die 10a is subject to lesser amounts of stress. Further, the absence of a flag in leadframe 22a allows for a thinner package which is always desirable to save space. Additionally, there are a limited number of material interfaces because there is no flag and no die bond material bonding semiconductor die 10a thereto.

FIG. 3 is an enlarged top view and FIG. 4 is an enlarged cross-sectional view taken from line 4-4 of FIG. 3, of portions of a molded semiconductor package having a flagless leadframe 22b. In this embodiment, the periphery of semiconductor die 10b is larger than die opening 20b, depicted by dotted line 32. Second surface 14b of semiconductor die 10b is bonded to leadframe 22b above die opening 20b. Again, semiconductor die 10b is bonded to leadframe 22b by organic adhesive 26b although other adhesives may be employed. Although the embodiment depicted if FIGS. 3 and 4 is not symmetrical, it includes a reduced number of material interfaces because it contains no flag and does not have a complete layer of die bond material. The reduced number of material interfaces creates fewer voids within the semiconductor package thereby making it less susceptible to moisture saturation.

FIG. 5 illustrates an enlarged top view and FIG. 6 is an enlarged cross-sectional view taken from line 6-6 of FIG. 5 of portions of a molded semiconductor package having a flagless leadframe 22c. This embodiment further includes a guard ring 34. Guard ring 34 is a part of leadframe 22c in this embodiment and is comprised of the same material as leadframe 22c. It can be seen that guard ring 24 attaches to support leads 36 of leadframe 22c. Guard ring 34 is not physically connected to electrical bonding leads 28c and therefore, does not hinder electrical connections within the package.

Guard ring 34 defines the periphery of die opening 20c. Semiconductor die 10c is disposed in die opening 20c.and is bonded thereto by organic adhesive 26c at it's corners 18c. Again, it should be understood that semiconductor die 10c may be bonded to guard ring 34 of leadframe 22c by organic adhesive 26c or another adhesive at its sides 16c. Because guard ring 34 protects the high stress sides 16c and corners 18c of semiconductor die 10c, stress does not readily damage it and the lifetime of the semiconductor package is thereby increased. Again, it should be noted that a symmetrical

package has been created. This is extremely beneficial for the reasons discussed earlier.

FIG. 7 is an enlarged top view and FIG. 8 is an enlarged cross-sectional view taken from line 8-8 of FIG. 7 of portions of a molded semiconductor package having a flagless leadframe 22d. This embodiment includes a guard ring 38 that is not part of leadframe 22d. Guard ring 38 may be comprised of metal, plastic, silicon, ceramic or one of many other materials having coefficient of thermal expansion similar to the material of leadframe 22d. It should be understood guard ring 38 may be comprised of a different material than leadframe 22d. Guard ring 38 is disposed around semiconductor die 10d and is bonded thereto by organic adhesive 26d. Again, semiconductor die 10d is bonded to guard ring 38 at corners 18d, however, it should be understood that bonding may occur at sides 16d. Guard ring 38 is also bonded to bonding leads 42 of leadframe 22d. Although an organic adhesive 40 is employed in this embodiment to adhere bonding leads 42 to guard ring 38, it should be understood that many other adhesives may be used. Guard ring 38 serves to protect the high stress areas of semiconductor die 10d as does guard ring 34, discussed earlier. Again, the resulting package is relatively thin, substantially symmetrical and has a minimum number of material interfaces due to the exclusion of a leadframe flag.

FIGS. 9, 10 and 11 are enlarged cross-sectional views of portions of molded semiconductor packages having flagless leadframes. A commonly encountered problem of packages of this type is the dissipation of heat given off by semiconductor die 10. Generally, the majority of heat dissipates from the package through metal leads 44 while a minimal amount of heat dissipates through plastic encapsulation 30. A heat spreader 46 serves to transfer heat from semiconductor die 10 to metal leads 44. Heat spreader 46 is comprised of copper in this embodiment,

however, it should be understood that many good thermal conducting materials may be employed.

FIG. 9 illustrates an insulting adhesive 48 being disposed between heat spreader 46 and semiconductor die 10. Insulating adhesive 48 may be comprised of a material such as polyimide, however it should be understood that many other electrically insulating materials may be employed. Because of the thinness of insulating adhesive 48, heat will easily dissipate from semiconductor die 10 through electrical insulator 48 to heat spreader 46. Once the heat dissipates to heat spreader 46, it will travel towards end 50 of heat spreader 46 and then dissipate through plastic encapsulation 30 to metal leads 44. FIG. 10 is very similar to FIG. 9 except that insulating adhesive 48 is further disposed between metal leads 44, heat spreader 46 and semiconductor die 10 in a predetermined position and therefore, no other adhesives need be used.

FIG. 11 illustrates a slightly different method of enhancing the dissipation of heat from semiconductor die 10. Insulating adhesive 48 extends along the length of heat spreader 46 so that it contacts metal support leads 44 as well as heat spreader 46. Here, insulator 48 also prevents shorting. In FIGS. 10 and 11, heat does not have to dissipate at all through plastic encapsulation 30 in order to reach metal support leads 44. FIG. 11 also discloses a recess 52 in plastic encapsulation 30 that exposes heat spreader 46. As a result, heat may dissipate from the semiconductor package directly through heat spreader 46 without the need of dissipating through plastic encapsulation 30 or metal support leads 44.

CLAIMS

1 A molded semiconductor package having a flagless leadframe (22) comprising:

a semiconductor die (10) having a first surface (12), a second surface (14), sides (16) and corners (14); the invention being charcterized by:

a flagless leadframe (22) including leads (28) and a die opening (20), said leadframe (22) being physically bonded to said semiconductor die (10) and further, said leads (28) of said leadframe being electrically connected to said semiconductor die (10) in a selective manner; and

an encapsulation (30) disposed about said semiconductor die (10) and said leadframe (22) except for said leads (28) which protrude therefrom.

2. The molded semiconductor package of claim 1 wherein the die opening (20) is larger than the semiconductor die (10) disposed therein, the leadframe (22) is essentially coplanar with said semiconductor die (10) and said package is substantially symmetrical.

3. The molded semiconductor package of claim 2 wherein the leadframe (22) is bonded to one or both of the sides (16) and corners (18) of the semiconductor die (10) by an organic adhesive (26).

4. The molded semiconductor package of claim 2 further comprising a guard ring (34) being disposed about the semiconductor die (10), said guard ring (34) being a part of the leadframe (22) or separate from said leadframe (22).

5. The molded semiconductor package of claim 4 wherein the guard ring (34) is disposed between the leadframe (22) and the semiconductor die (10) and is bonded to at least one

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or both of the sides (16) and corners (18) of said semiconductor (10) die by an organic adhesive (26).

6. The molded semiconductor package of claim 1 wherein the die opening (20) is smaller than the semiconductor die (10) and the second surface (14) of said semiconductor die (10) is bonded to the leadframe (22) above said die opening (20).

7. The molded semiconductor package of claim 1 further comprising a thermally conductive heat spreader (46) coupled to the second surface (14) of the semiconductor die (10) by an insulating adhesive material (48).

8. The molded semiconductor package of claim 7 wherein the thermally conductive heat spreader (46) is coupled to the leads (28) of the leadframe (22) by an insulating adhesive material (48).

9. The molded semiconductor package of claim 7 wherein the thermally conductive heat spreader (46) is not completely encapsulated by the encapsulation (30).

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FLAGLESS SEMICONDUCTOR PACKAGE

Abstract of the Disclosure

A molded semiconductor package having a flagless leadframe (22) wherein a semiconductor die (10) is disposed in or above a die opening (20) of a leadframe (22). This allows for thin, symmetrical packages and packages having a minimum number of material interfaces to be manufactured because no leadframe flags and minimal die bond material are employed. The present invention further includes guard rings (34) to protect high stress areas of the semiconductor die (10) from damage and heat spreaders (46) to more effectively spread heat dissipated by the semiconductor die (10).

FIG. 1 accompanies this abstract.

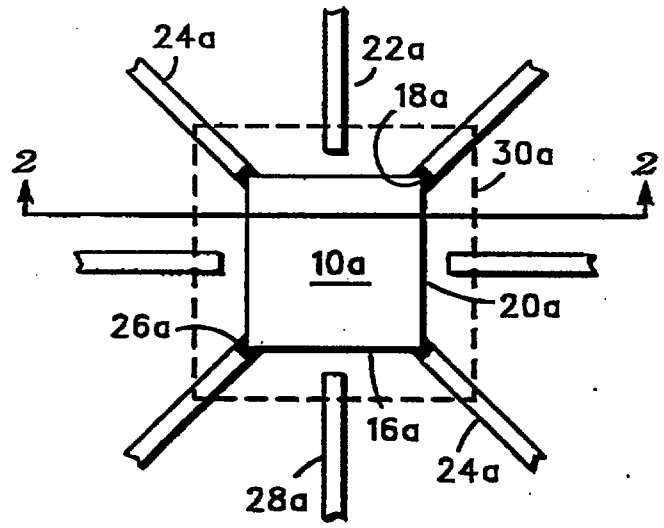


FIG. 1

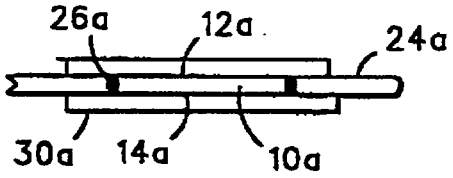


FIG. 2

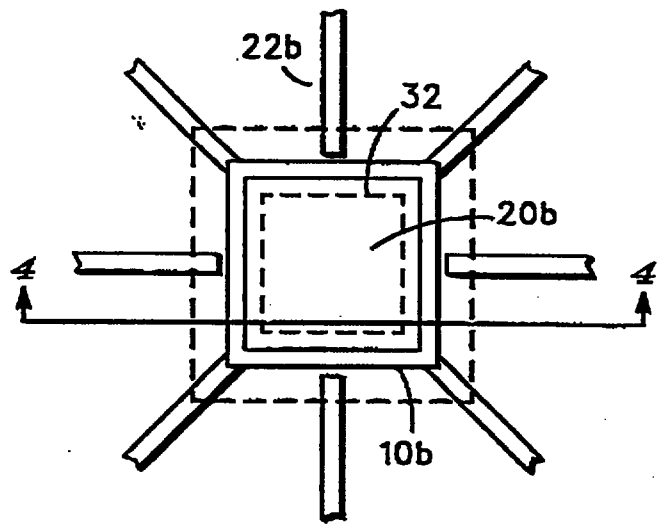


FIG. 3

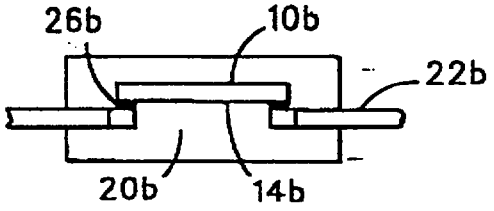


FIG. 4

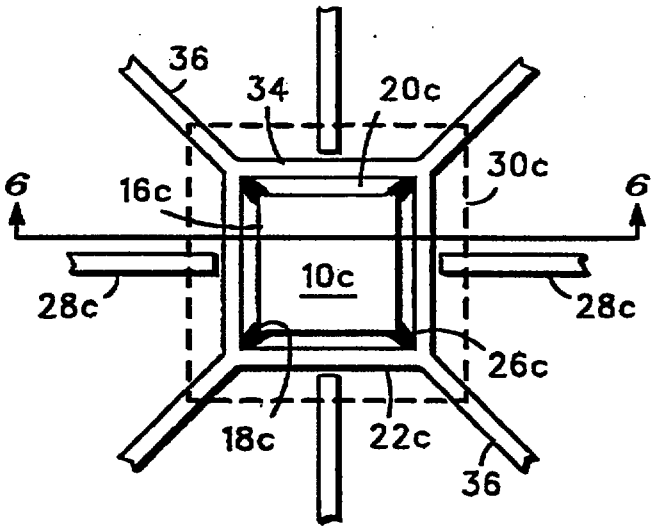
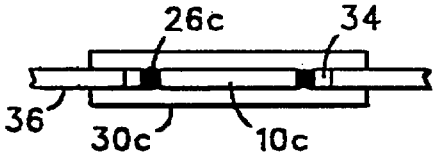


FIG. 5

FIG. 6



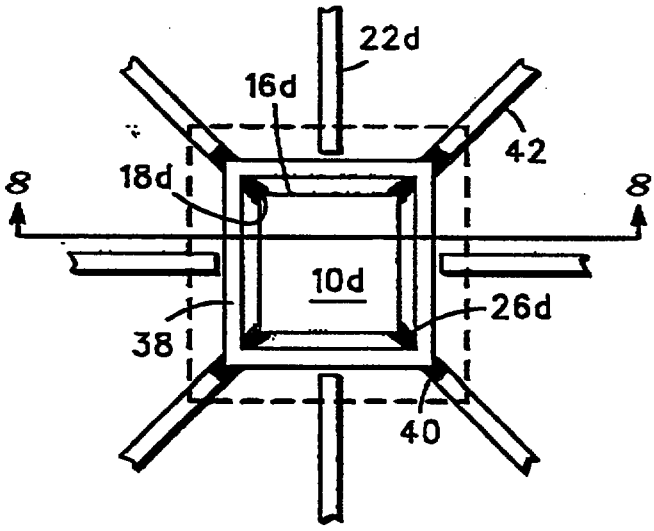
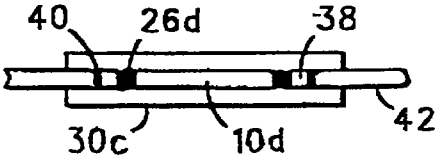


FIG. 7

FIG. 8



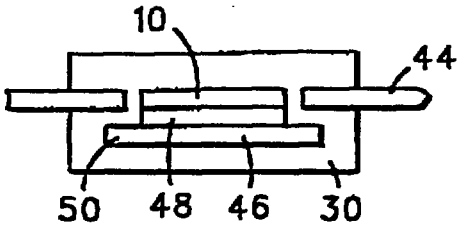
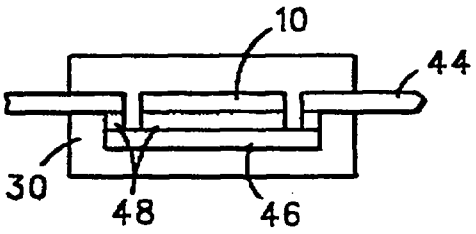


FIG. 9

FIG. 10



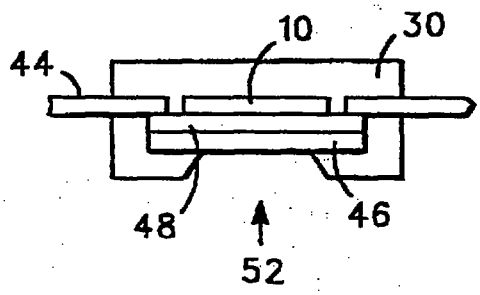


FIG. 11

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Claims

1. A molded semiconductor package having a flagless leadframe (22b) comprising:

a semiconductor die (10b) having a first surface (12b) and a second surface (14b);

a flagless leadframe (22b) including leads (24b,28b) defining a die opening (20b) smaller than the semiconductor die (10b) and said second surface (14b) of said semiconductor die (10b) [being bonded to said leadframe (22b) above said die opening (20b)] wherein only a portion of said leads (24b) are adhesively bonded to the second surface of said semiconductor die (10b), and wherein other of said leads (28) are spaced apart from said semiconductor die (10b) and further, said other of said leads (28b) of said leadframe being wire bonded to said semiconductor die (10b) in a selective manner; and

an encapsulation (30b) disposed about said semiconductor die (10b) and said leadframe (22b) except for said leads (24b,28b) which protrude therefrom.

2. The molded semiconductor package of claim 1 wherein the die opening (20a) is larger than the semiconductor (10a) disposed therein, the leadframe (22a) is essentially coplanar with said semiconductor die (10a) and said package is substantially symmetrical.

3. The molded semiconductor package of claim 1 wherein the die opening (20b) is smaller than the semiconductor die (10b) and the second surface (14b) of said semiconductor die (10b) is bonded to the leadframe (22b) above said die opening (20b).

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