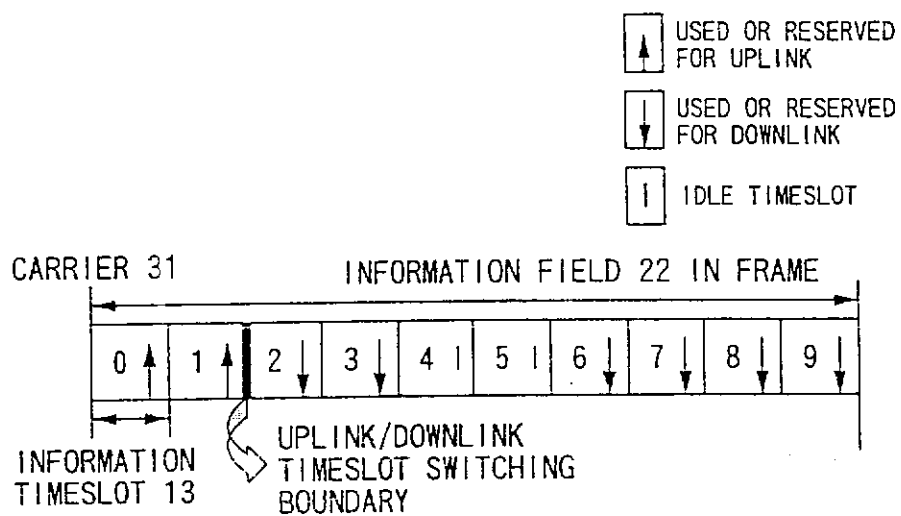
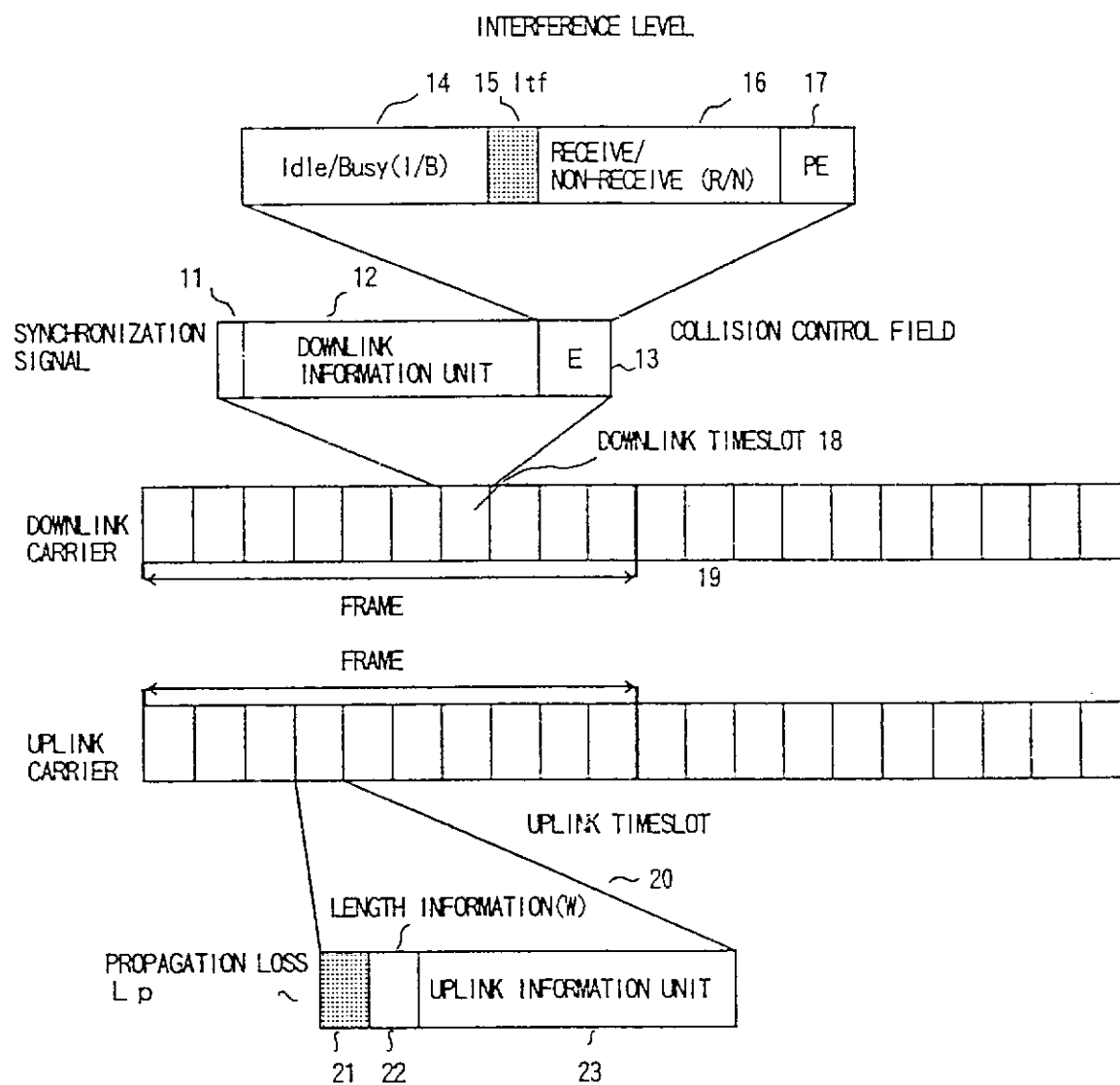


FIG. 1 PRIOR ART



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FIG. 2



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FIG. 3

BIT CONFIGURATION	PROPAGATION LOSS(dB)
0000000	- 1 3 OR LESS
0000001	- 1 2
0000010	- 1 1
.	.
:	:
.	.
1111110	1 1 4
1111111	1 1 5

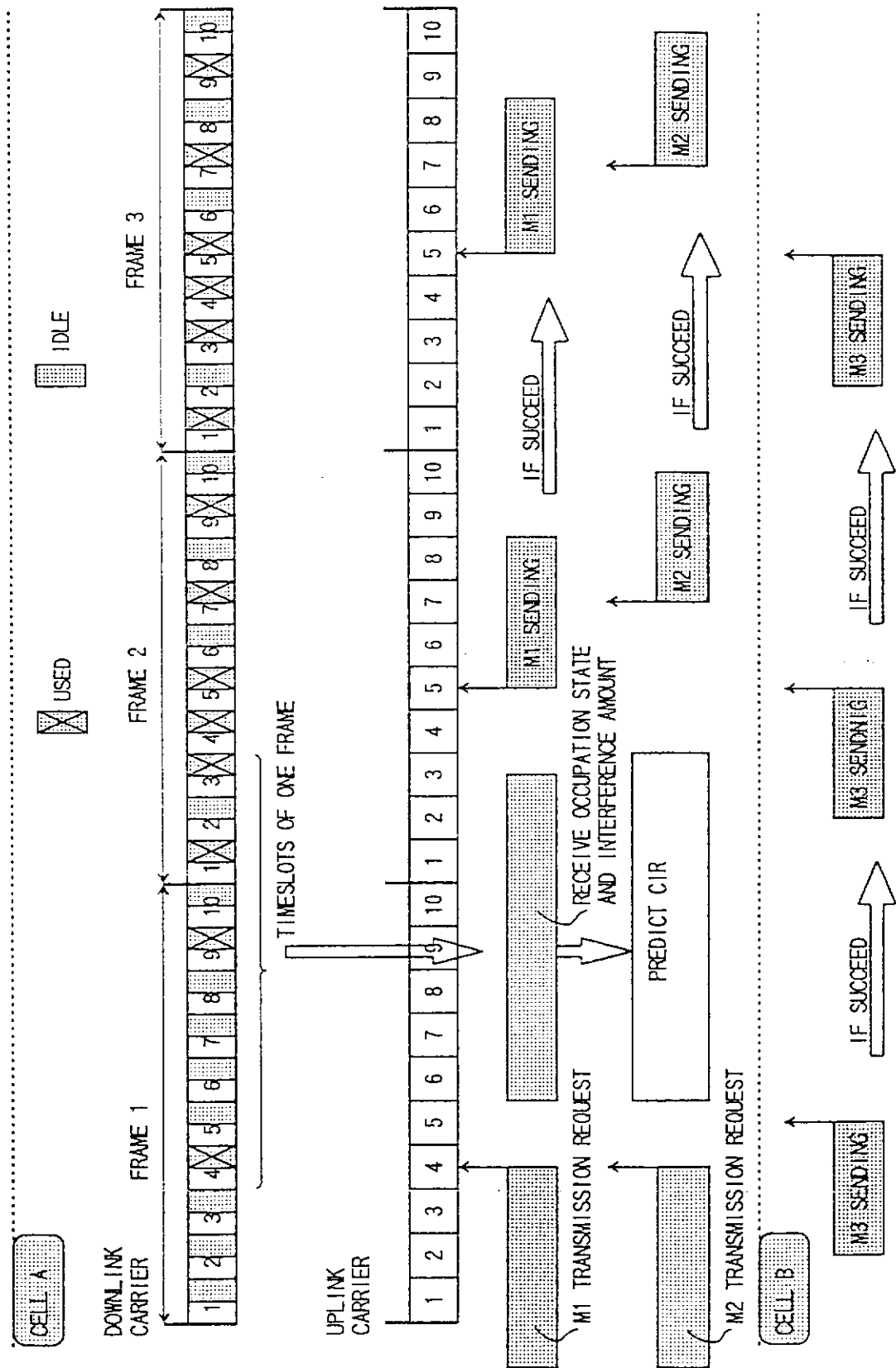
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FIG. 4

BIT CONFIGURATION	INTERFERENCE LEVEL (dBm)
00000000	- 295 OR LESS
00000001	- 294
00000010	- 293
.	.
:	:
.	.
11111110	- 40
11111111	- 39

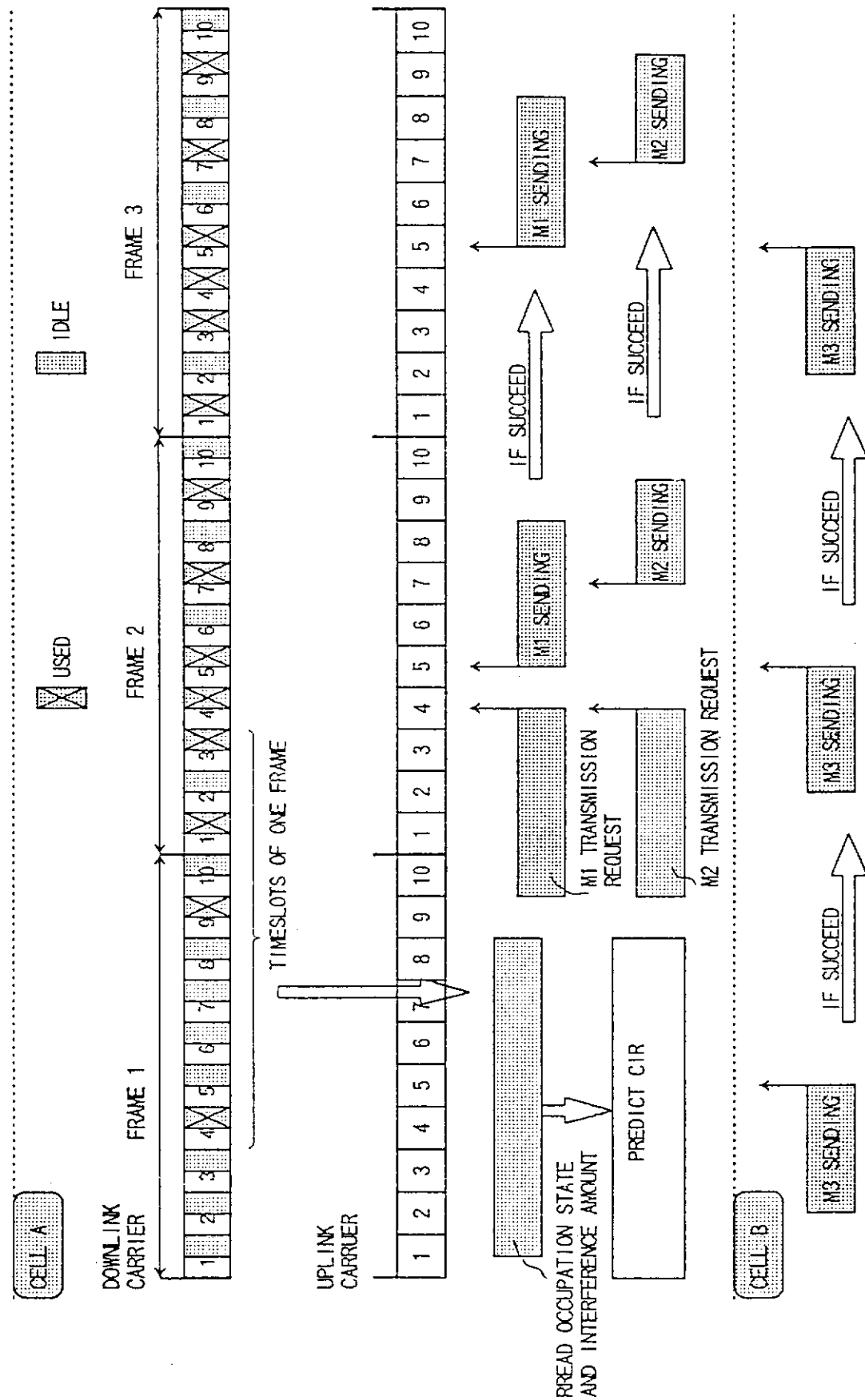
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FIG. 5



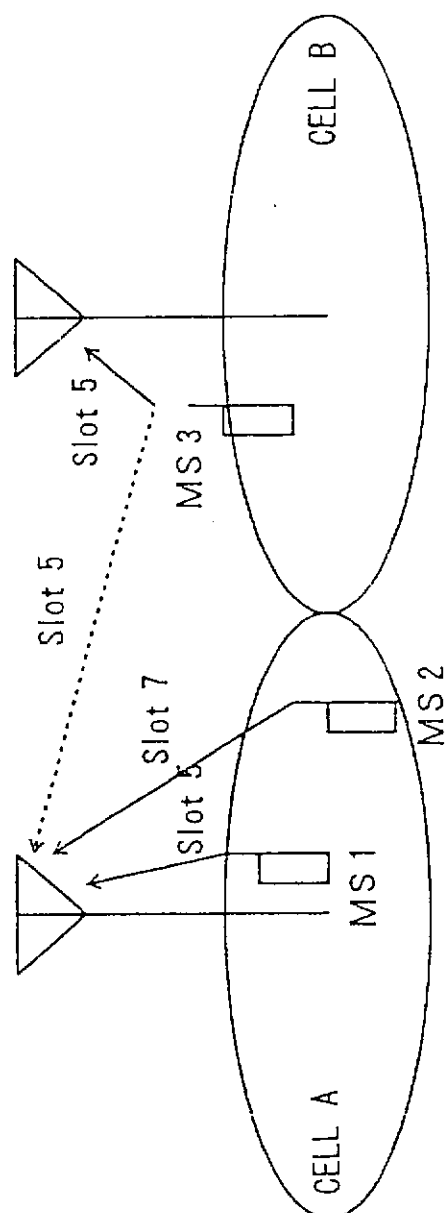
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FIG. 6



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FIG. 7



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FIG. 8A

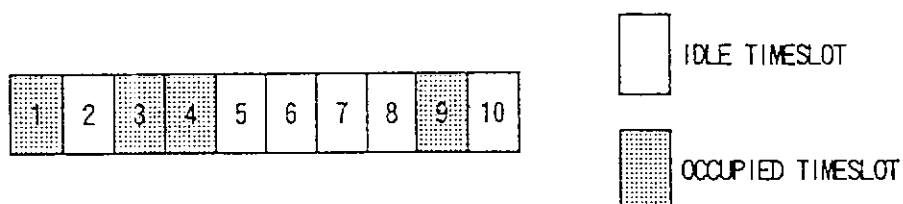
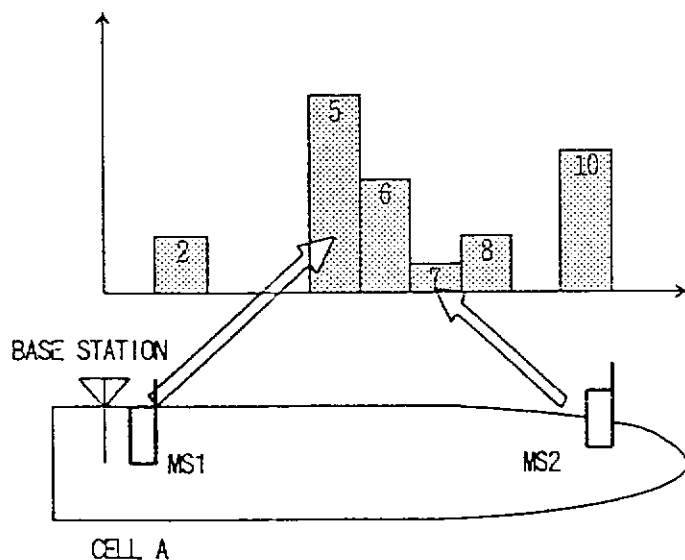


FIG. 8B



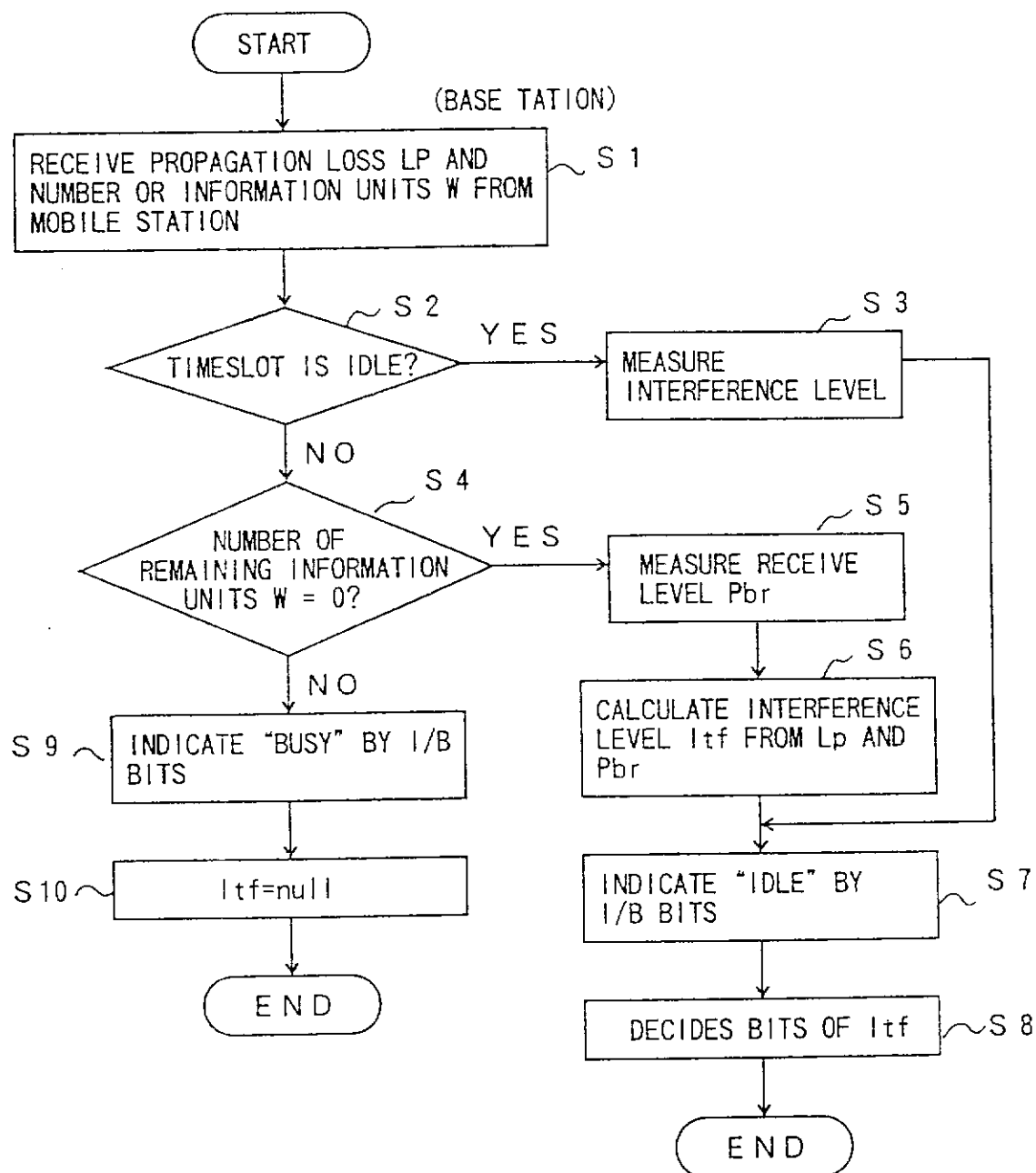
SELECT IDLE SLOT SUCH THAT
PREDICTED CIR BECOMES SMALLEST
WHILE SATISFYING REQUIRED CIR

MS1: SMALL PROPAGATION LOSS →
SELECT TIMESLOT OF LARGE
INTERFERENCE

MS2: LARGE PROPAGATION LOSS →
SELECT TIMESLOT OF SMALL
INTERFERENCE

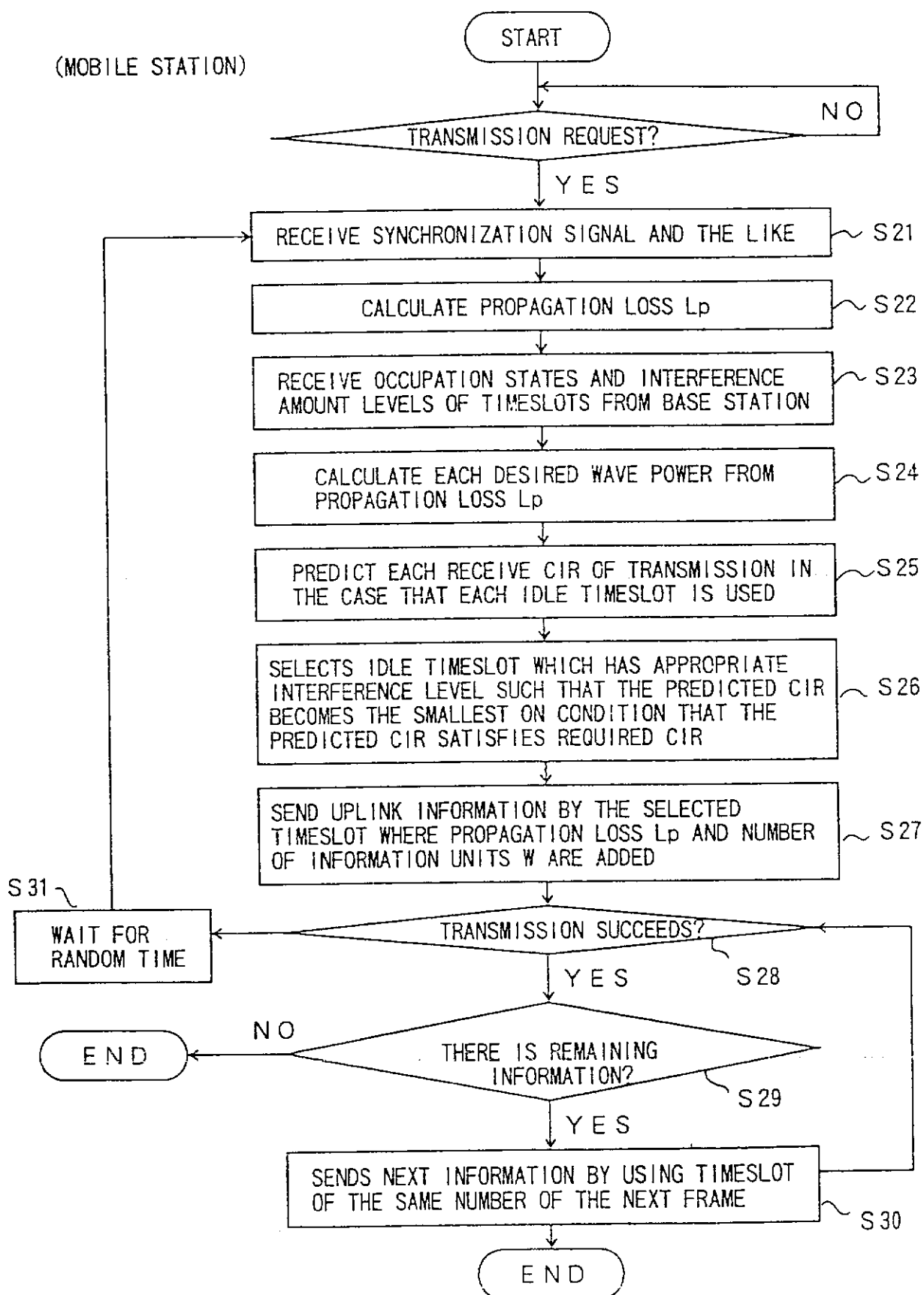
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FIG. 9



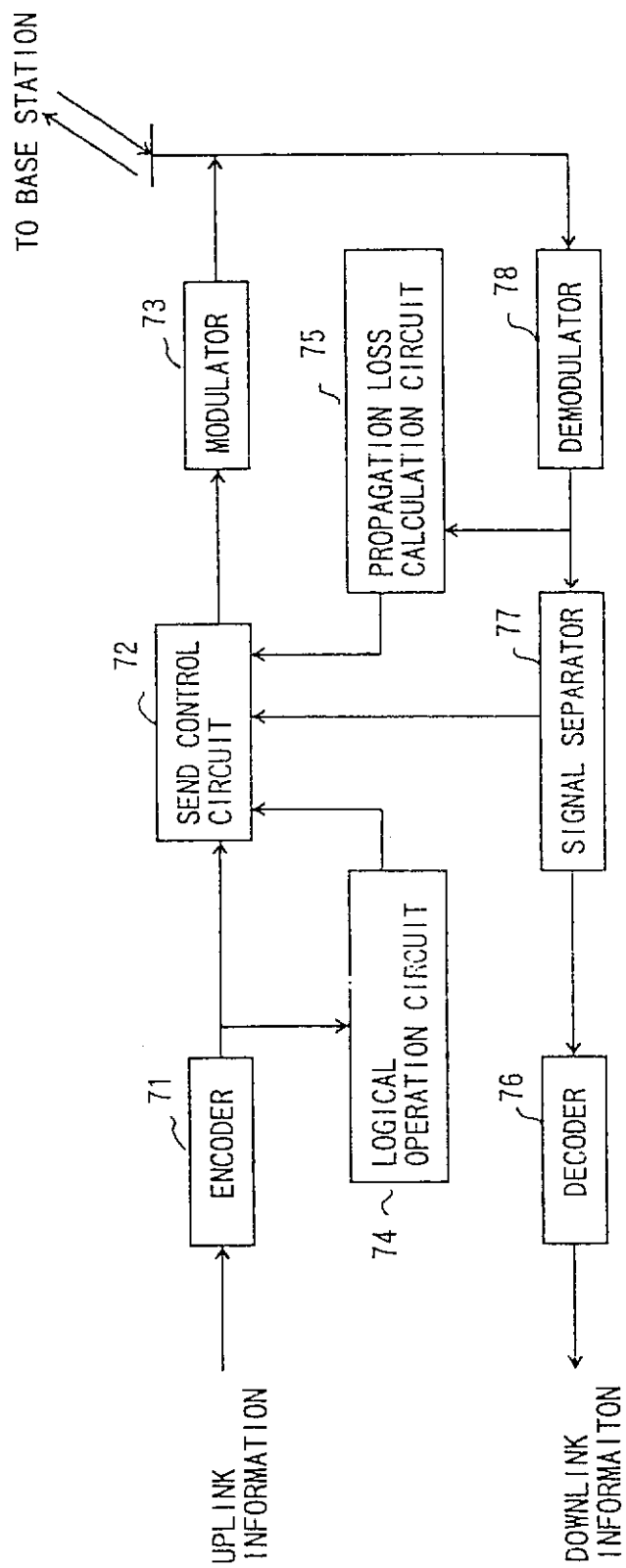
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FIG. 10



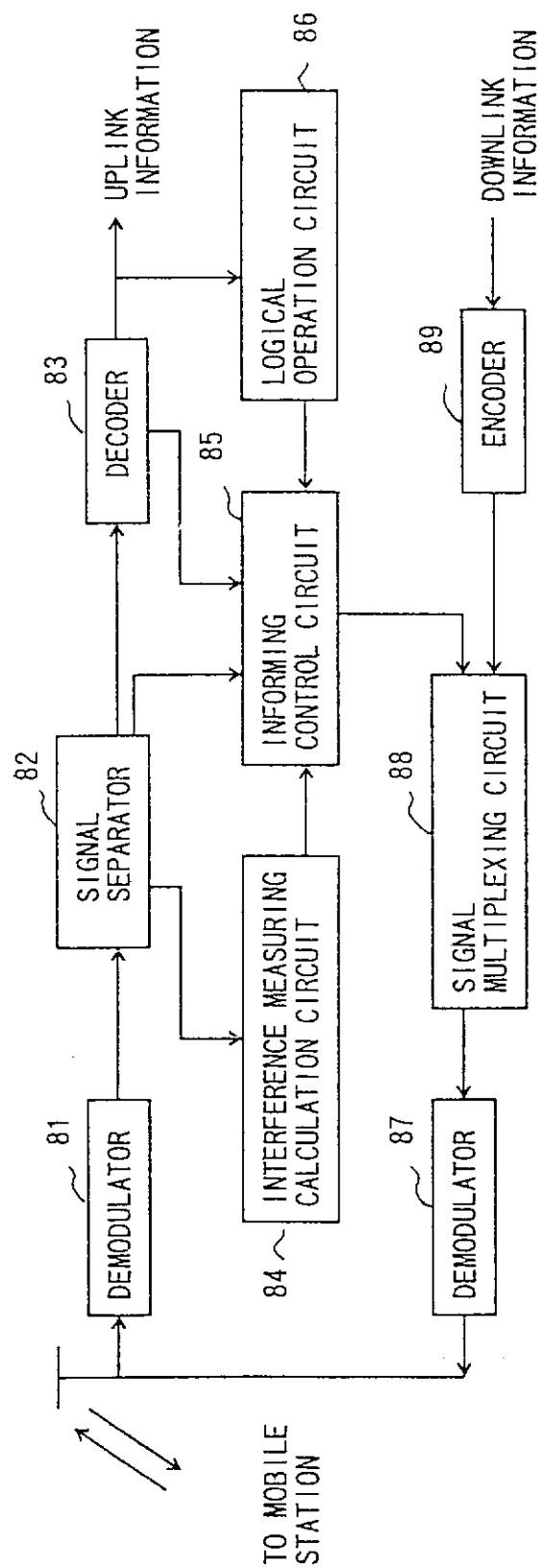
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FIG. 11



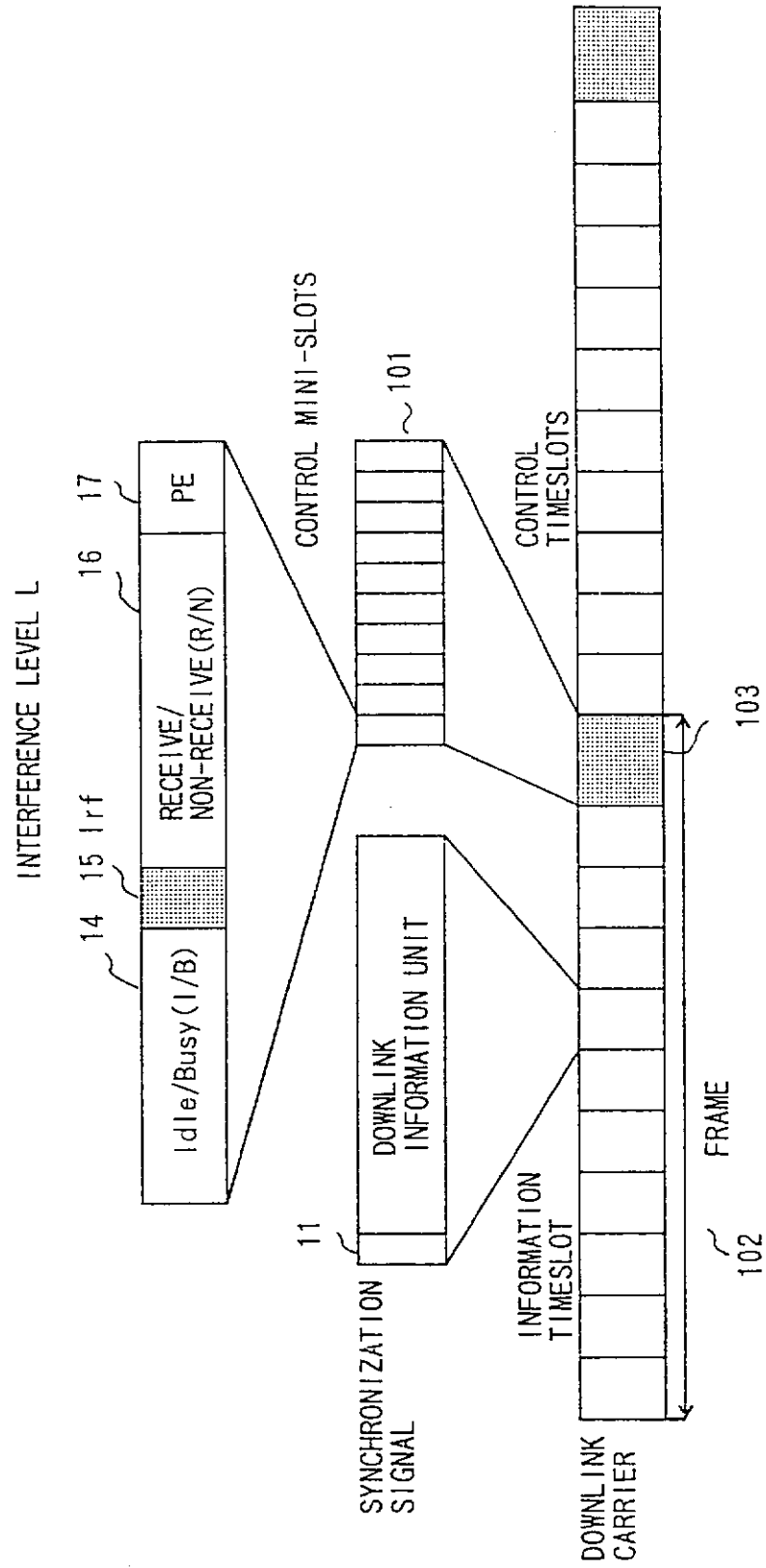
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FIG. 12



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FIG. 13



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FIG. 14A

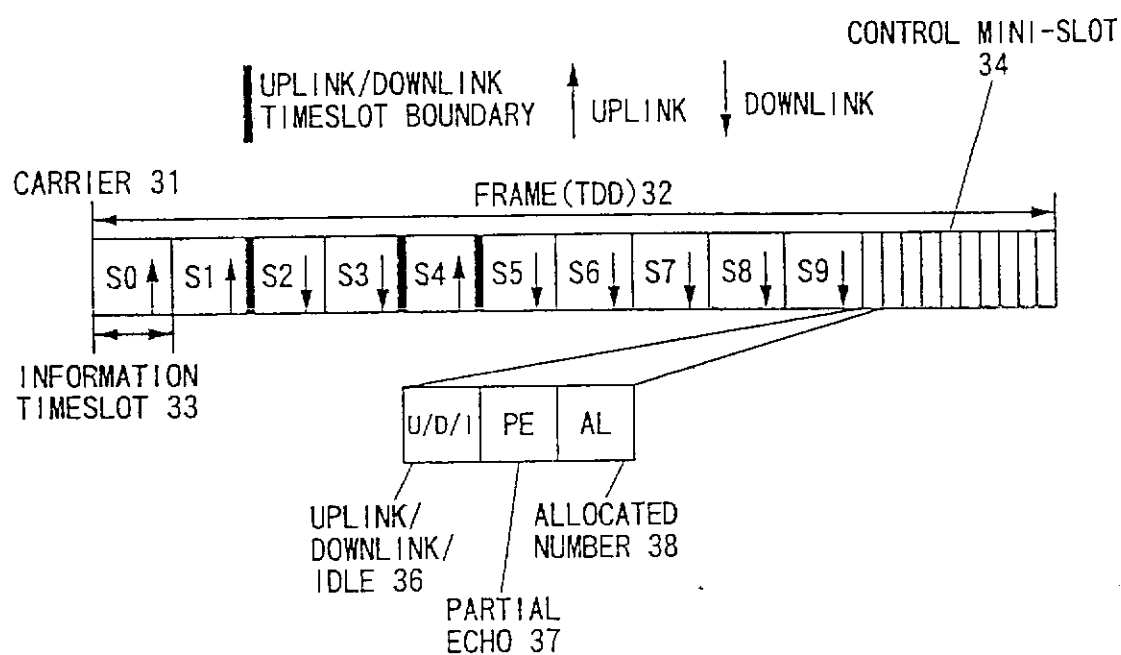
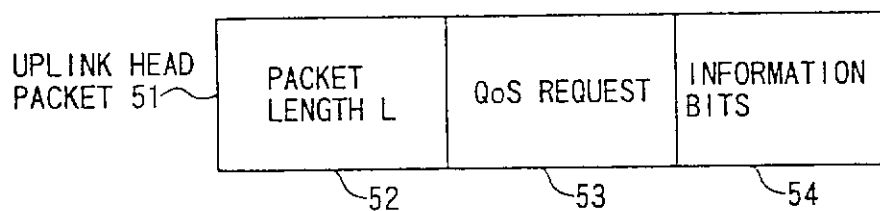
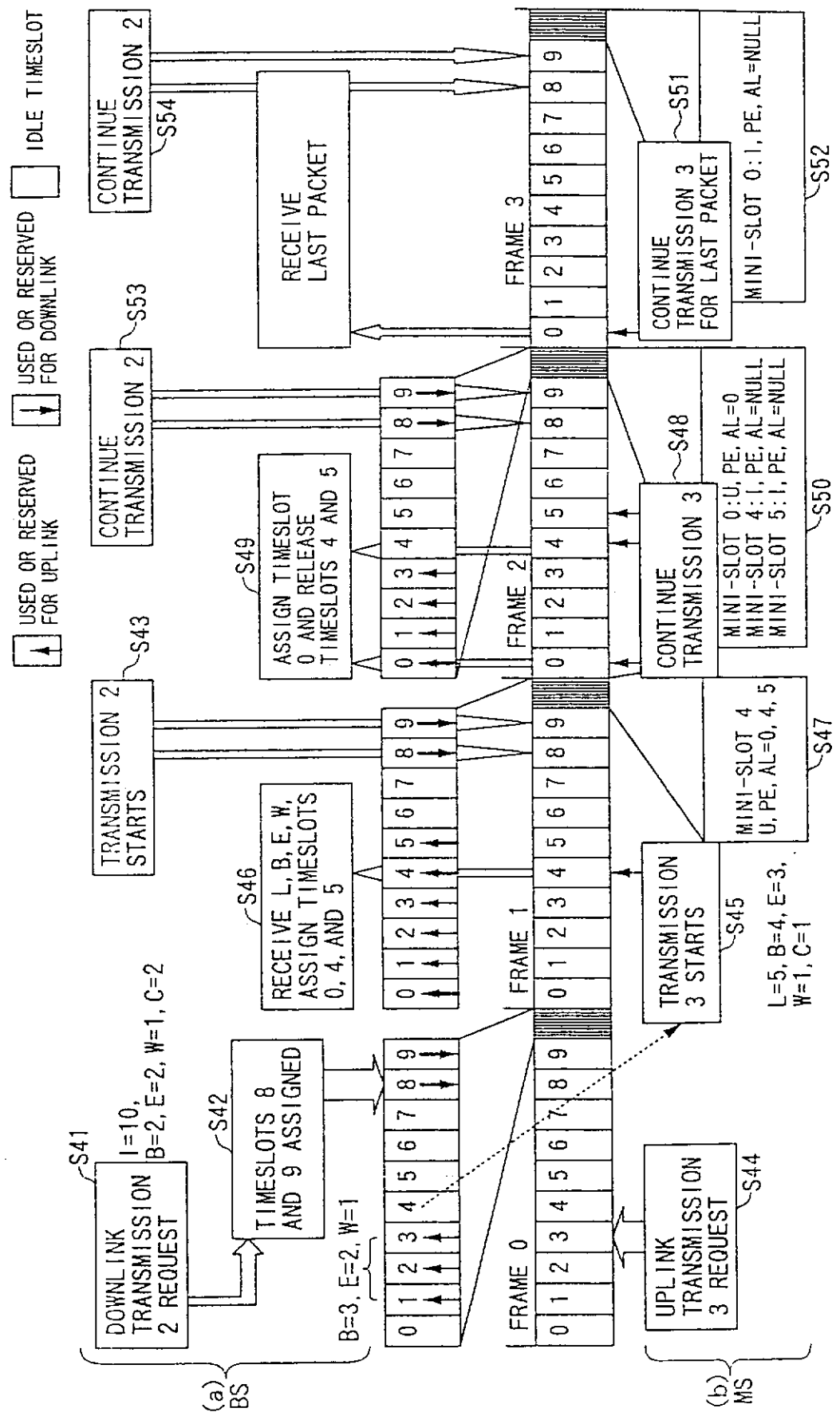


FIG. 14B



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FIG. 15



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FIG. 16

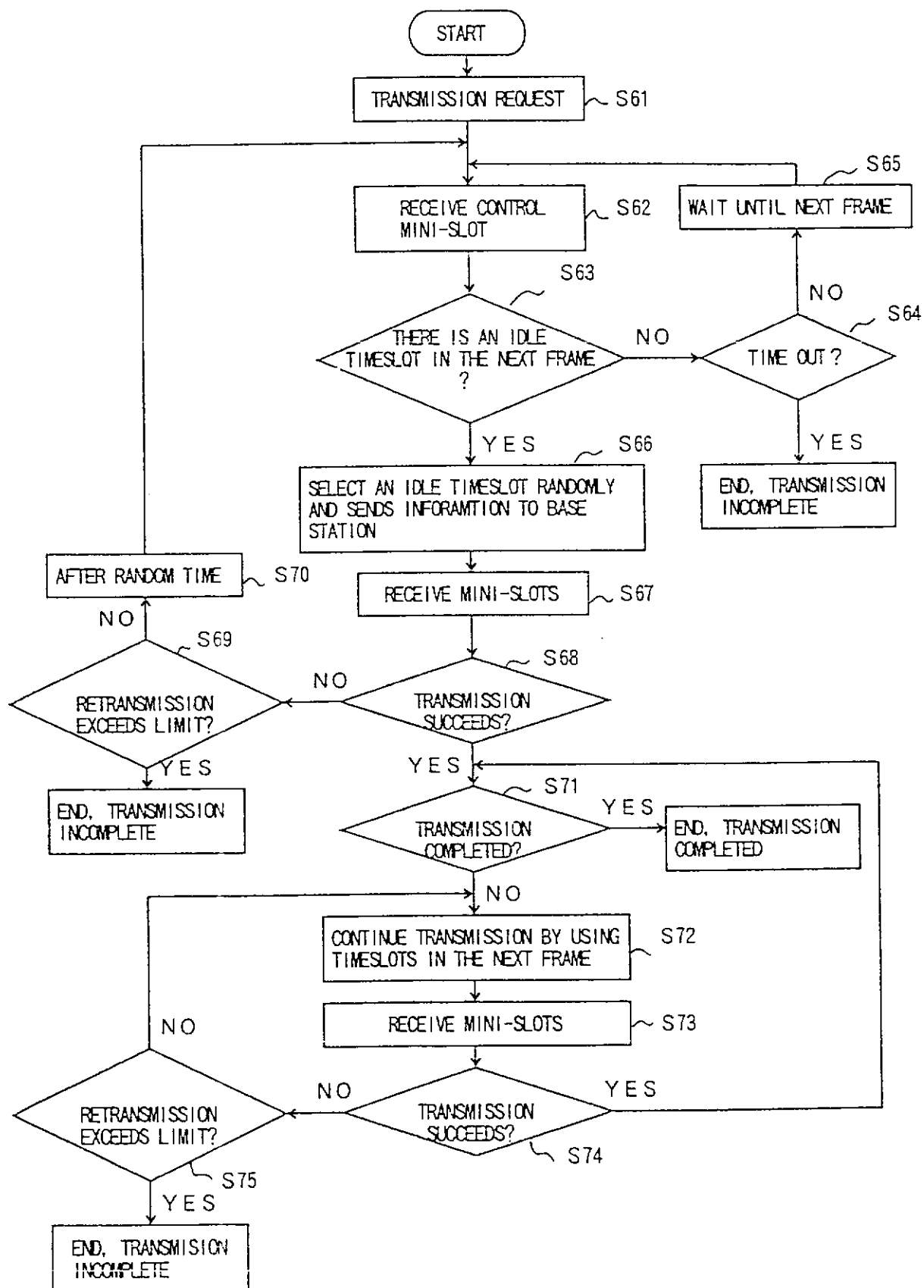
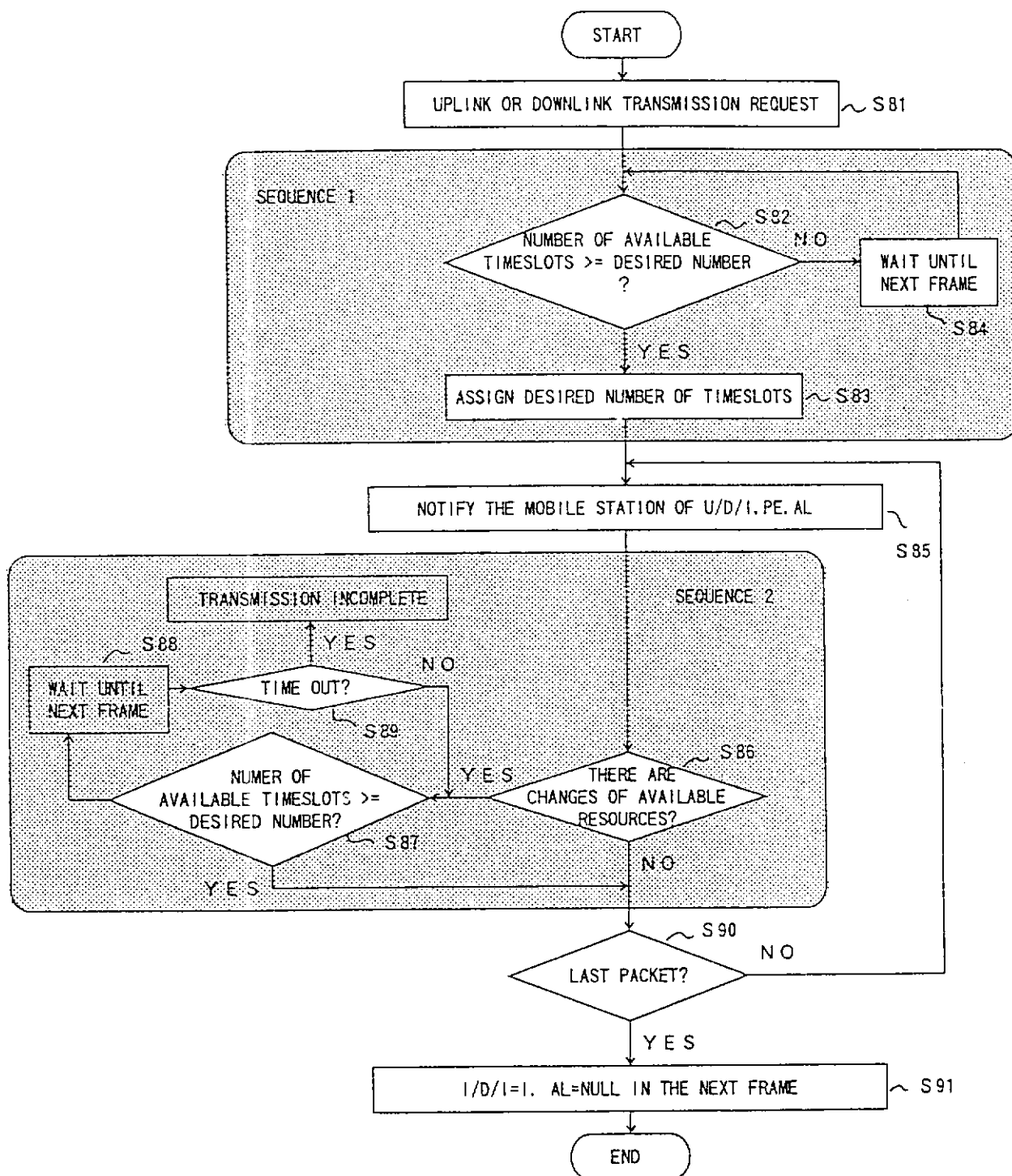


FIG. 17



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FIG. 18

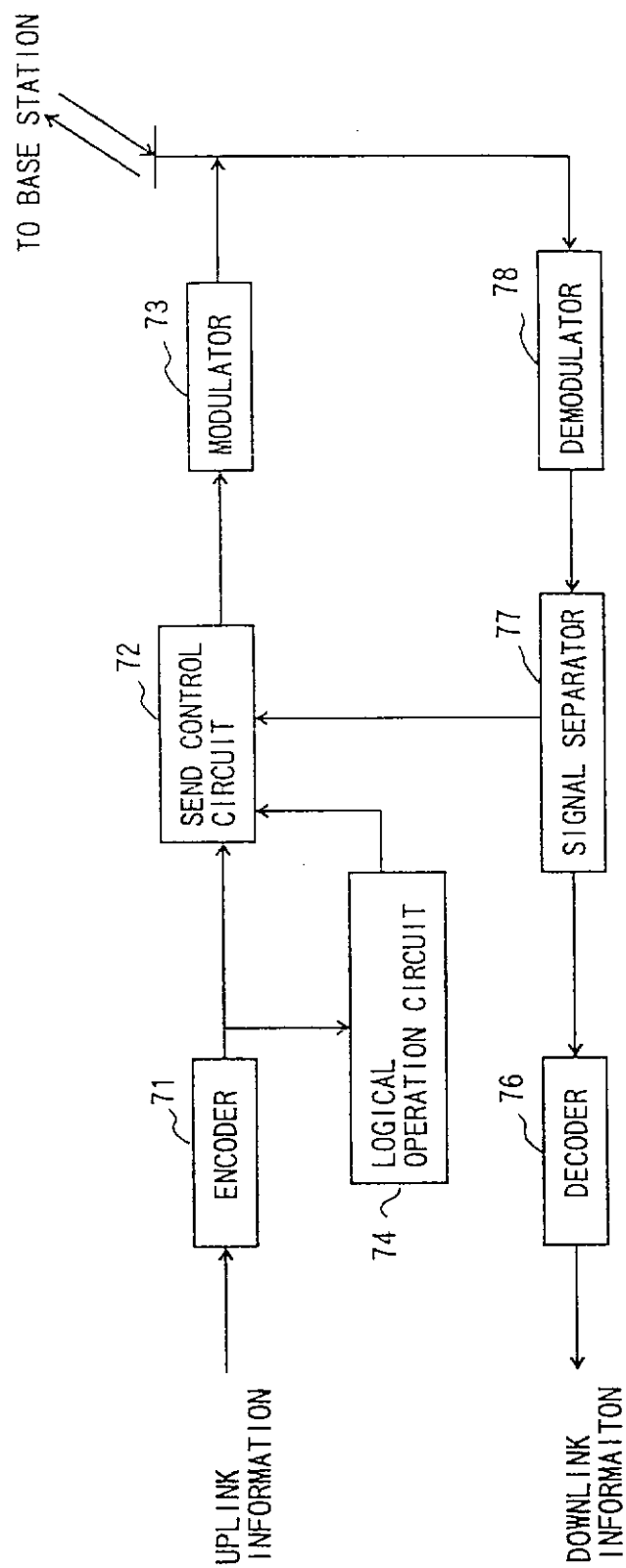
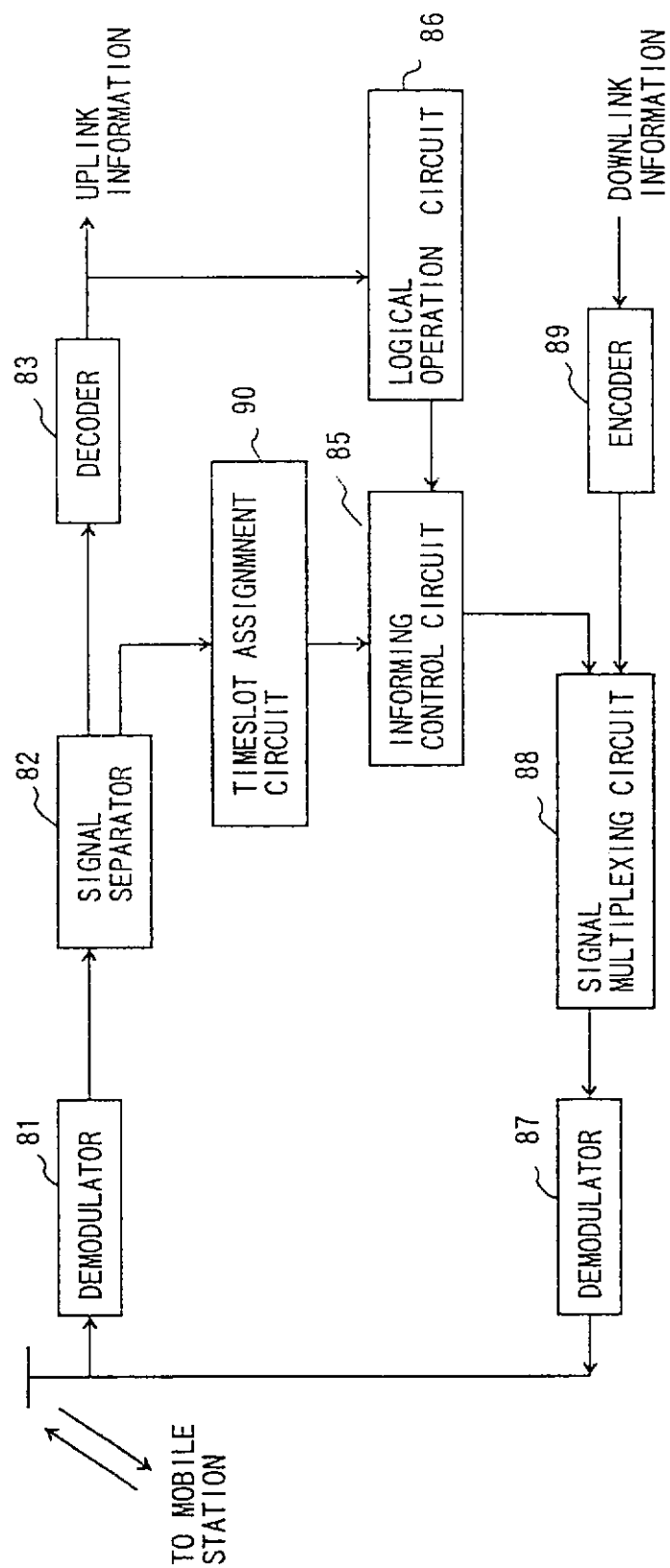
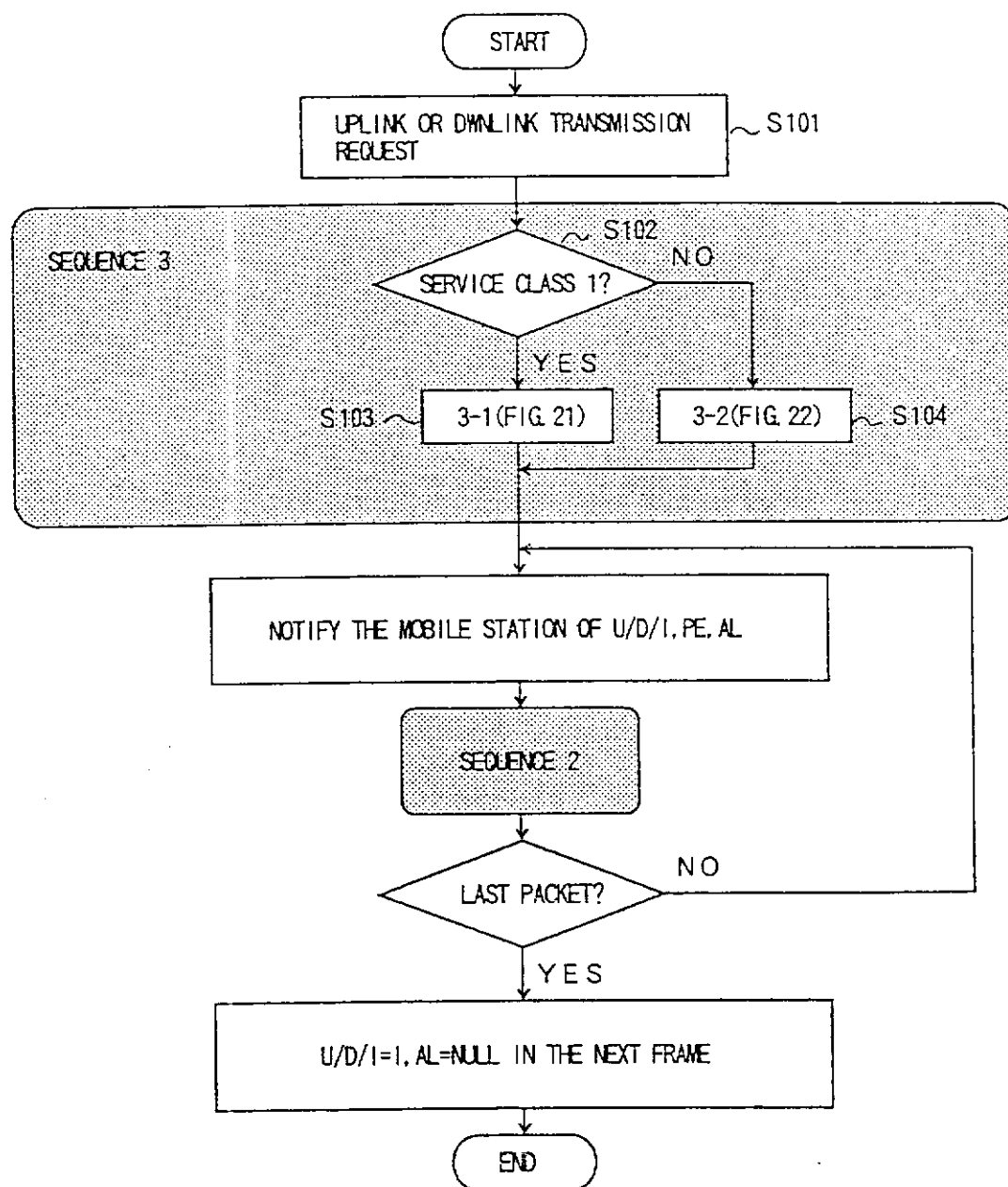


FIG. 19



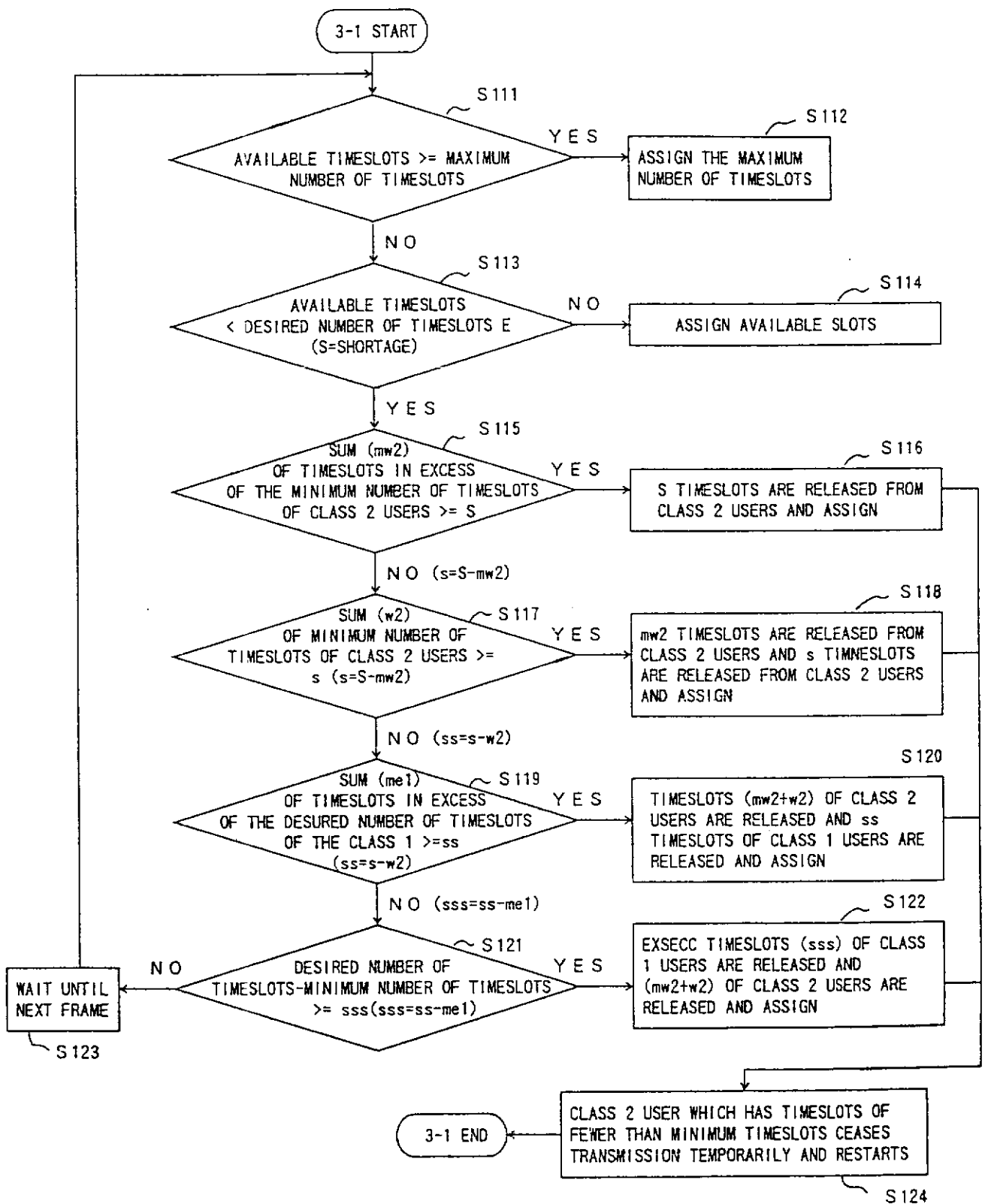
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FIG. 20



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FIG. 21



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FIG. 22

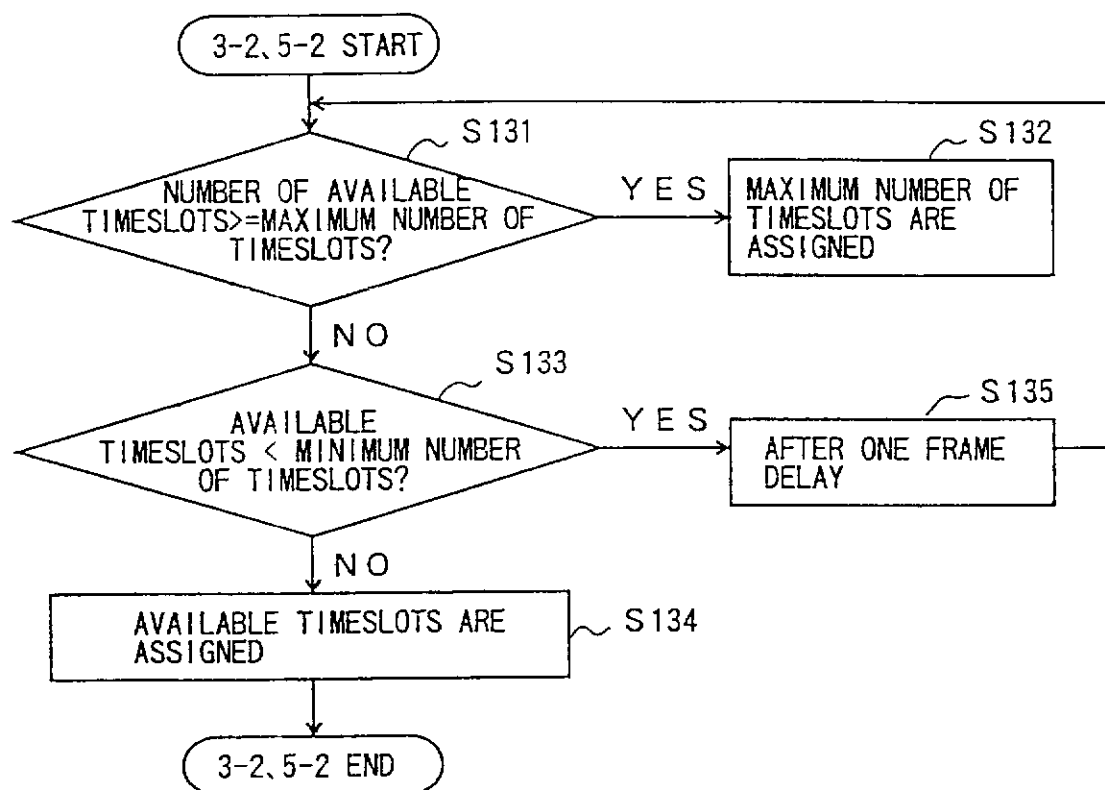


FIG. 23

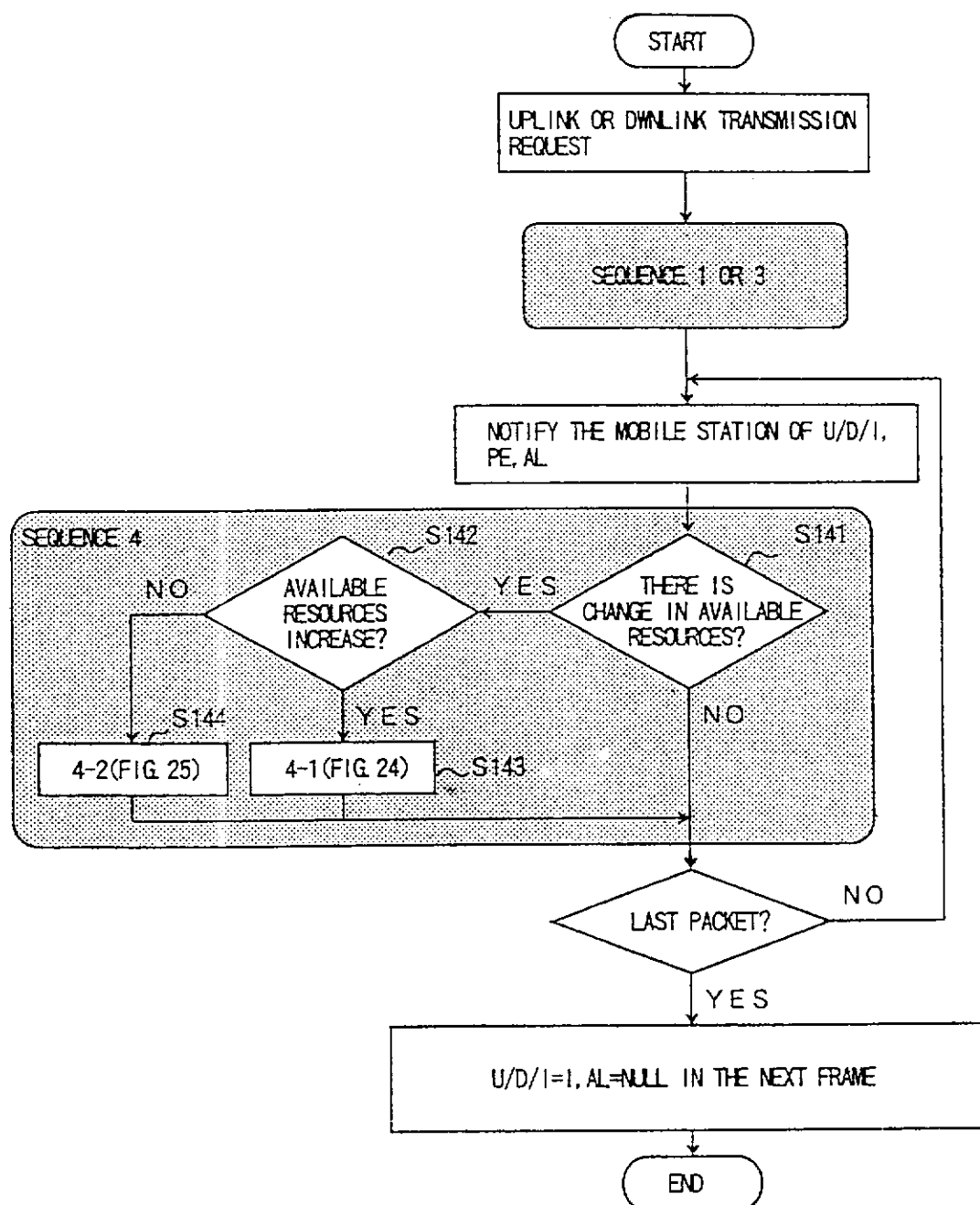


FIG. 24

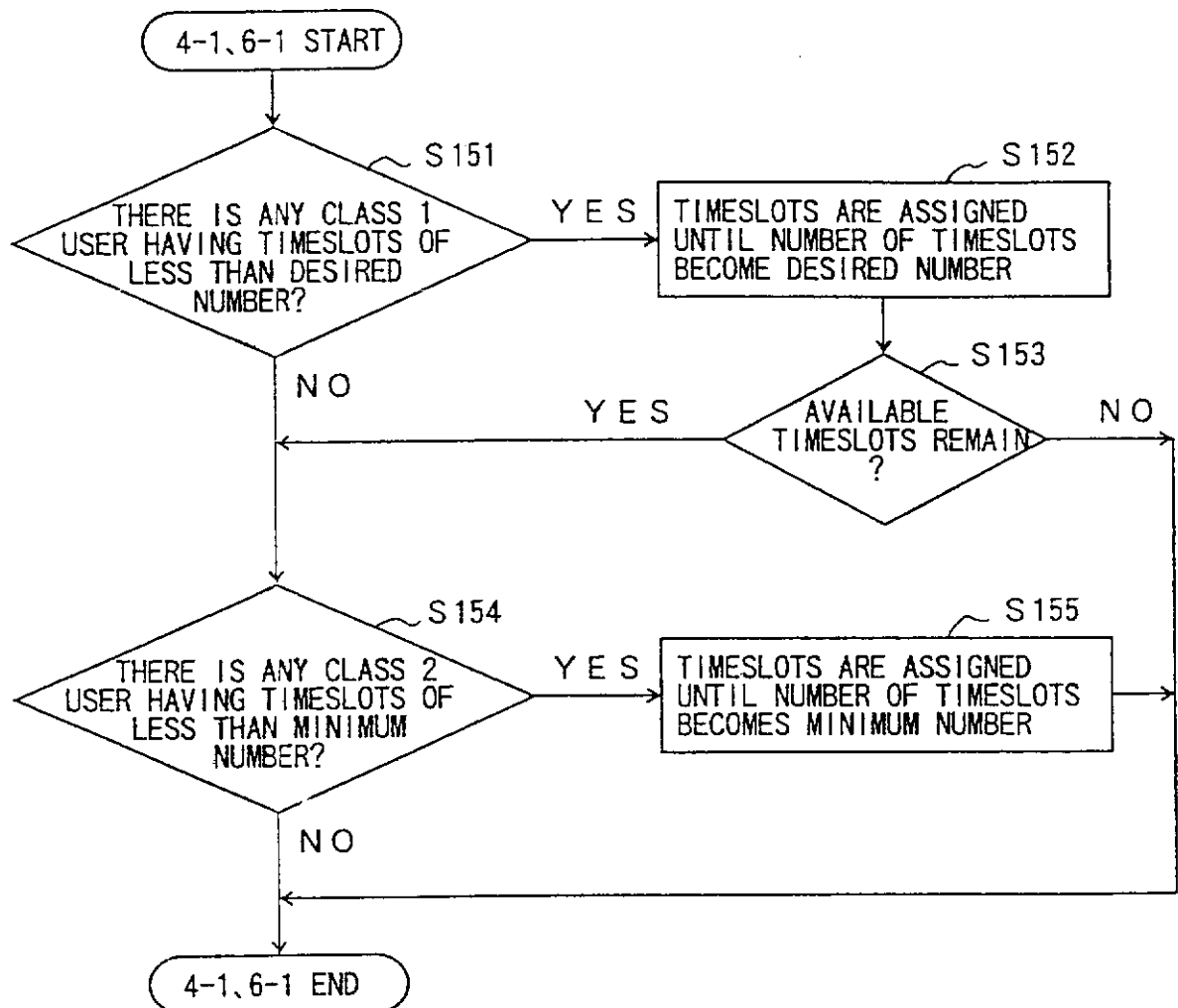
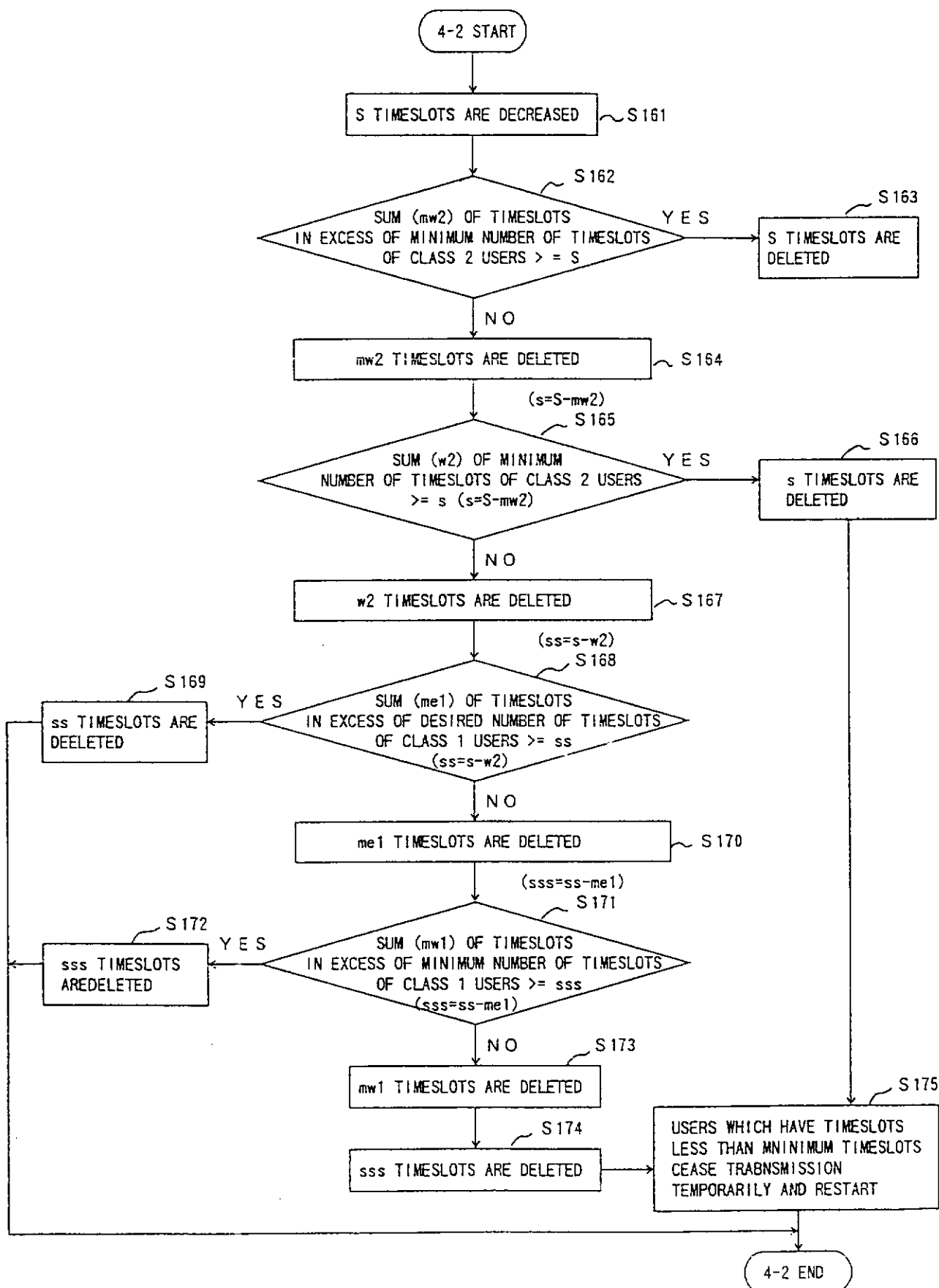
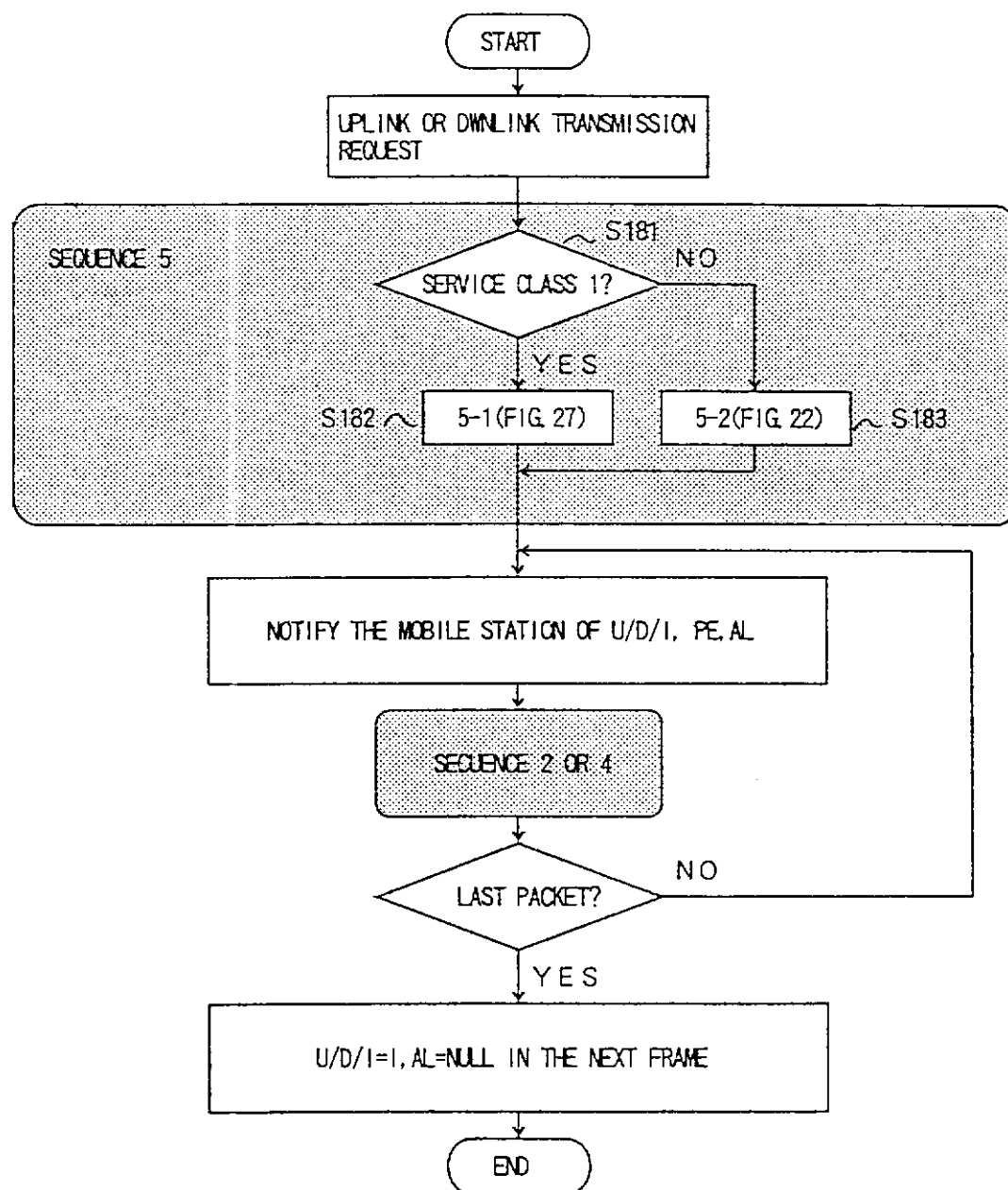


FIG. 25



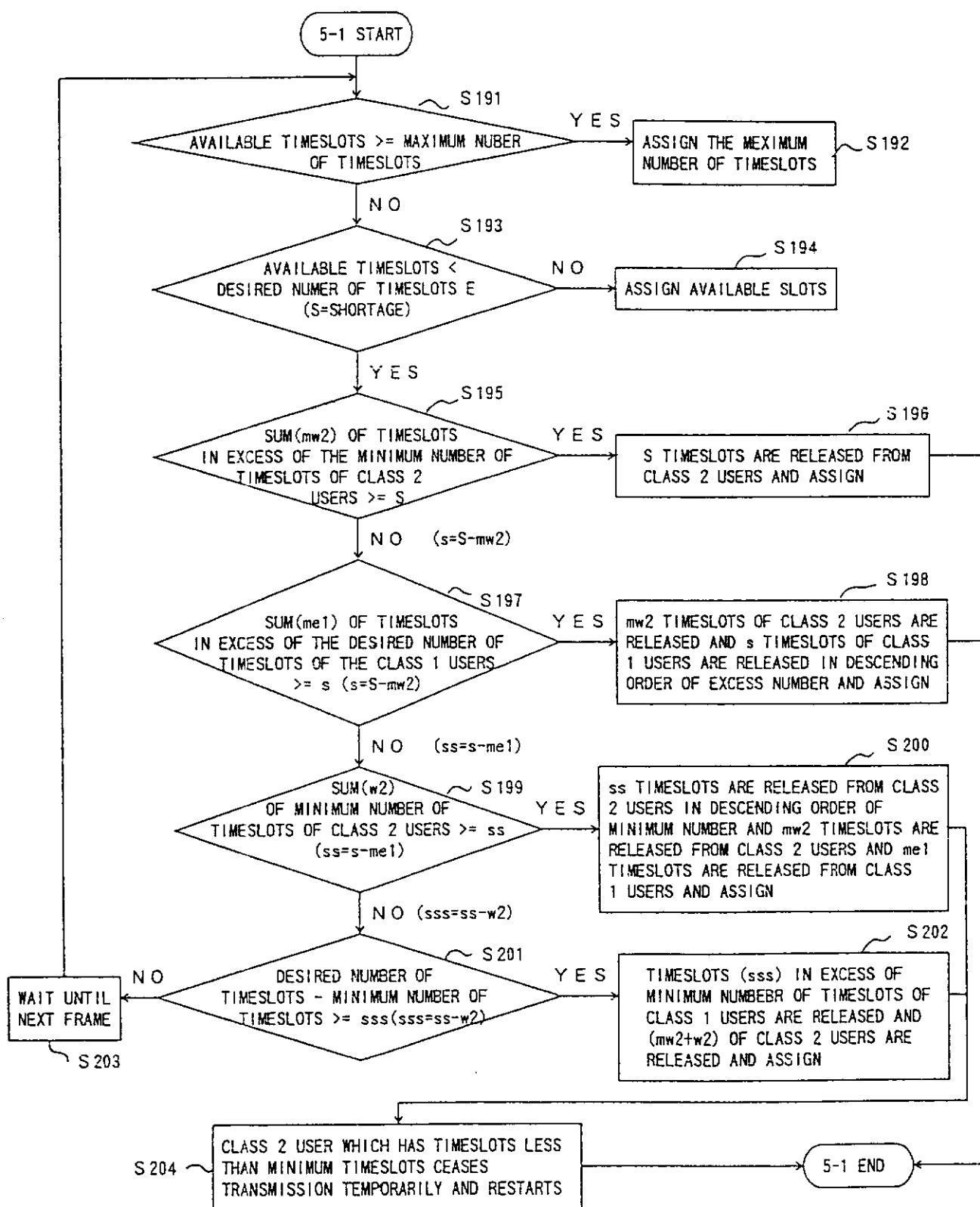
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FIG. 26



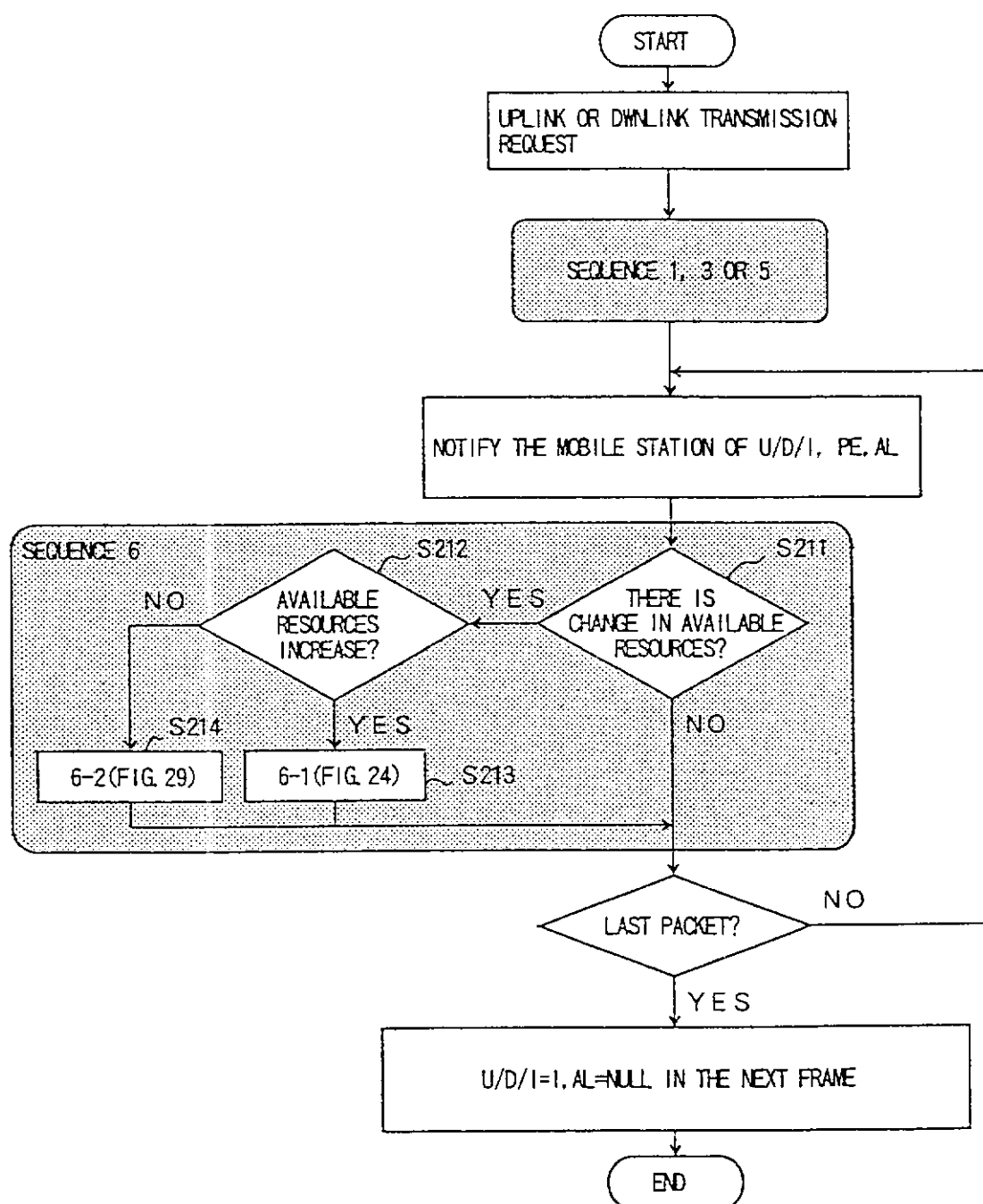
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FIG. 27



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FIG. 28



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FIG. 29

