

COMPUTER PROCESSOR UTILIZING LOGARITHMIC CONVERSION
AND METHOD OF USE THEREOF

5

Abstract of the Disclosure

A computer processor for performing mathematical operations includes a logarithm converter (12) which generates log values on a data bus, a plurality of processing elements connected to the data bus, an inverse-logarithm converter (28) which receives values from the processing elements (16), an accumulator (32) which sums converted values from the inverse-logarithm converter (28), and a control unit (38) for configuring the accumulator to perform various summing operations. The computer processor also includes a switch (36) for providing processor outputs as feedback. An instruction, selected from a set of instructions, is decoded by the control unit (38) to configure the computer processor to perform operations on a data stream.