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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 7 : G06F 9/38

A1

(11) International Publication Number: WO 00/41070

(43) International Publication Date: 13 July 2000 (13.07.00)

(21) International Application Number: PCT/US99/29805

(22) International Filing Date: 16 December 1999 (16.12.99)

(30) Priority Data:
09/222,805 30 December 1998 (30.12.98) US

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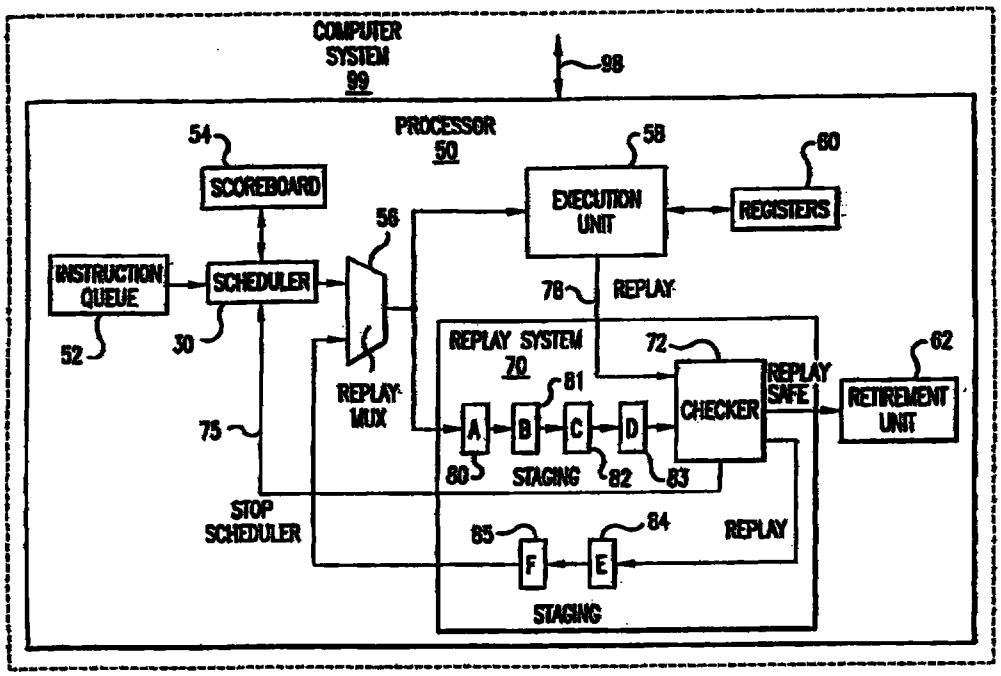
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(81) Designated States: CN, IN, RU, SG, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

Published
With international search report.
Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

(54) Title: A COMPUTER PROCESSOR HAVING A REPLAY UNIT



(57) Abstract

A computer processor that has a checker for receiving an instruction. The checker includes a scoreboard, an input for receiving an external replay signal, and decision logic coupled to the scoreboard and the input. The decision logic determines whether the instruction executed correctly based on both the scoreboard and the external replay signal.