

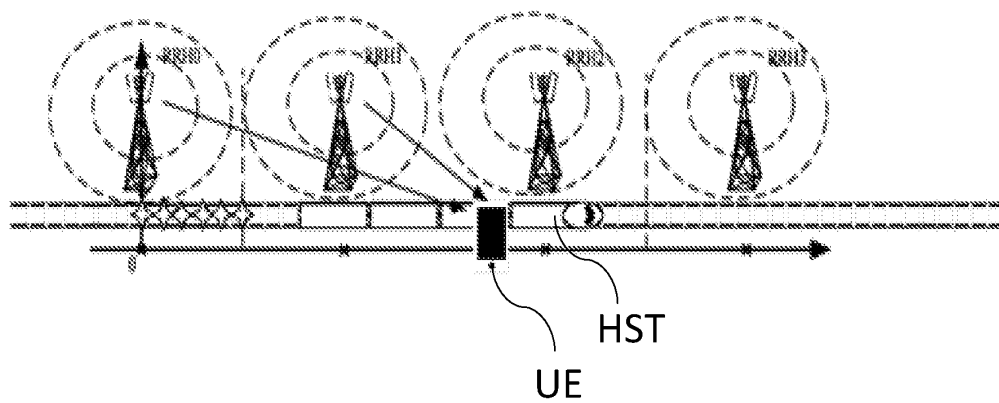


FIG. 1

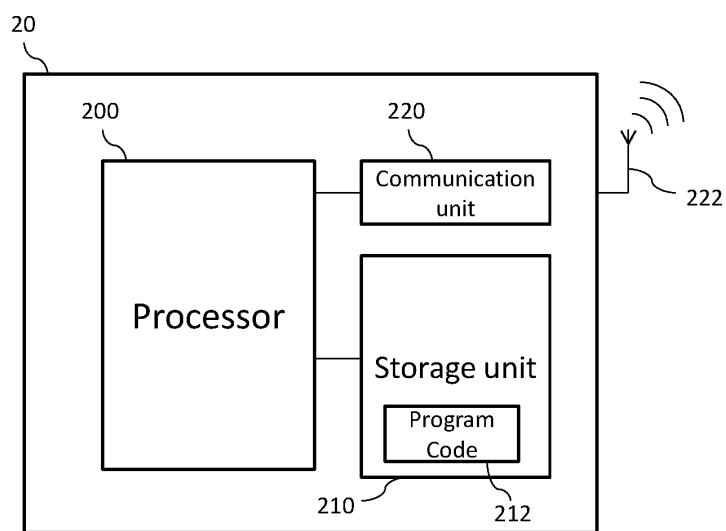


FIG. 2

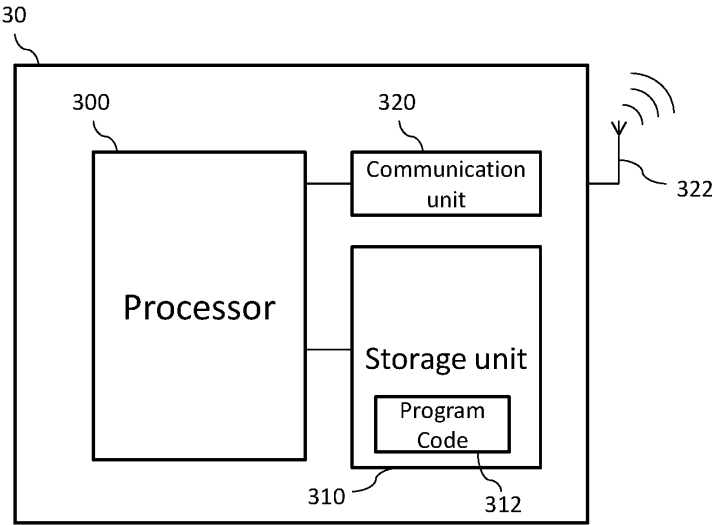


FIG. 3

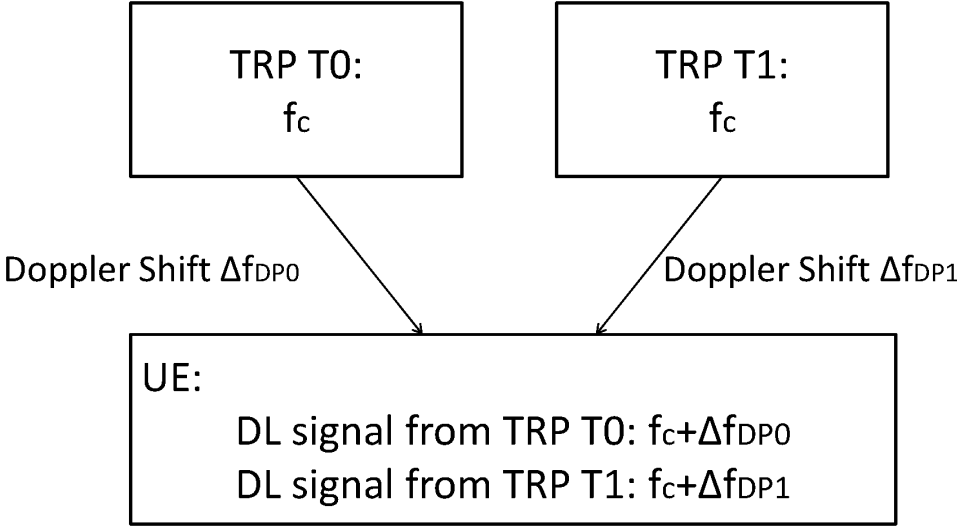


FIG. 4

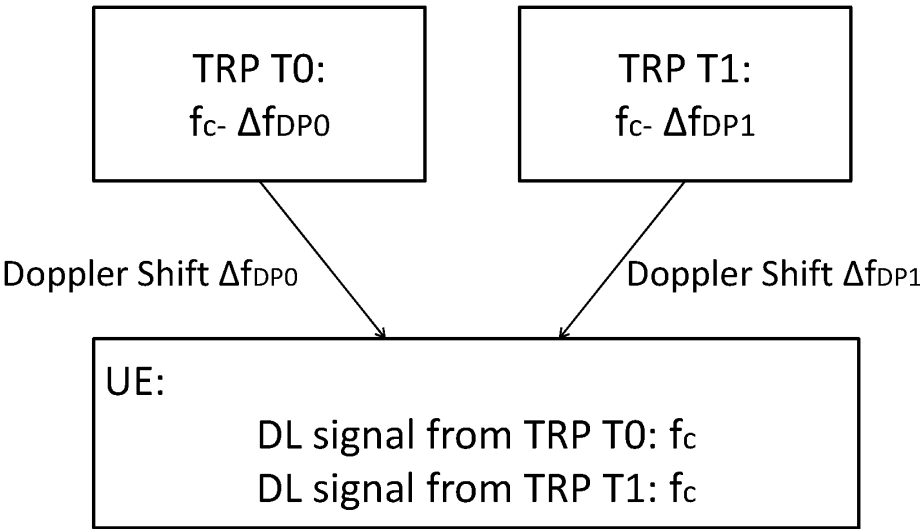


FIG. 5

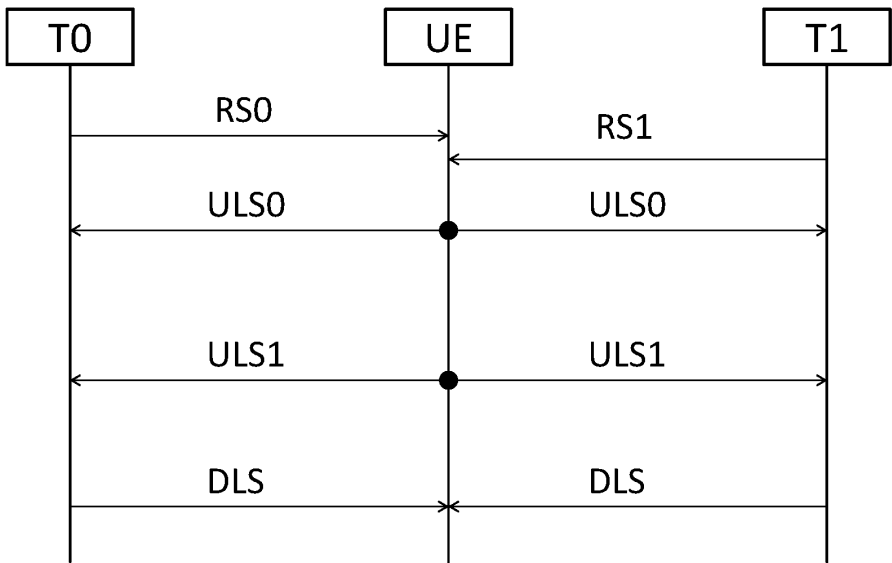


FIG. 6

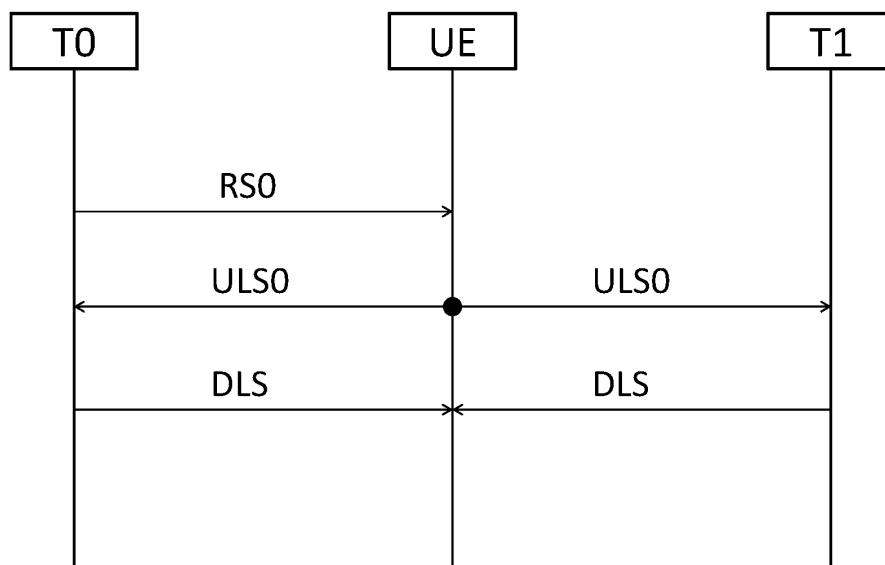


FIG. 7

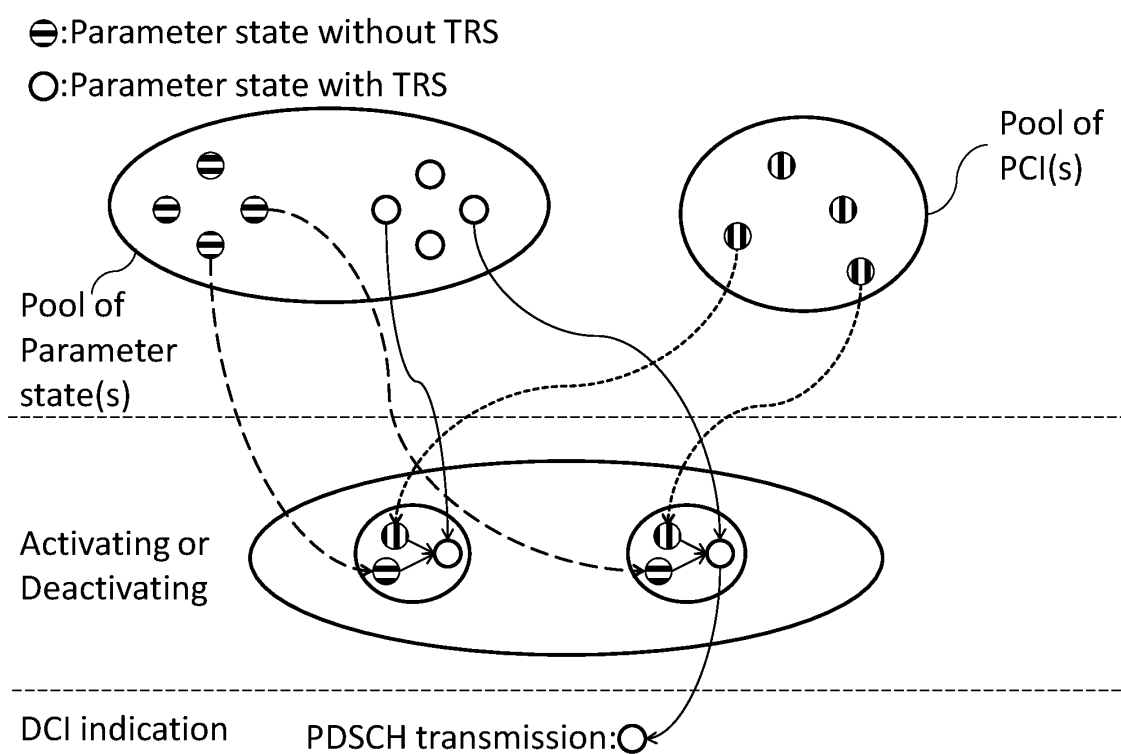


FIG. 8

Parameter state PS1 including TRS-1	Parameter state PS1 including TRS-1	Parameter state PS2 including TRS-6	Parameter state PS4 including TRS-8	Time domain step of 10ms
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FIG. 9

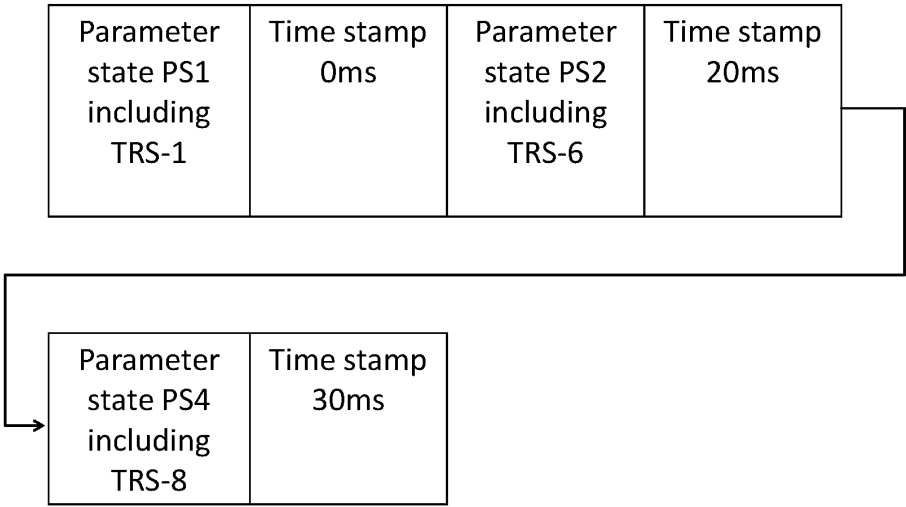


FIG. 10