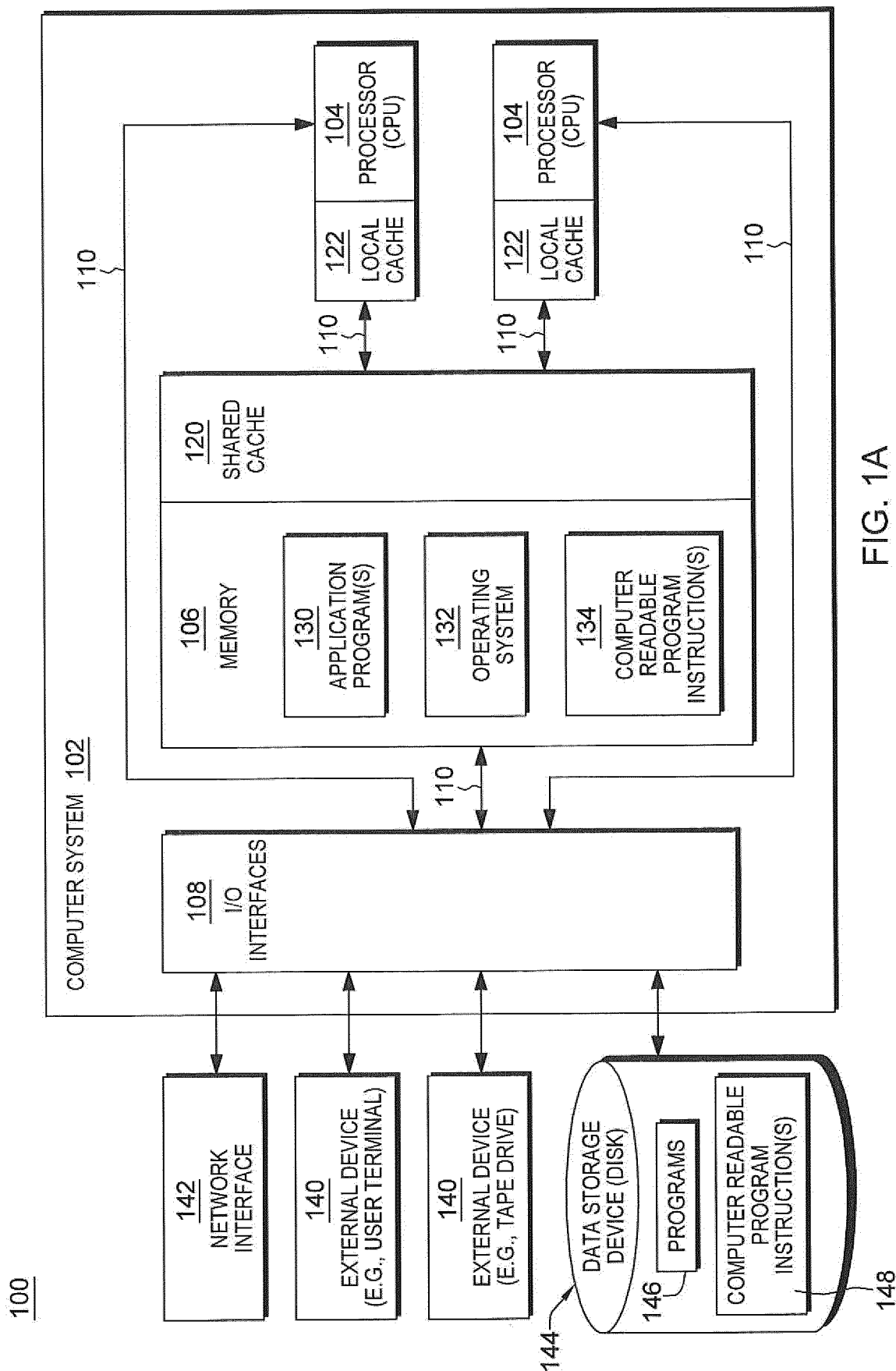


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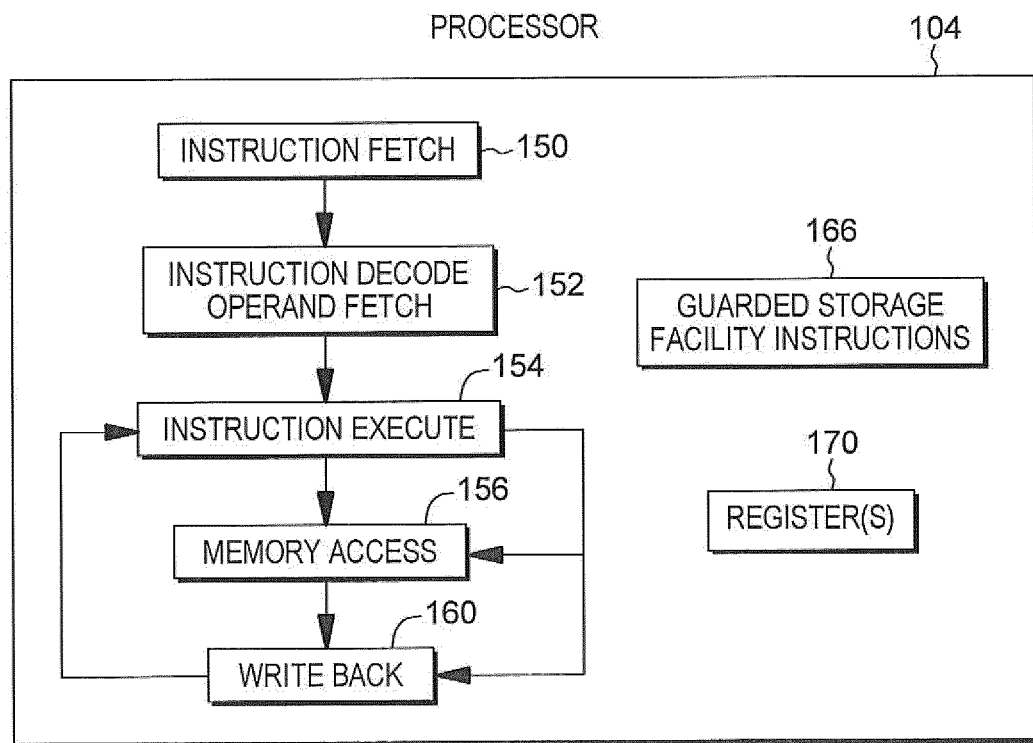


FIG. 1B

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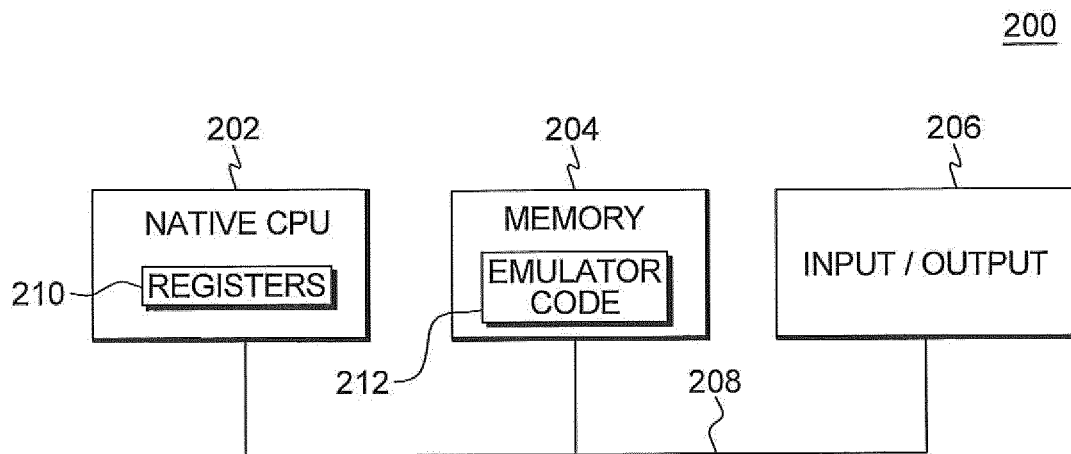


FIG. 2A

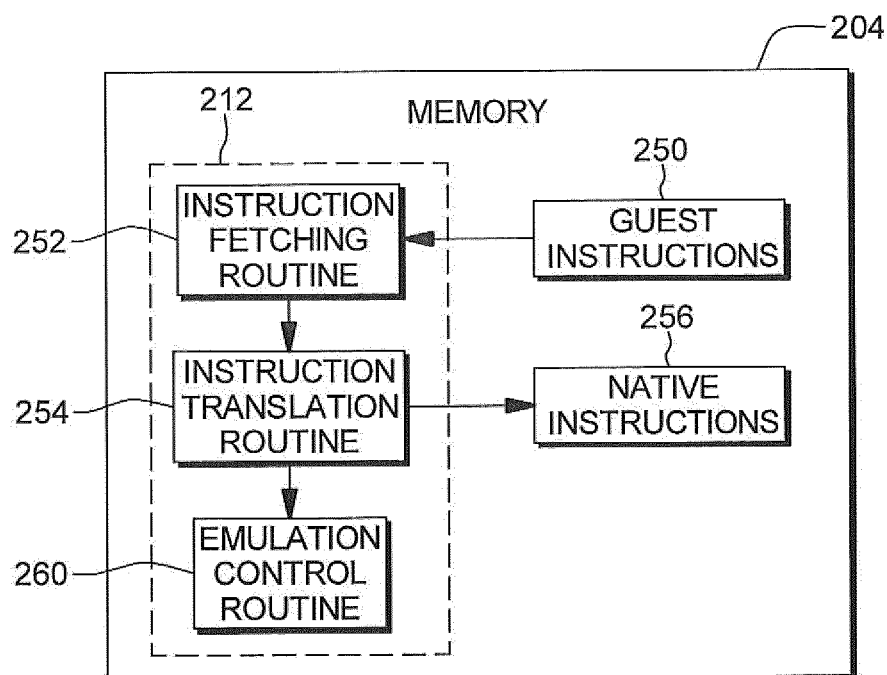


FIG. 2B

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300

GUARDED STORAGE DESIGNATION (GSD)

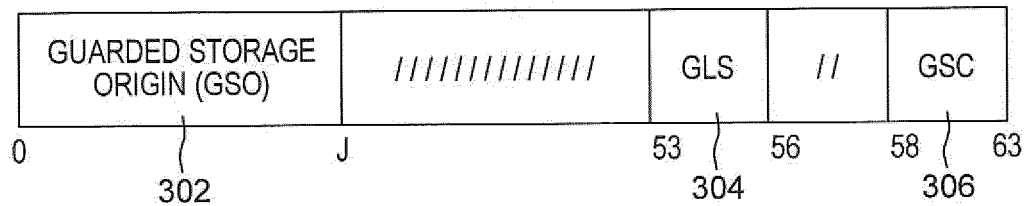


FIG. 3

GSC	GSO		SECTION SIZE
	ALIGN-MENT	GSD BITS	
25	32 M	0-38	512 K
26	64 M	0-37	1 M
27	128 M	0-36	2 M
28	256 M	0-35	4 M
29	512 M	0-34	8 M
30	1 G	0-33	16 M
31	2 G	0-32	32 M
32	4 G	0-31	64 M
33	8 G	0-30	128 M
34	16 G	0-29	256 M
35	32 G	0-28	512 M
36	64 G	0-27	1 G
37	128 G	0-27	2 G
38	256 G	0-25	4 G
39	512 G	0-24	8 G
40	1 T	0-23	16 G

GSC	GSO		SECTION SIZE
	ALIGN-MENT	GSD BITS	
41	2 T	0-22	32 G
42	4 T	0-21	64 G
43	8 T	0-20	128 G
44	16 T	0-19	256 G
45	32 T	0-18	512 G
46	64 T	0-17	1 T
47	128 T	0-16	2 T
48	256 T	0-15	4 T
49	512 T	0-14	8 T
50	1 P	0-13	16 T
51	2 P	0-12	32 T
52	4 P	0-11	64 T
53	8 P	0-10	128 T
54	16 P	0-9	256 T
55	32 P	0-8	512 T
56	64 P	0-7	1 P

FIG. 4

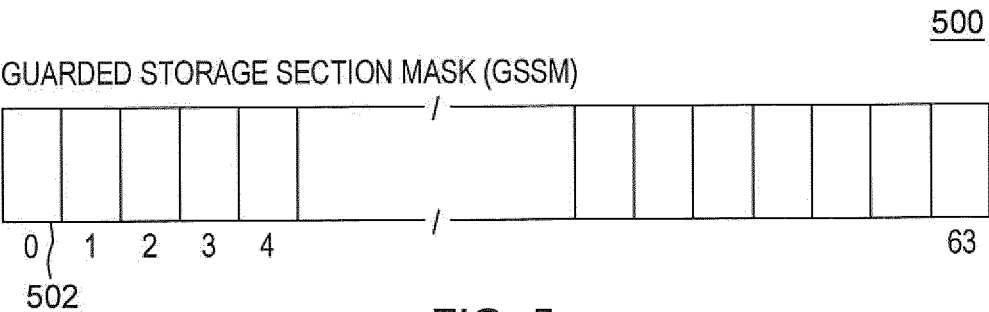


FIG. 5

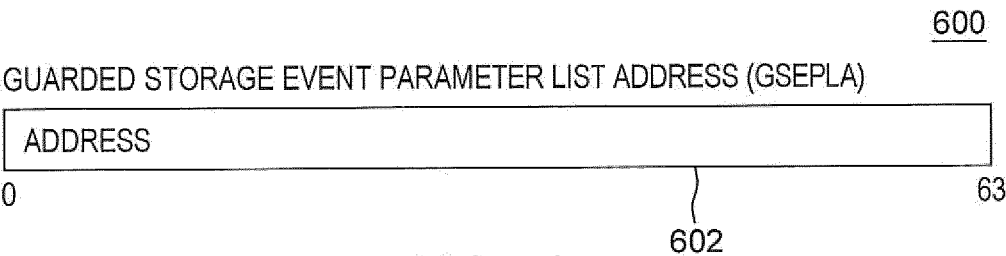


FIG. 6A

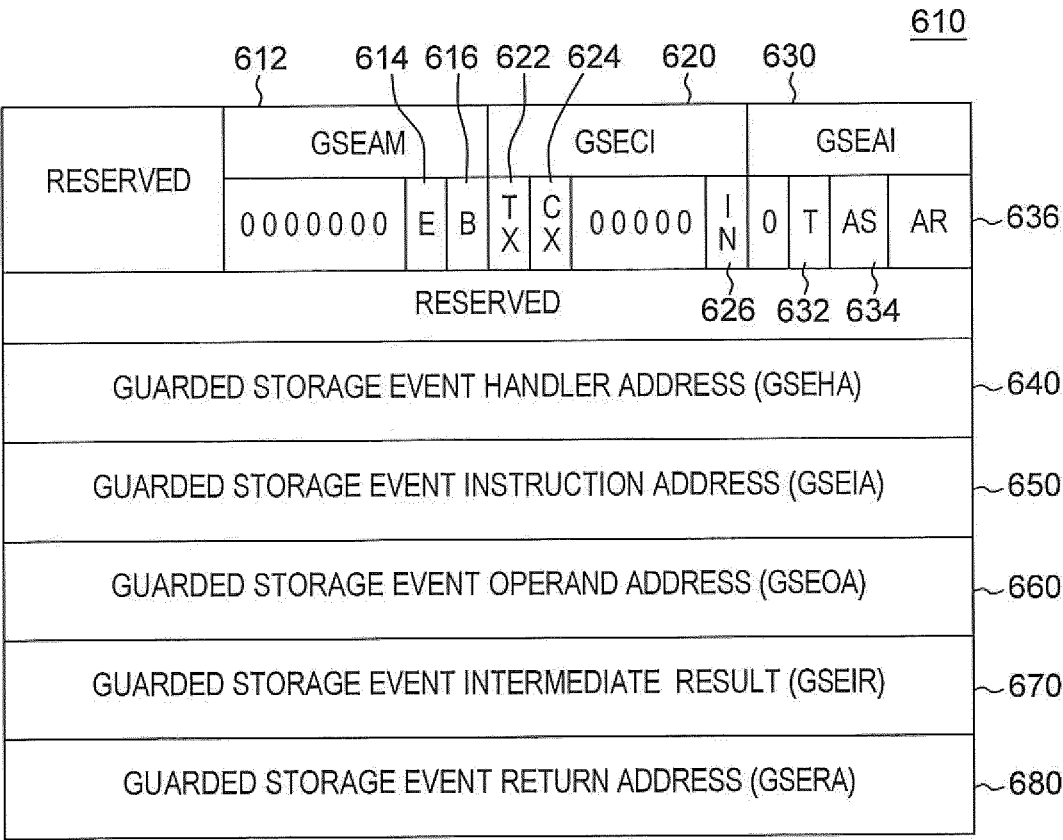


FIG. 6B

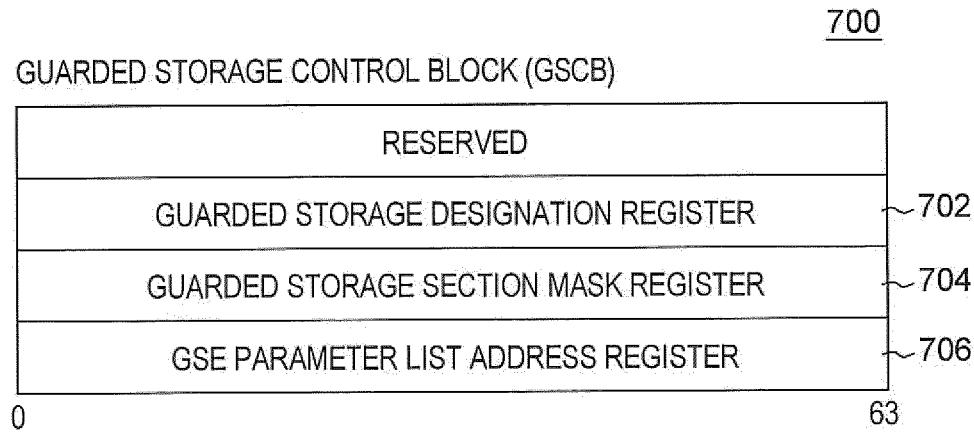


FIG. 7

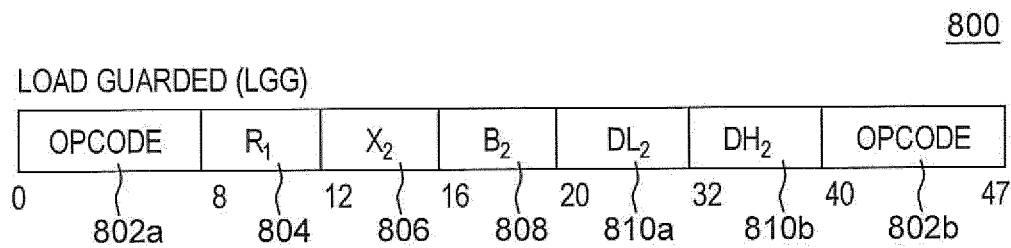


FIG. 8

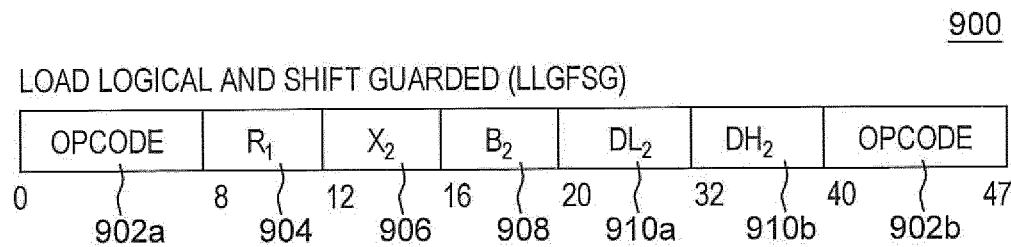


FIG. 9

1000

LOADED GUARDED STORAGE CONTROLS (LGSC)

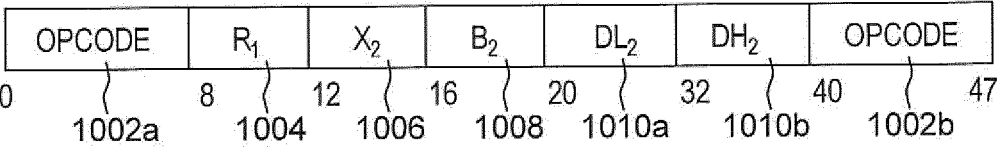


FIG. 10

1100

STORE GUARDED STORAGE CONTROLS (STGSC)

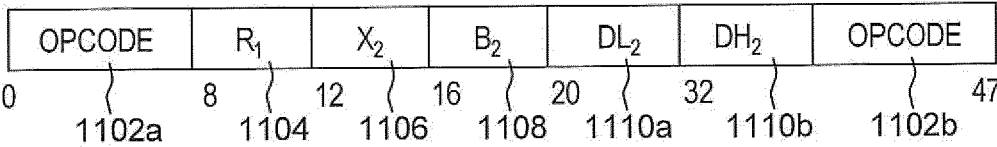


FIG. 11

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1200

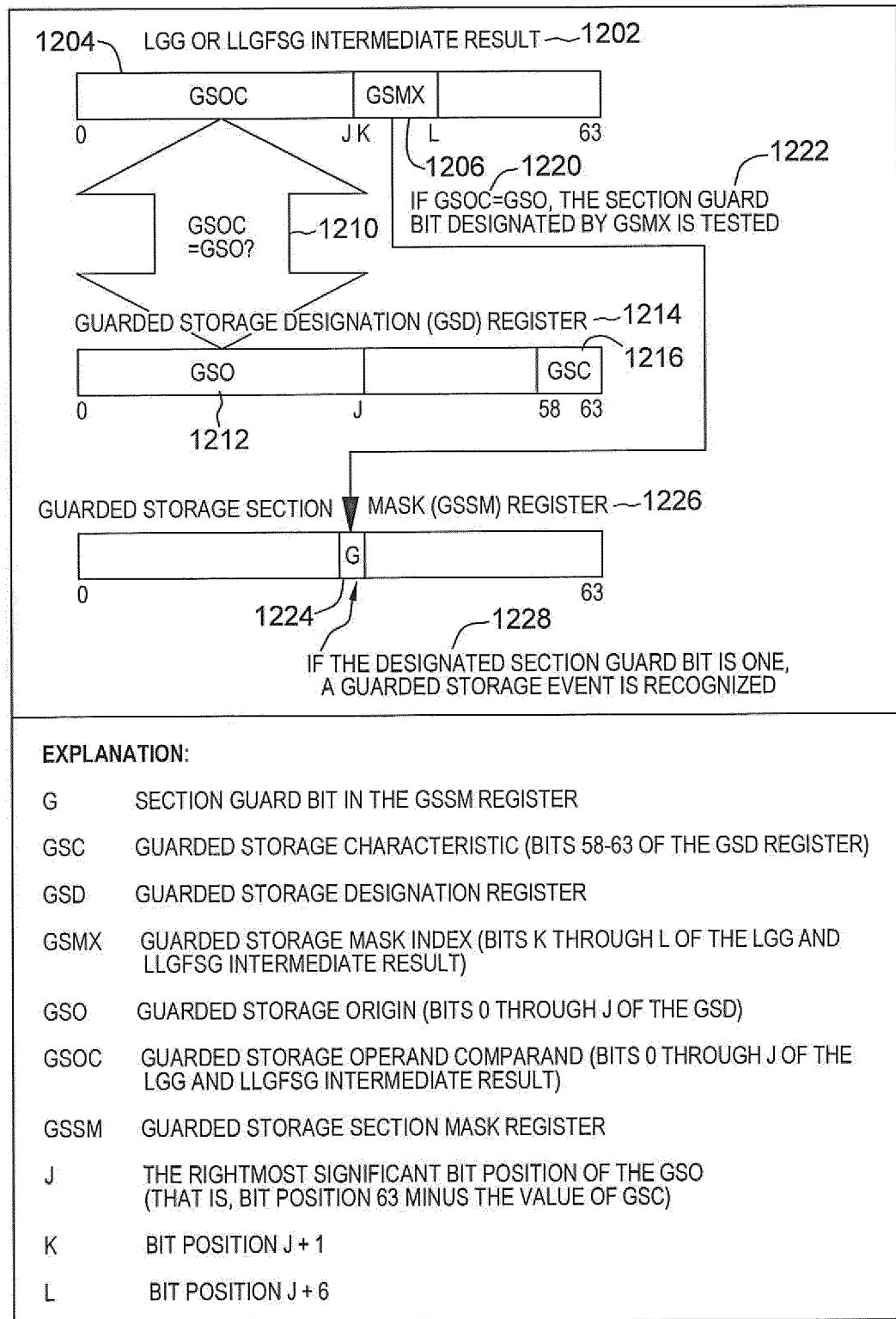


FIG. 12

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1302 OFFSET		1304 CONTENT	1306 MINIMUM LC
HEX	DEC		
000-1FF	0-511	VECTOR REGISTERS 0-31	10
200-3FF	512-1,024	RESERVED	
400-407	1,024-1,031	ZEROS	11
408-40F	1,033-1,039	GSD REGISTER 1306	
410-417	1,040-1,047	GSSM REGISTER 1308	
418-41F	1,048-1,055	GSEPL REGISTER 1310	

FIG. 13A

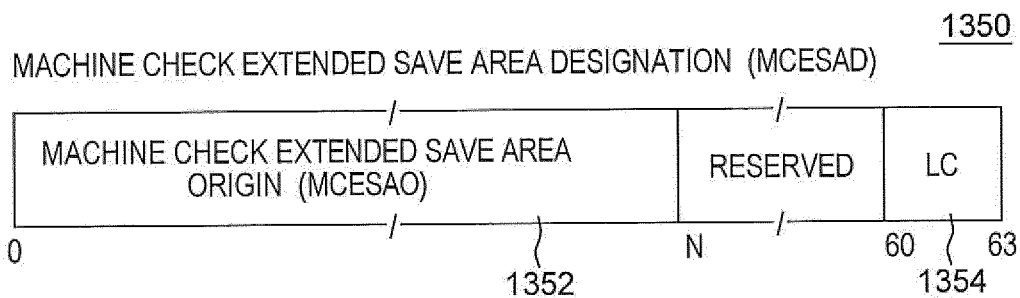


FIG. 13B

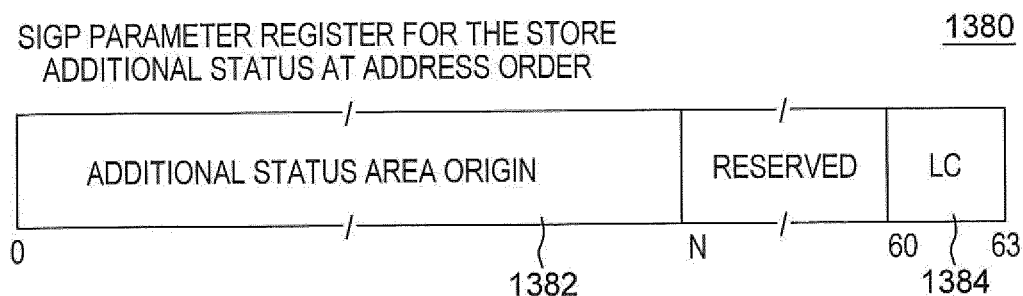


FIG. 13C

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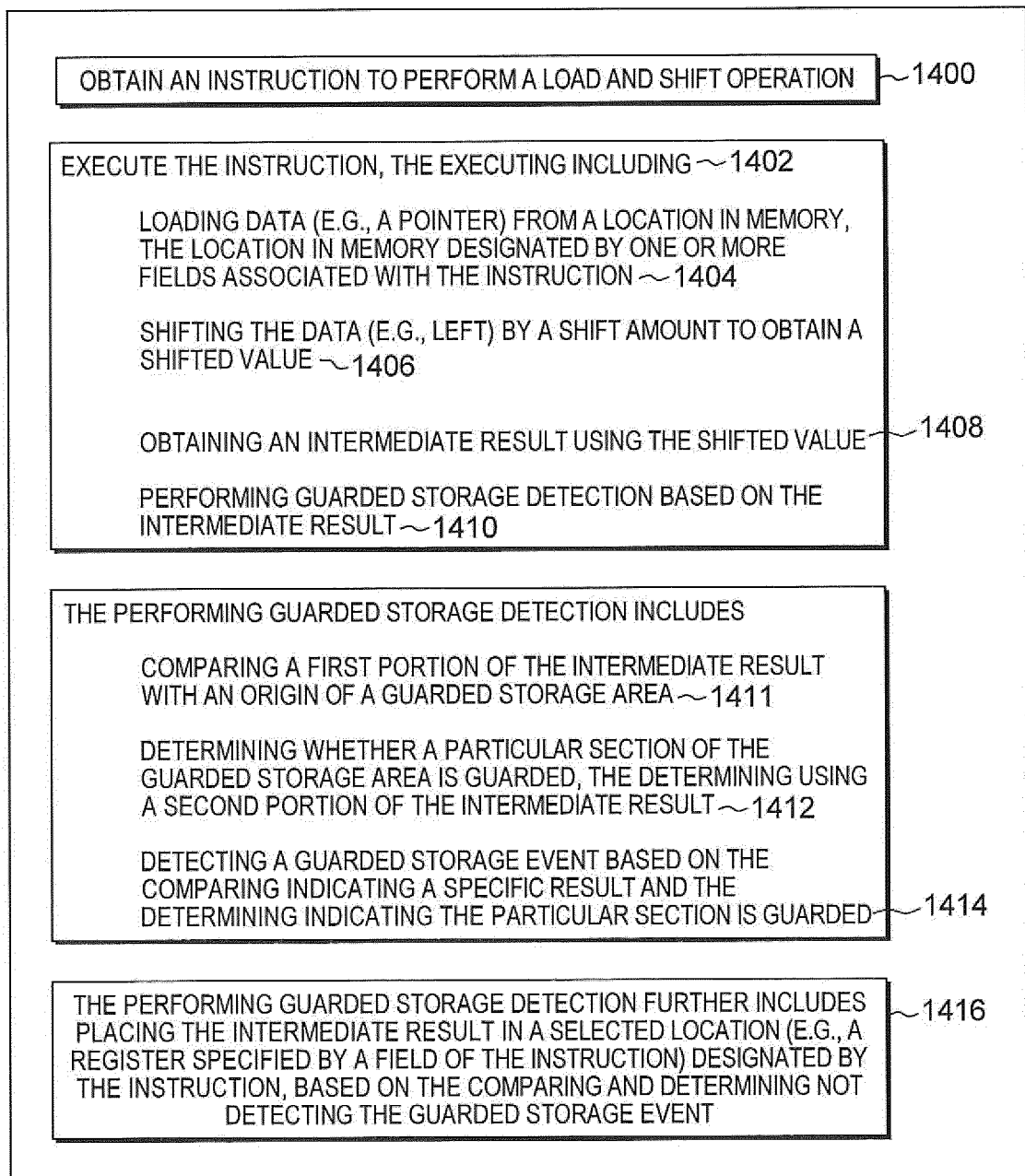


FIG. 14A

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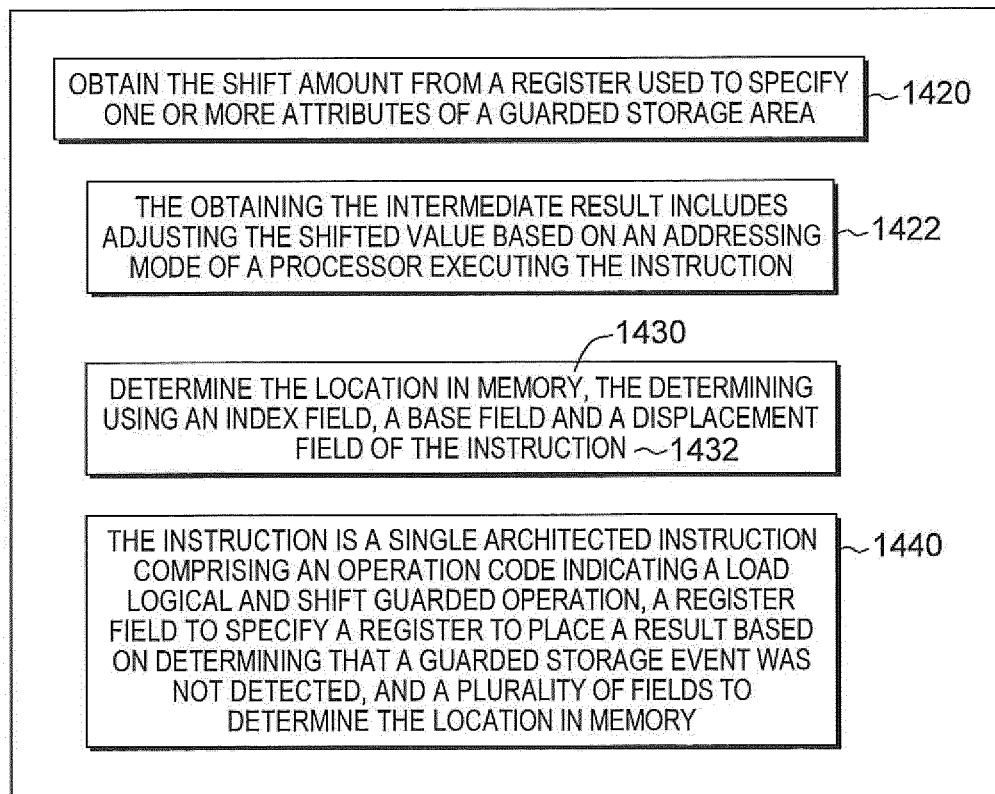


FIG. 14B