

Figure 1: Component Description of current SURF system (Prior Art)

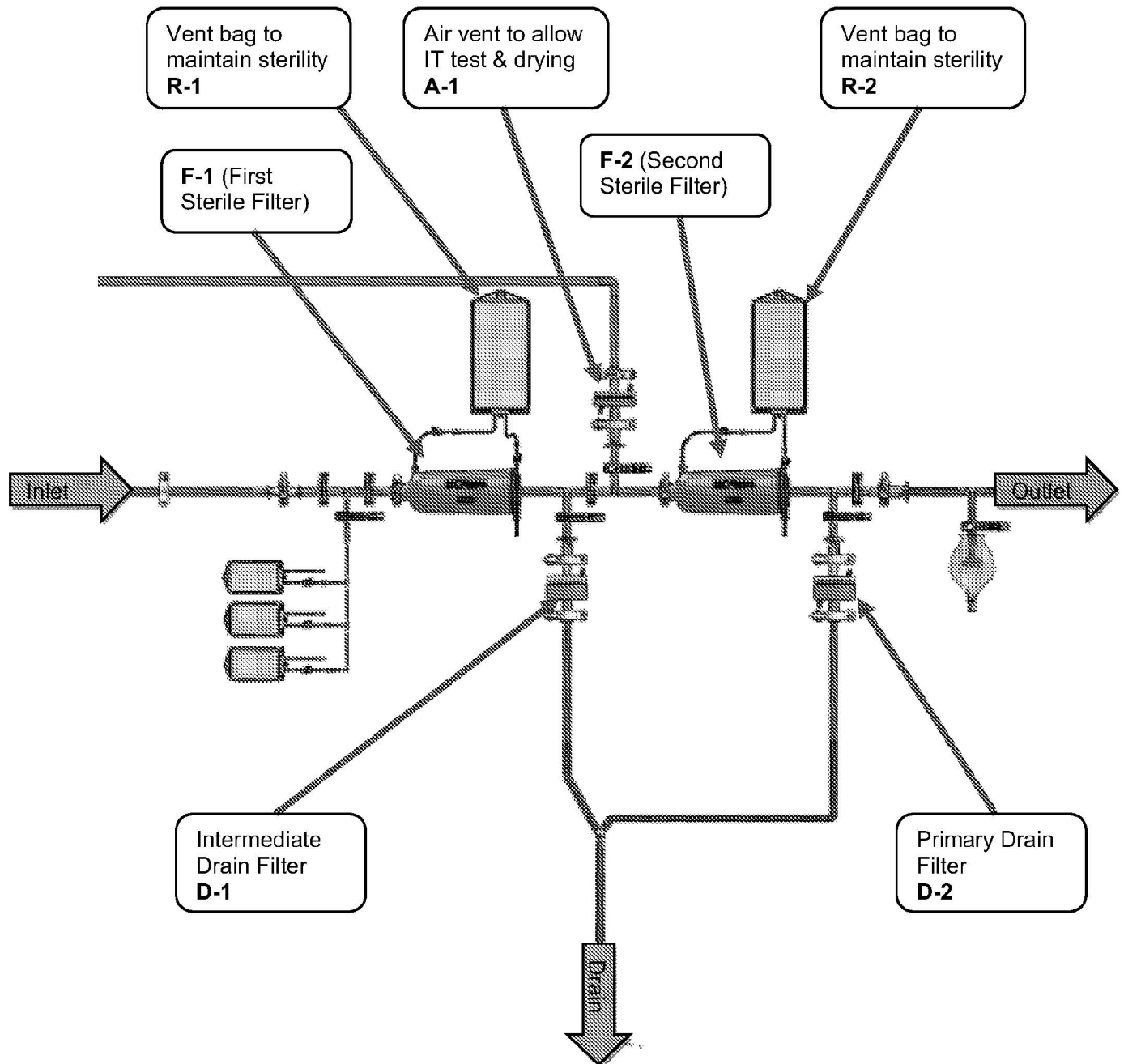


Figure 2: Existing SURF operation: Flush (Prior Art)

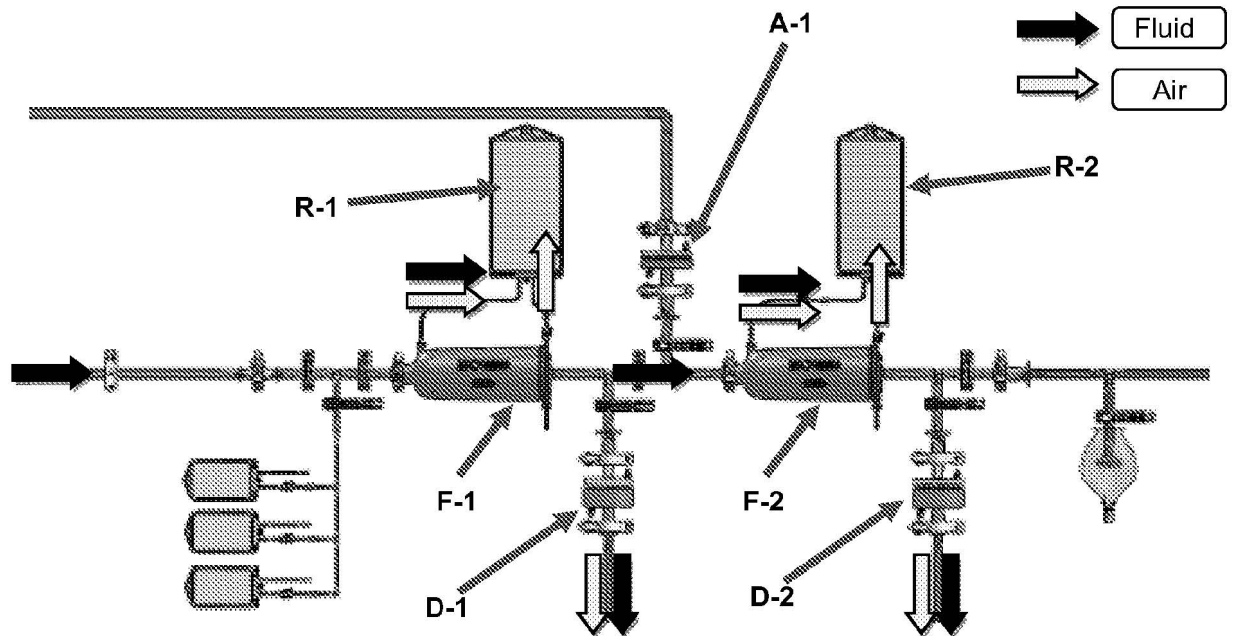


Figure 3: Existing SURF operation: Integrity Test & Dry (Prior Art)

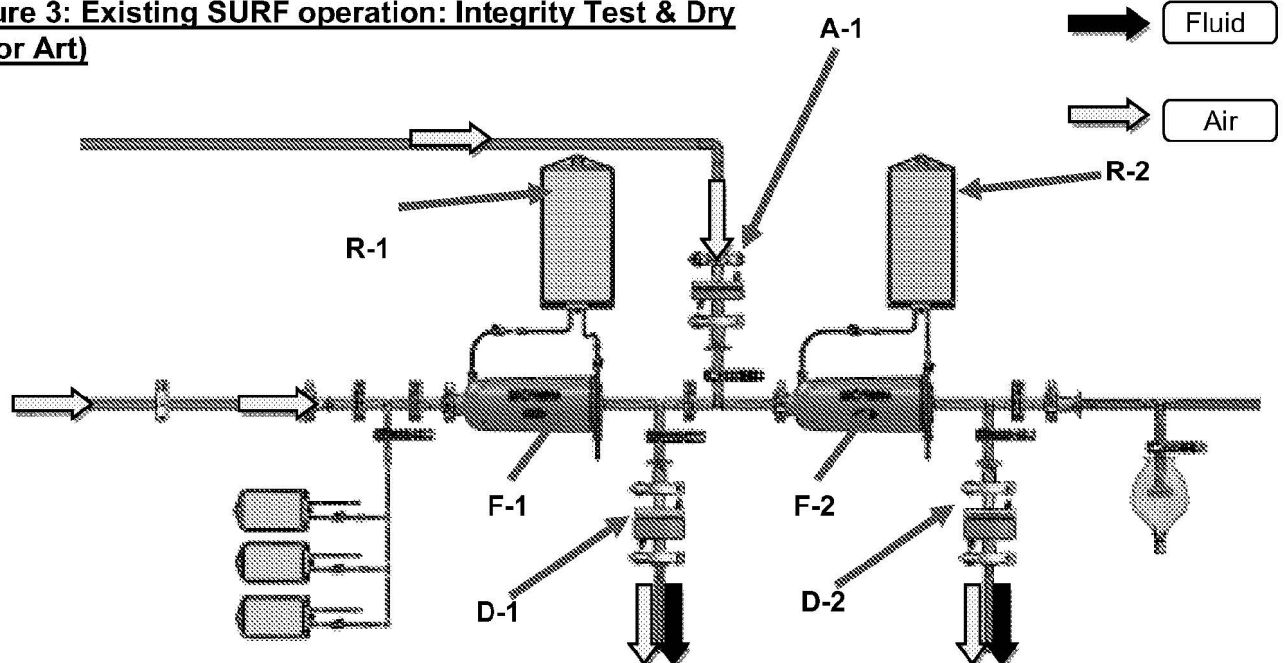


Figure 4: Barrier Filter used in F-2: Flush

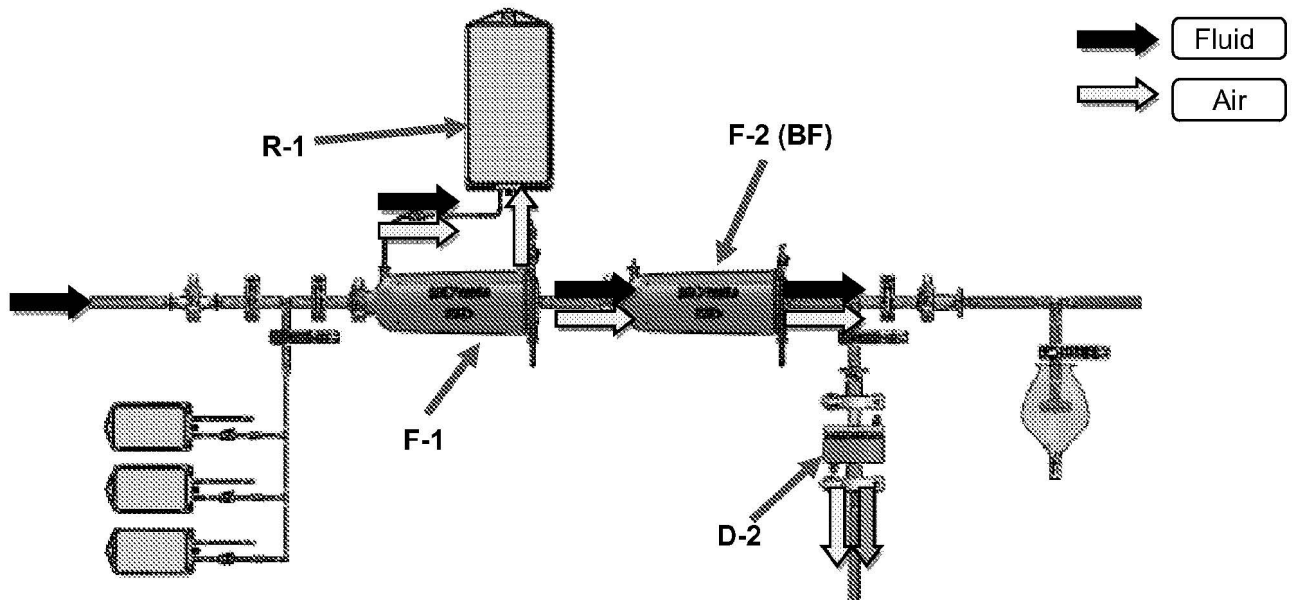


Figure 5: Barrier Filter used in F-2: Integrity Test & Dry

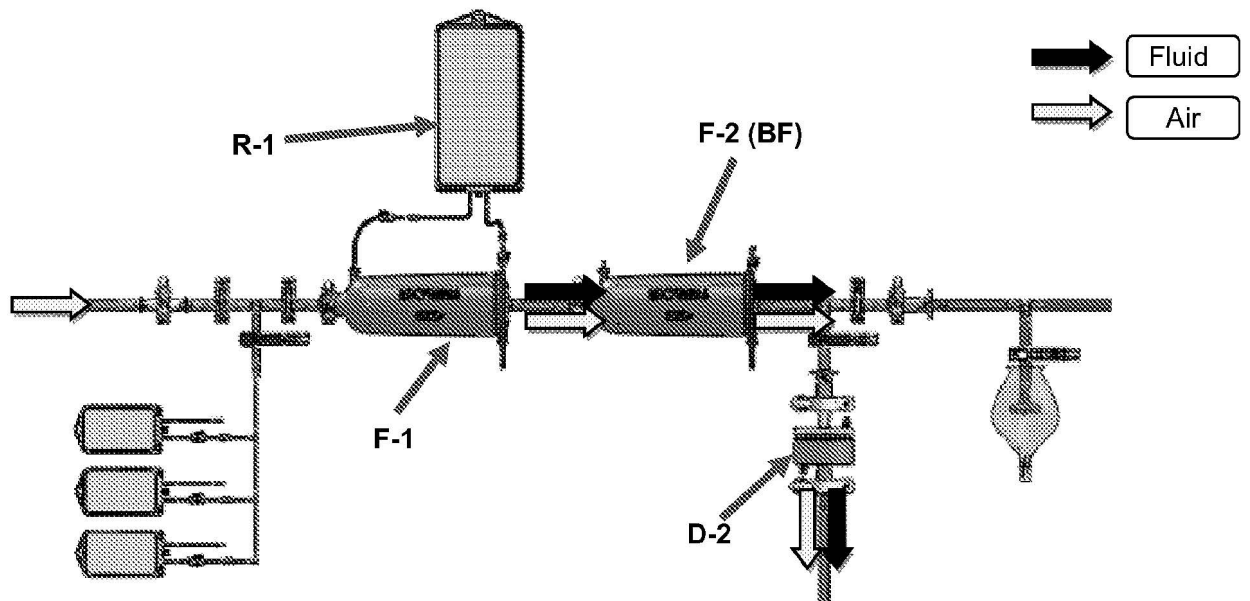


Figure 6: Barrier Filter used in F-1: Flush

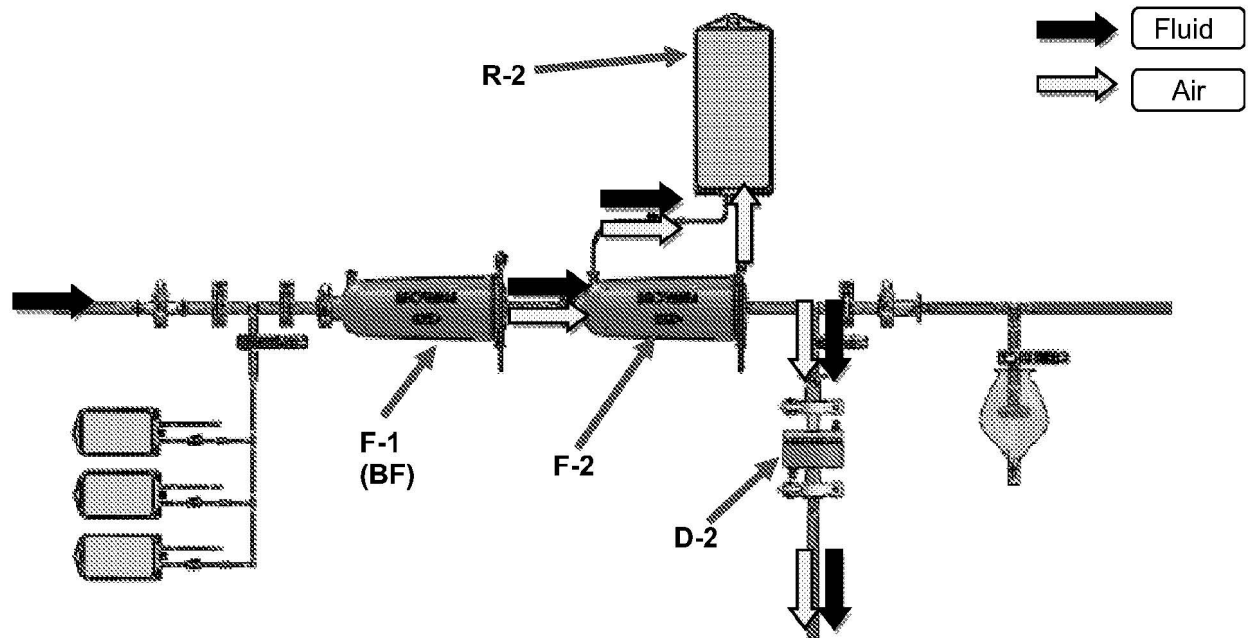


Figure 7: Barrier Filter used in F-1: Integrity Test & Dry

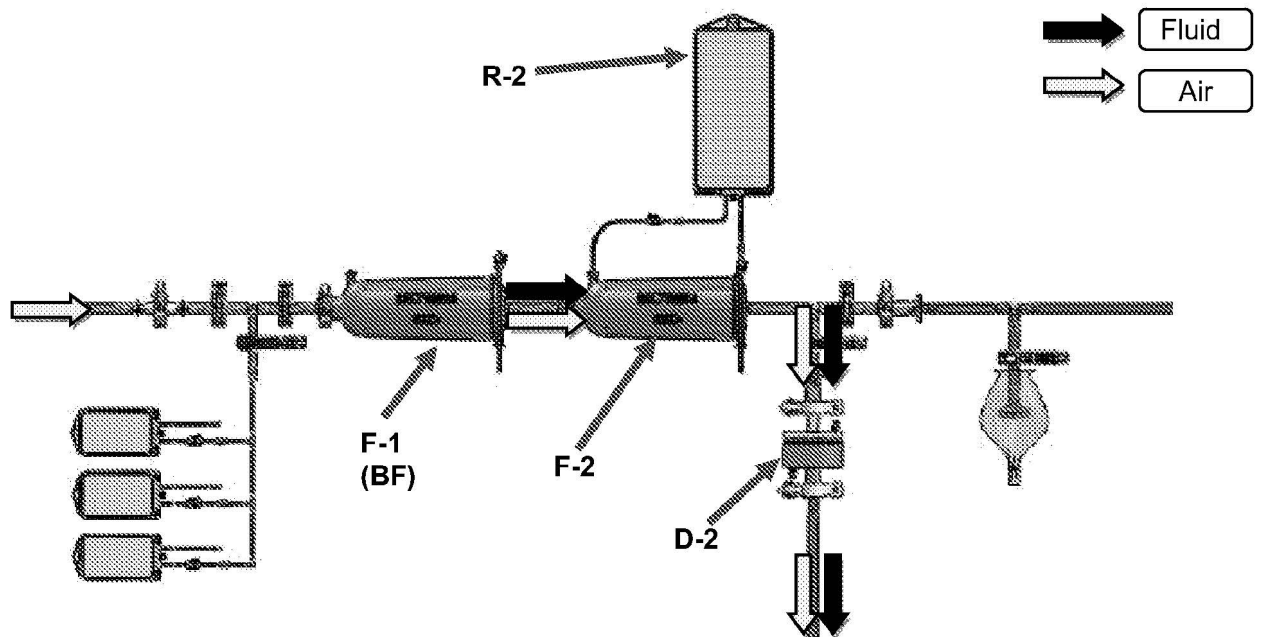


Figure 8: Barrier Filter used in F-1 & F-2: Flush

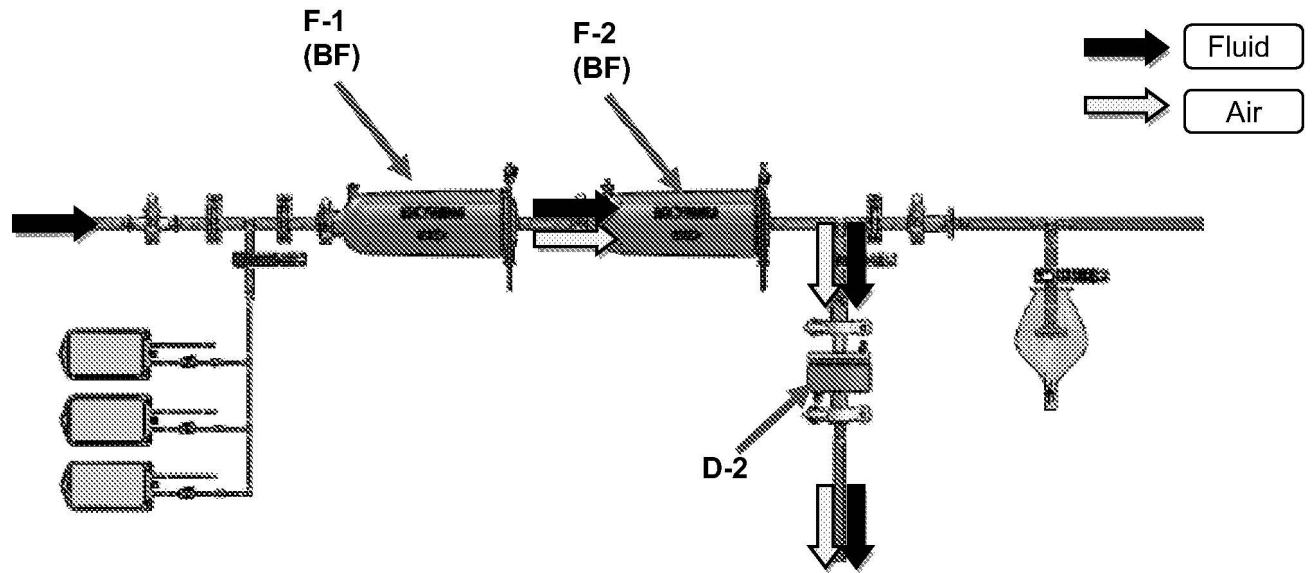


Figure 9: Barrier Filter used in F-1 & F-2: Integrity Test & Dry

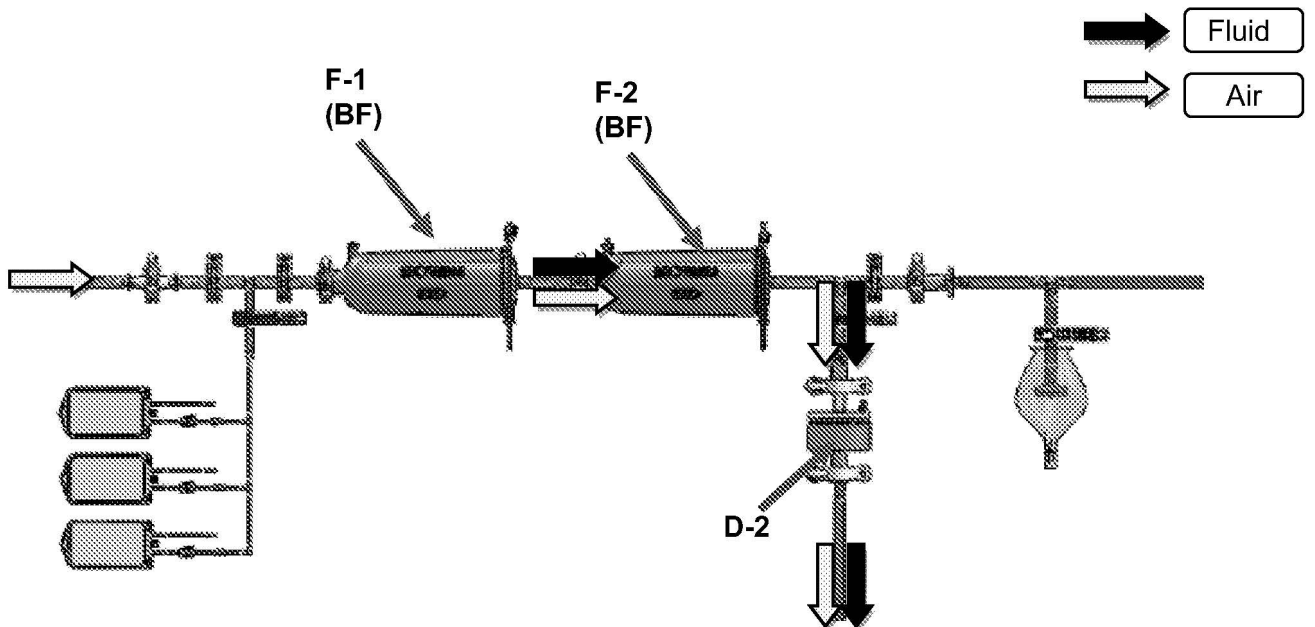


Figure 10: Device Design Utilizing Barrier Filter in F-1 Position

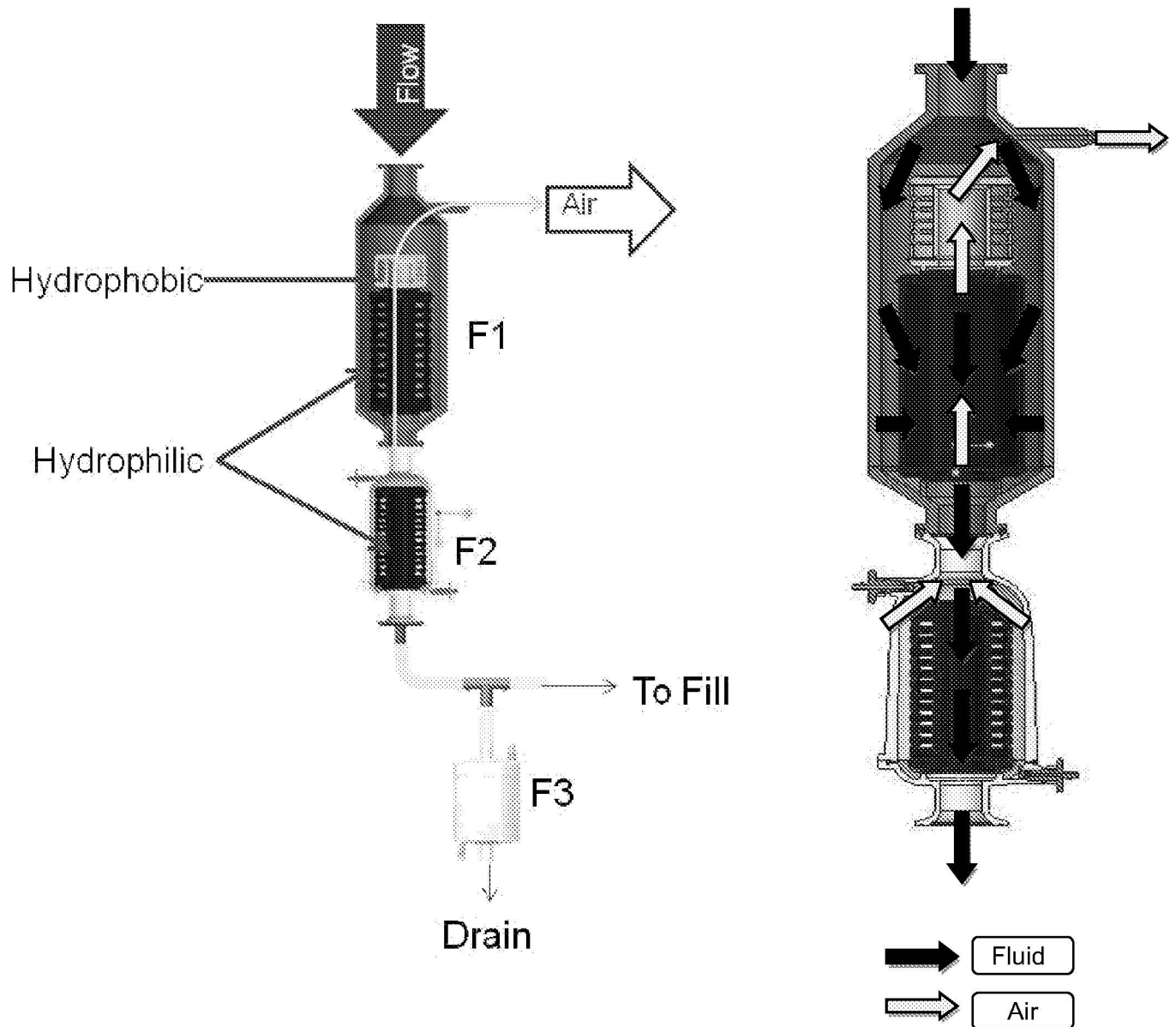


Figure 11: Device Design Utilizing Barrier Filter in F-2 Position

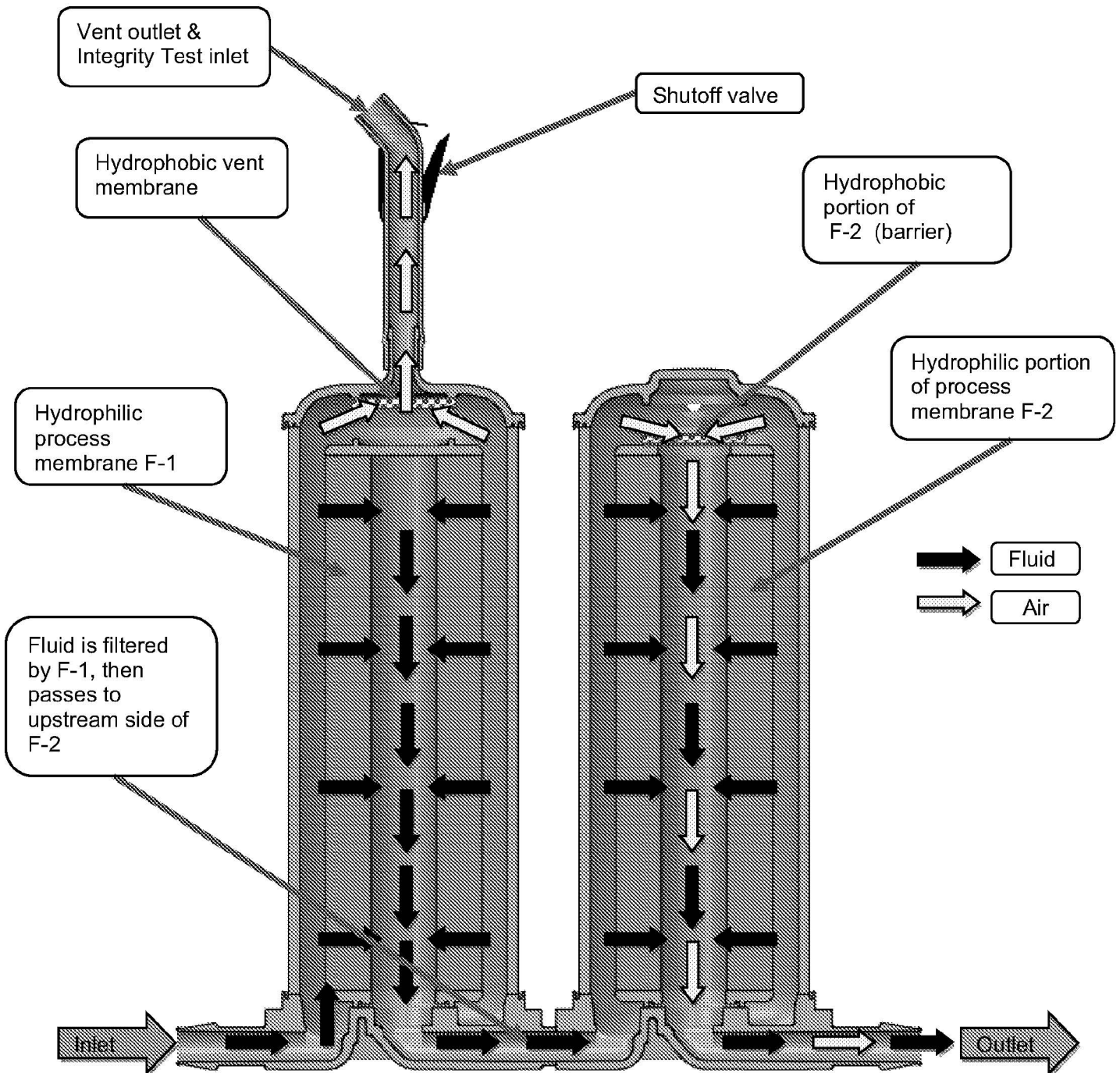


Figure 12: Device Design Utilizing Barrier Filter in F-1 Position

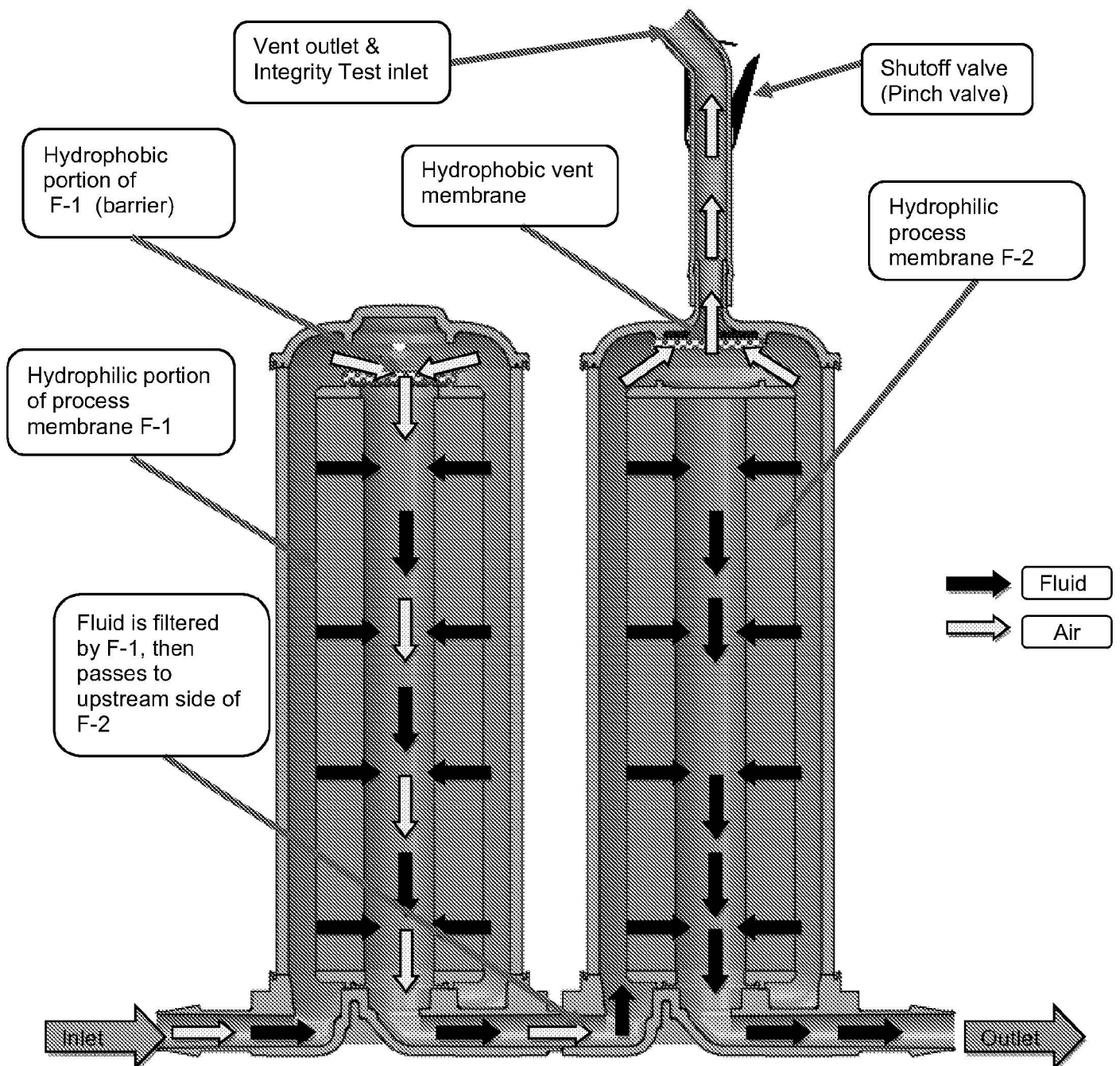


Figure 13: Device Design Utilizing Typical Hydrophilic Filters in F-1 & F-2 Position

