

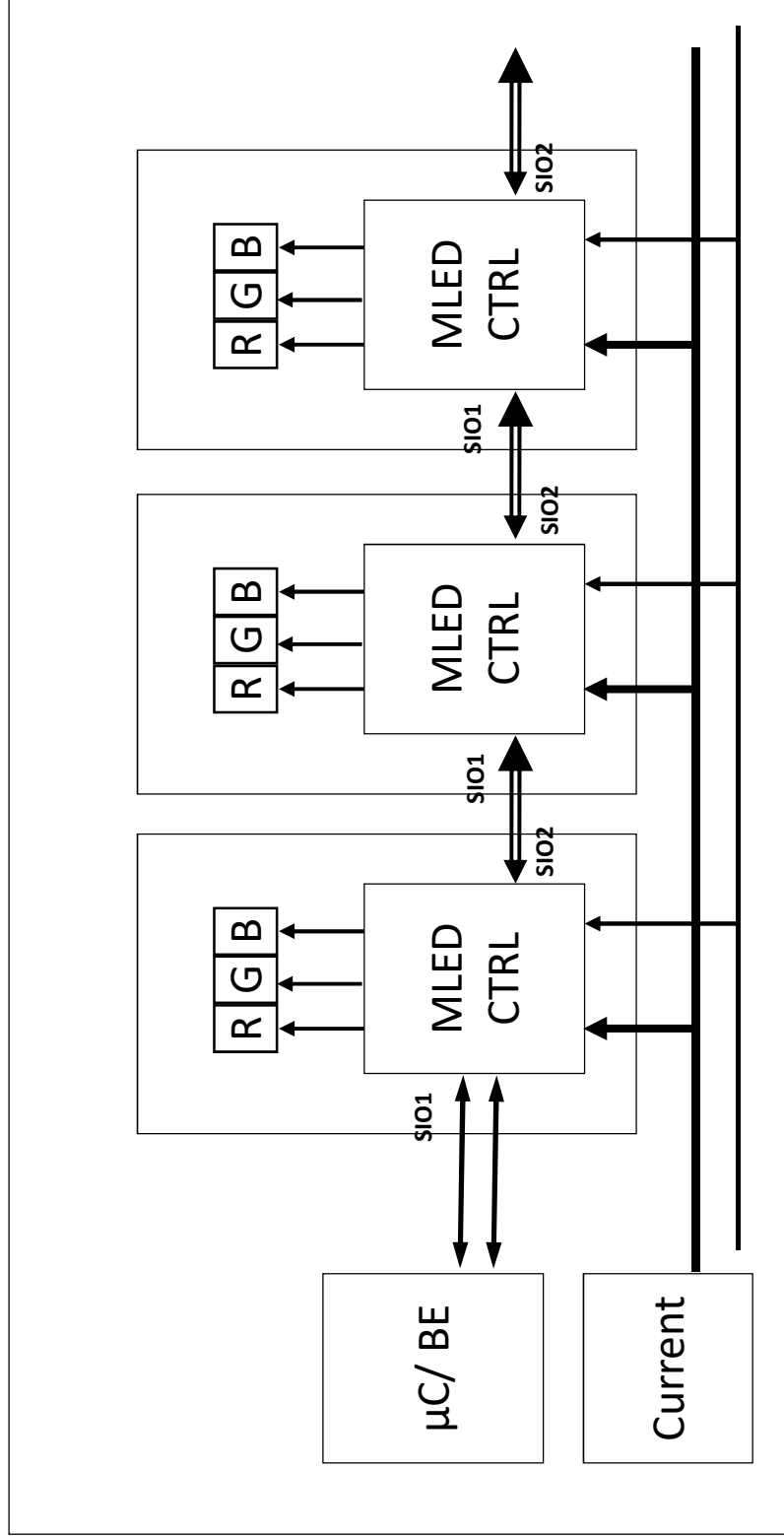


Fig. 1

Downstream Frame			
Frame Sync		15	bits
Freq. Sync		5	bits
Cmd Instr	4	5	bits
Cmd Adr	12	15	bits
Cmd Data	24	30	bits
Cmd Crc	8	10	bits
Latency (30%)		25	bits
		105	bits

Fig. 2

Upstream Frame				
	Frame Sync		15	bits
	Freq. Sync		5	bits
	Cmd Instr	0	0	bits
	Cmd Adr	12	15	bits
	Cmd Data	12	15	bits
	Cmd Crc	4	5	bits
	Latency (30%)		17	bits
			72	bits

Fig. 3

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LED Update Rate (all LEDs different color)				
	1	52,5	us	
	10	0,525	ms	
	100	5,25	ms	
	300	15,75	ms	
	4096	215,04	ms	

Read Duration				
	1	36	us	
	10	0,36	ms	
	100	3,6	ms	
	300	10,8	ms	
	4096	147,456	ms	

Fig. 4

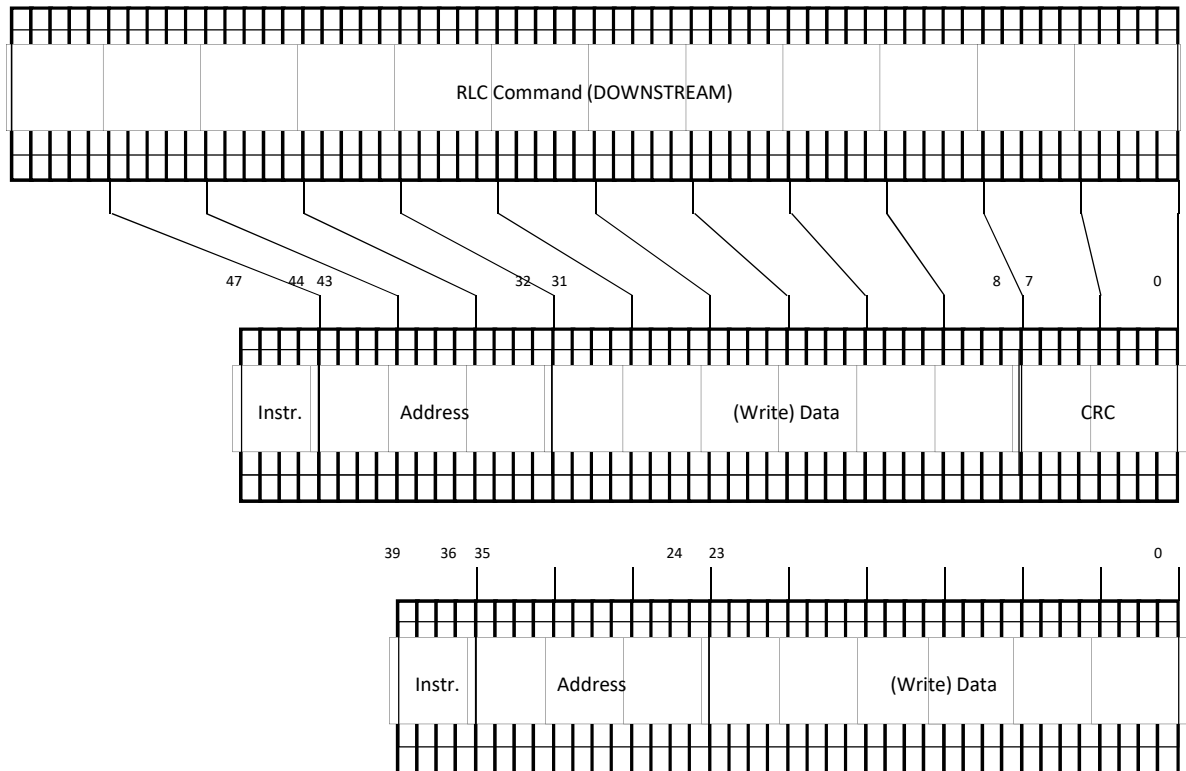


Fig. 5

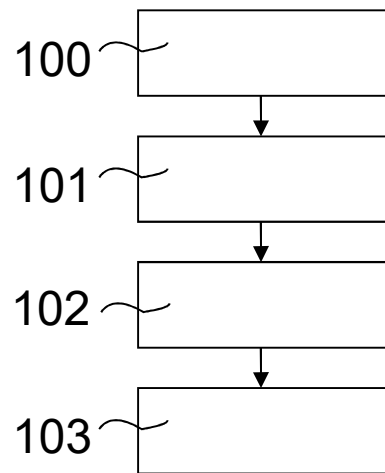


Fig. 6