(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau

(43) International Publication Date
10 May 2002 (10.05.2002)

PCT

(10) International Publication Number
WO 2002/037266 A3

(51) International Patent Classification[7]: G06F 11/36, 9/38, 9/30

(21) International Application Number: PCT/US2001/046336

(22) International Filing Date: 31 October 2001 (31.10.2001)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
09/704,467 31 October 2000 (31.10.2000) US

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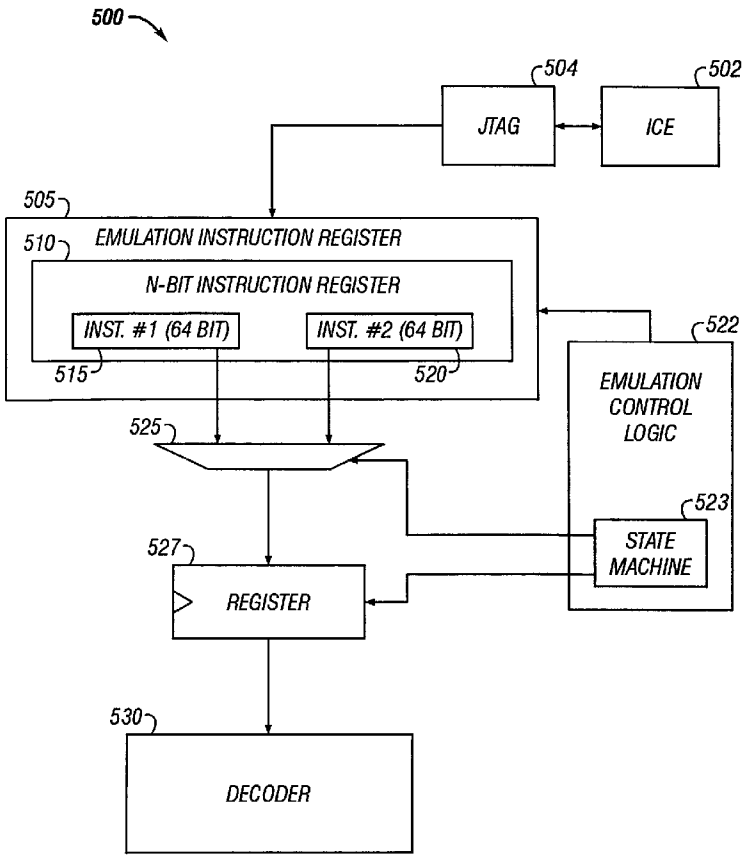
(81) Designated States *(national)*: CN, JP, KR, SG.

Published:
— *with international search report*

(88) Date of publication of the international search report:
26 February 2004

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: INSTRUCTION DISPATCH FROM THE INSTRUCTION REGISTER OF AN IN-CIRCUIT-EMULATOR, BASED ON INSTRUCTION WIDTH



(57) Abstract: In one embodiment, a state machine receives a plurality of instructions from an instruction register to be processed by a digital signal processor. After receiving a single RTI, the state machine loads each of the plurality of instructions one at time and determines the validity of each instruction. If the instruction is valid, the state machine transfers the instruction to the decoder. If the instruction is invalid or if a no-operation instruction is present, the state machine discards the instruction and immediately loads the next instruction.

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