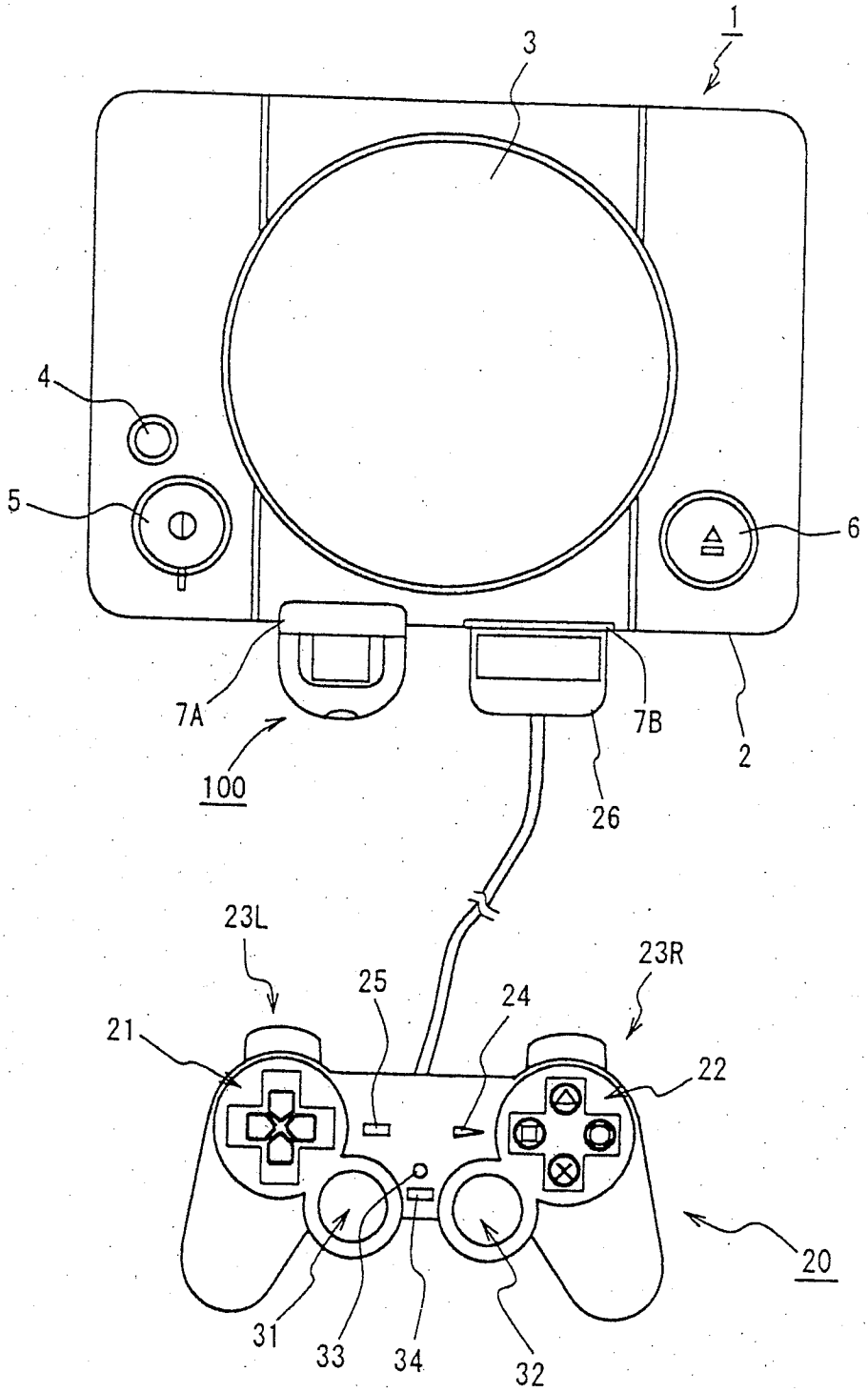


FIG. 1

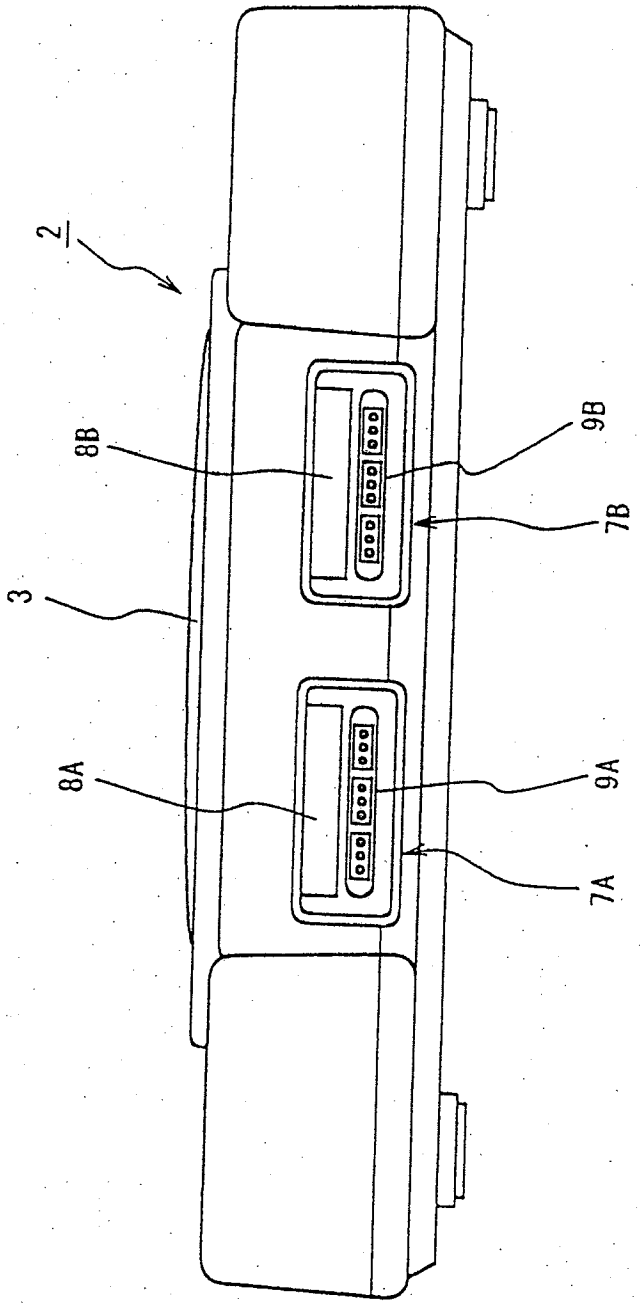


FIG. 2

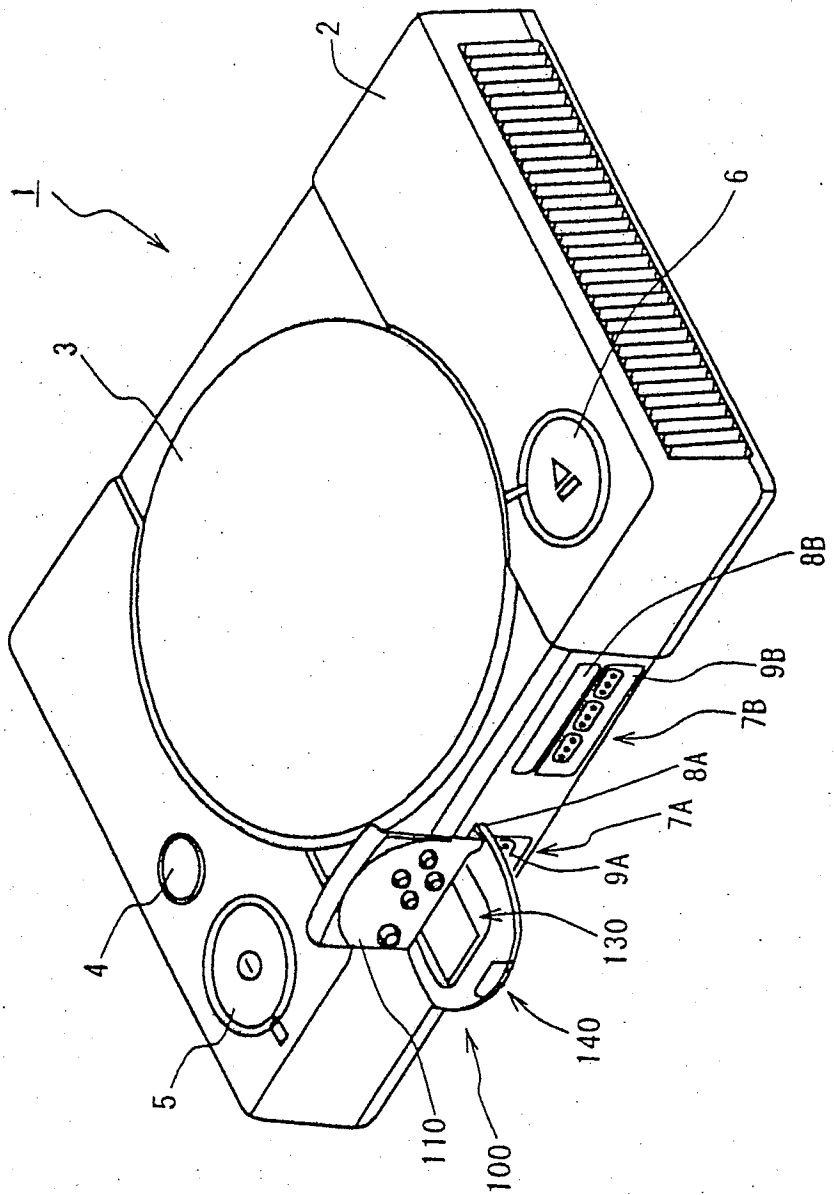


FIG. 3

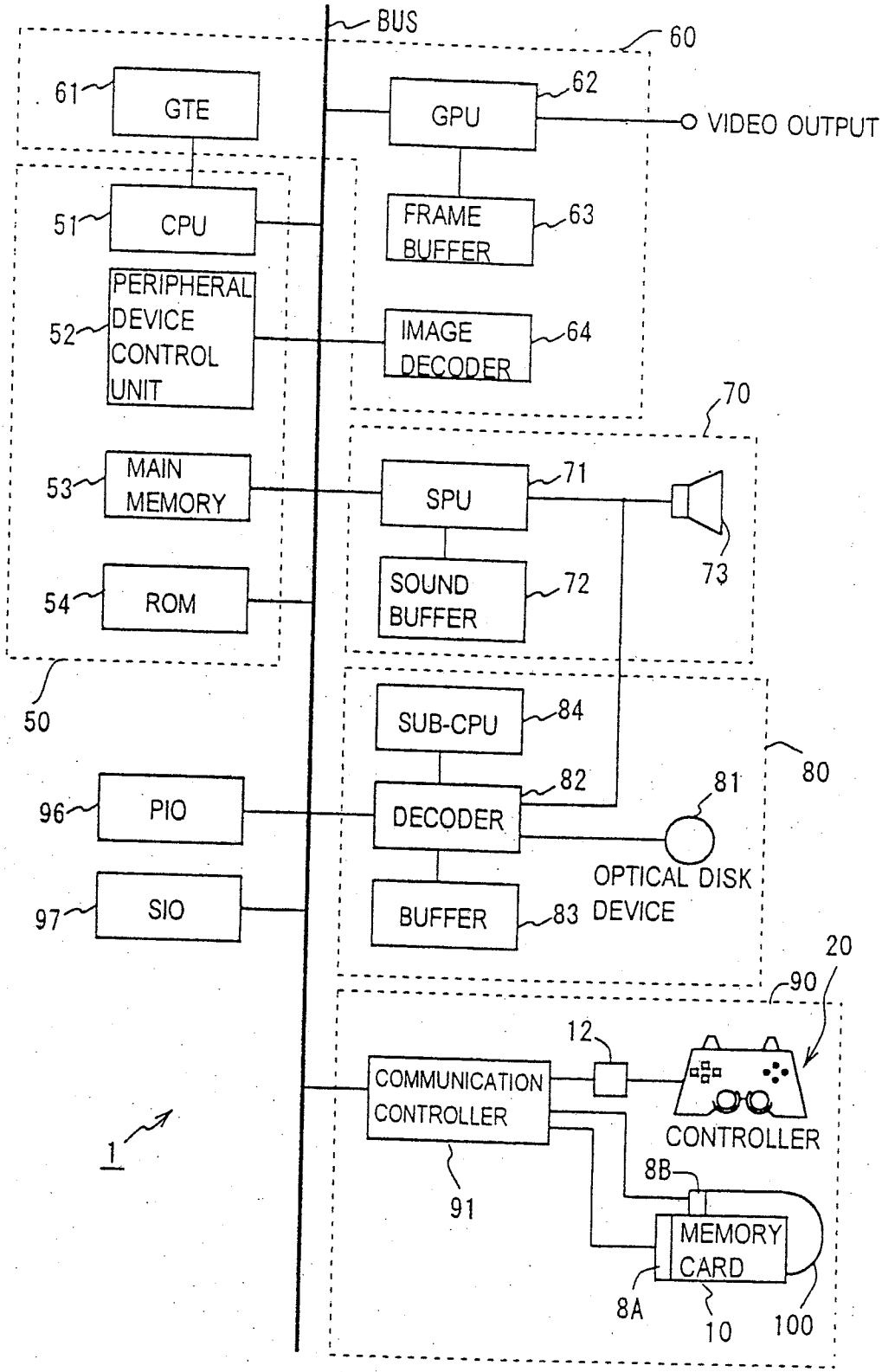

BUS
60
61
GTE
62
GPU
VIDEO OUTPUT
51
CPU
FRAME BUFFER
63
PERIPHERAL DEVICE CONTROL UNIT
52
IMAGE DECODER
64
70
53
MAIN MEMORY
SPU
71
73
54
ROM
SOUND BUFFER
72
50
SUB-CPU
84
80
82
81
96
PIO
DECODER
OPTICAL DISK DEVICE
97
SIO
BUFFER
83
20
90
12
COMMUNICATION CONTROLLER
CONTROLLER
1
8B
91
MEMORY CARD
8A
10
100

FIG. 4

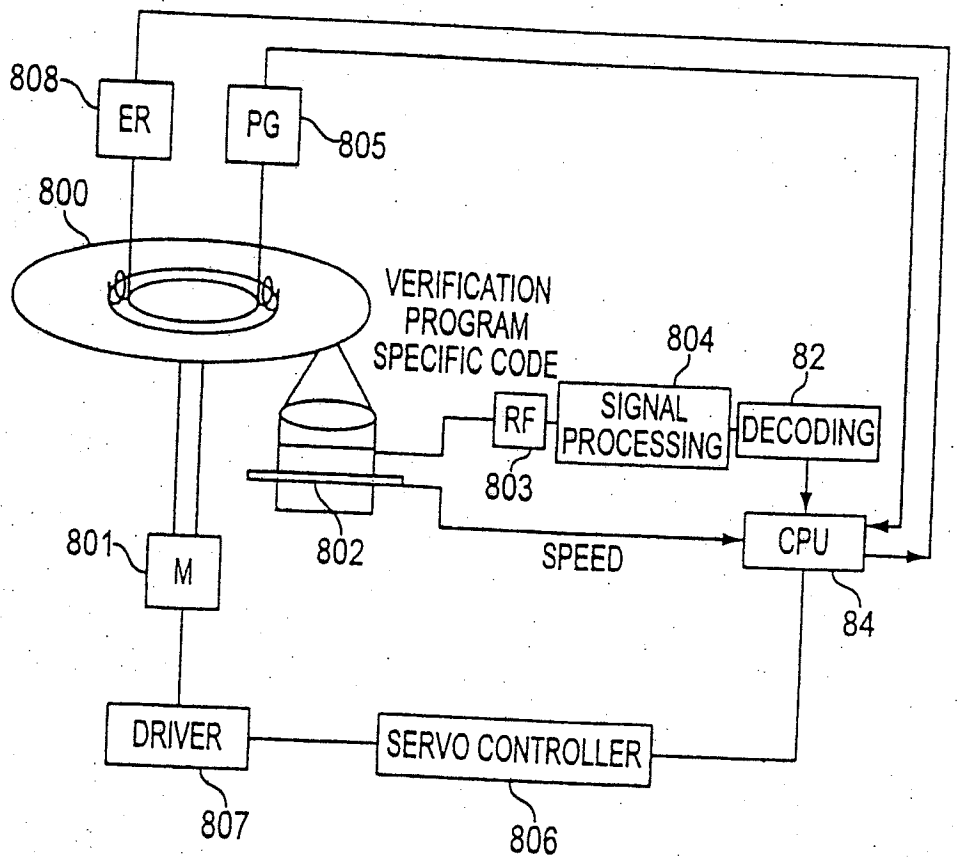

808
ER
PG
805
800
VERIFICATION
PROGRAM
SPECIFIC CODE
804
82
RF
SIGNAL
PROCESSING
DECODING
803
801
M
802
SPEED
CPU
84
DRIVER
SERVO CONTROLLER
807
806

FIG. 5

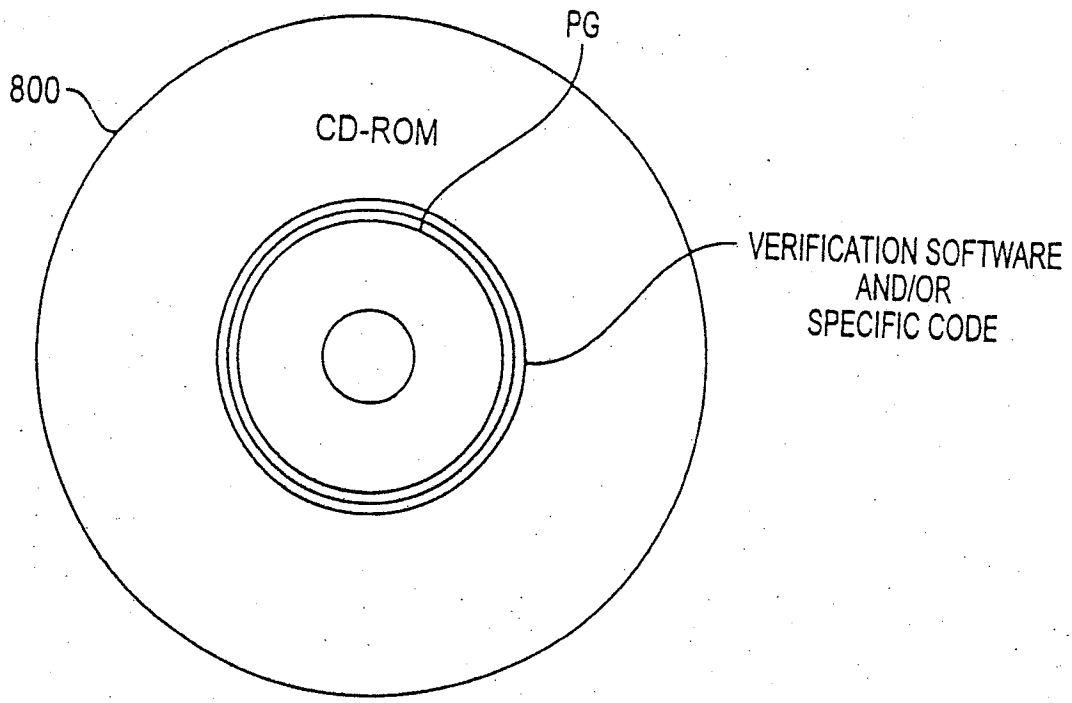


FIG. 6

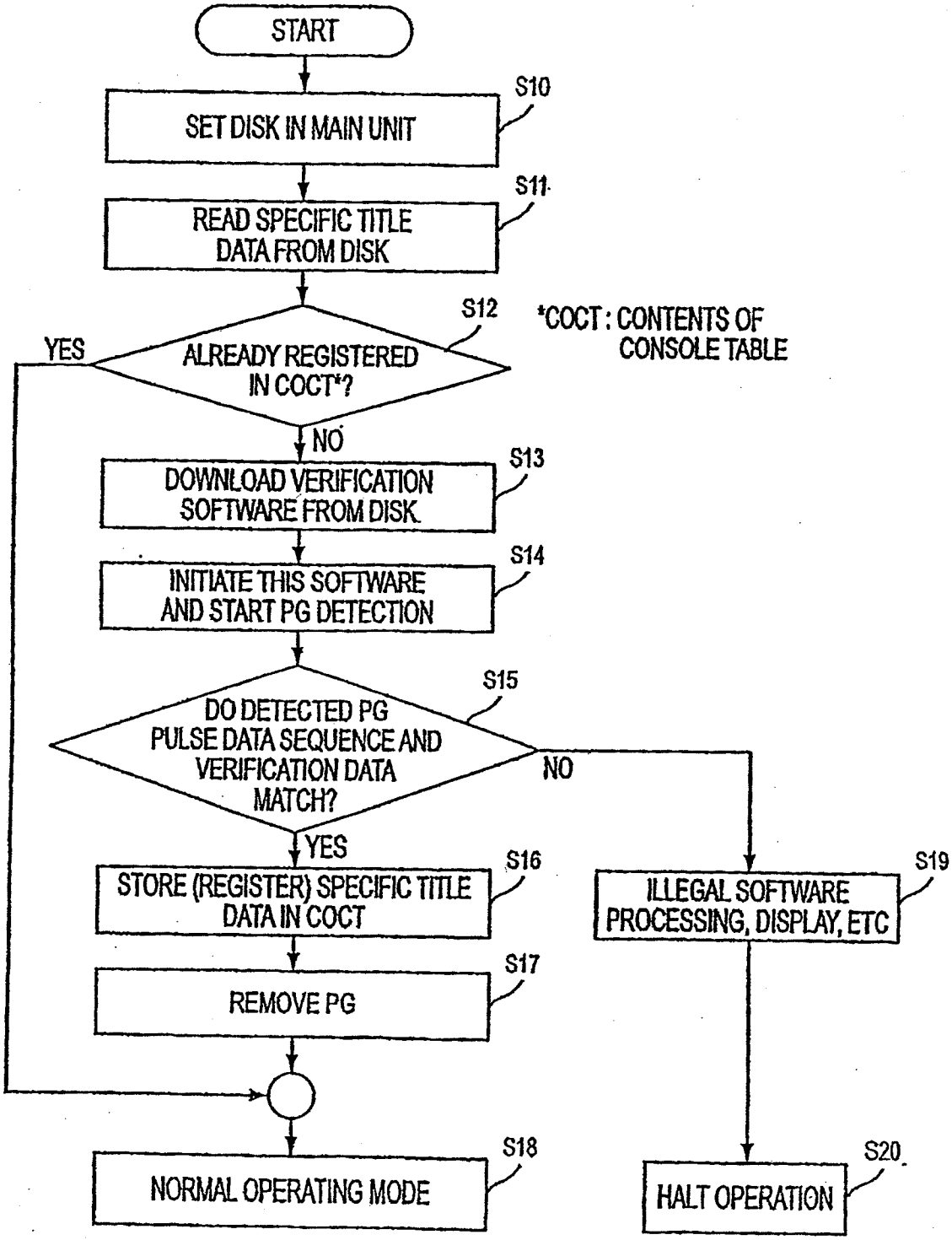

START
S10
SET DISK IN MAIN UNIT
S11
READ SPECIFIC TITLE DATA FROM DISK
S12
ALREADY REGISTERED IN COCT*?
*COCT : CONTENTS OF CONSOLE TABLE
YES
NO
S13
DOWNLOAD VERIFICATION SOFTWARE FROM DISK
S14
INITIATE THIS SOFTWARE AND START PG DETECTION
S15
DO DETECTED PG PULSE DATA SEQUENCE AND VERIFICATION DATA MATCH?
NO
YES
S16
STORE (REGISTER) SPECIFIC TITLE DATA IN COCT
S17
REMOVE PG
S18
NORMAL OPERATING MODE
S19
ILLEGAL SOFTWARE PROCESSING, DISPLAY, ETC
S20
HALT OPERATION

FIG. 7

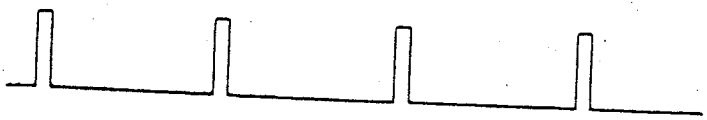
FIG. 8A 

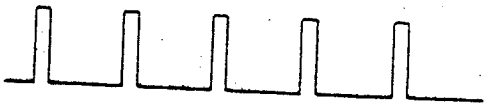
FIG. 8B 

FIG. 8C 

FIG. 8D 

FIG. 8E 