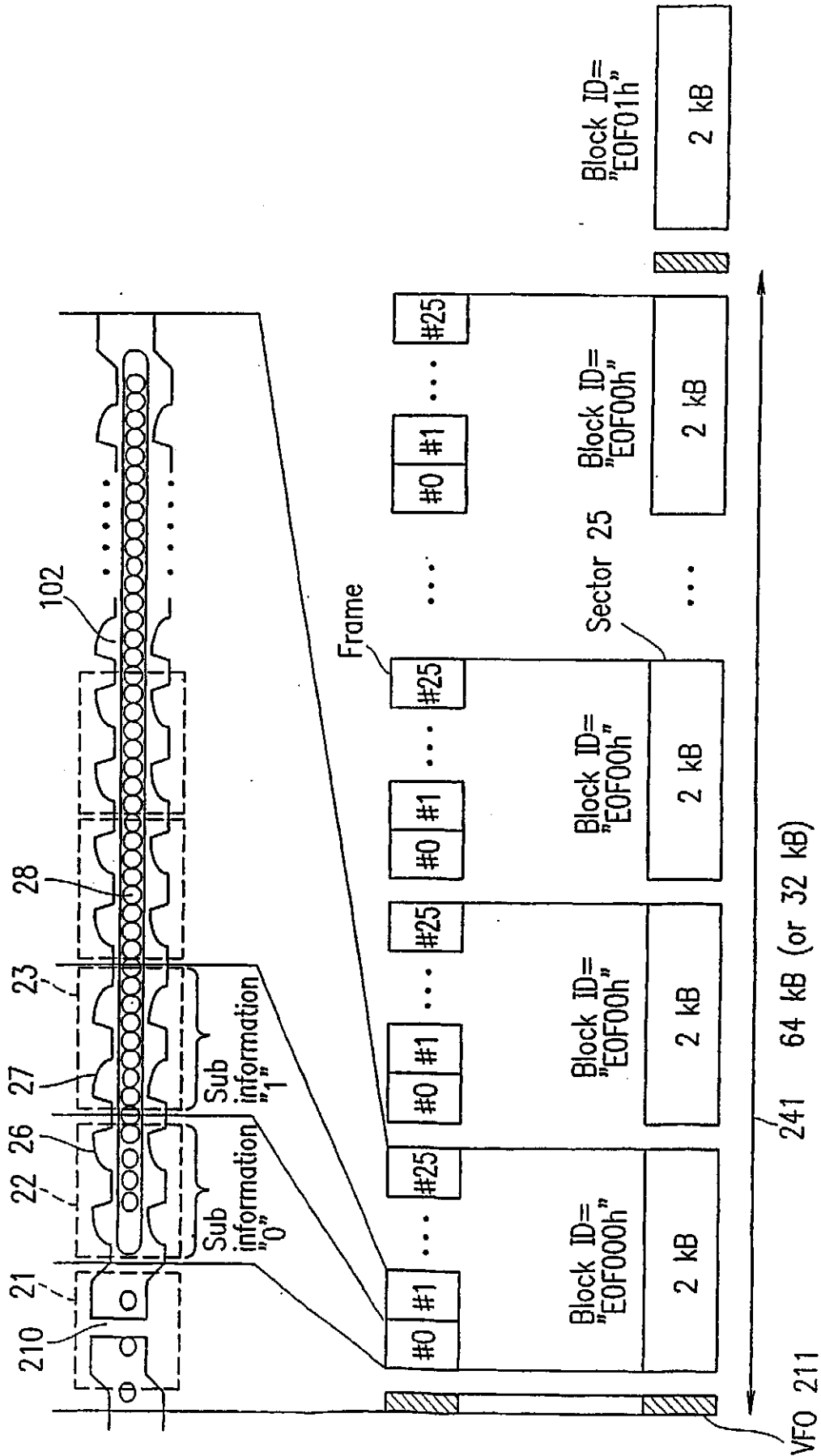


FIG. 1



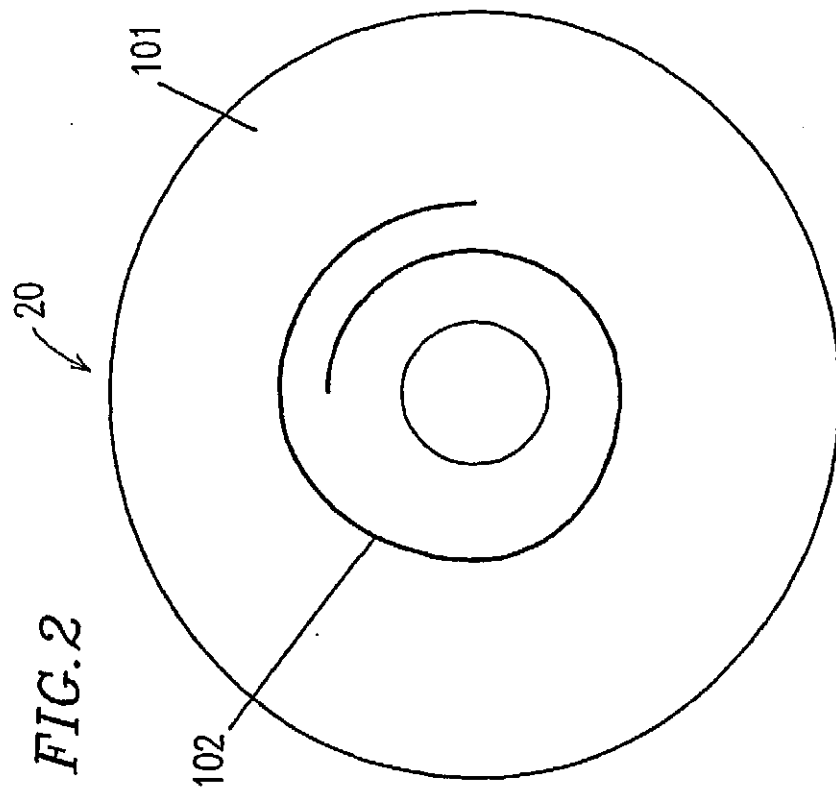


FIG. 3.

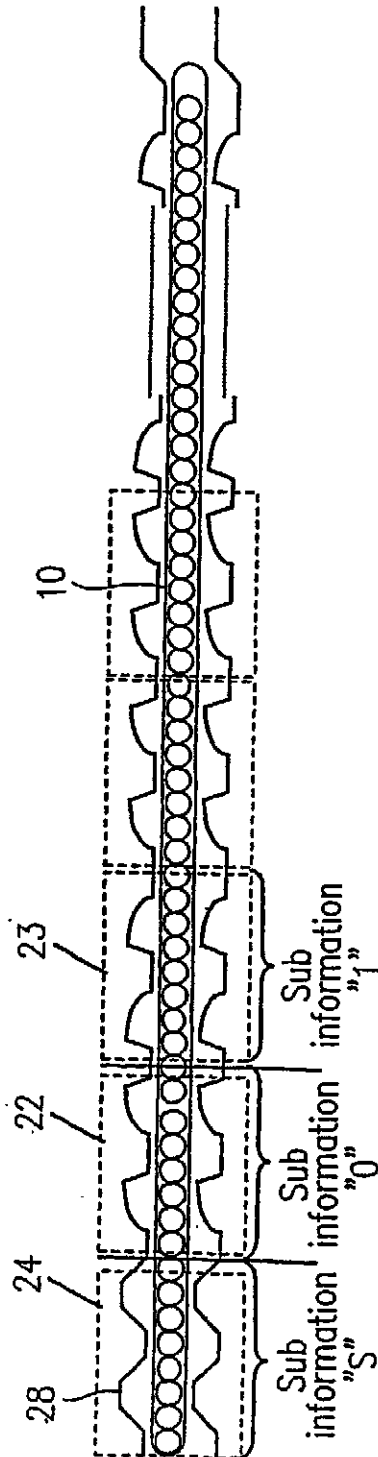
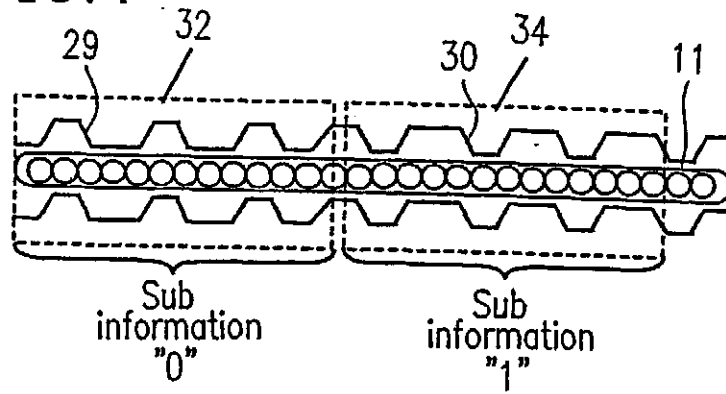


FIG. 4



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FIG. 5

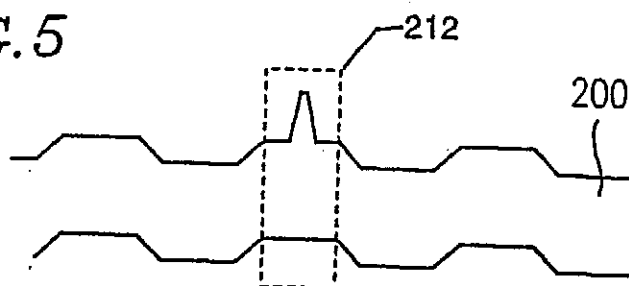


FIG. 6

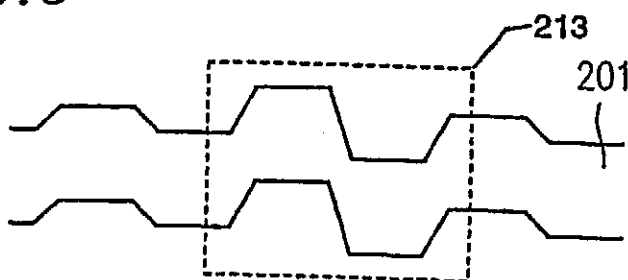


FIG. 7

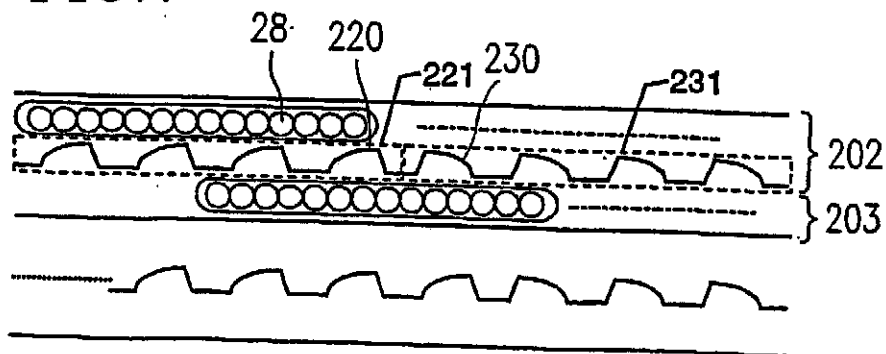
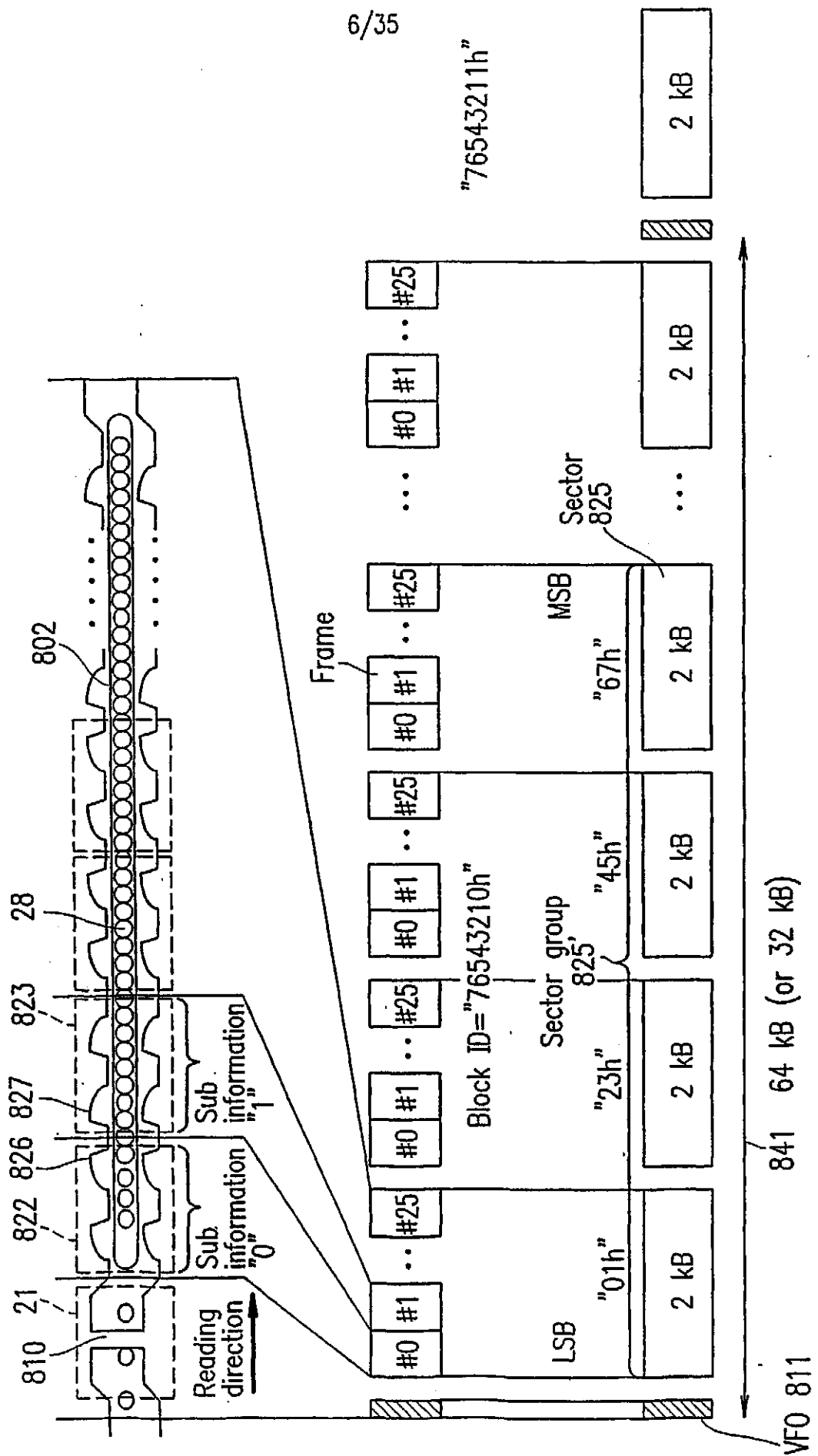


FIG. 8



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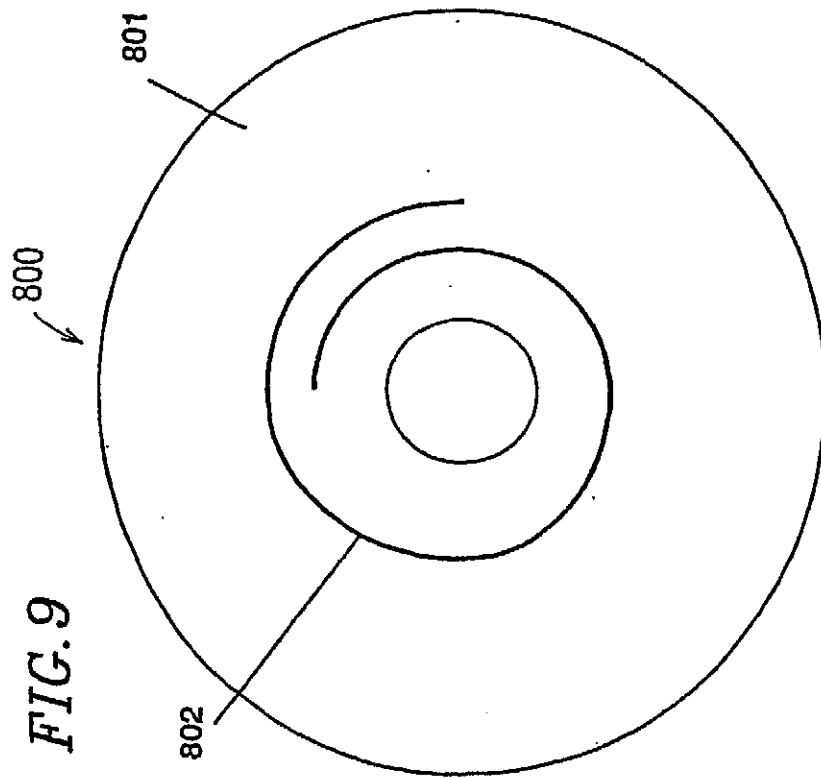


FIG. 10

Sector number	Frame number			
	#0~#7	#8~#15	#16~#20	#21~#25
0	1st 1 byte of ECC block address(LSB)	1st 1 byte of parity	5-bit sector number	5-bit parity of sector number
1	2nd 1 byte of ECC block address	2nd 1 byte of parity	5-bit sector number	5-bit parity of sector number
2	3rd 1 byte of ECC block address	3rd 1 byte of parity	5-bit sector number	5-bit parity of sector number
3	4th 1 byte of ECC block address	4th 1 byte of parity	5-bit sector number	5-bit parity of sector number
4	1st 1 byte of ECC block address(LSB)	1st 1 byte of parity	5-bit sector number	5-bit parity of sector number
5	2nd 1 byte of ECC block address	2nd 1 byte of parity	5-bit sector number	5-bit parity of sector number
6	3rd 1 byte of ECC block address	3rd 1 byte of parity	5-bit sector number	5-bit parity of sector number
7	4th 1 byte of ECC block address	4th 1 byte of parity	5-bit sector number	5-bit parity of sector number
• • •				
28(12)	1st 1 byte of ECC block address(LSB)	1st 1 byte of parity	5-bit sector number	5-bit parity of sector number
29(13)	2nd 1 byte of ECC block address	2nd 1 byte of parity	5-bit sector number	5-bit parity of sector number
30(14)	3rd 1 byte of ECC block address	3rd 1 byte of parity	5-bit sector number	5-bit parity of sector number
31(15)	4th 1 byte of ECC block address	4th 1 byte of parity	5-bit sector number	5-bit parity of sector number

Sector group 825

FIG. 12

Sector number	Frame number				
	#0~#12			#13~#25	
0	B0	1st 1 bit of ECC block address (LSB)	B1	2nd 1 bit of ECC block address	
1	B2	3rd 1 bit of ECC block address	B3	4th 1 bit of ECC block address	
2	B4	5th 1 bit of ECC block address	B5	6th 1 bit of ECC block address	
.					
13	B26	27th 1 bit of ECC block address	B27	28th 1 bit of ECC block address	
14	B28	29th 1 bit of ECC block address	B29	30th 1 bit of ECC block address	
15	B30	31st 1 bit of ECC block address	B31	32nd 1 bit of ECC block address	
16	B0	1st 1 bit of ECC block address (LSB)	B1	2nd 1 bit of ECC block address	
17	B2	3rd 1 bit of ECC block address	B3	4th 1 bit of ECC block address	
18	B4	5th 1 bit of ECC block address	B5	6th 1 bit of ECC block address	
.					
29	B26	27th 1 bit of ECC block address	B27	28th 1 bit of ECC block address	
30	B28	29th 1 bit of ECC block address	B29	30th 1 bit of ECC block address	
31	B30	31st 1 bit of ECC block address	B31	32nd 1 bit of ECC block address	

Sector group

Repeated sector group when 1 block is 64 kB

FIG. 14

Sector number	Frame number			
	#0 ~ #25			
0	B0	1st 1 bit ECC block address (LSB)		
1	B0	1st 1 bit ECC block address (LSB)		
2	B1	2nd 1 bit ECC block address		
3	B1	2nd 1 bit ECC block address		
		:		
12	B6	7th 1 bit ECC block address		
13	B6	7th 1 bit ECC block address		
14	B7	8th 1 bit ECC block address		
15	B8	9th 1 bit ECC block address		
		:		
23	B16	17th 1 bit ECC block address		
24	B17	18th 1 bit ECC block address		
		#0 ~ #12		#13 ~ #25
25	B18	19th 1 bit	B19	20th 1 bit
26	B20	21st 1 bit	B21	22nd 1 bit
		:		
30	B28	29th 1 bit	B29	30th 1 bit
31	B30	31st 1 bit	B31	32nd 1 bit

FIG. 15

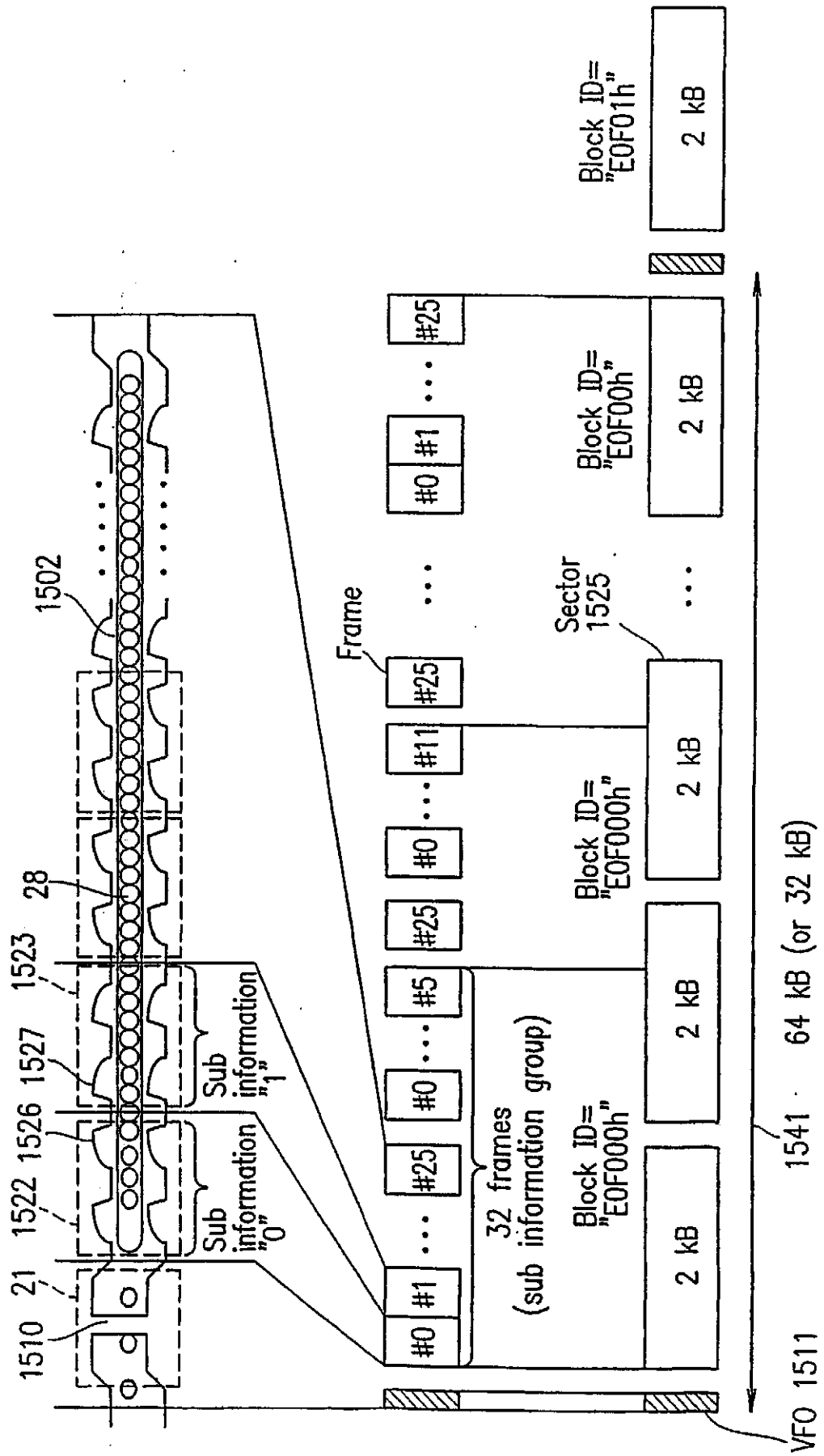


FIG. 16

Sector number	Frame number				
	#0~#7	#8~#15	#16~#17	#18~#19	#20~#25
0	1st 1 byte of block address (LSB)	1st 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 1
1	2nd 1 byte of block address	2nd 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 1
2	3rd 1 byte of block address	3rd 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 1
3	4th 1 byte of block address	4th 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 1
4	1st 1 byte of block address (LSB)	1st 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 2
5	2nd 1 byte of block address	2nd 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 2
6	3rd 1 byte of block address	3rd 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 2
7	4th 1 byte of block address	4th 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 2
⋮					
28(12)	1st 1 byte of block address (LSB)	1st 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 8(4)
29(13)	2nd 1 byte of block address	2nd 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 8(4)
30(14)	3rd 1 byte of block address	3rd 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 8(4)
31(15)	4th 1 byte of block address	4th 1 byte of parity	2-bit ID number	2-bit parity of ID number	Order number 8(4)

Sector group

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FIG. 17

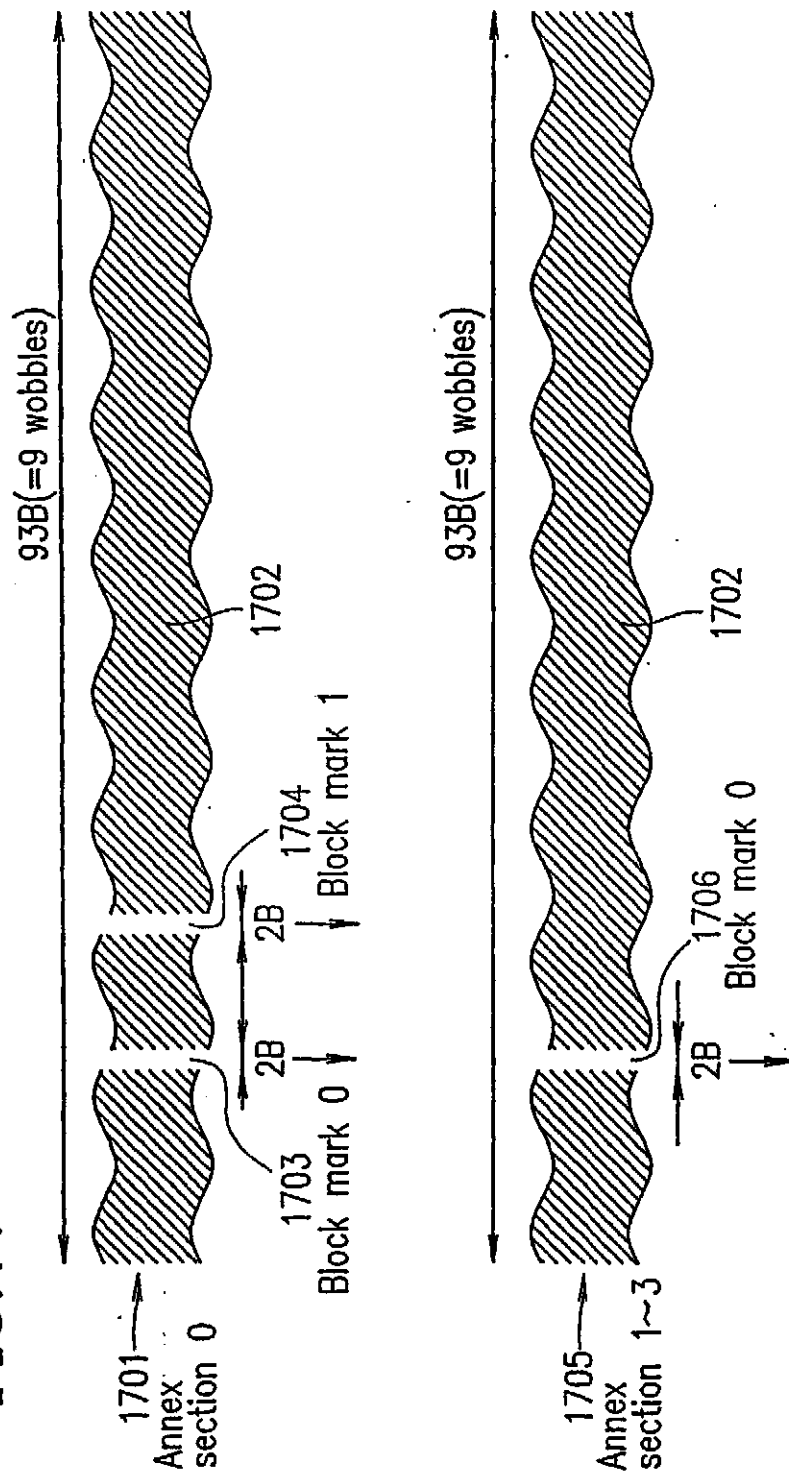


FIG. 19

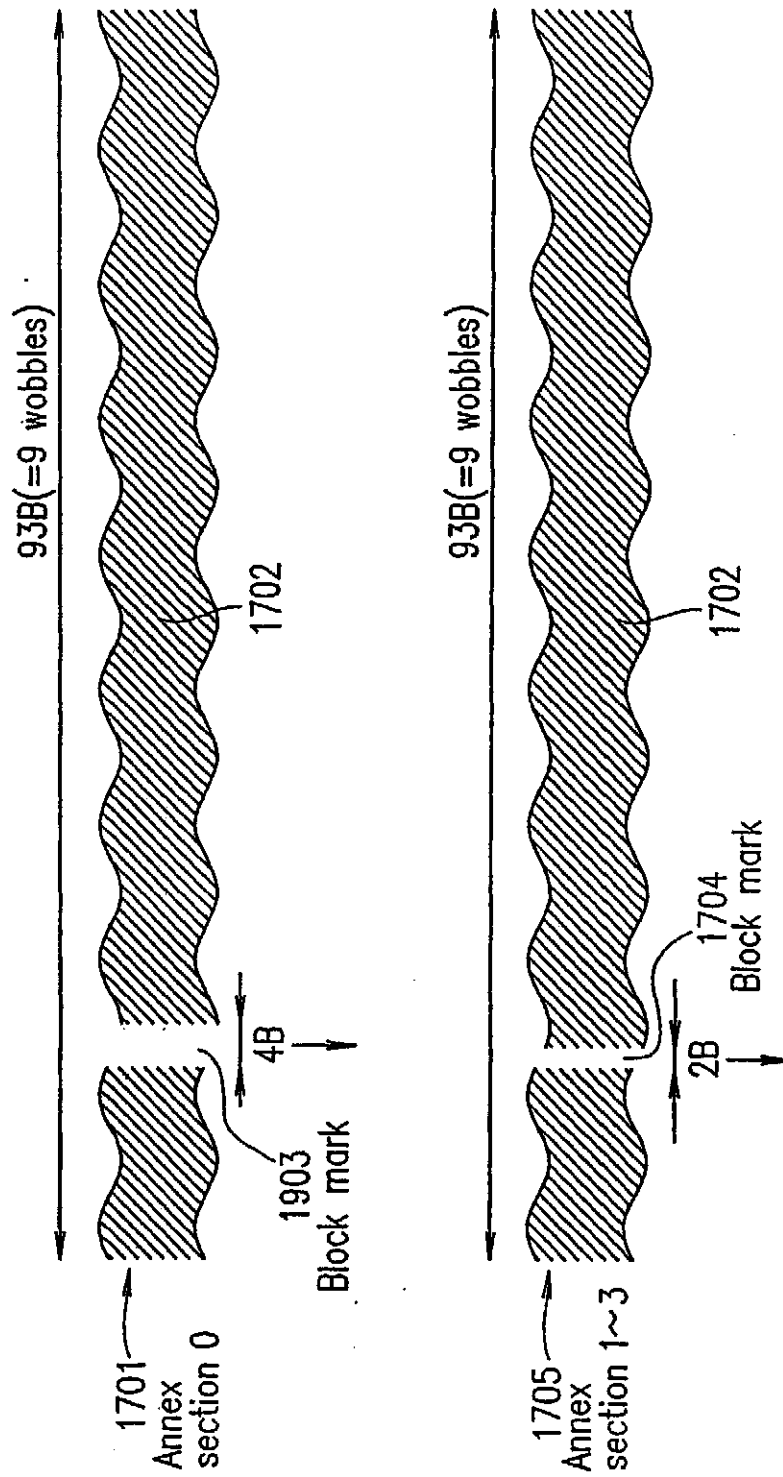
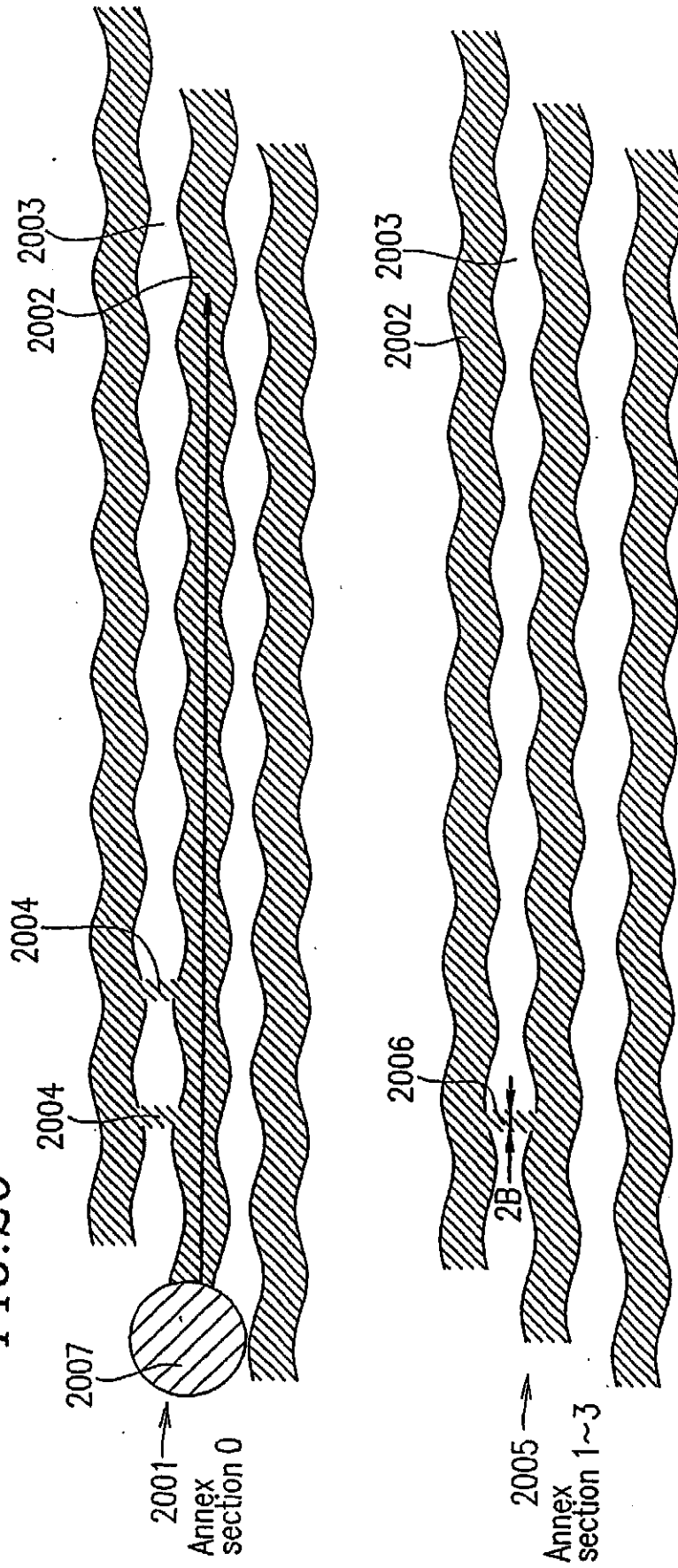


FIG. 20



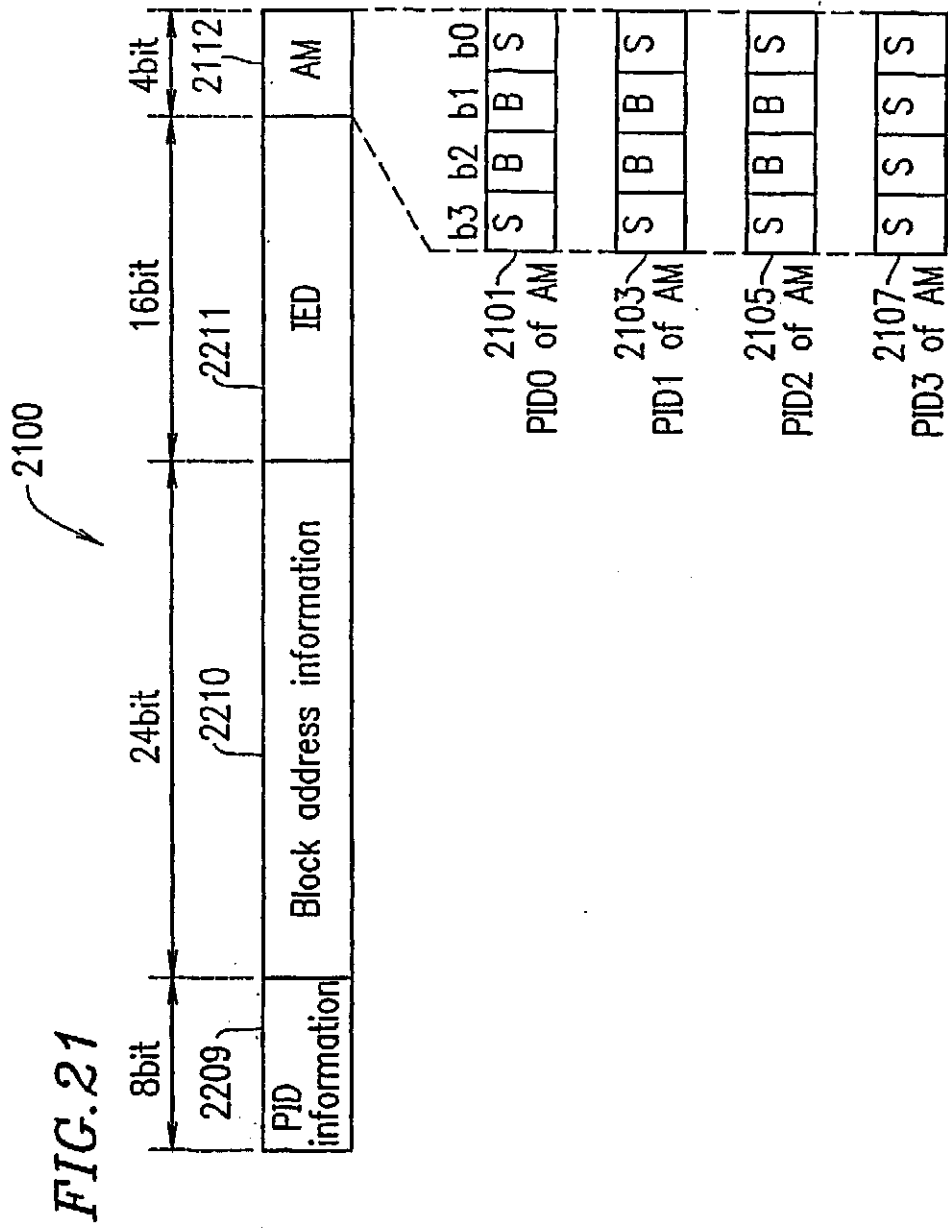
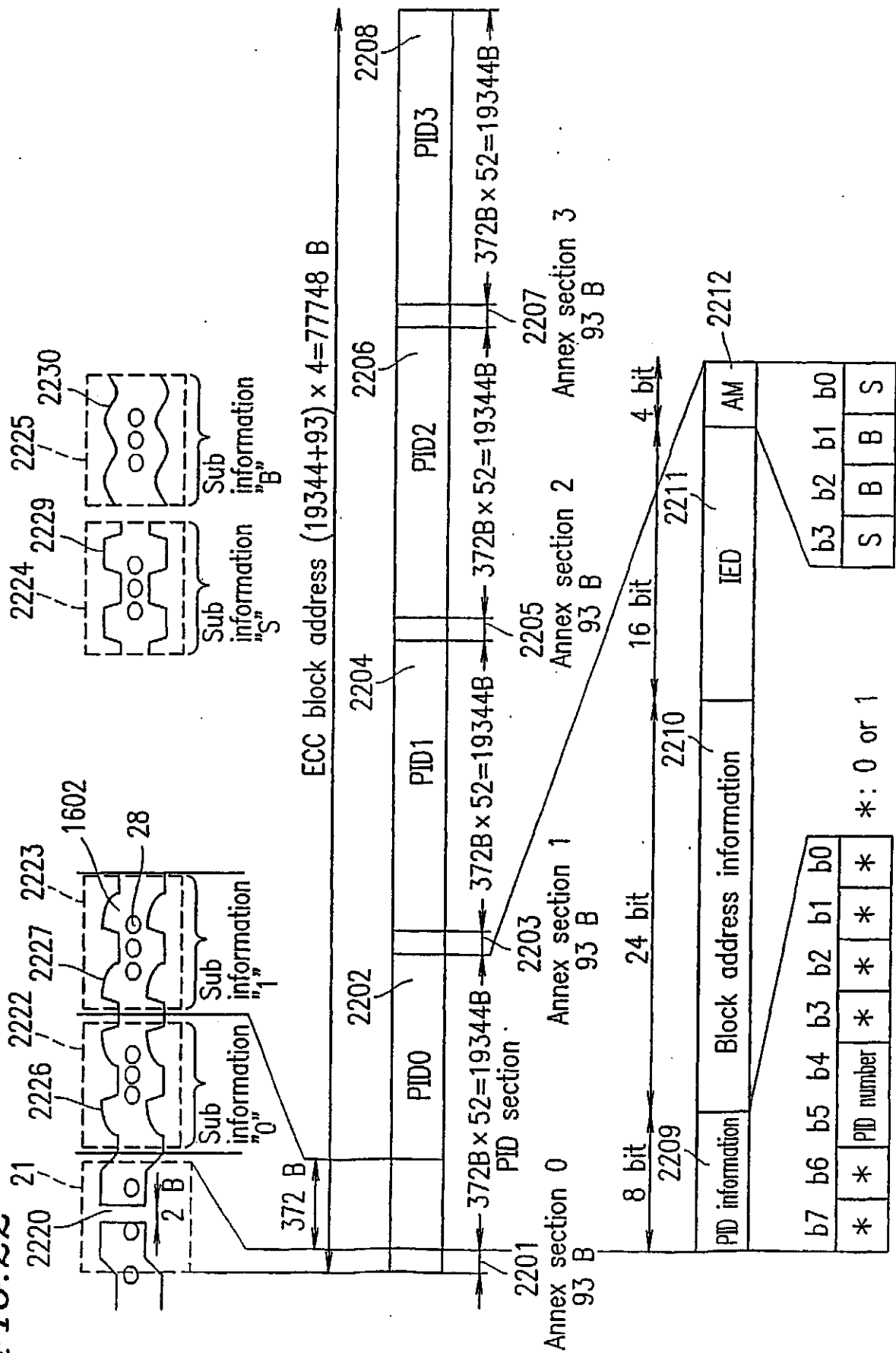


FIG. 22



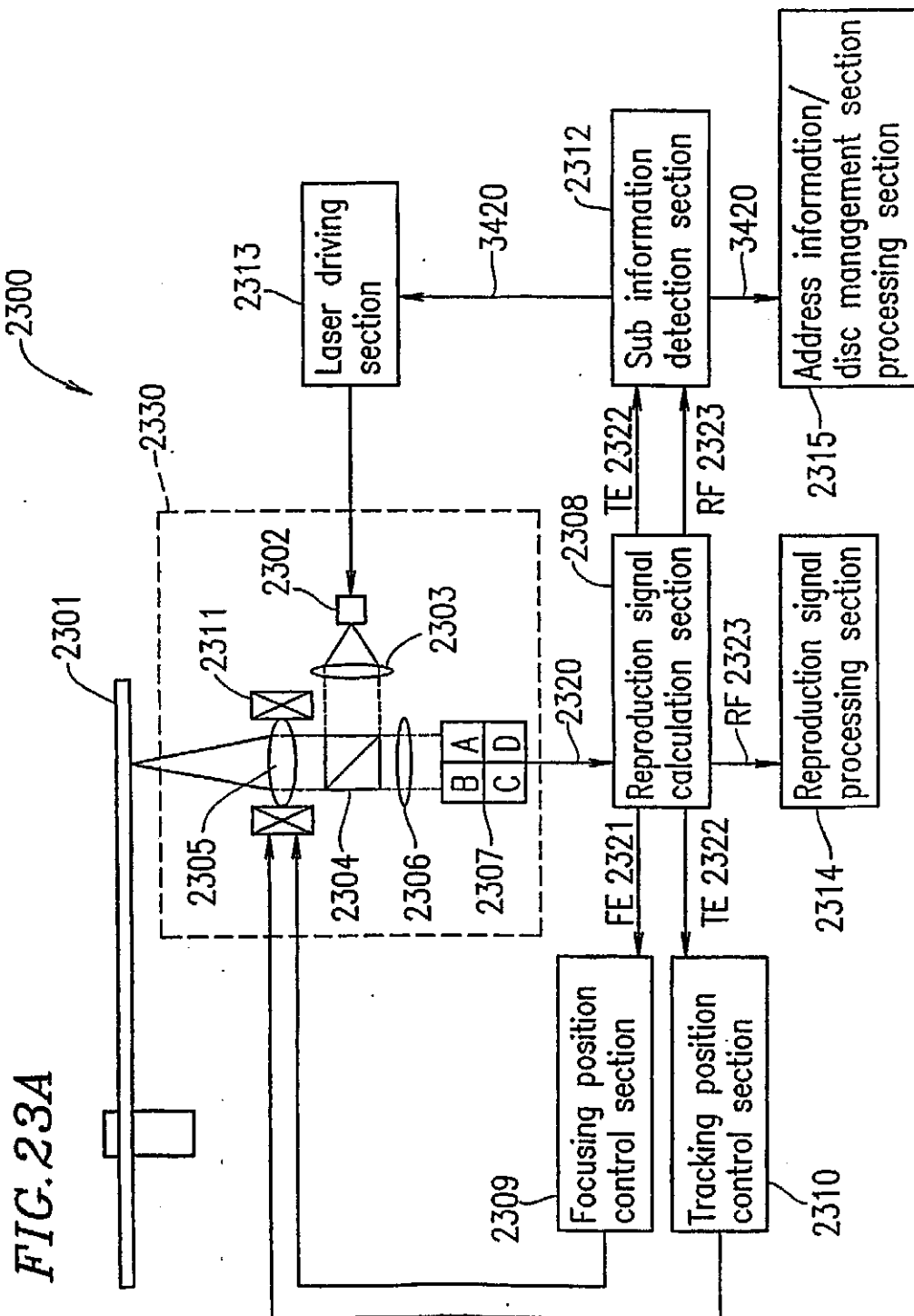
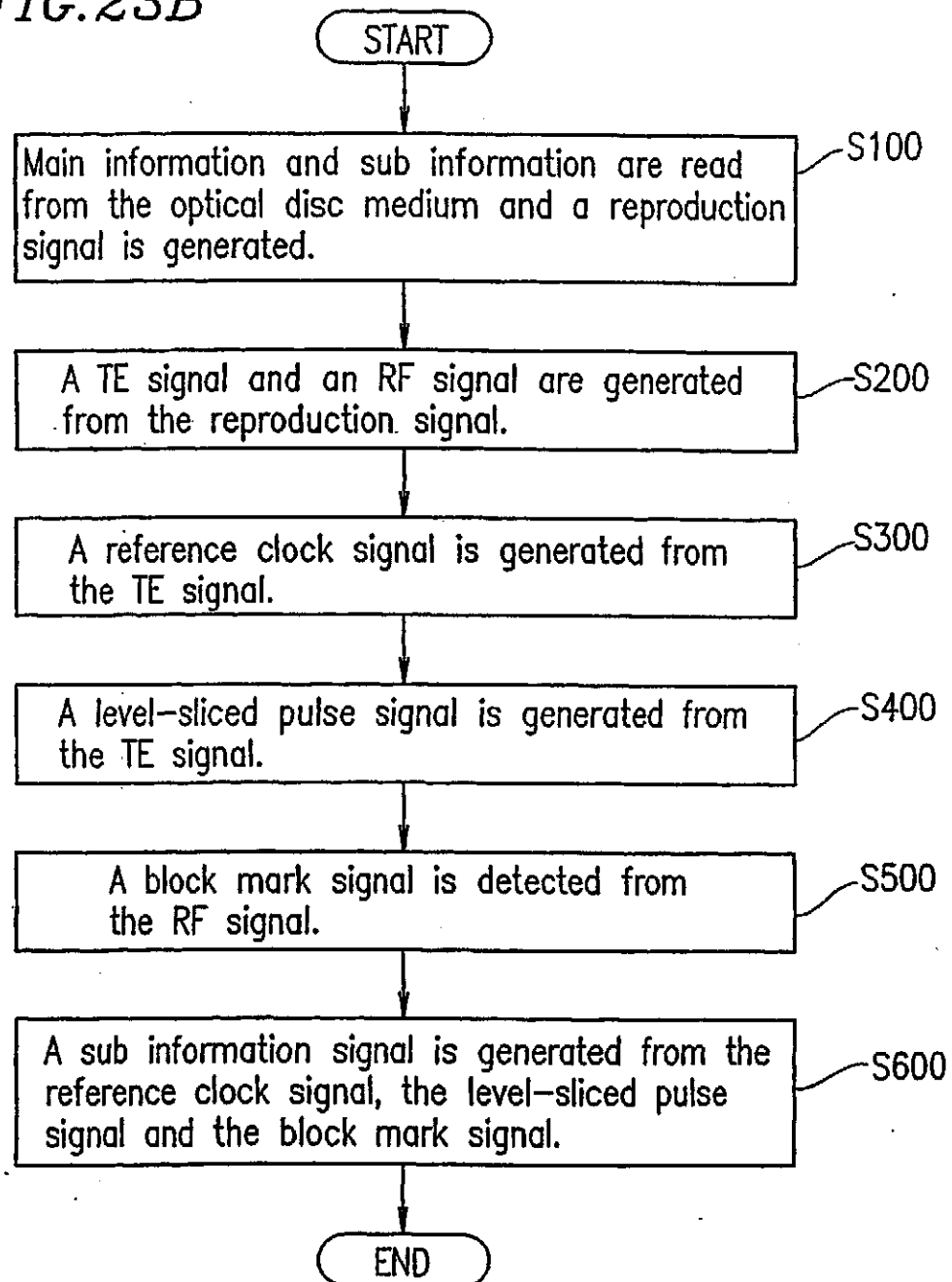
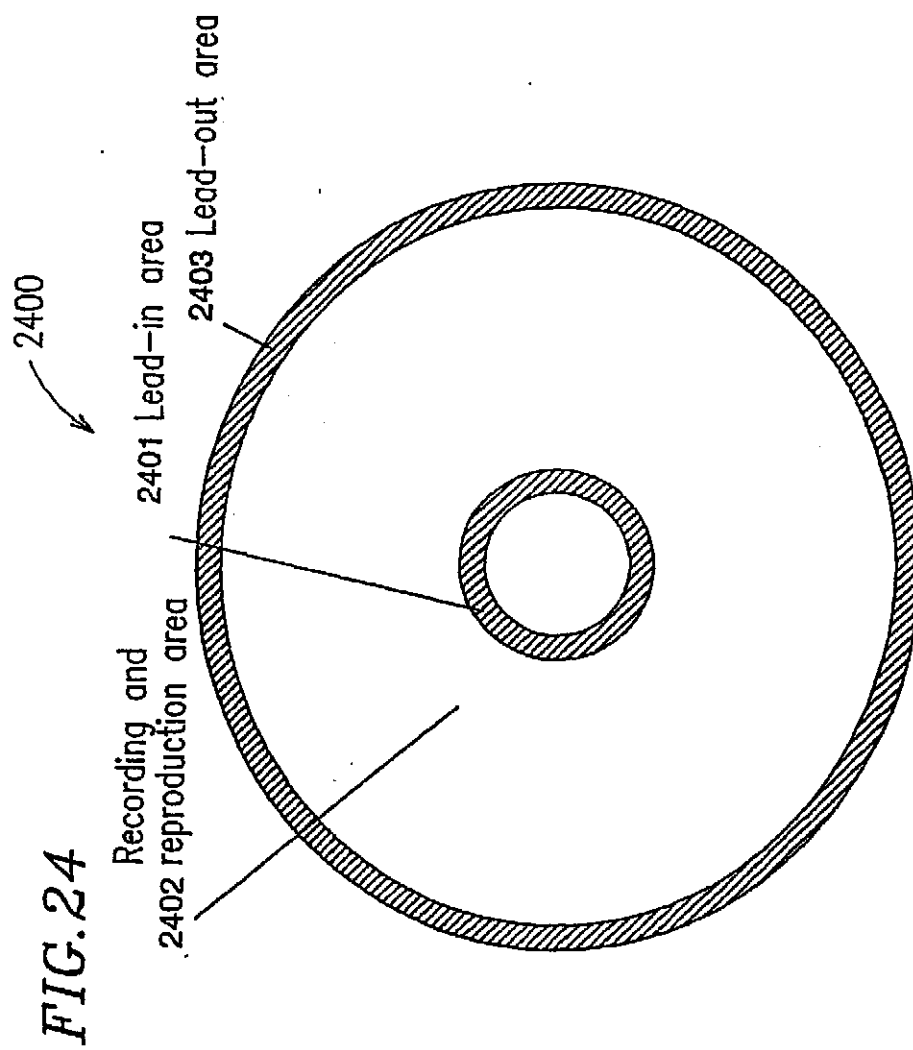


FIG. 23B



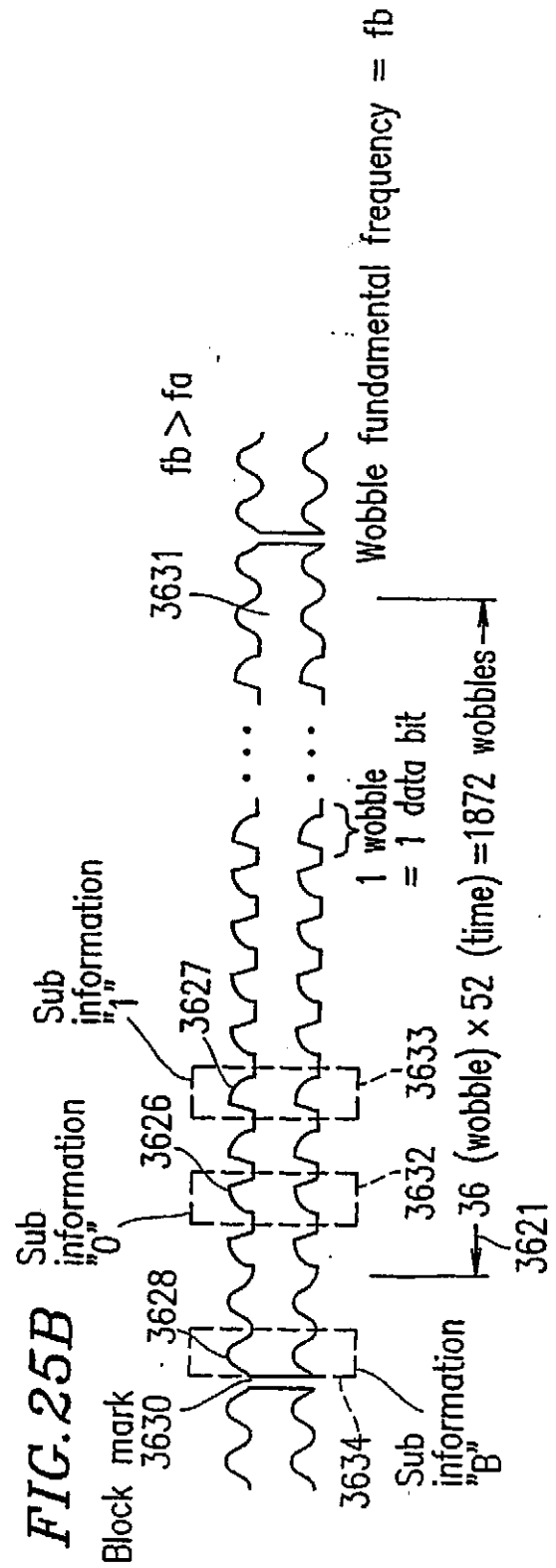
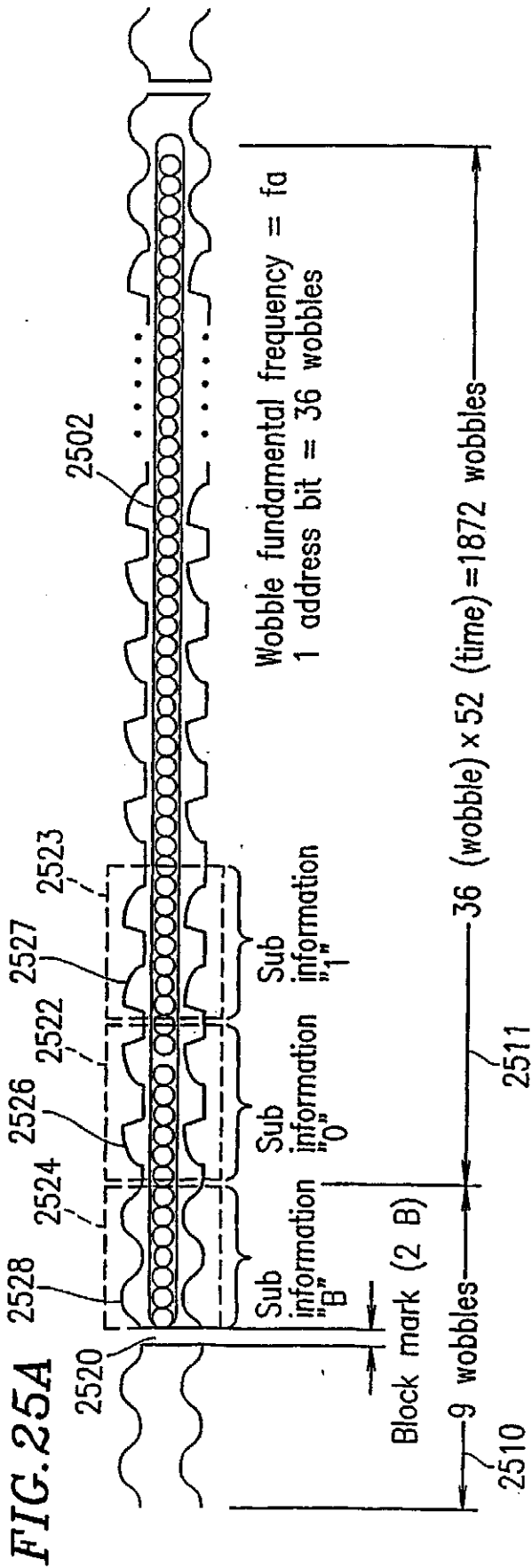


FIG. 26A

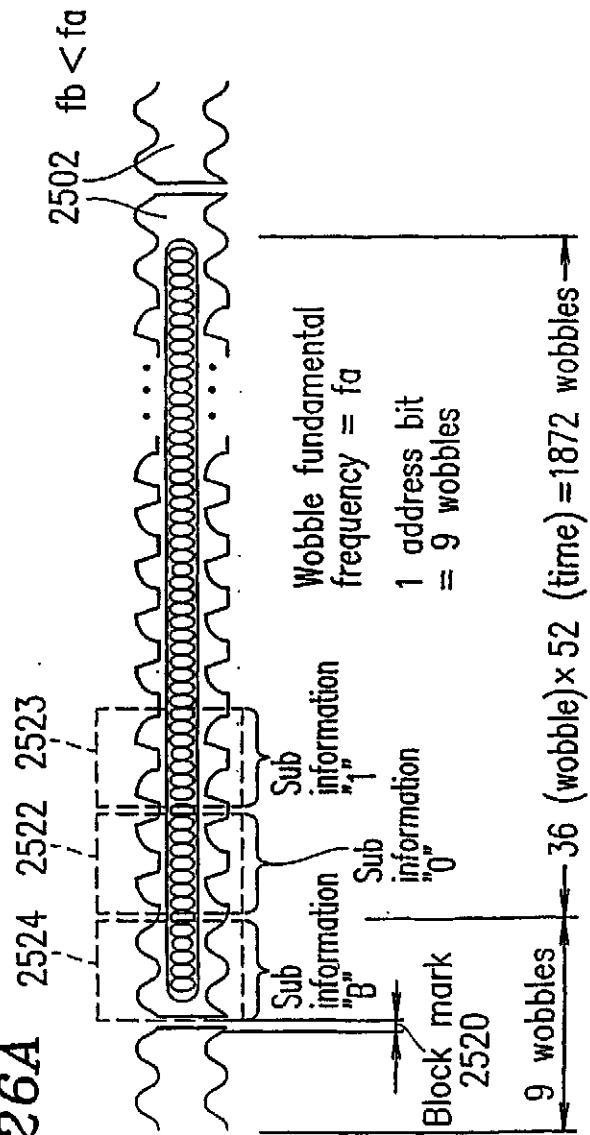
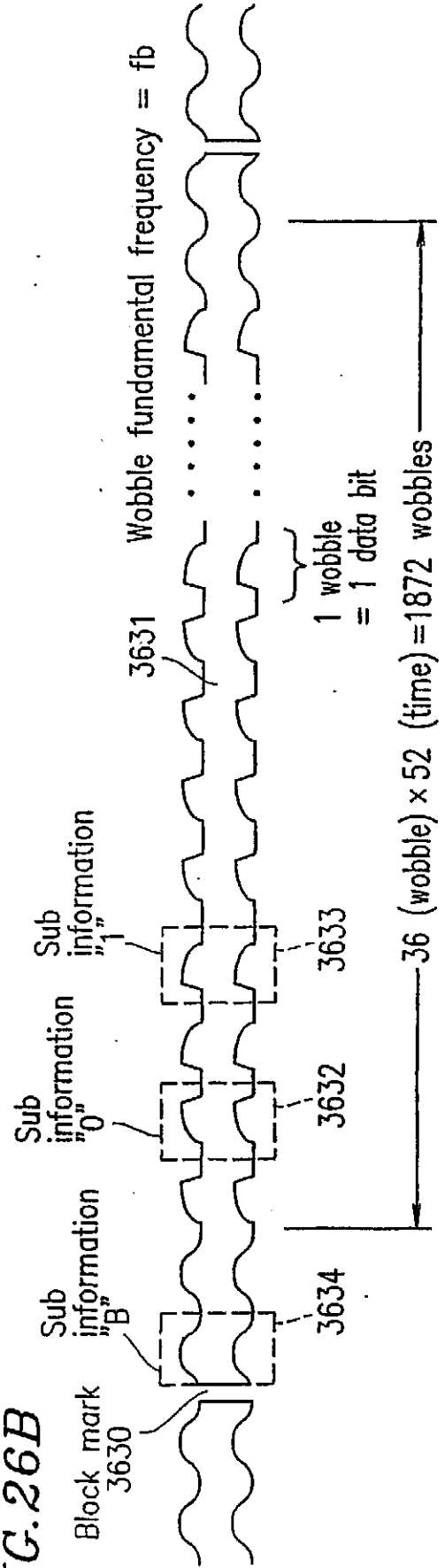
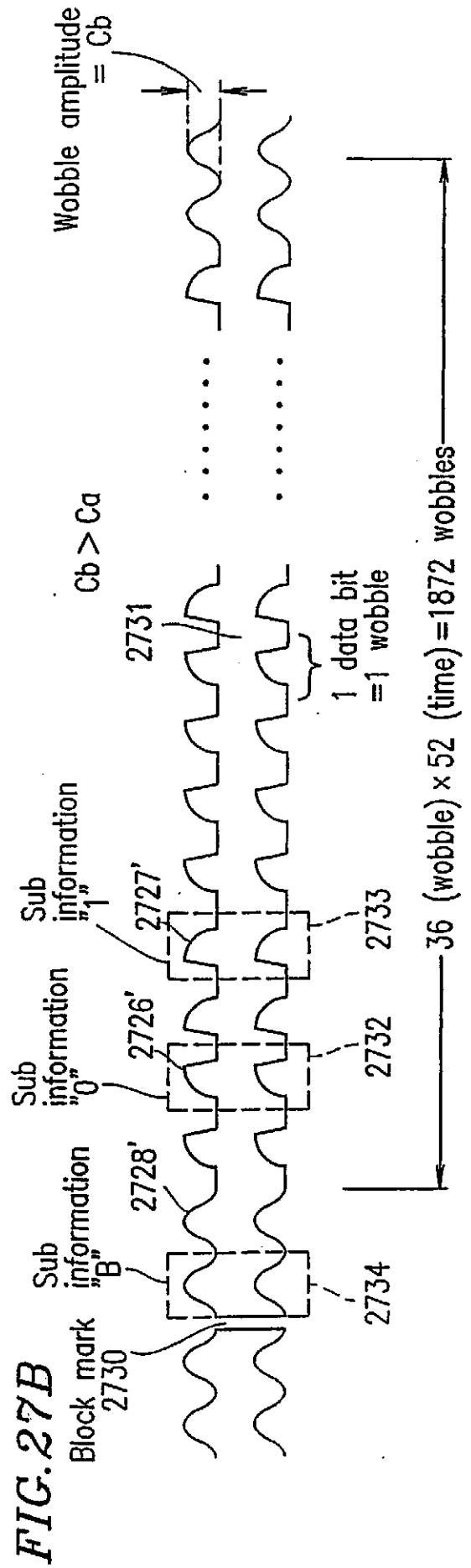
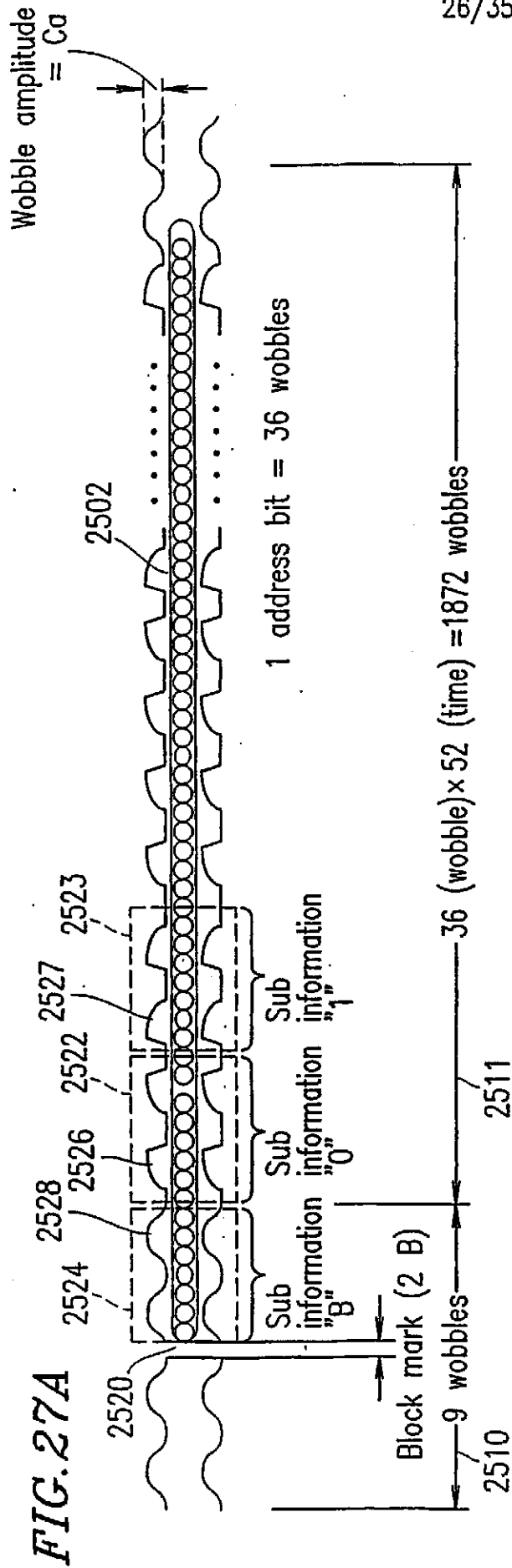


FIG. 26B





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FIG. 28A

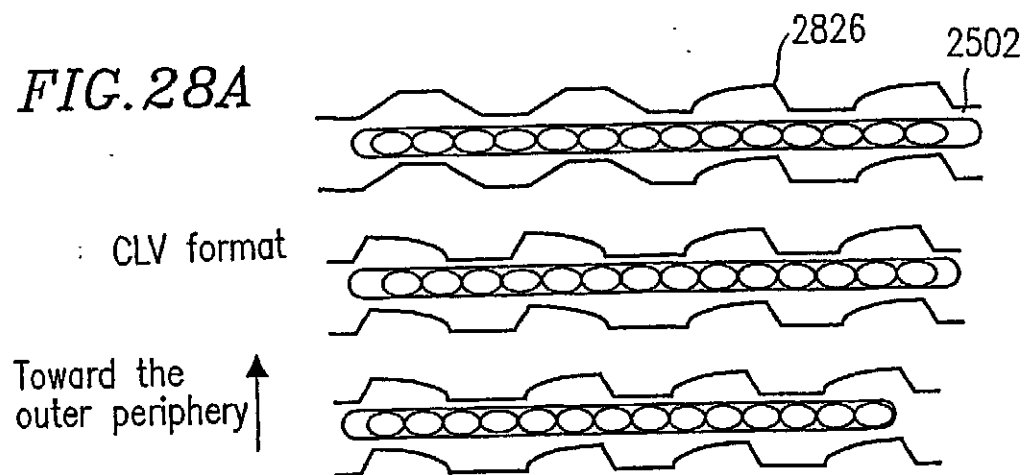


FIG. 28B

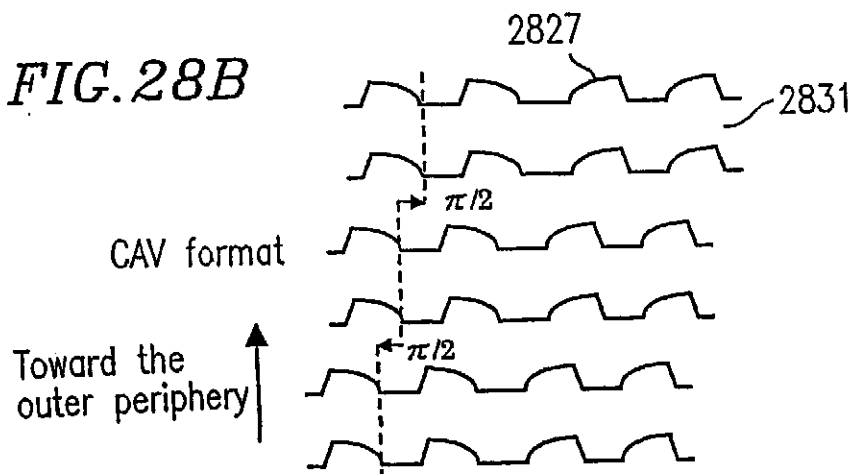
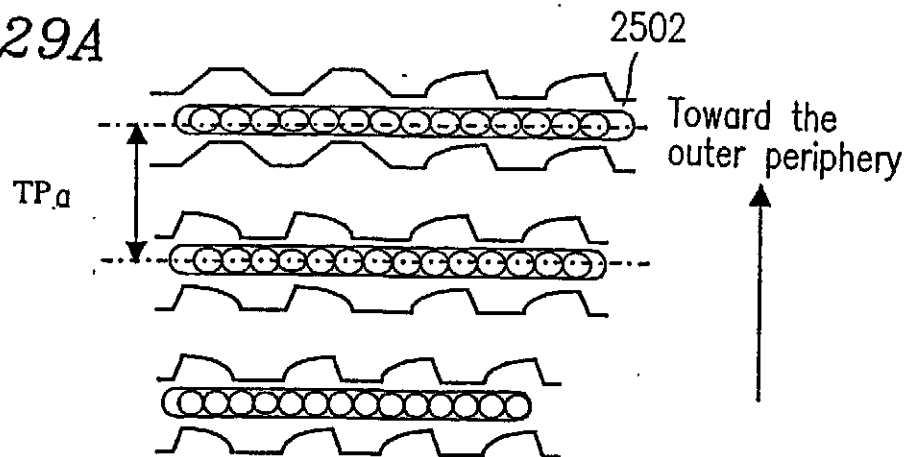
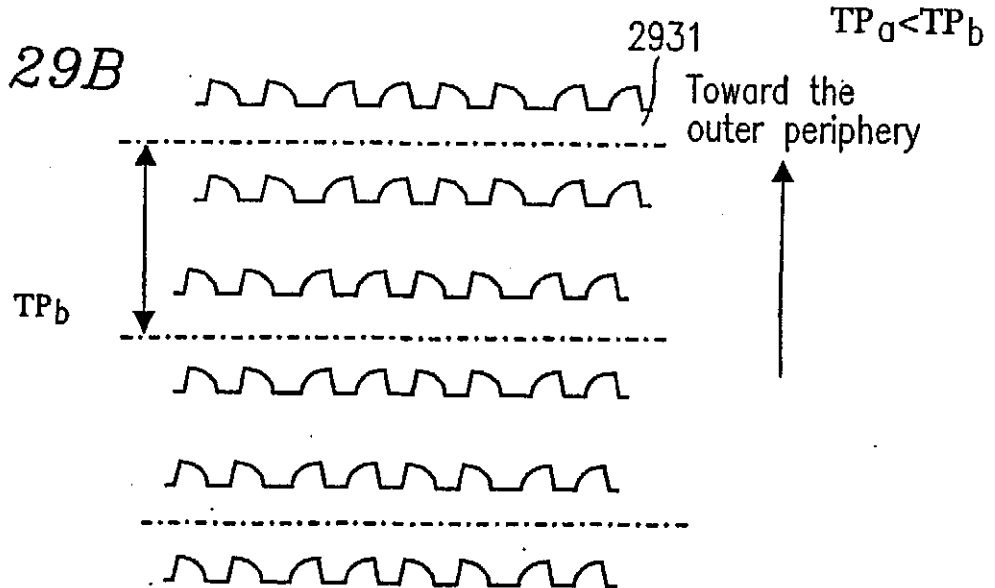


FIG. 29A*FIG. 29B*

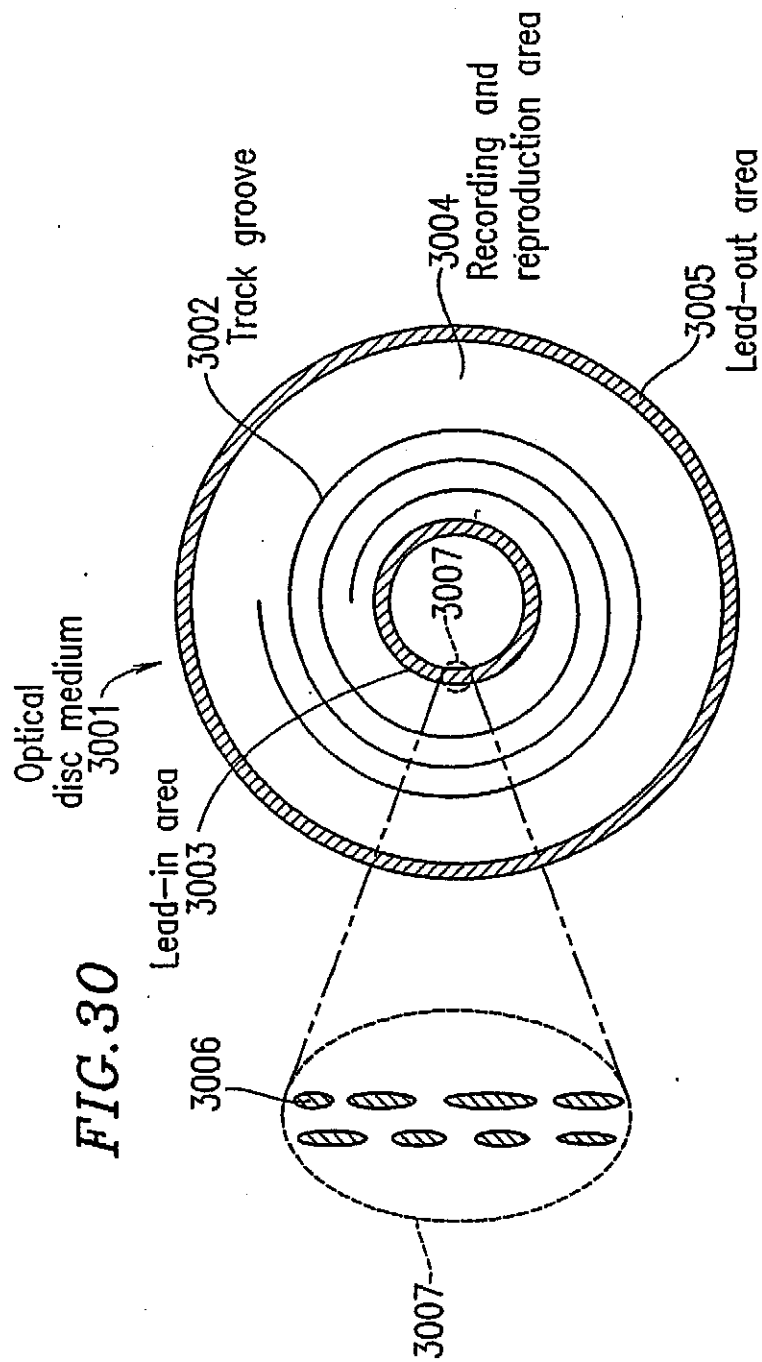


FIG. 31

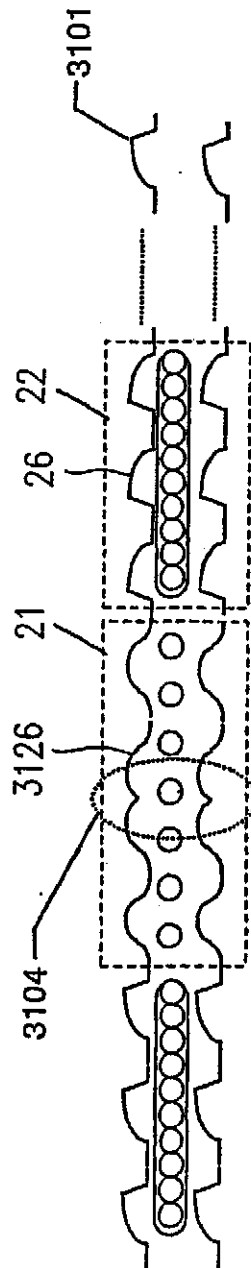


FIG. 32

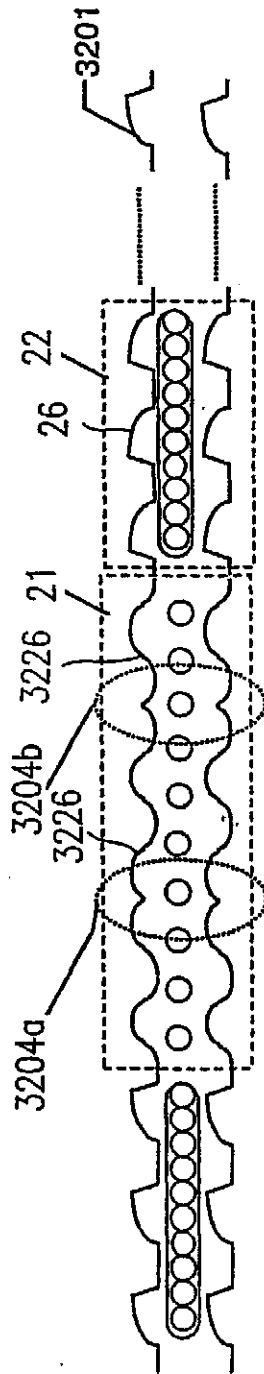
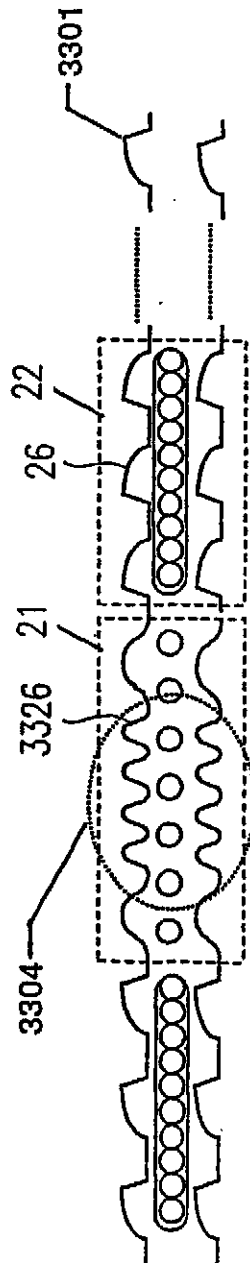


FIG. 33



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FIG. 34

