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(71) Applicant: KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

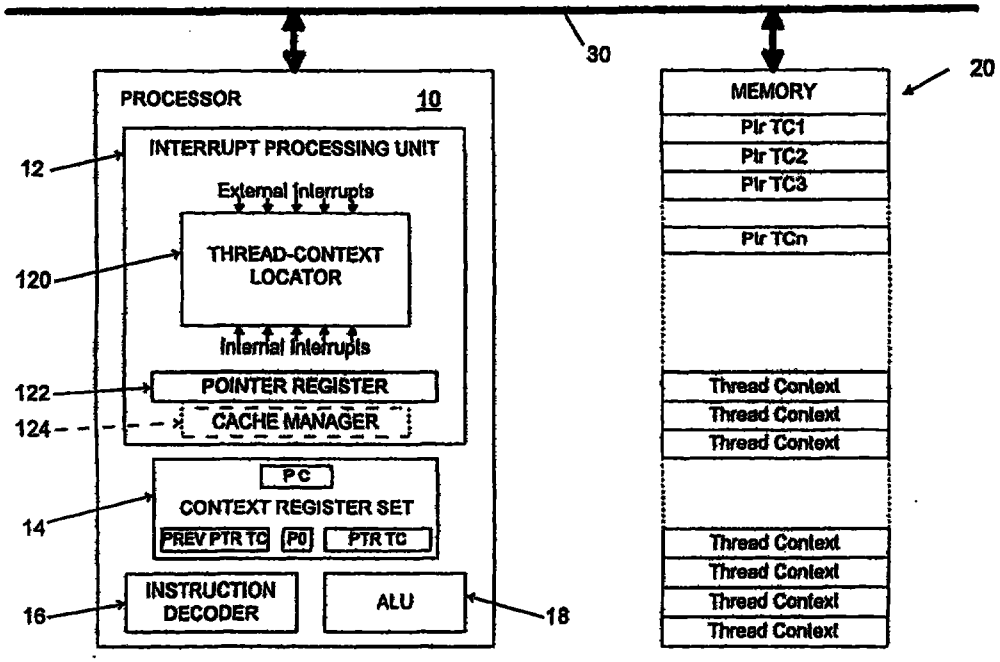
(72) Inventors: SAVILLE, Winthrop, L.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). ROSS, Kevin; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(74) Agent: FAESSEN, Louis, Marie, Hubertus; Internationaal Octrooibureau B.V., Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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(54) Title: INTERRUPT/SOFTWARE-CONTROLLED THREAD PROCESSING



(57) Abstract

Rapid thread processing is achieved by transferring complete thread contexts between a memory and a context register set. Each thread context is read from a respective memory location in response to either a designated interrupt or an instruction.