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SIGNAL DISTRIBUTION AND SURGE DETECTION AND PROTECTION MODULE AND METHOD

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TECHNICAL FIELD

[0002] This invention is generally related to surge protection and, more specifically, to systems and methods for signal distribution and surge detection and protection in various systems including split mount wireless radio systems.

BACKGROUND

[0003] Surge protection circuits are designed to protect electronic equipment from damage due to surge events that may cause large transient over voltage or current spike. Surge events may result from lightning strikes, switching impulses, electrical line noise, and other transients or abnormal conditions and malfunctions. For example, a lightning strike may cause a surge of 20kV that can destroy equipment absent surge protection to prevent such destruction. Surge protection can block surges before they reach the electronic equipment by limiting the voltage, diverting the current through an alternate signal path, or both.

[0004] Examples of surge protectors include primary and secondary protection devices. Generally, a primary protection device is one that typically controls the electric power service entrance, operating as a main disconnect, and a secondary protection device can be connected upstream or downstream of the main disconnect. Surge protection devices may be constructed using fuses, inductors, clamping devices such as metal oxide varistors (MOVs), zener diodes, gas tubes, or various combinations of such devices as well as others.

[0005] Different systems may require different protection measures and possibly different types of surge protection mechanisms. Among such systems are wireless communication or wireless radio systems that can be configured as point-to-point, point-to-multipoint, and mesh networks consisting of multiple terminals. In some wireless communication systems each terminal or node includes digital and analog transceiver portions. The analog portion can include IF (intermediate frequency) and RF (radio frequency) content. With a split mount configuration,



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each node has an indoor unit (IDU) and an outdoor unit (ODU). The IDU connects to the network (e.g., Ethernet or Internet networks) and the ODU connected to an antenna. In this case, the IDU has a power supply and a modem or network interface and the ODU has an RF transceiver. The IDU can supply to the ODU DC power and modulated IF signals for transmission and it can receive from the ODU modulated IF signals received from the antenna. To this end, the IDU and ODU have an up-down connection between them using coaxial cable that can carry both power and IF signals (i.e., DC and non-DC signals).

[0006] In split mount systems such as the aforementioned wireless radio systems, both the ODU and IDU may need protection from surge events. Under regulatory guidelines, the IDU typically requires primary surge protection, i.e., at the power service entrance and at a coaxial cable connection point. Under such guidelines, the surge protection of the ODU can be provided inside or outside the ODU housing and it can be primary or secondary. In the ODU, internal surge suppression circuits provide only secondary protection and external surge arrestors may be costly and require a separate housing. An external surge arrestor is typically sealed from the environment and needs to be properly grounded.

[0007] Failures may occur, however, even with protection mechanisms as described above. For example, in split mount systems the up-down cable connectors may admit surge energy to components inside the wireless radio system and represent a vulnerable point in such systems. Thus, when there is a failure the system manufacturer (e.g., ODU and IDU manufacturer) may be required to perform a warranty repair without necessarily knowing whether the failure is due to a surge event, an inappropriate installation or another cause. There is therefore also a need to detect and better determine the causes of such malfunctions in order to determine, for instance, who might be responsible for the repair.

SUMMARY

[0008] In view of the foregoing, various embodiments of the present invention include distributing RF, DC, and surge components of a signal and detecting, suppressing, and logging surge events. The following outlines some examples of systems and methods for performing these functions.

[0009] In accordance with one embodiment, a system for distributing signals and for detecting, suppressing and recording surge events, comprises: an RF (radio frequency) path; a DC (direct current) path inductively coupled to the RF path; and a surge protection and detection component. The surge protection and detection component comprises a surge protection

component with one or more clamping devices operatively linked between the DC path and ground to clamp surge energy associated with a surge event, and a surge detector operatively linked to the RF path from which it receives an attenuated surge energy associated with such surge event, wherein the surge detector is adapted to provide indicia of the of the surge event for the purpose of logging it for future reference.

[0010] Such system may further comprise a processor and a memory operatively connected for processing and logging the indicia from the surge detector; and it may also include a display, recording or printing device operatively connected to the processor and adapted to receive from the processor information associated with surge events. The information would preferably represent logged surge events, and may represent also current surge events, or both.

[0011] In such system the RF path, DC path and surge protection and detection component may be implemented together in a signal distribution and protection module (SDPM). The SDPM may be adapted to suppress, detect and provide the indicia of surge events resulting from lightning surges or power surges, wherein the indicial is in the form of digital information. The RF path includes an RF matching network with impedance matching inductors. The RF matching network includes a DC blocker connected on each of its ends to an inductor. The RF path may include also two RF connectors with the RF matching network interposed between them.

[0012] The DC path is inductively coupled to the RF path via an inductor adapted to allow DC and low-frequency signal components to reach the DC path and to prevent passage to the DC path of higher frequency components. The DC path is inductively coupled to the RF path via another inductor adapted to allow a portion of low frequency surge energy to reach the RF path bypassing the DC blocker.

[0013] The surge detector is adapted to sample power surge energy and lightning surge energy. The surge detector comprises a sampler and an analog-to-digital converter (ADC) adapted to receive signals from the sampler. The surge detector further comprises a buffer operatively interposed between the sampler and the ADC.

[0014] In the exemplary system, the clamping devices include any combination of a gas tube, a metal oxide varistor, and a zener diode. Such clamping devices are adapted to react to a surge event in a predetermined order. Surge events are typically associated with a lightning strike, a power surge, or electrical noise. Moreover, the surge protection component may further include one or more capacitors each of which being connected across one of the clamping devices. The

DC path includes an inductor interposed between each pair of clamping devices. Furthermore, the DC path includes a power supply connector in series with the inductors.

[0015] A system according to another embodiment of the invention comprises a surge detection and protection module (SDPM) including: an RF path, a DC path inductively coupled to the RF path, a surge protection component operatively coupled between the DC path and ground and adapted to suppress surge energy associated with surge events, and a surge detector adapted to provide indicia of surge events. Such system further includes a processor operatively coupled to the surge detector and adapted to receive and process the indicia in order to manage a log of surge events; a memory adapted to maintain the log of surge events; and an output device operatively coupled to the processor and adapted for one or more of transmitting, printing and displaying information associated with surge events. In this system, the surge detector receives at least a portion of the surge energy via a connection to the RF path, wherein the DC path is adapted to carry DC and low frequency signal components while the RF path includes an RF matching network adapted to carry higher frequency signal components, and wherein low frequency signal components bypass the RF matching network reaching the connection to the RF path via the DC path and the inductive coupling between the DC path and RF path.

[0016] In accordance with yet another embodiment, a system may further comprise an indoor unit (IDU), an outdoor unit (ODU) and a cable connecting between the IDU and ODU, wherein the IDU, ODU, or both, has an SDPM associated therewith. The ODU includes a transceiver operatively connected to the RF path, wherein the RF path is adapted to carry IF (intermediate frequency) signal components to and from the transceiver and wherein the transceiver is adapted to wirelessly transmit and receive microwave signals associated with the IF signal components.

[0017] In accordance with the purpose of the invention as embodied and broadly described herein, a method for distributing signals and for detecting, suppressing and recording surge events, comprises the steps of: providing an RF path; providing a DC path inductively coupled to the RF path; and providing a surge protection and detection device including: providing a surge protection component with one or more clamping devices operatively linked between the DC path and ground to clamp surge energy associated with a surge event, and providing a surge detector operatively linked to the RF path from which it receives an attenuated surge energy associated with such surge event, wherein the surge detector is adapted to provide indicia of the surge event for the purpose of logging it for future reference. This method further comprises the step of

providing a processor adapted to manage a log of surge events based on the indicia from the surge detector.

[0018] Further in accordance with the purpose of the invention another embodiment of a method for distributing signals and for detecting, suppressing and recording surge events, comprising the steps of: receiving a signal including an RF component, a DC component, and a surge component associated with a surge event; propagating the RF component through an RF path while blocking the DC component from the RF path; propagating the DC component through a DC path, wherein the DC path is inductively coupled to the RF path; propagating the surge component through the DC path which includes one or more clamping devices operatively linked ground, and clamping the surge component at the one or more clamping devices; and detecting a residue of the surge component and based thereon logging the surge event.

[0019] These and other features, aspects and advantages of the present invention will become better understood from the description herein, appended claims, and accompanying drawings as hereafter described.

BRIEF DESCRIPTION OF THE DRAWINGS

[0020] The accompanying drawings, which are incorporated in and constitute a part of this specification, illustrate various aspects of the invention and together with the description, serve to explain its principles. Wherever convenient, the same reference numbers will be used throughout the drawings to refer to the same or like elements.

[0021] FIG. 1A is a block diagram of a split mount wireless radio system with a signal distribution and protection module according to one embodiment of the present invention.

[0022] FIG. 1B is a block diagram of a split mount wireless radio system with two signal distribution and protection modules according to one embodiment of the present invention.

[0023] FIG. 2 is a block diagram of a signal distribution and protection module according to one embodiment of the present invention.

[0024] FIG. 3 is a block diagram of a surge detector according to one embodiment of the present invention.

[0025] FIG. 4 is a flow diagram of a method for surge detection and recording according to one embodiment of the invention.

DETAILED DESCRIPTION

[0026] In the following detailed description, reference is made to the accompanying drawings in which are shown by way of illustration a number of embodiments and the manner of practicing the invention. It is to be understood that other embodiments may be utilized and structural changes may be made without departing from the scope of the present invention.

[0027] FIG. 1A is a block diagram illustrating a split mount wireless radio system with a signal distribution and protection module (SDPM) according to one embodiment of the present invention. In this system, an indoor unit (IDU) 110 is coupled to an outdoor unit (ODU) 120 via a cable 115. The IDU 110 is coupled to a network (not shown) with which it communicates to receive and send data. Accordingly, the IDU has both transmit and receive paths. In the transmit path, the IDU 110 is configured to receive data from the network which it uses in modulating an RF signal using, e.g., QAM (quadrature amplitude modulation), to produce a modulated RF signal (which is often an IF signal). The IDU 110 supplies to the ODU 120 the modulated RF signal along with DC power. Thus, the cable 115 is preferably a coaxial cable designed for carrying a signal that includes both an RF component and a DC component. During a surge event, the cable 115 may carry the surge or transient impulse. In the receive path, the IDU 110 receives modulated signals (often intermediate frequency (IF) signals) from the ODU 120 and after demodulating them the IDU sends the data over the network.

[0028] In this embodiment, the ODU 120 includes a motherboard 122, a processor 124, and a DC power module 126. The motherboard 122 receives the modulated RF signals transmitted from the IDU 110 and in the transceiver module convert them to microwave or high frequency signals for transmission via an antenna. The processor 124 in the ODU is operative to control operations of the ODU including, preferably, processing and logging surge events. The processor 124 may be implemented as a processor module on a printed circuit board or an integrated circuit, or in other suitable configuration. Examples of integrated circuit processors may include a microcontroller, a microprocessor, an application specific integrated circuit (ASIC) and a field programmable gate array (FPGA).

[0029] The ODU is preferably enclosed in a housing and an SDPM 130 is preferably also located within the housing of the ODU 120. In some system configurations the SDPM may be located externally to the housing. The block diagrams of FIG. 1A and FIG. 1B illustrate that wireless radio systems may have an SDPM at the ODU, IDU or both. Moreover, although embodiments of the SDPM 130 and 132 are shown here as used in wireless radio systems various embodiments of the SDPM may be used in systems other than wireless radio systems.

[0030] As further shown in FIG. 1A, the SDPM 130 distributes RF signals and DC signals received over cable 115 to the motherboard 122 and the DC power module 126, respectively; and it distributes received RF signals in the opposite direction over the cable 115 to the IDU 110. Additionally, the SDPM 130 detects and suppresses surges during surge events and provides indicia of such surge events to the processor 124. Examples of surge events that an SDPM may be configured to intercept include surge energy appearing on the cable connectors at the ODU or IDU and surge energy appearing at a service entrance to the IDU (or any other electronic equipment). Thus, in some wireless radio systems the IDU may also have an SDPM to intercept surge events, and an SDPM may be installed at the service entrance, the up-down cable connection, or both. In the illustrated embodiment, the SDPM 130 is located inside the ODU housing to intercept surge energy introduced at the cable 115 connection point to the ODU.

[0031] FIG. 2 is a block diagram of an SDPM 130 illustrating its functional components according to one embodiment of the invention. In this embodiment, an SDPM 130 comprises separate paths for the RF and DC signal components. The RF path 215 and DC path 216 are designed for signal distribution with proper electrical isolation between them to minimize interference. Two inductors, such as electromagnetic coils or RF chokes 206 and 207, inductively couple the RF path 215 and the DC path 216 in the SDPM 130. Additionally, the SDPM 130 includes a surge protection component and a surge detection and reporting component. Preferably, the surge protection component includes clamping devices 210, 211 and 212. The surge detection and reporting component includes a surge detector 214 operatively coupled to the RF path 215 and the processor 124.

[0032] The RF path 215 provides a path to and from the transceiver in the ODU for the modulated RF or non-DC components of the signals; and it provides a path for attenuated surge pulses. For modulated RF signals in a typical IF range the RF path 215 may include RF connectors 201 and 205 coupled through a DC blocker 203 that is connected at each side of an RF inductor 202 and 204. The RF inductors 202 and 204 provide respective RF matching to the RF connectors 201 and 205 in order to improve return loss. Such RF components can be designed for operation in an intermediate frequency range (IF; e.g., 50MHz - 450MHz) or other operating frequencies of the non-DC signals. Moreover, depending on the return loss requirements and the SDPM physical layout, the inductors 202 and 204 may be altogether omitted from the RF path 215. Coupled between these RF inductors, the DC blocker 203, which may be a capacitor, blocks the DC power from encroaching on the RF signals path. The DC blocking capacitor can be designed to fit the desired frequency response. The RF inductors and capacitor 202, 203 and 204

form an RF matching network (with high-pass or band-pass characteristics) allowing RF signals to pass through with minimal insertion losses.

[0033] The aforementioned inductors 206 and 207 prevent the RF signal component from reaching the DC signal path 216, and thus may also be known as RF isolators or RF chokes. Specifically, in the illustrated SDPM 130, the first inductor 206, close to RF connector 201, allows the DC power and the surge energy into the DC path. In a different scenario an SDPM may be used, say in the IDU, at the cable connection point (as shown in FIG. 1B), at a power line connection point, or both.

[0034] Returning to FIG. 2, the second inductor 207 isolates DC path 216 from RF path 215 and to prevent lightning leakage voltage (from RF path 215) rise at RF connector 205 when a lightning surge event occurs. This is because the second inductor 207 shunts such voltage rise to ground via the TVS 212. It also provides a sampling path for suppressed low frequency components of a surge pulse from DC path 216 to surge detector 214.

[0035] As further shown in FIG. 2, the RF path includes also the surge detector 214 that is coupled to the RF connector 205. The surge detector 214 is adapted to receive attenuated surges from the RF path 215 and from DC path 216 and detect surge events. For example, an RF matching network tuned to IF frequency (e.g., 450MHz) attenuates a 1MHz lightning surge energy significantly but allows sufficient surge energy level for detection by the surge detector 214. In the case of power surge, the attenuated surge energy is provided via the second inductor 207. So, although an SDPM can be adapted to detect and protect against energy surges in various scenarios including the aforementioned lightning surge and power surge, in the ODU it is adapted to distribute IF signals and detect and protect against damage from lightning surge events. Accordingly, the surge detector 214 is further adapted to send indicia of such surge events to the processor 124 for logging and reporting them.

[0036] Inductively coupled to the RF path by the inductors 206 and 207, the DC path 216 provides a path for the DC power, as well as, for the surge energy that, as mentioned before, contains mostly DC and lower frequency energy. In the illustrated embodiment, the DC path 216 includes two inductors 208 and 209 and a power supply (PS) connector 213 that carries the DC power to the power module 126 in the ODU 120. The inductors, shown here as power coils 208 and 209, affect the time constant of surge or transient impulses and thus introduce a delay. The DC power can be provided through the power supply connector 213 as regulated or unregulated DC voltage (for example, -48V DC or -26V through -60V DC).

[0037] To suppress the surge energy, the surge protection component includes one or more clamping devices operatively coupled between the DC path 216 and ground. In this instance, there are three clamping devices including a gas tube 210, a metal oxide varistor (MOV) 211, and a transient voltage suppressor (TVS) 212. The surge protection device may further include capacitors in parallel with the clamping devices, to provide RF grounding. As shown in this example, a capacitor (CAP) is provided across the gas tube 210 and the TVS 212.

[0038] The gas tube 210 is a device that shorts when an applied voltage exceeds its rated clamping voltage. A typical gas tube includes a sealed chamber containing a gas that ionizes and allows current to flow to ground. The gas tube 210 may be a component which has a rated clamping voltage and nominal impulse discharge current. The MOV 211 is another device that shorts when an applied voltage exceeds its rated clamping voltage. Generally, the MOV 211 reacts more quickly to a surge or transient impulses than the gas tube 210, but can dissipate less power than gas tube 210. A typical MOV functions as a non-linear or variable resistor that exhibits a high resistance to ground for applied voltage that is below the rated clamping voltage and low resistance to ground for applied voltage that is at or above the rated clamping voltage. The MOV 211 may be a component, which has a maximum clamping voltage and a maximum peak impulse discharge current. The transient voltage suppressor (TVS) 212 may be a zener diode. For example, TVS 212 may be a component, which has a maximum clamping voltage and a maximum peak impulse current. Relative to the MOV 211 and gas tube 210, the TVS 212 reacts more quickly to a surge or transient impulse but can dissipate less power.

[0039] During normal operation, the RF signal component traverses the RF path 215 (formed in this embodiment by RF connector 201, RF inductor 202, DC blocker 203, RF inductor 204, and RF connector 205). The DC power signal component traverses a path including RF connector 201, inductor 206, and the DC path 216 (formed by inductors 208, inductor 209 and PS connector 213). When a surge event occurs, the surge energy (impulse) traverses a signal path formed by RF connector 201, inductor 206, inductor 208, inductor 209, gas tube 210, MOV 211, TVS 212, inductor 207, and surge detector 214. The clamping devices 210, 211 and 212 suppress the surge energy pulses in the DC path 216, diverting most of the surge energy to ground. Preferably, the clamping devices are arranged so that they encounter the various level of the propagating surge energy in a particular order. Gas tube 210 acts as the primary protector. MOV 211 acts as the secondary protector, and TVS 212 as the tertiary protection. These components must be separated by an isolating impedance that is inductive or resistive to ensure proper coordination of the protective devices. In most applications an inductive element is used to minimize power

dissipation and voltage drop during normal operation. In a gas-tube arrestor, the low impedance of the arc after sparkover causes dissipation of most of the energy elsewhere, e.g. power-follow energy. A voltage-clamping suppressor, such as MOV, must absorb a large amount of transient surge energy. Its clamping action does not involve the power-follow energy resulting from a gas's short action. The sequence of events for the transient suppression is described below: (a) the TVS 212 clamps the leading edge of the transient to a value just above the rated operation voltage; (b) as the transient voltage continues to rise, it reaches the operating range of the MOV 211 with a peak current handling capability higher than the TVS; (c) as the current through the MOV increases, a voltage develops across the inductor 208, which causes the gas tube to fire, shunting the transient energy through the gas tube and away from the protected circuit; (d) gas tube remains in full condition for the duration of the transient; and (e) after the transient passes the gas tube extinguishes, ready for the next transient. This circuit uses each component to do what each does best: the gas tube diverts the high energy portion of the transient, the MOV provides intermediate power handling and protection of TVS, which accomplishes accurate clamping of the low energy leading edge.

[0040] Note that the surge protection component may include more than three clamping devices (e.g., four) or it may include fewer than three (e.g., two) depending on actual applications and protection requirements.

[0041] For the purpose of detection and logging, whatever energy is left (not clamped) is sufficient for detection and is passed to the surge detection component 214. FIG. 3 is a block diagram illustrating a surge detection component according to one embodiment of the invention. In this embodiment, the surge detector 214 is at the ODU and includes a number of components connected in series. Specifically, it includes a sampler 302 that is coupled to a limiter 303 that is in turn coupled to a buffer 304. The buffer 304 is coupled to an analog-to-digital converter (ADC) 305 that is coupled to the processor 124.

[0042] The surge detector 214 is adapted to receive a signal from the RF path 215 (FIG. 2), and such RF signal may include an RF component and a surge energy component. The sampler 302 samples the surge energy component and provides the samples to limiter 303. The sampler 302 might be a resistive voltage divider or another suitable device. The limiter 303 limits the amplitude of the sampled surge energy pulses. The limiter 303 may be a full-wave or a half-wave clamping device such as a low power TVS diode configured to limit the voltage to a level below the breakdown voltage of the buffer. The buffer 304 isolates the ADC 305 from the limiter 303 and can also scale up or down the sampled surge energy pulse levels. ADC 305 provides an

analog-to-digital conversion of the scaled, sampled surge pulses, providing digital values as indicia of a surge event to processor 124. A threshold is established inside ODU processor 124 to distinguish an incoming surge pulse and a "noise". Once the detected surge pulse level is over the threshold, the surge detecting and recording program is triggered and the surge event is logged. If the incoming pulse is below the threshold, the processor will recognize it as a low level "noise" and no event is recorded.

[0043] FIG. 4 is a flow diagram illustrating a method for detecting and recording surge events according to one embodiment of the present invention. According to this method, upon detecting a surge event in step 400, a processor receives from the surge detection component a digital value representative of the surge energy at step 401. The processor performs a check at step 402 to determine whether a bit error has occurred. If not, the surge event is logged at step 409. Logging the surge event may include storing a surge event identifier as well as a time associated with the surge event. If a bit error is detected at step 402, the processor performs a check at 403 to determine whether an ODU alarm has been triggered. . If an ODU alarm has not been triggered, the surge event is logged at step 409. Otherwise, the processor resets the ODU at step 404. Following the reset, the processor performs a check at step 405 to determine whether an ODU alarm has been triggered. If not, the surge event is logged at step 409. Otherwise, the processor resets the ODU at step 406. Following the reset, the processor performs a check at step 407 to determine whether an ODU alarm has been triggered. If not, the surge event is logged at step 409. Otherwise, the processor determines at step 408 that the ODU has been damaged. The processor then logs the surge event at step 409. The processor outputs information associated with the logged surge event to a display (or it may output this information to a printer or send the information to another device, say over a network) at step 410. The processor then resets the surge detector at step 411. The detection and logging is provided in order to determine possible causes of a failure and who might be responsible for the repair.

[0044] In sum, the present invention contemplates various embodiments of a surge detection and protection module. While the invention has been described and illustrated in connection with a number of embodiments other embodiments are possible without departing from the spirit and scope of the invention. Accordingly, the scope of the invention as recited in the claims below should not be limited to the precise details of methodology or construction set forth above as variations and modification may also be included within the scope of the invention.

CLAIMS

We claim:

1. A system for distributing signals and for detecting, suppressing and recording surge events, comprising:
 - an RF (radio frequency) path;
 - a DC (direct current) path inductively coupled to the RF path; and
 - a surge protection and detection component including
 - a surge protection component with one or more clamping devices operatively linked between the DC path and ground to clamp surge energy associated with a surge event, and
 - a surge detector configured to receive the clamped surge energy wherein the surge detector is adapted to provide indicia of the of the surge event for the purpose of logging it for future reference.
2. A system as in claim 1, wherein the RF path, DC path and surge protection and detection component are implemented together in a signal distribution and protection module (SDPM).
3. A system as in claim 2, wherein the SDPM is adapted to suppress, detect and provide the indicia of surge events resulting from lightning surges or power surges.
4. A system as in claim 1, wherein the indicial is in the form of digital information.
5. A system as in claim 1, further comprising a processor and a memory operatively connected for processing and logging the indicia from the surge detector.
6. A system as in claim 5, further comprising a display, recording or printing device operatively connected to the processor and adapted to receive from the processor information associated with surge events.

7. A system as in claim 6, wherein the information represents logged surge events, current surge events or both.
8. A system as in claim 1, wherein the RF path includes a DC blocker.
9. A system according to claim 1, wherein the RF path includes an RF matching network with impedance matching inductors.
10. A system according to claim 8, wherein the RF path includes an RF matching network that includes the DC blocker connected on each of its ends to an inductor.
11. A system as in claim 1, wherein the RF path includes two RF connectors with an RF matching network interposed between them.
12. A system as in claim 1, wherein the DC path is inductively coupled to the RF path via an inductor adapted to allow DC and low-frequency signal components to reach the DC path and to prevent passage to the DC path of higher frequency components.
13. A system as in claim 12, wherein the RF path includes a DC blocker, and wherein the DC path is inductively coupled to the RF path via another inductor or resistor adapted to allow a portion of low frequency surge energy to reach the RF path bypassing the DC blocker.
14. A system as in claim 1, wherein the surge detector is adapted to receive power surge energy and lightning surge energy.
15. A system as in claim 14, wherein the surge detector is adapted to receive power surge energy via the DC path and lightning surge energy via the RF path.

16. A system as in claim 1, wherein the one or more clamping devices includes any combination of a gas tube, a metal oxide varistor, and a zener or transient voltage suppressor (TVS) diode.
17. A system as in claim 1, wherein the surge protection component further includes one or more capacitors each of which being connected across one of the clamping devices.
18. A system as in claim 1, wherein the DC path includes an inductor or a resistor interposed between each pair of clamping devices.
19. A system as in claim 1, wherein the DC path includes two inductors in series and three clamping devices operatively coupled between the two inductors or resistors and ground, each inductor or resistor being adapted to introduce a time delay into surge energy propagating therethrough to a clamping device to which the inductor or resistor is connected.
20. A system as in claim 19 wherein the DC path includes a power supply connector in series with the inductors.
21. A system as in claim 1, wherein when more than one of the clamping devices are present in the surge protection component such clamping devices are adapted to react to a surge event in a predetermined order.
22. A system as in claim 1, wherein the one or more clamping devices in the surge protection component include a gas tube, a metal oxide varistor, and a zener diode, wherein the surge protection unit further includes two inductors or resistors operative to introduce a time delay.
23. A system as in claim 1, wherein surge events are associated with a lightning strike, a power surge, or electrical noise.

24. A system as in claim 1, wherein the surge detector comprises a sampler and an analog-to-digital converter (ADC) adapted to receive signals from the sampler.

25. A system as in claim 24, wherein the surge detector further comprises a buffer operatively interposed between the sampler and the ADC.

26. A system for distributing signals and for detecting, suppressing and recording surge events, comprising:

- a surge detection and protection module (SDPM) including:

- an RF path,

- a DC path inductively coupled to the RF path,

- a surge protection component operatively coupled between the DC path and ground and adapted to suppress surge energy associated with surge events, and

- a surge detector adapted to receive suppressed surge energy and provide indicia of surge events;

- a processor operatively coupled to the surge detector and adapted to receive and process the indicia in order to manage a log of surge events;

- a memory adapted to maintain the log of surge events; and

- an output device operatively coupled to the processor and adapted for one or more of transmitting, printing and displaying information associated with surge events.

27. A system as in claim 26, wherein the surge detector receives at least a portion of the surge energy via a connection to the RF path, wherein the DC path is adapted to carry DC and low frequency signal components while the RF path includes an RF matching network adapted to carry higher frequency signal components, and wherein low frequency signal components bypass the RF matching network reaching the connection to the RF path via the DC path and the inductive coupling between the DC path and RF path.

28. A system as in claim 26, further comprising an indoor unit (IDU), an outdoor unit (ODU) and a cable connecting between the IDU and ODU, wherein the IDU, ODU, or both, has an SDPM associated therewith.

29. A system as in claim 28, wherein an SDPM associated with the IDU is located at a connection to the cable.

30. A system as in claim 28, wherein an SDPM associated with the IDU is located at a power service entrance.

31. A system as in claim 28, wherein the ODU is enclosed in a housing, and wherein an SDPM associated with the ODU is located at a connection to the cable either inside or outside the housing.

32. A system as in claim 28, wherein the ODU includes a transceiver operatively connected to the RF path.

33. A system as in claim 32, wherein the RF path is adapted to carry IF (intermediate frequency) signal components to and from the transceiver and wherein the transceiver is adapted to wirelessly transmit and receive microwave signals associated with the IF signal components.

34. A system as in claim 28, wherein the ODU includes a DC power module operatively coupled to the DC path.

35. A method for distributing signals and for detecting, suppressing and recording surge events, comprising the steps of:

- providing an RF path;

- providing a DC path inductively coupled to the RF path; and

- providing a surge protection and detection device including:

- providing a surge protection component with one or more clamping devices operatively linked between the DC path and ground to clamp surge energy associated with a surge event, and

providing a surge detector, configured to receive the clamped surge energy, the surge detector being operatively linked to the RF path from which the surge detector receives an attenuated surge energy associated with such surge event, wherein the surge detector is adapted to provide indicia of the surge event for the purpose of logging it for future reference.

36. A method as in claim 35, further comprising the step of providing a processor adapted to manage a log of surge events based on the indicia from the surge detector.

37. A method for distributing signals and for detecting, suppressing and recording surge events, comprising the steps of:

receiving a signal including an RF component, a DC component, and a surge component associated with a surge event;

propagating the RF component through an RF path while blocking the DC component from the RF path;

propagating the DC component through a DC path, wherein the DC path is inductively coupled to the RF path;

propagating the surge component through the DC path which includes one or more clamping devices operatively linked to ground, and clamping the surge component at the one or more clamping devices; and

detecting a residue of the clamped surge component and based thereon logging the surge event.