



SYNCHRONIZED OFFSET EXTRACTION IN A DATA RECEIVER

ABSTRACT

5 A selective call receiver (106) receives a radio
frequency signal having digital signals contained within
symbols. The signal demodulated by the receiver (804)
comprises a substantially constant DC offset voltage and an
AC voltage (1810) indicative of the information contained
10 within the symbols. A threshold level extraction circuit
(808) which extracts the offset voltage is synchronously
controlled (1840) providing for sampling of the received
signal by the threshold level extraction circuit in
substantially the center of each symbol.

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Figs. 8 and 18 accompany this abstract.