FIG. 1

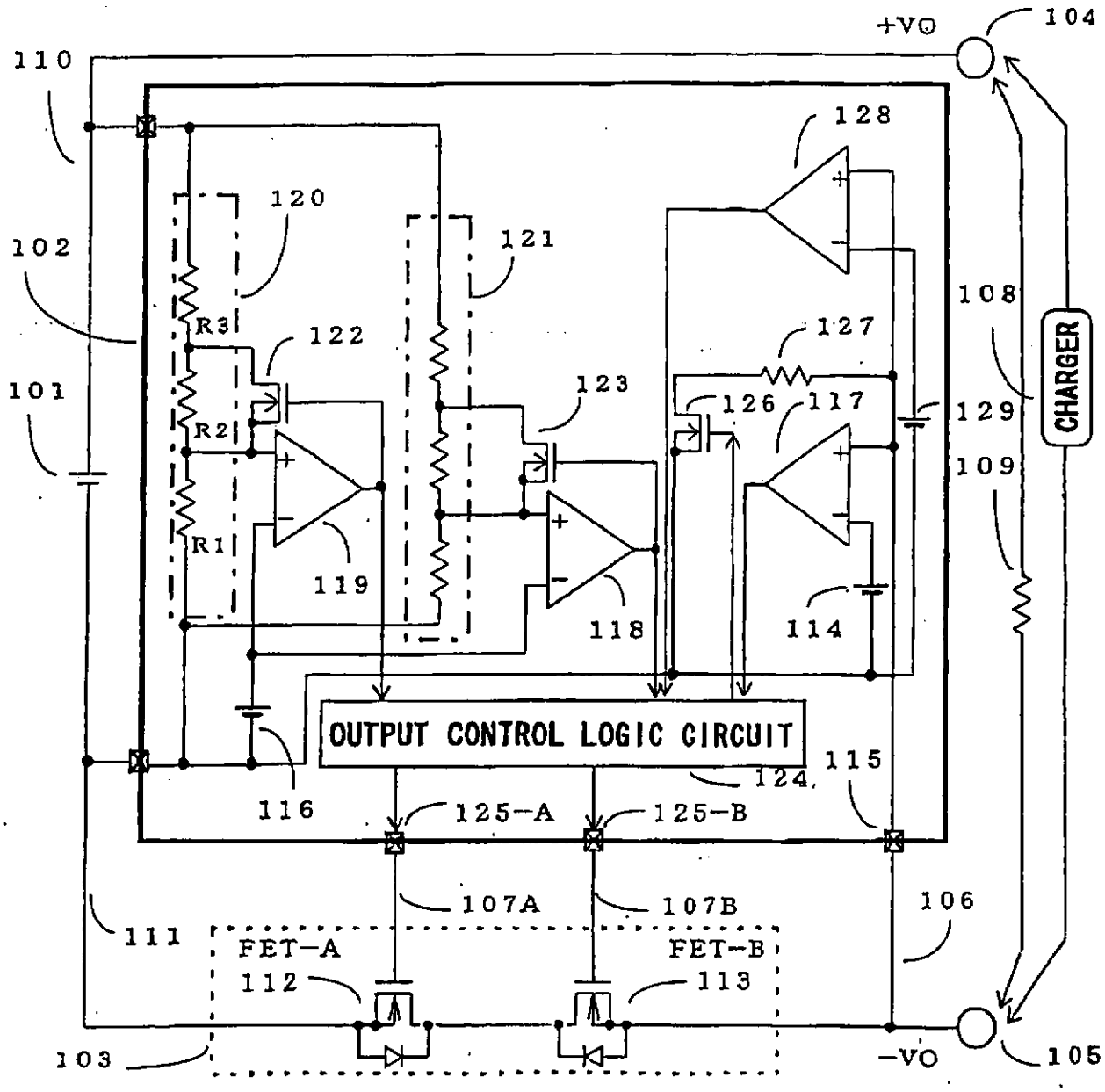

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CHARGER
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R2
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OUTPUT CONTROL LOGIC CIRCUIT
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125-A
125-B
107A
107B
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FET-A
FET-B
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-VO
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FIG. 2 PRIOR ART

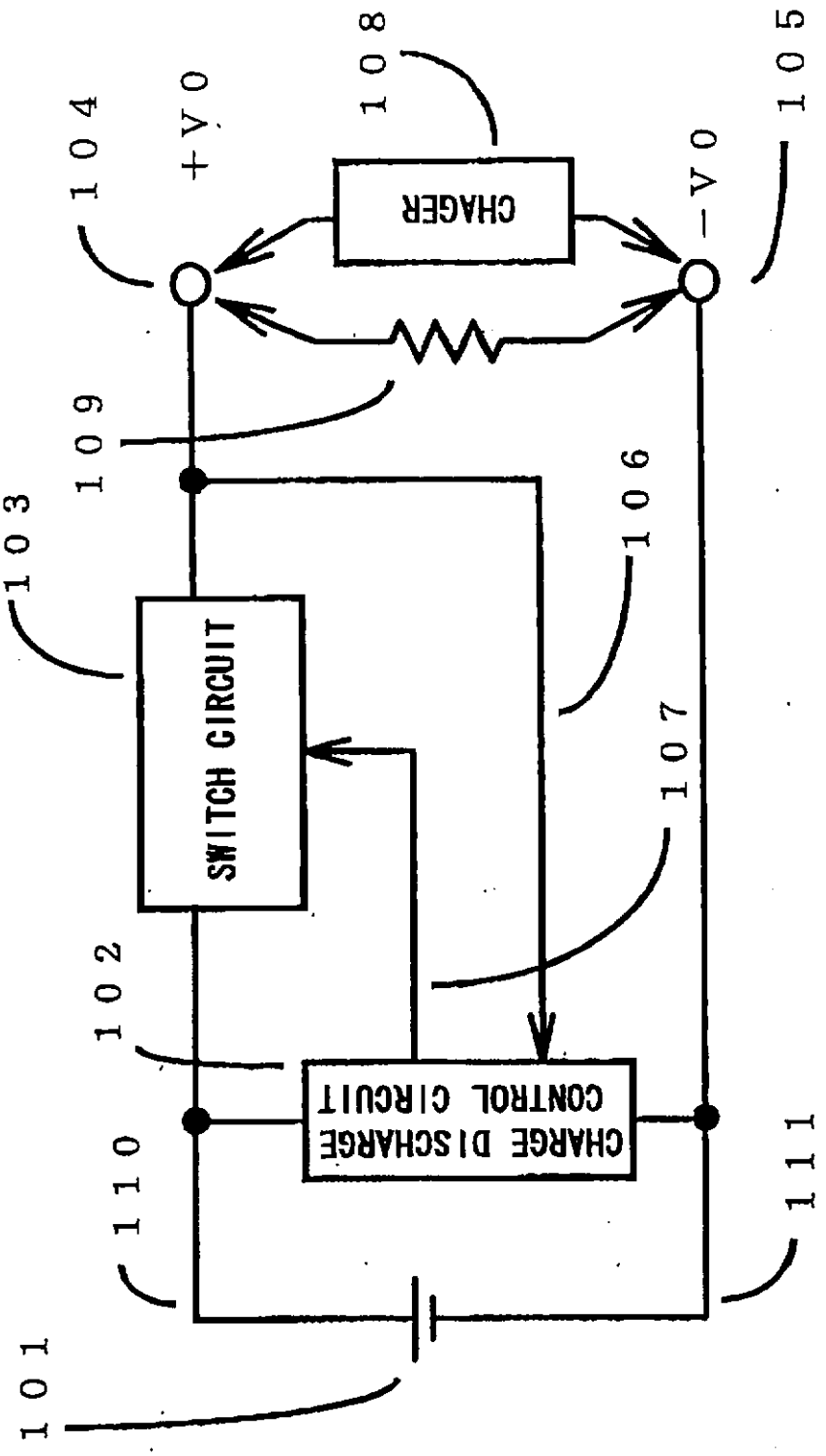


FIG. 3 PRIOR ART

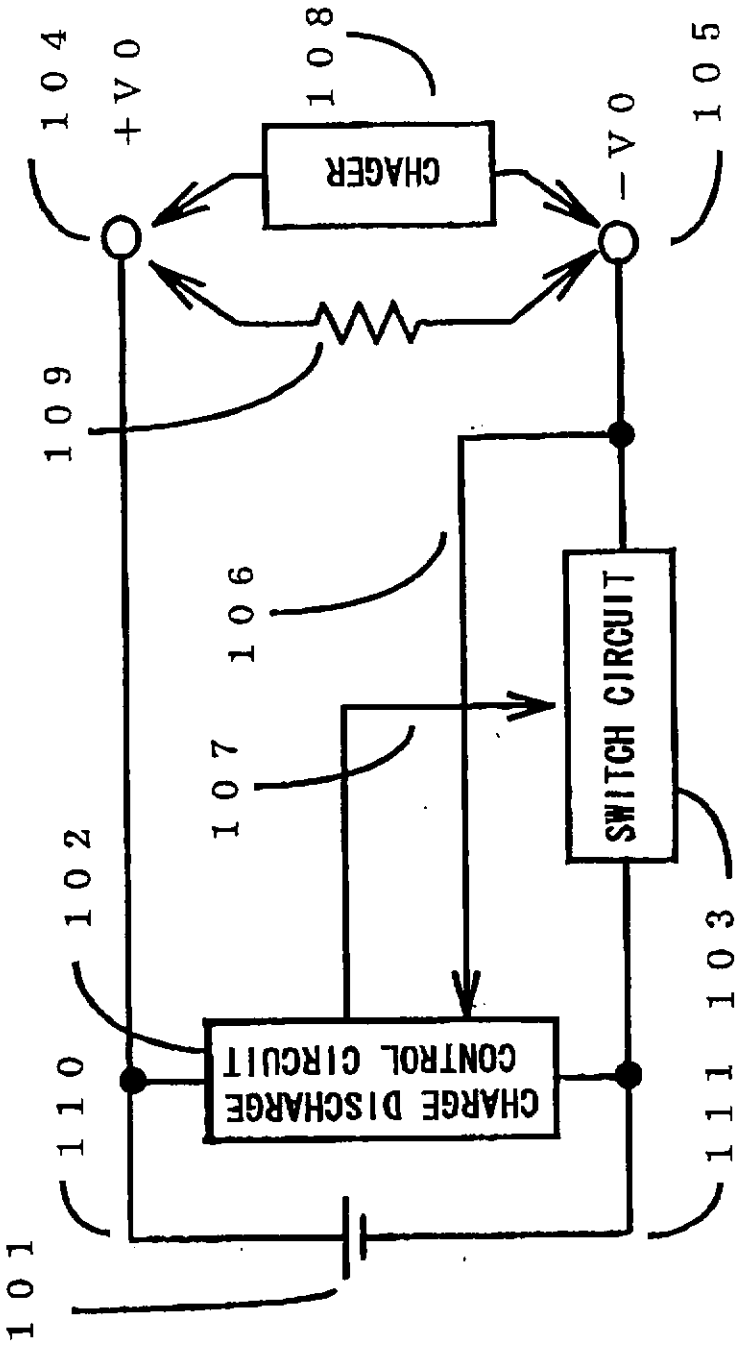


FIG. 4 PRIOR ART

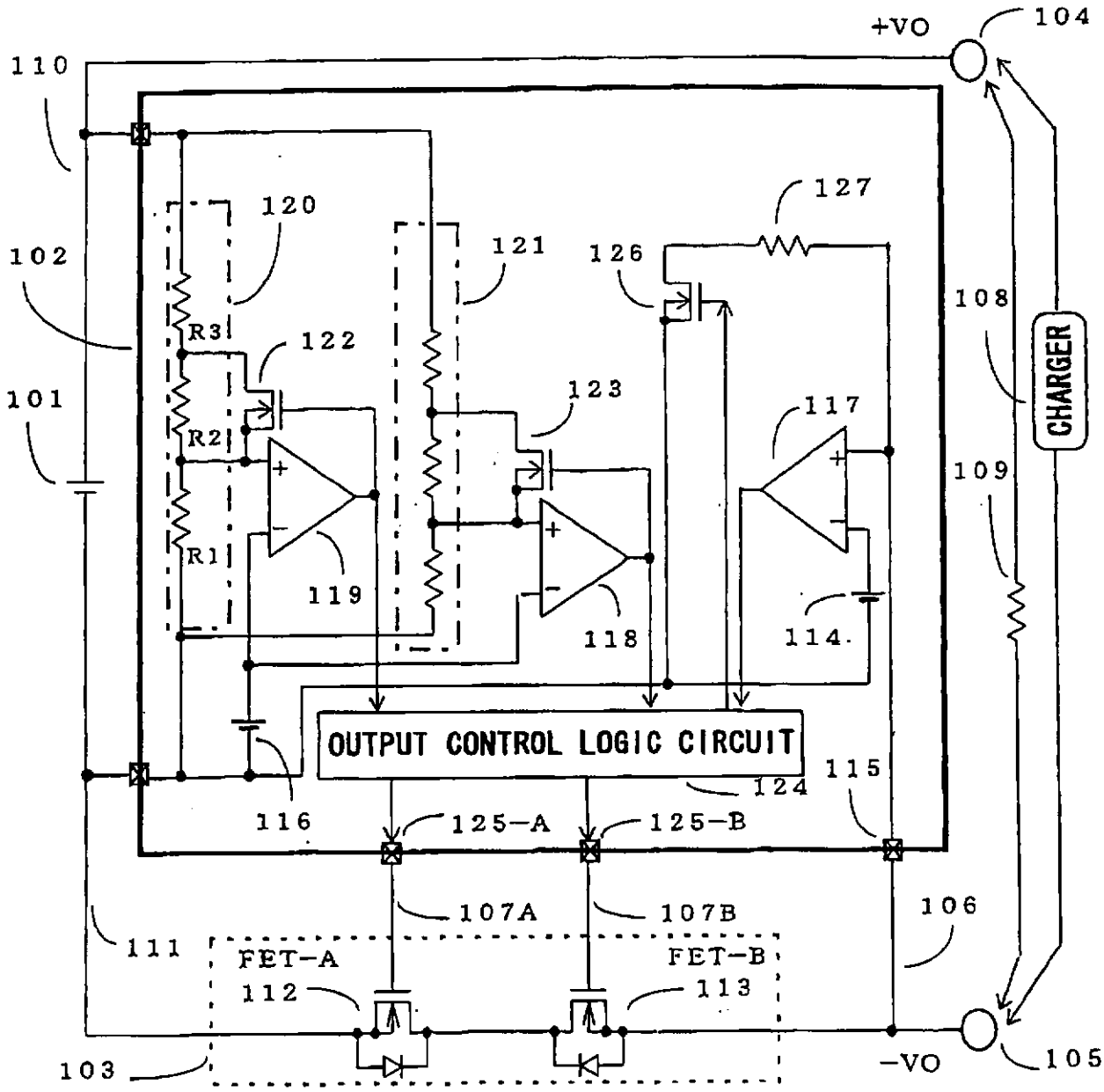


FIG. 5

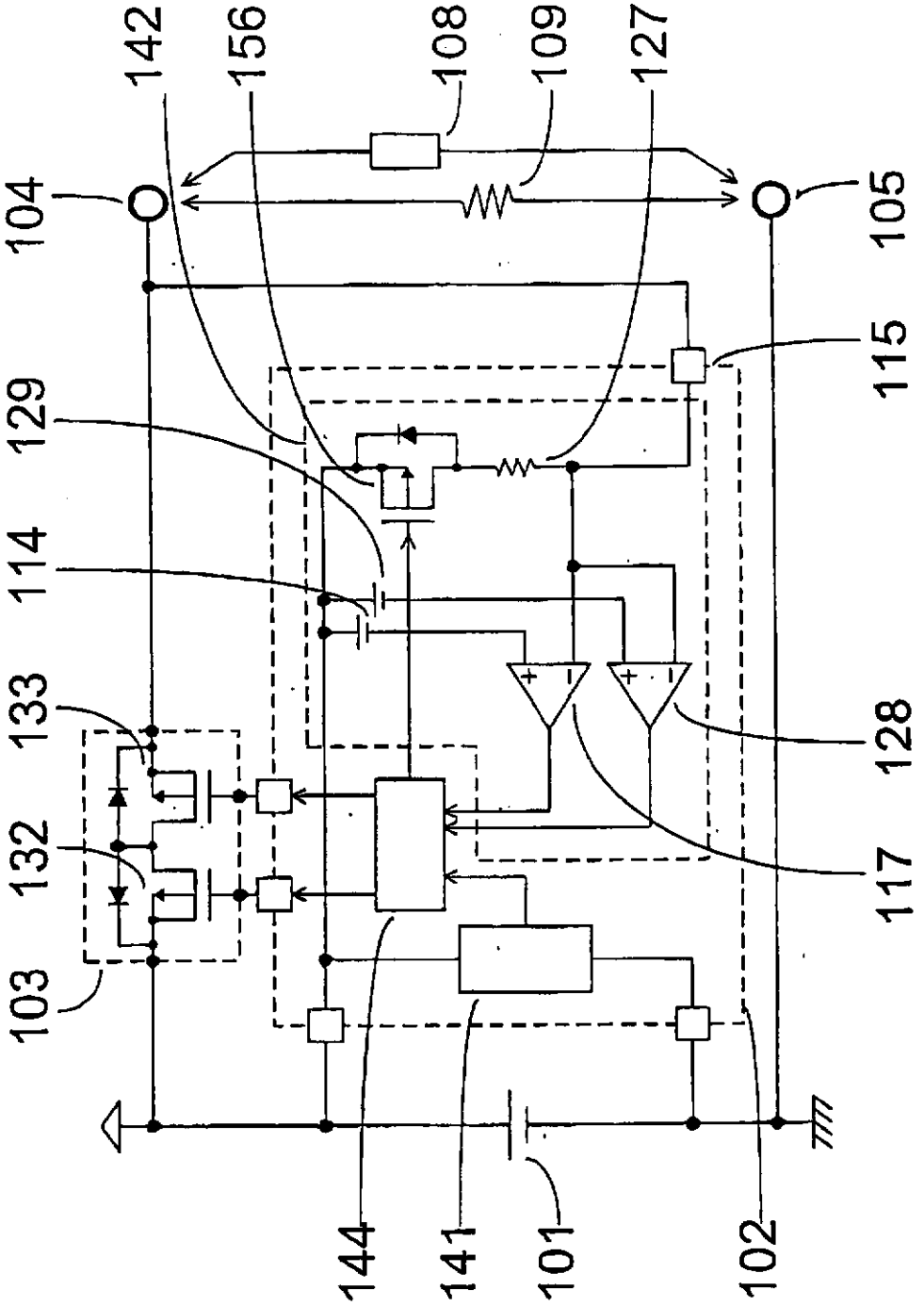

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FIG. 6

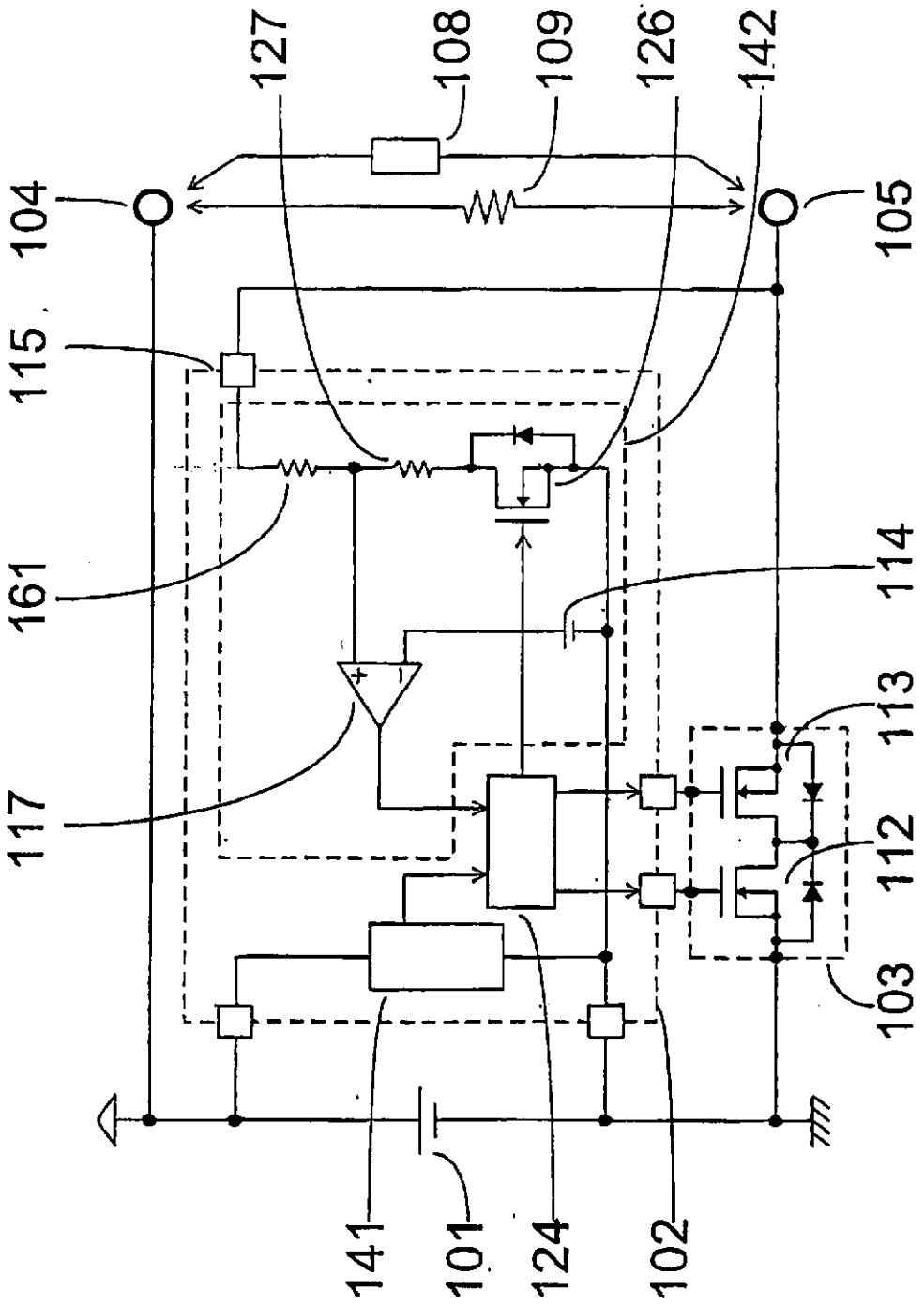

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FIG. 7

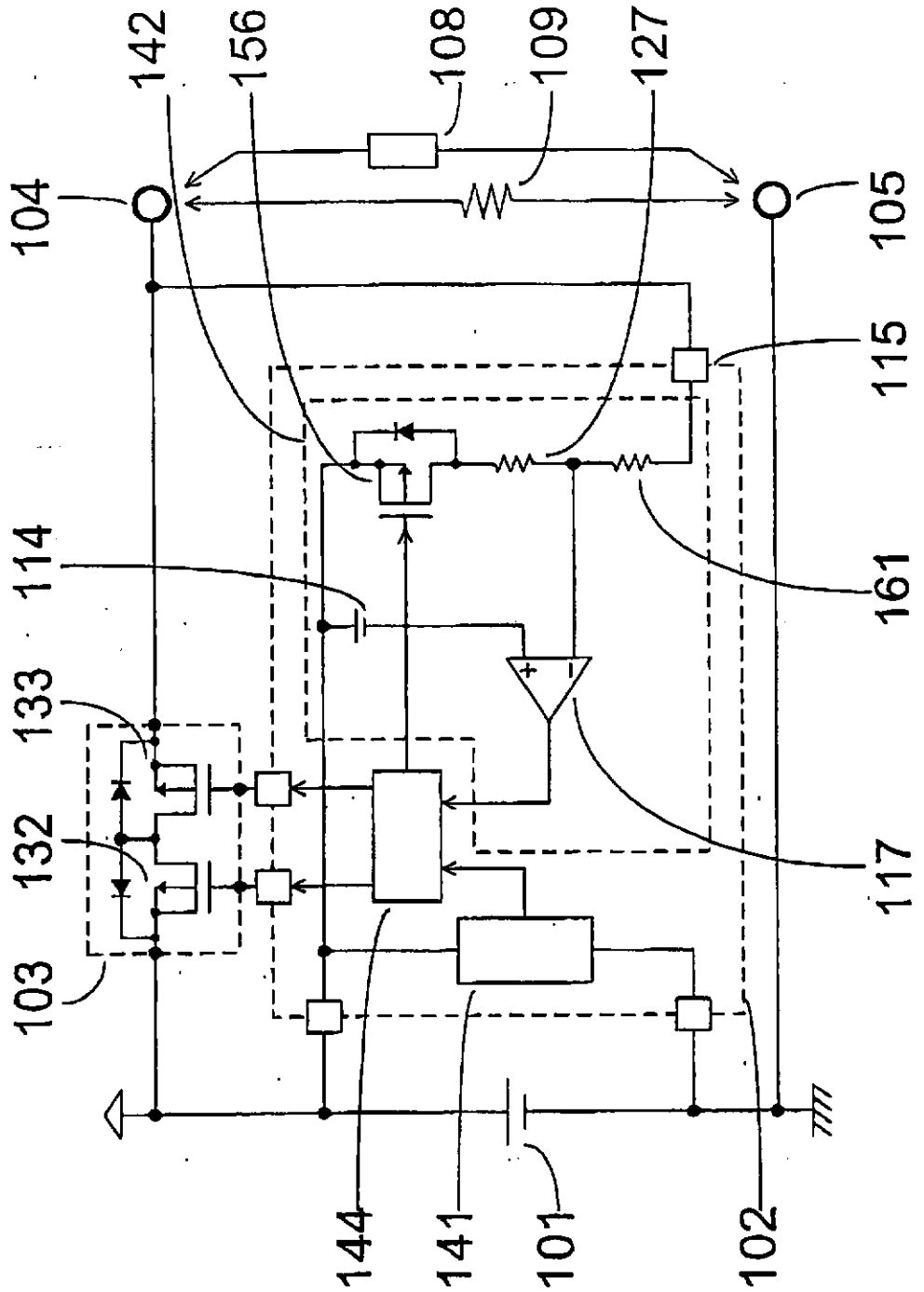

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FIG. 8

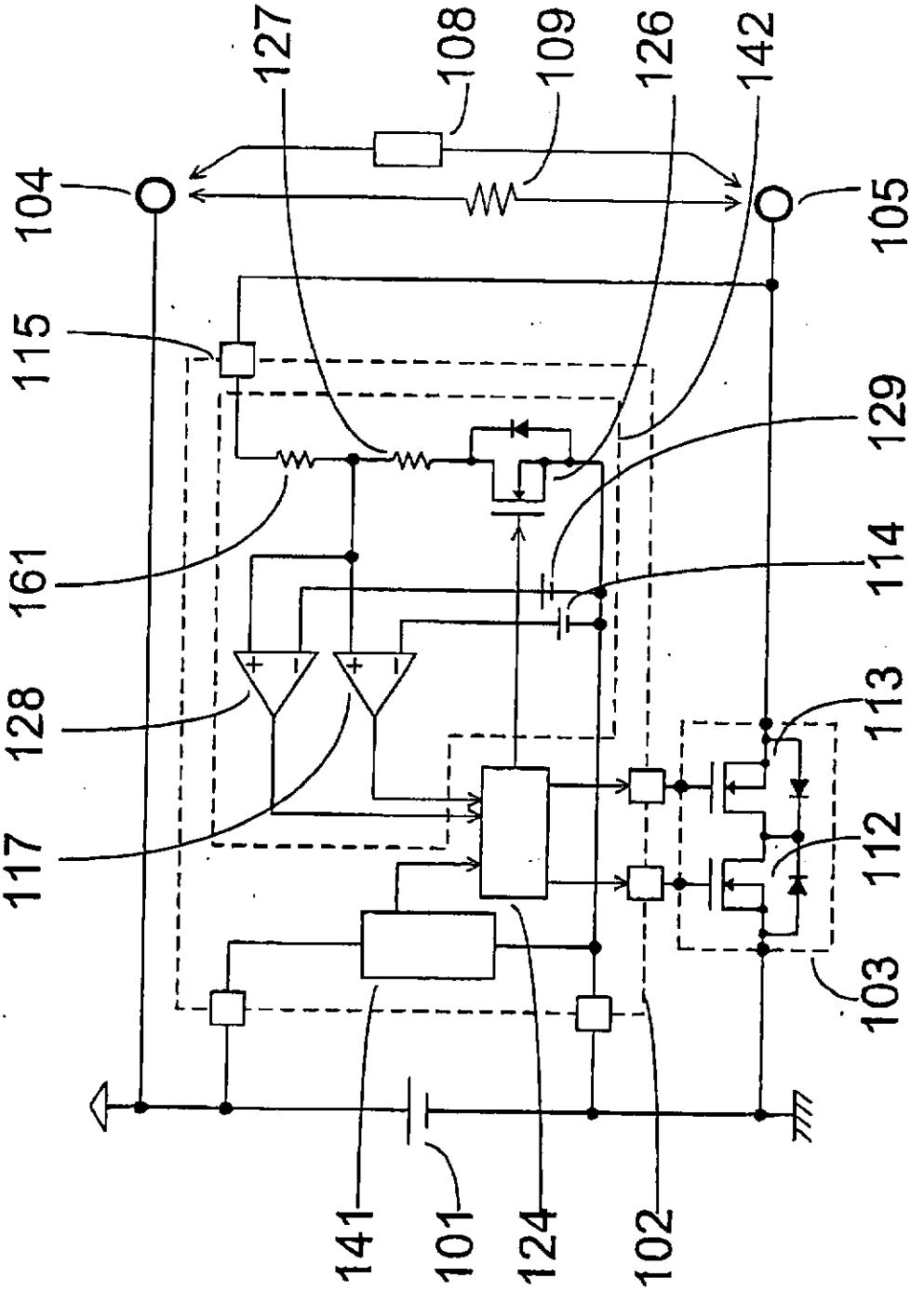

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FIG. 9

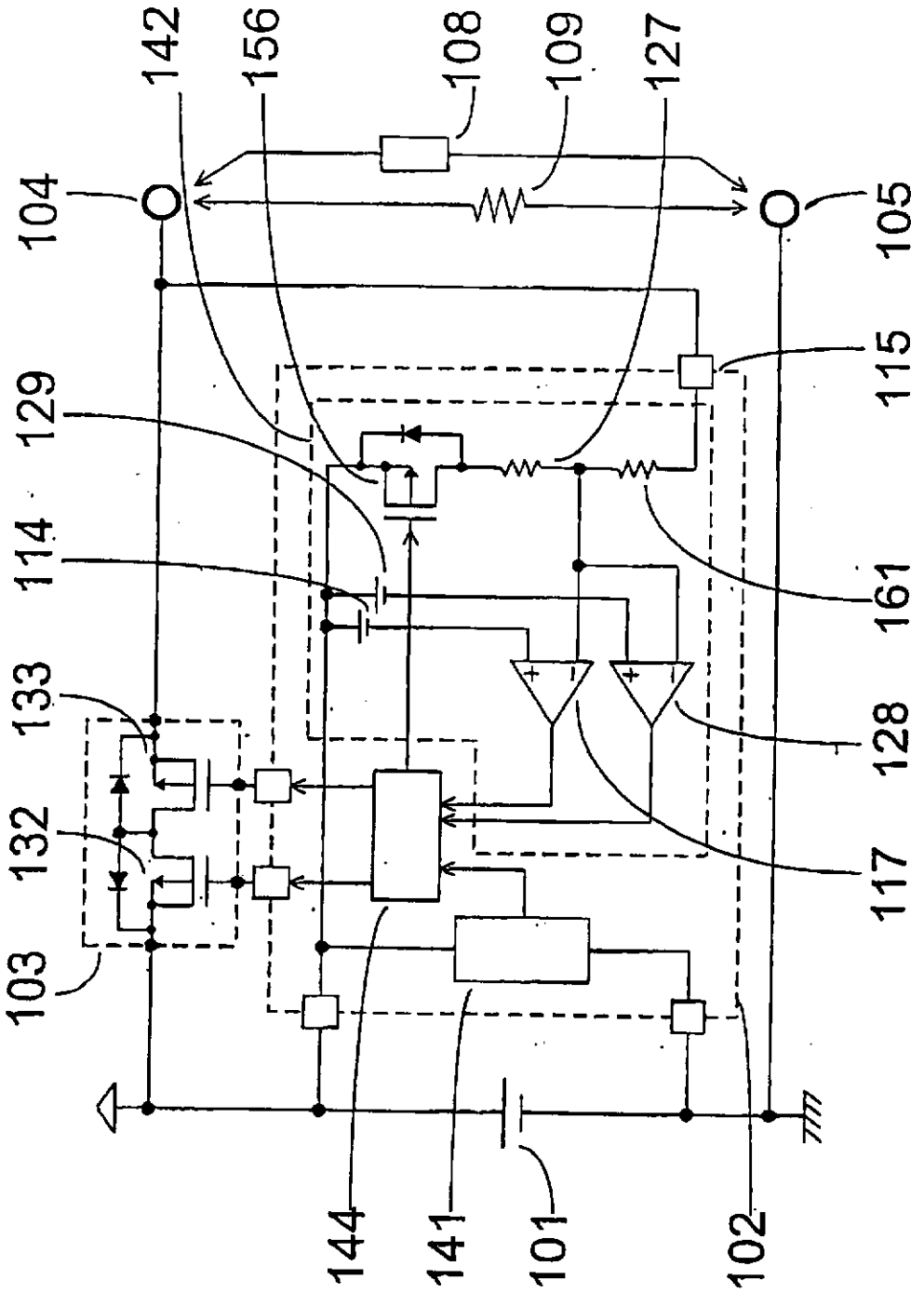
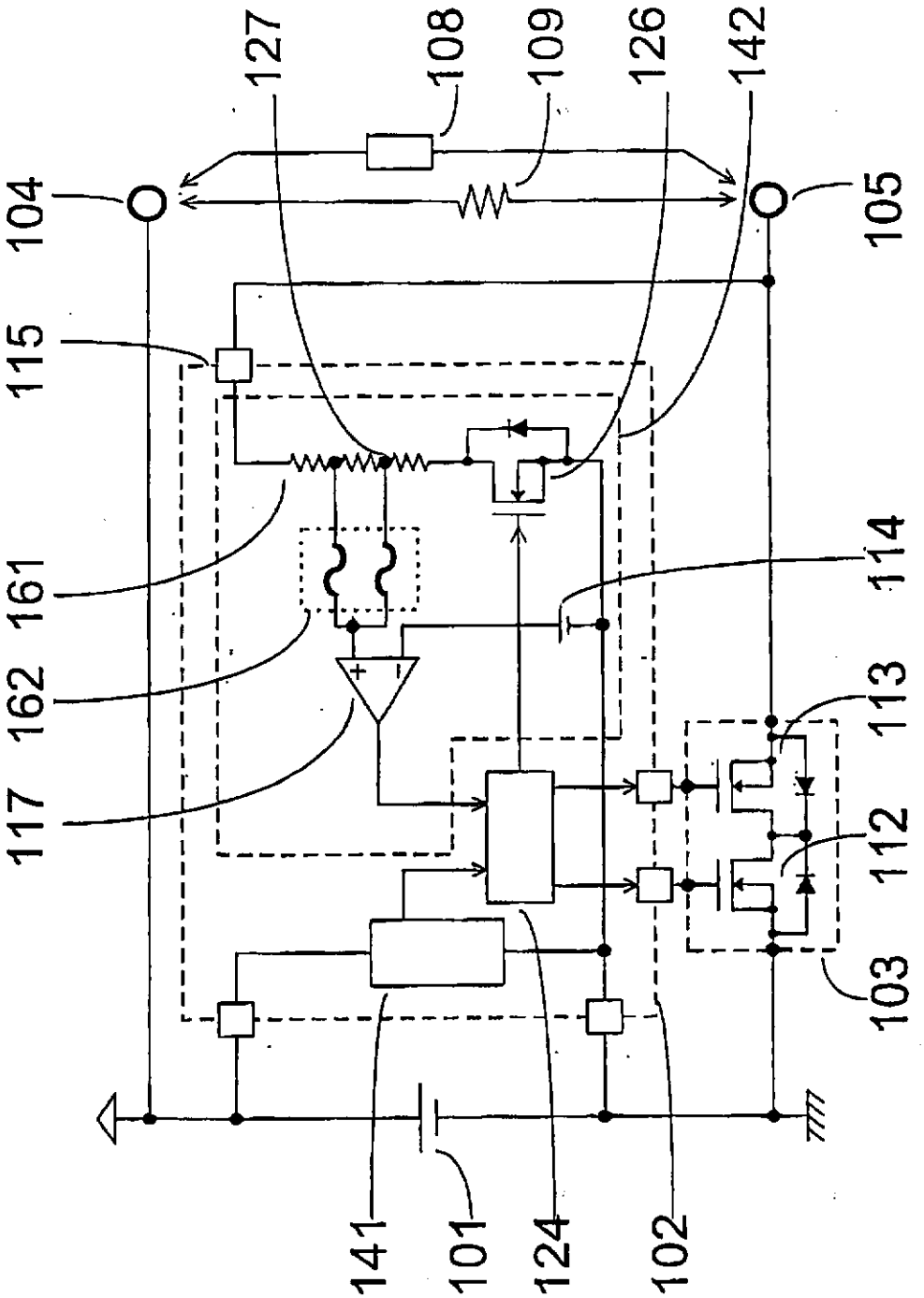

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FIG. 10


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