~~WHAT IS CLAIMED IS:~~

CLAIMS

1.- A method of wireless communication performed by a user equipment (UE), comprising:

~~determining a remaining minimum system information control resource set (RMSI CORESET) monitoring configuration based at least in part on information received by the UE in~~ receiving a first frame~~; and~~

~~monitoring, during a second frame and based at least in part on the RMSI CORESET monitoring configuration, for an RMSI CORESET based at least in part on a~~ that includes one or more synchronization signal ~~block~~blocks (SSB), each SSB being associated with ~~the RMSI CORESET, that is received in the first frame.~~a respective remaining minimum system information (RMSI) control resource set (CORESET);

~~2. The method~~determining, for at least one of ~~claim 1, wherein~~ the ~~second frame occurs after the first frame.~~

~~3. The method of claim 1, wherein the RMSI CORESET is transmitted after the~~SSBs, an SSB index associated with the ~~RMSI CORESET.~~respective SSB;

~~4. The method of claim 1, wherein a length of a time window for receiving the RMSI CORESET is based at least in part on the RMSI CORESET monitoring configuration.~~

~~5. The method of claim 1, further comprising:~~

determining a ~~modification period based at least in part on the RMSI CORESET monitoring configuration, wherein the modification period is longer than the first frame.~~

~~6. The method of claim 5, wherein the modification period includes at least part of the second frame.~~

~~7. The method of claim 1, further comprising:~~

~~determining an index of a first slot of the RMSI CORESET based at least in part on the RMSI CORESET monitoring configuration and the SSB.~~

~~8. The method of claim 1, further comprising:~~

~~determining a location~~ time offset of the RMSI CORESET associated with the ~~SSB based~~ at least ~~in part on an SSB bitmap, an index value~~one of the ~~SSB, and the RMSI CORESET monitoring configuration.~~SSBs;

~~9. The method of claim 1, wherein the RMSI CORESET monitoring configuration is associated with a time offset value of 5 and~~ determining a repetition frequency of ~~2 slots, and wherein the SSB is~~ the RMSI CORESET associated with the at least one of SSBs;

determining, for the at least one of the SSBs, a frame index associated with ~~an SSB index greater than 60.~~

~~10. The method~~a frame that includes a monitoring occasion of ~~claim 9, wherein, when~~ the RMSI CORESET ~~monitoring configuration is not~~ associated with ~~a~~ the at least one of the SSBs based on the respective determined SSB index, time offset ~~value of 5~~ and ~~a~~ repetition frequency ~~of 2 slots, or when the SSB is not associated with an SSB~~, the determination of the frame index ~~greater than 60, then the UE is to monitor for the RMSI CORESET in the first frame.~~comprising:

~~11. A method of wireless communication performed by a base station, comprising:~~

~~transmitting information indicating a remaining minimum system information control resource set (RMSI CORESET) monitoring configuration for a first frame; and~~

~~transmitting, during a second frame and in accordance with the RMSI CORESET monitoring configuration, an RMSI CORESET~~ determining the frame index such that it is associated with the first frame if

$$((O * 2^{\mu} + [i * M]) / N_{slot}^{frame,\mu}) \bmod 2 = 0, \text{ and}$$

determining the frame index such that it is associated with a ~~synchronization signal block (SSB) that was transmitted in the first frame.~~

~~12. The method of claim 11, wherein the~~ second frame ~~occurs~~ after the first frame~~.~~ if

~~13. The method of claim 11, wherein the RMSI CORESET is transmitted after~~ $((O * 2^{\mu} + [i * M])/ N_{slot}^{frame,\mu}$) mod 2 = 1;

determining a location of the monitoring occasion in the ~~SSB~~frame associated with the ~~RMSI CORESET.~~

~~14. The method of claim 11, wherein a length of a~~ frame index, the determination of the location of the monitoring occasion comprising determining a location of a first slot in time ~~window for receiving the RMSI CORESET is based at least in part on the RMSI CORESET~~ of the monitoring ~~configuration.~~ occasion as

~~15. The method of claim 11, further comprising:~~

~~determining a modification period based at least in part on the RMSI CORESET monitoring configuration, wherein the modification period~~ $n_0 = (O * 2^{\mu} + [i * M]) mod\ N_{slot}^{frame,\mu}$,

wherein n_0 is ~~longer than the first frame.~~

~~16. The method of claim 15, wherein the modification period includes at least part of the second frame.~~

~~17. The method of claim 11, further comprising:~~

~~determining an index of a first slot of the RMSI CORESET based at least in part on the RMSI CORESET monitoring configuration and the SSB.~~

~~18. The method of claim 11, further comprising:~~

~~determining a location~~ a slot index associated with the ~~SSB based at least in part on an SSB bitmap, an index value of the SSB, and the RMSI CORESET monitoring configuration.~~

~~19. The method of claim 11, wherein the RMSI CORESET monitoring configuration~~first slot, O is ~~associated with a~~ the time offset ~~value of 5 and a~~, M is the repetition frequency ~~of 2~~in slots, μ is a numerology index, and ~~wherein~~$N_{slot}^{frame,\mu}$ is a number of slots per frame;

monitoring, during the ~~SSB is~~ monitoring occasion in the frame associated with the frame index, for the RMSI CORESET associated with ~~an SSB index greater than 60.~~the at least one of the SSBs; and

~~20. The method of claim 19, wherein, when~~receiving the RMSI CORESET ~~monitoring configuration is not~~ associated with ~~a~~the at least one of the SSBs based on the monitoring.

2. The method of claim 1, wherein the first frame includes an RMSI CORESET monitoring configuration that indicates the time offset ~~value of 5~~ and ~~a~~the repetition frequency ~~of 2 slots, or when the SSB is not associated with an SSB index greater than 60, then the RMSI CORESET is to be in the first~~ .

3. The method of claim 1, wherein the determination of the frame~~.~~ index comprises determining the frame index such that

$$\text{~~21.~~ SFN mod 2 = } ((O * 2^{\mu} + [i * M])/\, N_{slot}^{frame,\mu}) \bmod 2$$

is satisfied, wherein SFN is the frame index.

4. The method of claim 1, wherein the time offset is 5 milliseconds, the repetition frequency is 2 slots, and the SSB index is greater than 60.

5. The method of claim 1, wherein the time offset is not equal to 5 milliseconds, the repetition frequency is not equal to 2 slots, and the SSB index is not greater than 60, and wherein the determination of the frame index comprises determining the frame index such that it is associated with the first frame based on the time offset being not equal to 5 milliseconds, the repetition frequency being not equal to 2 slots, and the SSB index being not greater than 60.

6. A user equipment (UE) ~~for wireless communication,~~ comprising:

~~a memory~~at least one processor; and

~~one or more processors~~at least one memory communicatively coupled ~~to~~with the ~~memory,~~at least one processor and storing processor-readable code that, when executed by the ~~memory and the~~at least one ~~or more processors~~processor is configured to cause the UE to:

~~determine a~~receive a first frame that includes one or more synchronization signal blocks (SSB), each SSB being associated with a respective remaining minimum system information (RMSI) control resource set (CORESET);

determine, for at least one of the SSBs, an SSB index associated with the respective SSB;

determine a time offset of the RMSI CORESET~~)~~ associated with the at least one of the SSBs;

determine a repetition frequency of the RMSI CORESET associated with the at least one of SSBs;

determine, for the at least one of the SSBs, a frame index associated with a frame that includes a monitoring ~~configuration of the UE for a first frame; and~~ occasion of the RMSI CORESET associated with the at least one of the SSBs based on the respective determined SSB index, time offset and repetition frequency, the determination of the frame index comprising:

~~monitor, during a second frame and based at least in part on the RMSI CORESET monitoring configuration, for an RMSI CORESET based at least in part on a synchronization signal block (SSB), associated with the RMSI CORESET, that is received in the first frame.~~

~~22. The UE of claim 21, wherein the second frame occurs~~ determining the frame index such that it is associated with the first frame if

$$((O * 2^{\mu} + [i * M])/ N_{slot}^{frame,\mu}) \bmod 2 = 0, \text{ and}$$

determining the frame index such that it is associated with a second frame after the first frame~~.~~ if

~~23. The UE of claim 21, wherein a length of a time window for receiving the RMSI CORESET is based at least in part on the RMSI CORESET monitoring configuration.~~

~~24. The UE of claim 21, wherein the one or more processors are further configured to:~~

~~determine a modification period based at least in part on the RMSI CORESET monitoring configuration, wherein the modification period is longer than the first frame.~~

~~25. The UE of claim 24, wherein the modification period includes at least part of the second frame.~~

~~26. The UE of claim 21, wherein the one or more processors are further configured to:~~

~~determine an index of a first slot of the RMSI CORESET based at least in part on the RMSI CORESET monitoring configuration and the SSB.~~

~~27. The UE of claim 21, wherein the one or more processors are further configured to:~~

$$((O * 2^{\mu} + [i * M])/ N_{slot}^{frame,\mu}) \bmod 2 = 1;$$

determine a location ~~associated with the SSB based at least in part on an SSB bitmap, an index value of the SSB, and the RMSI CORESET monitoring configuration.~~

~~28. The UE of claim 21, wherein the RMSI CORESET monitoring configuration is~~ of the monitoring occasion in the frame associated with ~~a time offset value of 5 and a repetition frequency of 2 slots, and wherein the SSB is associated with an SSB~~the frame index ~~greater than 60.~~

~~29. A base station for wireless communication,~~, the determination of the location of the monitoring occasion comprising~~:~~ determining a location of a first slot in time of the monitoring occasion as

~~a memory; and~~

~~one or more processors coupled to the memory, the memory and the one or more processors configured to:~~

~~transmit information indicating a remaining minimum system information control resource set (RMSI CORESET) monitoring configuration of a user equipment (UE) for a first frame; and~~

~~transmit, during a second frame and based at least in part on the RMSI CORESET monitoring configuration, an RMSI CORESET based at least in part on a synchronization signal block (SSB),~~

$$n_0 = (O * 2^{\mu} + [i * M]) mod\ N_{slot}^{frame,\mu},$$

wherein n_0 is a slot index associated with the first slot, O is the time offset, M is the repetition frequency in slots, μ is a numerology index, and $N_{slot}^{frame,\mu}$ is a number of slots per frame;

monitor, during the monitoring occasion in the frame associated with the ~~RMSI CORESET, that was transmitted in the first frame.~~

~~30. The base station of claim 29, wherein the second~~ frame ~~occurs after the first frame.~~

~~31. The base station of claim 29, wherein the one or more processors are further configured to:~~

~~determine a modification period based at least in part on the RMSI CORESET monitoring configuration, wherein the modification period is longer than the first frame.~~

~~32. The base station of claim 31, wherein the modification period includes at least part of the second frame.~~

~~33. The base station of claim 29, wherein the one or more processors are further configured to:~~

~~determine an index of a first slot of the RMSI CORESET based at least in part on the RMSI CORESET monitoring configuration and the SSB.~~

~~34. The base station of claim 29, wherein the one or more processors are further configured to:~~

~~determine a location~~ index, for the RMSI CORESET associated with the ~~SSB based~~ at least

in part on an SSB bitmap, an index valueone of the SSB,SSBs; and the RMSI CORESET monitoring configuration.

35. The base station of claim 29, wherein the RMSI CORESET monitoring configuration is receive the RMSI CORESET associated with athe at least one of the SSBs based on the monitoring.

7. The UE of claim 6, wherein the first frame includes an RMSI CORESET monitoring configuration that indicates the time offset value of 5 and athe repetition frequency of 2 slots, and wherein the SSB is associated with an SSB index greater than 60.

36. A non-transitory computer-readable medium storing one or more instructions for wireless communication, the one or more instructions comprising:

one or more instructions that, when executed by one or more processors of a user equipment (UE), cause the one or more processors to:

determine a remaining minimum system information control resource set (RMSI CORESET) monitoring configuration of the UE for a first frame; and

monitor, during a second frame and based at least in part on the RMSI CORESET monitoring configuration, for an RMSI CORESET based at least in part on a synchronization signal block (SSB), associated with the RMSI CORESET, that is received in the first frame.

37. The non-transitory computer-readable medium of claim 36, wherein the one or more instructions, when executed by the one or more processors, further cause the one or more processors to:

determine a modification period based at least in part on the RMSI CORESET monitoring configuration, wherein the modification period is longer than the first frame.

38. The non-transitory computer-readable medium of claim 37, wherein the modification period includes at least part of the second frame.

~~39. The non-transitory computer-readable medium of claim 36, wherein the one or more instructions, when executed by the one or more processors, further cause the one or more processors to:~~

~~determine an index of a first slot of the RMSI CORESET based at least in part on the RMSI CORESET monitoring configuration and the SSB.~~

~~40. The non-transitory computer-readable medium of claim 36, wherein the RMSI CORESET monitoring configuration is associated with a~~ 8. The UE of claim 6, wherein the determination of the frame index comprises determining the frame index such that

$$\text{SFN mod 2} = ((O * 2^{\mu} + [i * M])/ N_{slot}^{frame,\mu}) \bmod 2$$

is satisfied, wherein SFN is the frame index.

9. The UE of claim 6, wherein the time offset ~~value of~~is 5 ~~and a~~ milliseconds, the repetition frequency ~~of~~is 2 slots, and ~~wherein~~ the SSB ~~is associated with an SSB~~ index is greater than 60.

~~41. A non-transitory computer-readable medium storing one or more instructions for wireless communication, the one or more instructions comprising:~~

~~one or more instructions that, when executed by one or more processors of a base station, cause the one or more processors to:~~

~~transmit information indicating a remaining minimum system information control resource set (RMSI CORESET) monitoring configuration of a user equipment (UE) for a first frame; and~~

~~transmit, during a second frame and based at least in part on the RMSI CORESET monitoring configuration, an RMSI CORESET based at least in part on a synchronization signal block (SSB), associated with the RMSI CORESET, that was transmitted in the first frame.~~

~~42. The non-transitory computer-readable medium of claim 41, wherein the one or more instructions, when executed by the one or more processors, further cause the one or more processors to:~~

~~determine a modification period based at least in part on the RMSI CORESET monitoring configuration, wherein the modification period is longer than the first frame.~~

~~43. The non-transitory computer-readable medium of claim 42, wherein the modification period includes at least part of the second frame.~~

~~44. The non-transitory computer-readable medium of claim 41, wherein the one or more instructions, when executed by the one or more processors, further cause the one or more processors to:~~

~~determine an index of a first slot of the RMSI CORESET based at least in part on the RMSI CORESET monitoring configuration and the SSB.~~

~~45. The non-transitory computer-readable medium of claim 41, wherein the RMSI CORESET monitoring configuration is associated with a~~ 10. The UE of claim 6, wherein the time offset ~~value of 5 and a~~is not equal to 5 milliseconds, the repetition frequency ~~of~~is not equal to 2 slots, and ~~wherein the SSB is associated with an SSB index greater than 60.~~

~~46. An apparatus for wireless communication, comprising:~~

~~means for determining a remaining minimum system information control resource set (RMSI CORESET) monitoring configuration of the apparatus for a first frame; and~~

~~means for monitoring, during a second frame and based at least in part on the RMSI CORESET monitoring configuration, for an RMSI CORESET based at least in part on a synchronization signal block (SSB), associated with the RMSI CORESET, that is received in the first frame.~~

~~47. The apparatus of claim 46, further comprising:~~

means for determining a modification period based at least in part on the RMSI CORESET monitoring configuration, wherein the modification period is longer than the first frame.

48. The apparatus of claim 47, wherein the modification period includes at least part of the second frame.

49. The apparatus of claim 46, further comprising:

means for determining anthe SSB index of a first slot of the RMSI CORESET based at least in part on the RMSI CORESET monitoring configurationis not greater than 60, and the SSB.

50. The apparatus of claim 46, wherein the RMSI CORESET monitoring configuration is associated with a time offset value of 5 and a repetition frequency of 2 slots, and wherein the SSB is associated with an SSBdetermination of the frame index greater than 60.

51. An apparatus for wireless communication, comprising:

means for transmitting information indicating a remaining minimum system information control resource set (RMSI CORESET) monitoring configuration of a user equipment (UE) for a first frame; and

means for transmitting, during a second frame and based at least in part on the RMSI CORESET monitoring configuration, an RMSI CORESET based at least in part on a synchronization signal block (SSB), associated with the RMSI CORESET, that was transmitted in the first frame.

52. The apparatus of claim 51, further comprising:

means for determining a modification period based at least in part on the RMSI CORESET monitoring configuration, wherein the modification period is longer than the first frame.

~~53. The apparatus of claim 52, wherein the modification period includes at least part of the second frame.~~

~~54. The apparatus of claim 51, further comprising:~~

~~means for~~comprises determining ~~an~~the frame index ~~of a first slot of the RMSI CORESET based at least in part on the RMSI CORESET monitoring configuration and the SSB.~~

~~55. The apparatus of claim 51, wherein the RMSI CORESET monitoring configuration~~such that it is associated with ~~a~~ the first frame based on the time offset ~~value of~~being not equal to 5 ~~and a~~ milliseconds, the repetition frequency ~~of~~being not equal to 2 slots, and ~~wherein~~ the SSB ~~is associated with an SSB~~ index being not greater than 60.