CLAIMS

1. An integrated circuit for a fluid ejection device comprising ~~to drive~~ a plurality of fluid actuation devices, the integrated circuit comprising:

a configuration register;

a plurality of interfaces comprising a mode interface and a data interface; and

control logic to enable writing to the configuration register in response to a signal on the mode interface transitioning to logic high with a logic high signal on the data interface, wherein the configuration register is to receive serial data from the data interface with the configuration register enabled for writing.

2. The integrated circuit of claim 1, wherein the control logic is to disable writing to the configuration register in response to a logic low signal on the mode interface.

3. The integrated circuit of claim 1 or 2, wherein the plurality of interfaces comprises a fire interface, and

wherein the control logic is to enable writing to the configuration register in response to the signal on the mode interface transitioning to logic high with a logic high signal on the data interface and a logic low signal on the fire interface.

4. The integrated circuit of claim 1, wherein the plurality of interfaces comprises a clock interface, and

wherein the configuration register is to receive the serial data from the data interface aligned with a clock signal from the clock interface.

5. The integrated circuit of any of claims 1-4, wherein the integrated circuit is a fluid ejection die.

6. The integrated circuit of claim 3~~1~~, the integrated circuit further comprising:

a status register;

wherein the control logic is to enable reading of the status register in response to both a signal on the mode interface transitioning to logic high with a logic high signal on the data interface and transitioning a signal on the fire interface to logic high with the signal on the data interface floating.

7. The integrated circuit of claim 6, wherein the control logic is to disable reading of the status register in response to a logic low signal on the mode interface.

8. The integrated circuit of claim 6 or 7, wherein the control logic is to disable reading of the status register in response to a logic low signal on the fire interface.

9. The integrated circuit of any of claims 6-8, wherein the status register is to output serial data to the data interface with the status register enabled for reading.

10. The integrated circuit of claim 9, wherein the plurality of interfaces comprises a clock interface, and

wherein the status register is to output serial data to the data interface aligned with a clock signal on the clock interface.

11. A method for accessing a fluid ejection die, the method comprising:

setting a signal on a data contact pad of the fluid ejection die to logic high;

transitioning a signal on a mode contact pad of the fluid ejection die to logic high; and

enabling writing to a configuration register of the fluid ejection die in response to transitioning the signal on the mode contact pad to logic high with the signal on the data contact pad logic high; and

applying a serial data signal to the data contact pad to write the serial data to the configuration register.

12. The method of claim 11, further comprising:

transitioning the signal on the mode contact pad to logic low; and

disabling writing to the configuration register in response to the logic low signal on the mode contact pad.

13. The method of claim 12, further comprising:

with the configuration register enabled for writing, applying the serial data signal to the data contact pad aligned with a clock signal on a clock contact pad of the fluid ejection die.

14. TheA method of claim 110, the method further comprising:

floating the signal on the data contact pad with the signal on the mode contact pad logic high;

transitioning a signal on a fire contact pad of the fluid ejection die to logic high with the signal on the data contact pad floating; and

enabling reading of a status register of the fluid ejection die in response to transitioning the signal on the fire contact pad to logic high with the signal on the data contact pad floating.

15. The method of claim 14, further comprising:

transitioning the signal on the mode contact pad to logic low; and

disabling reading of the status register in response to the logic low signal on the mode contact pad.

16. The method of claim 14 or 15, further comprising:

transitioning the signal on the fire contact pad to logic low; and

disabling reading of the status register in response to the logic low signal on the fire contact pad.

17. The method of any of claims 14-16, further comprising:

with the status register enabled for reading, outputting serial data from the status register to the data contact pad.

18. The method of claim 17, further comprising:

with the status register enabled for reading, outputting serial data from the status register to the data contact pad aligned with a clock signal on a clock signal pad of the fluid ejection die.