A DISPLAY DRIVER

Abstract of the Invention

A display driver circuit (3) is configurable to drive different displays having a variety of pixel arrangements. When configured for a liquid crystal display (LCD) (not shown) having a pixel arrangement with X x Y pixels, the DSP (12) stores pixel display data in sequentially addressable storage locations of a pixel addressable memory (22). To display the image represented by the stored pixel display data, the DSP (12) retrieves the pixel display data, generates pixel addressing data bits X and Y of the corresponding pixel on the LCD, and stores the pixel addressing data bits X and Y in addressable display register (15) as a pixel address of the pixel. A pixel address output provides the pixel address for the LCD to switch the pixel in accordance with the pixel display data.

(FIG. 1 accompanies this abstract)