

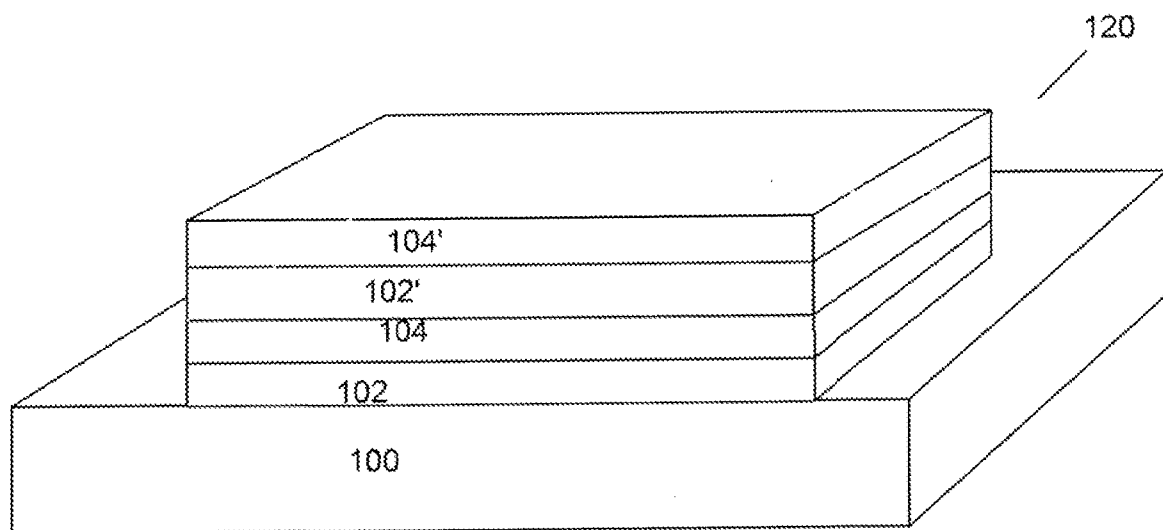


FIG. 1a

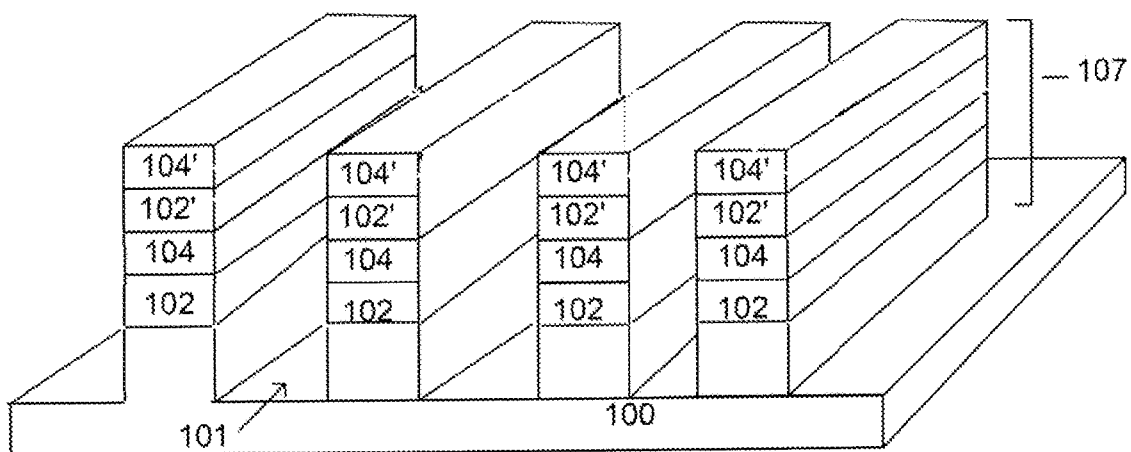


FIG. 1b

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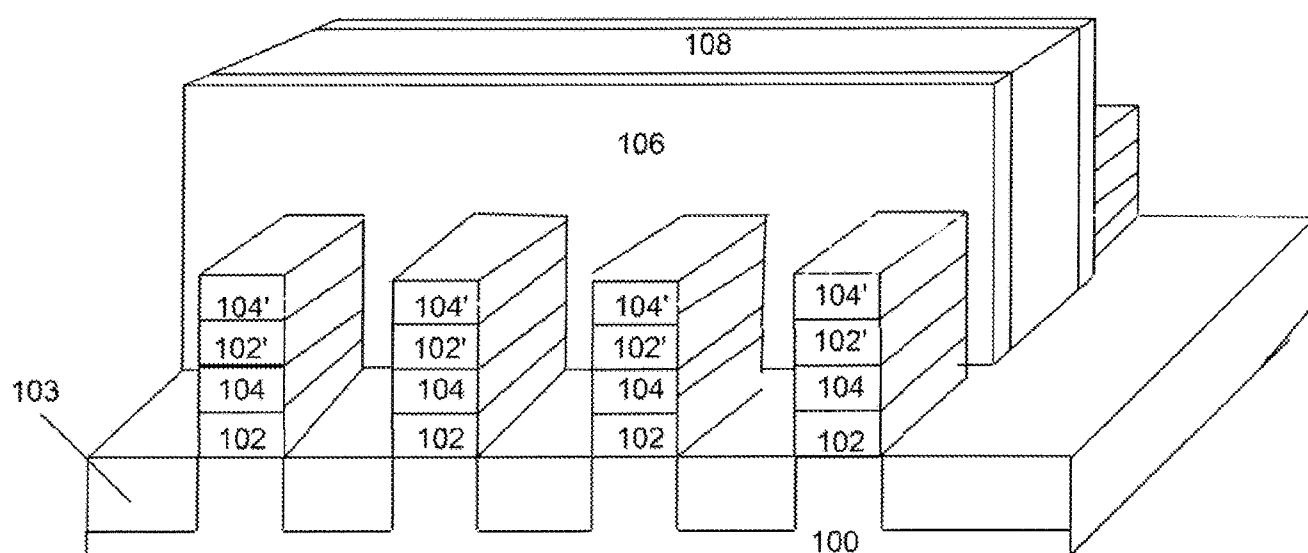


FIG. 1c

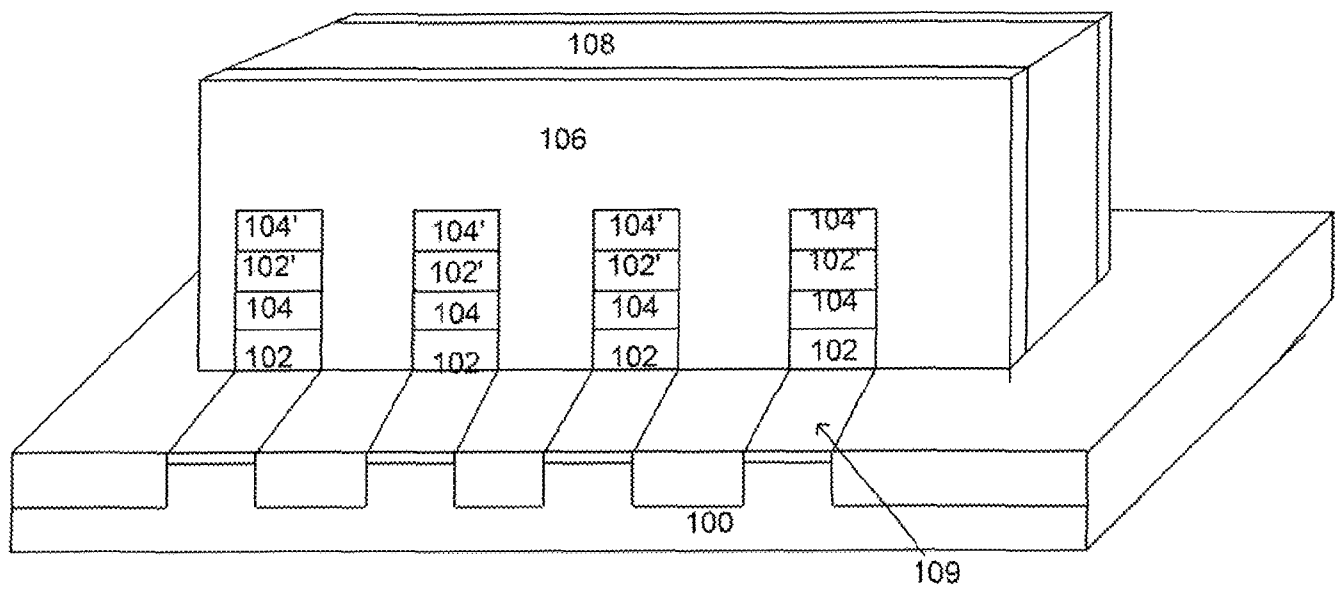


FIG. 1d

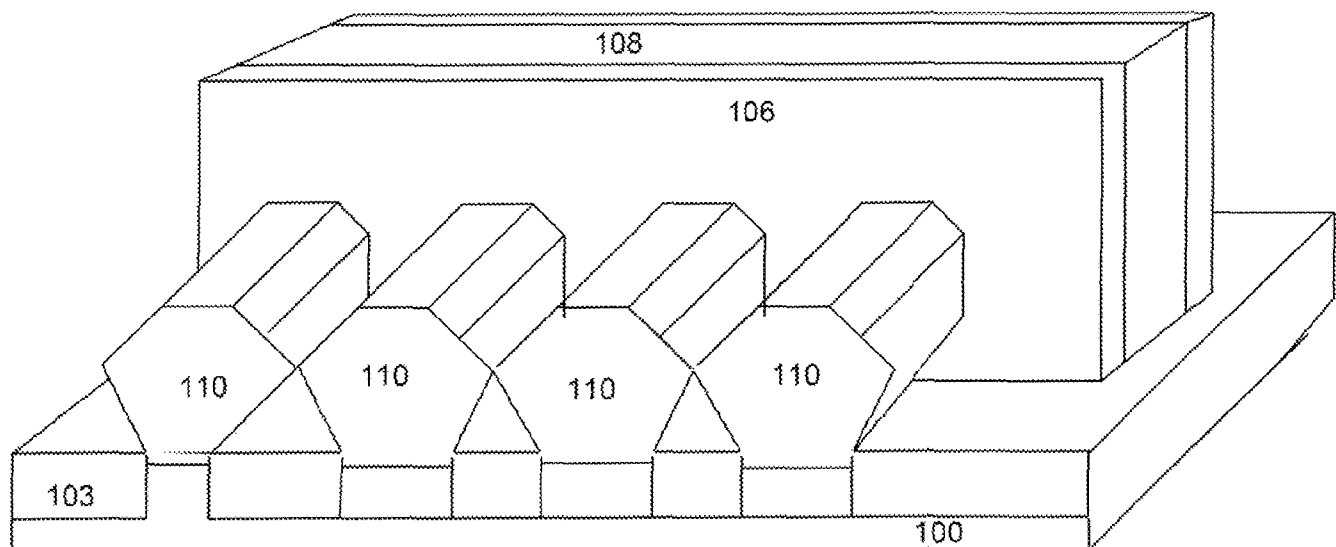


FIG. 1e

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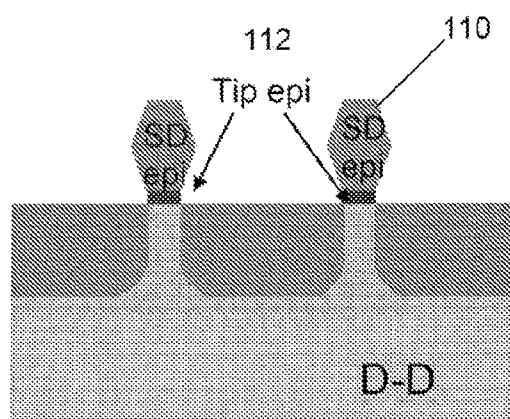


FIG. 1f

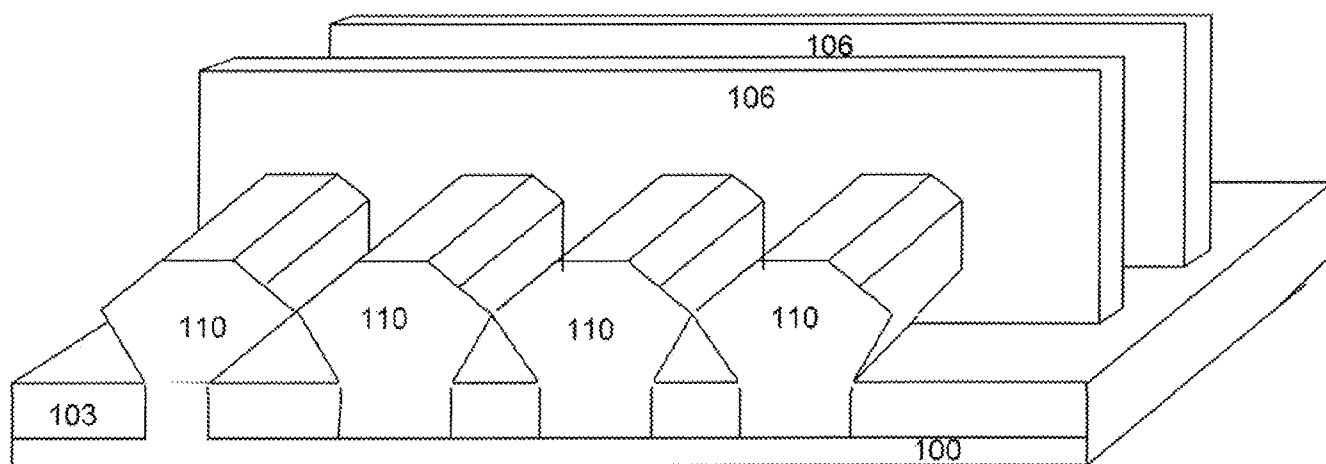
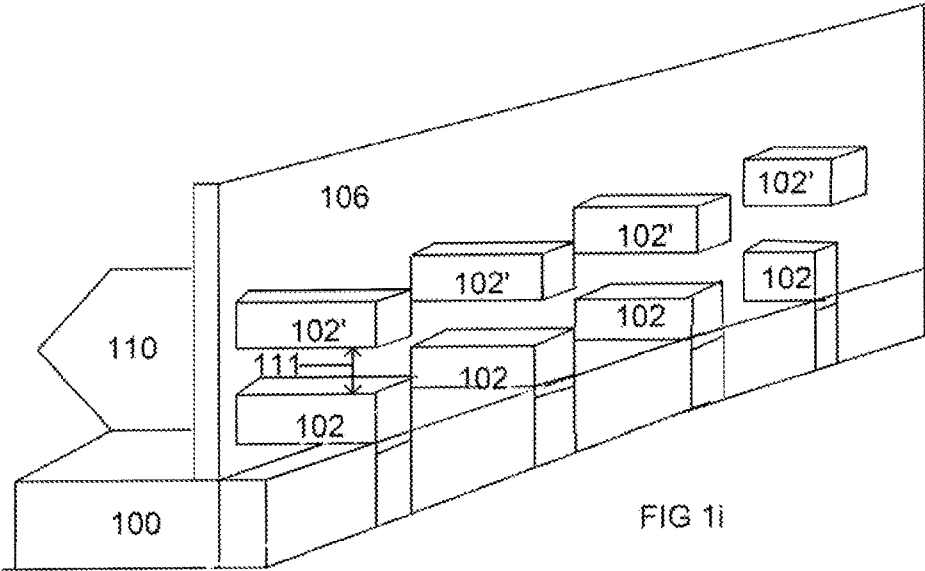
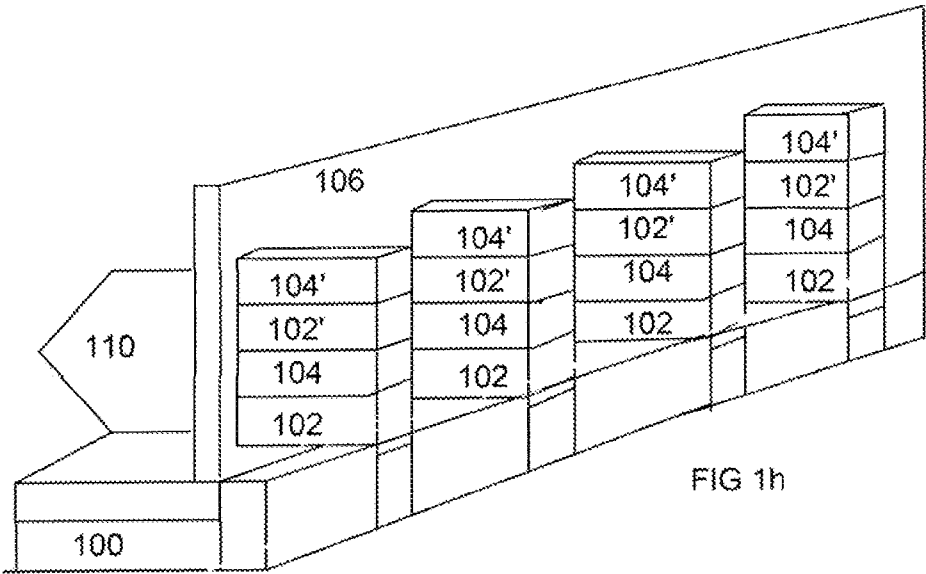
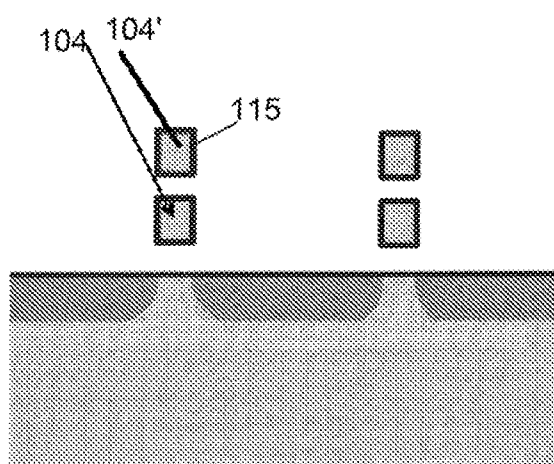
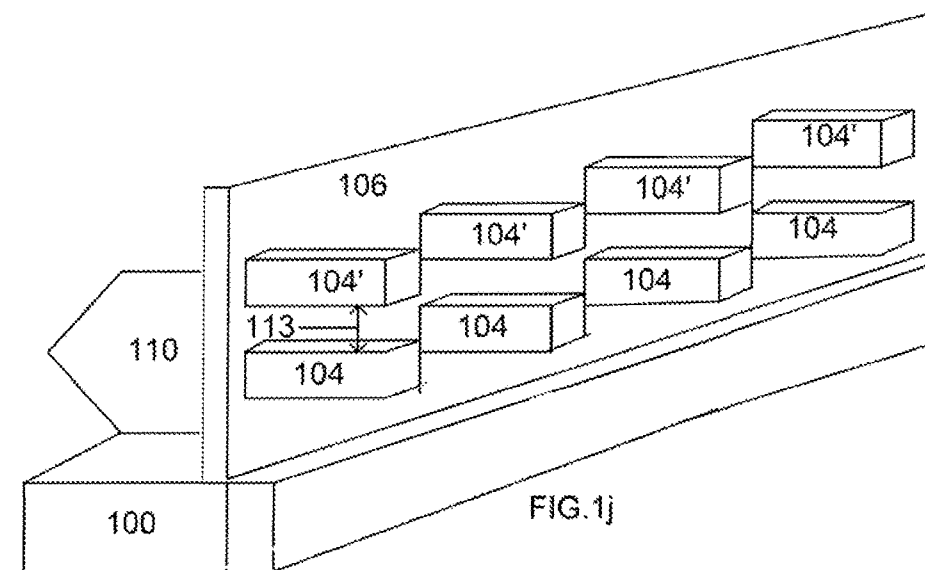


FIG. 1g





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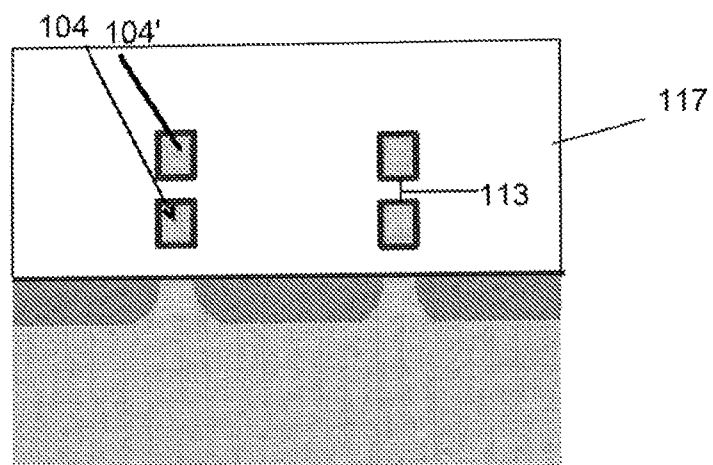
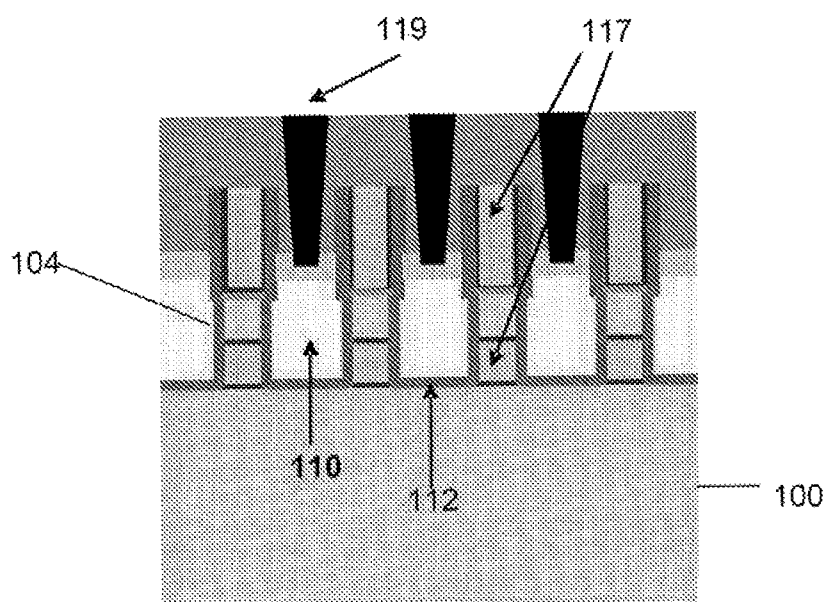


FIG. 1i



(a) NMOS

FIG. 1m

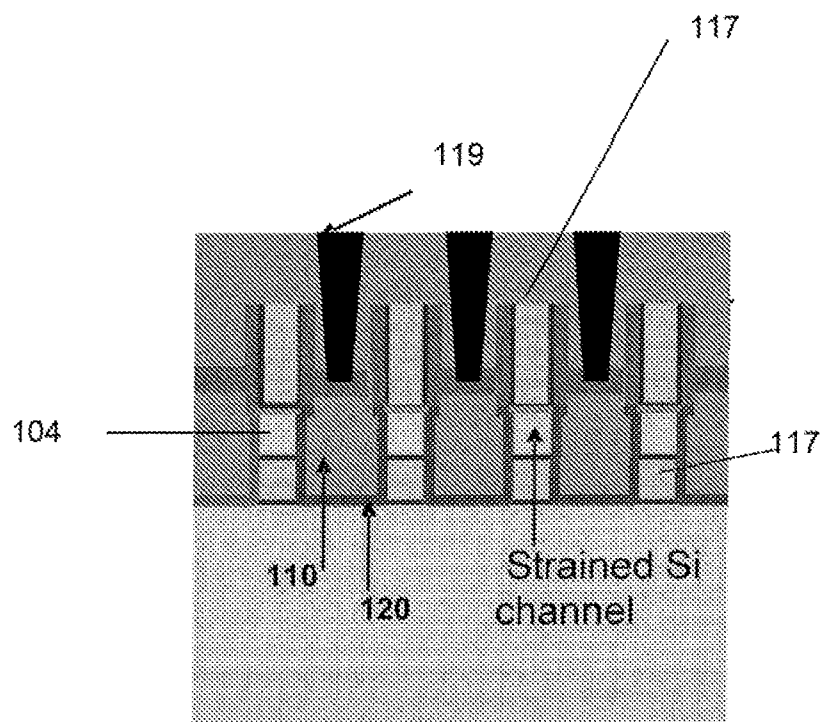


FIG. 1n

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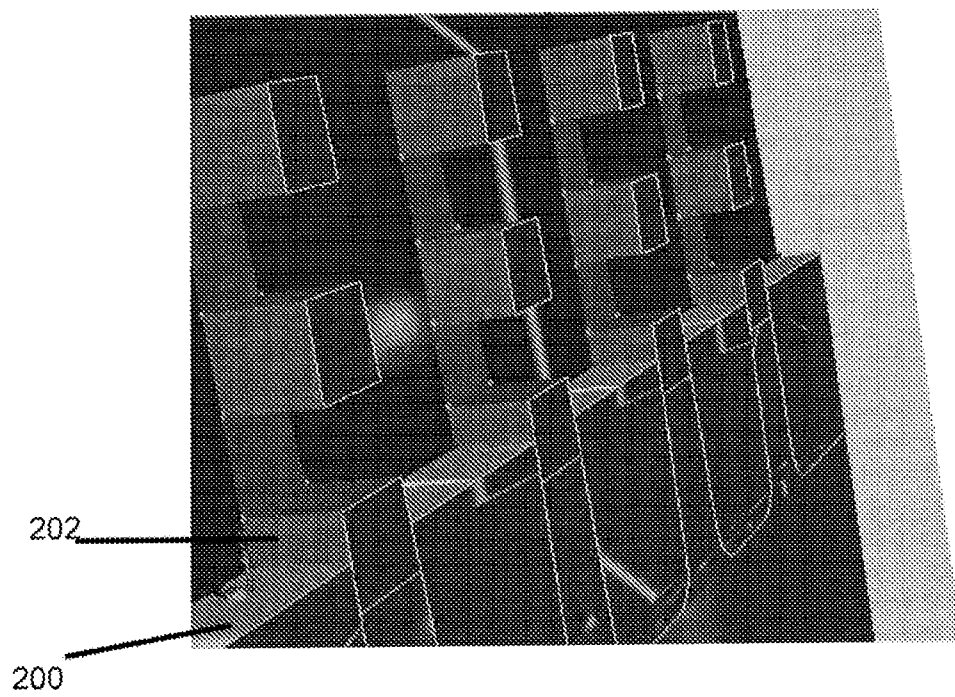


FIG. 2a

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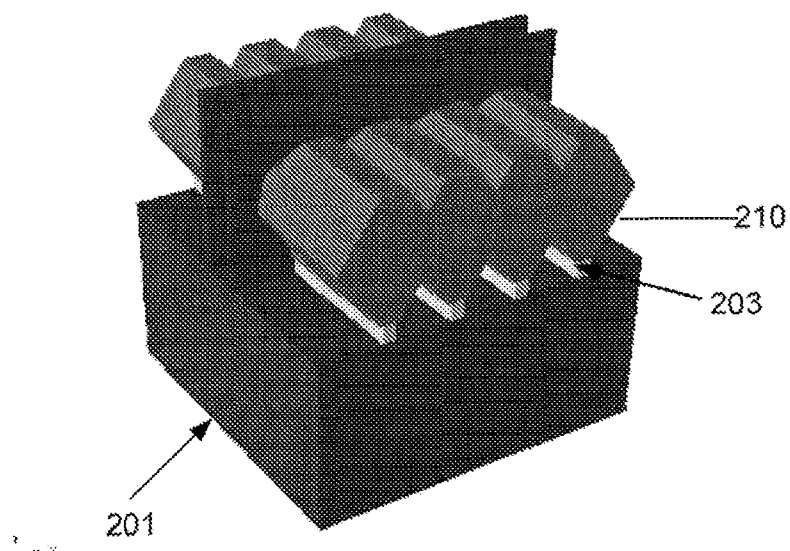


FIG. 2b

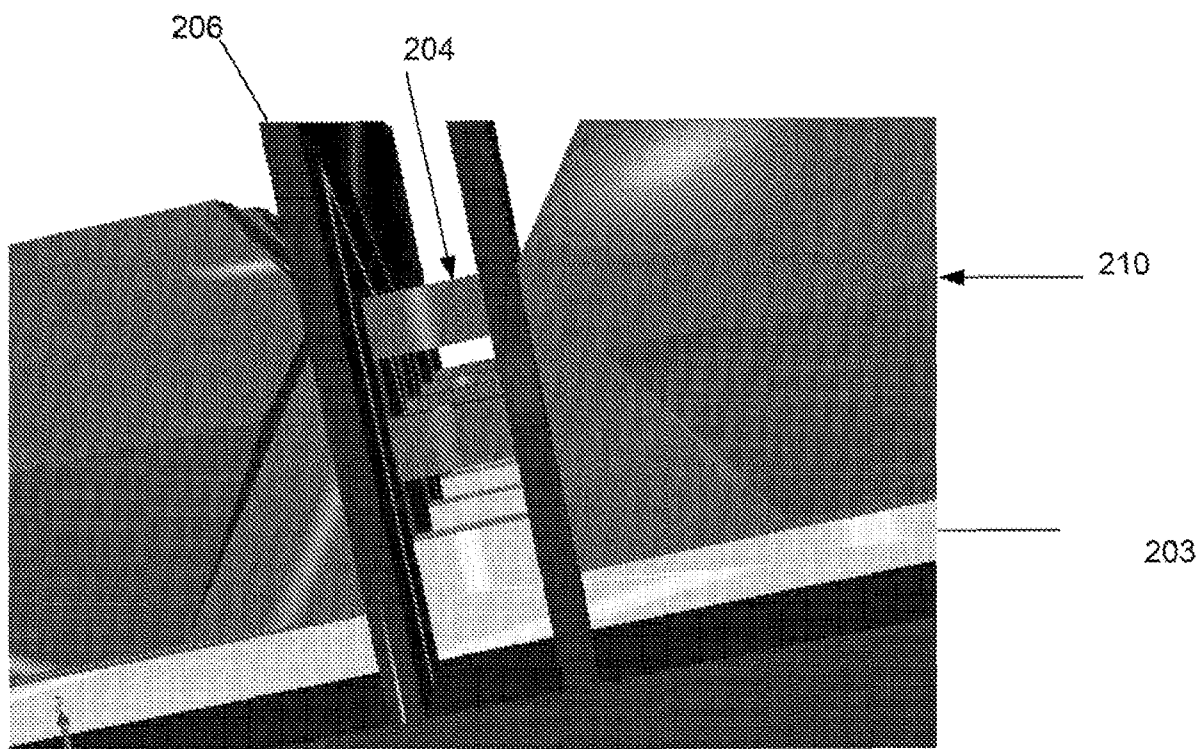


FIG. 2c

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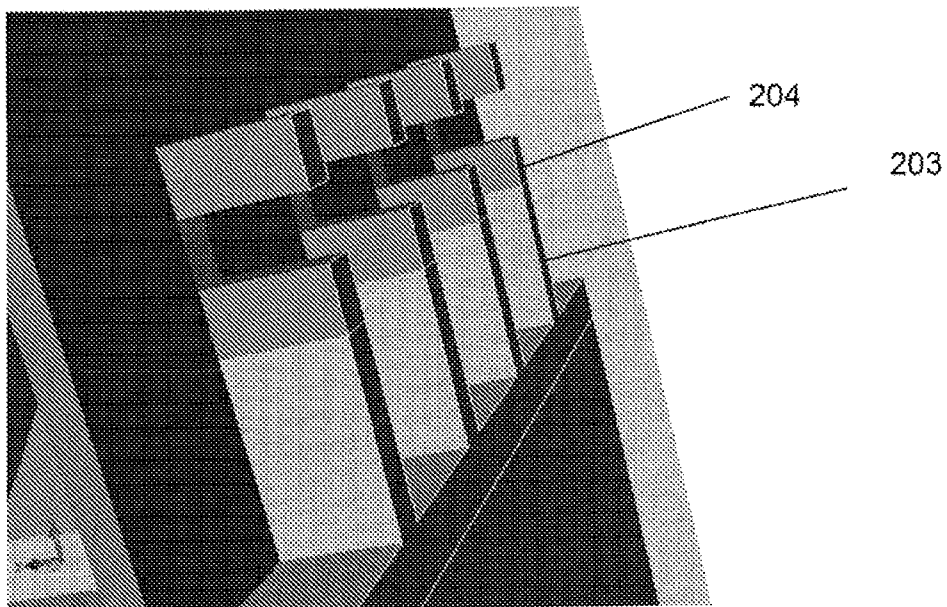


FIG. 2d

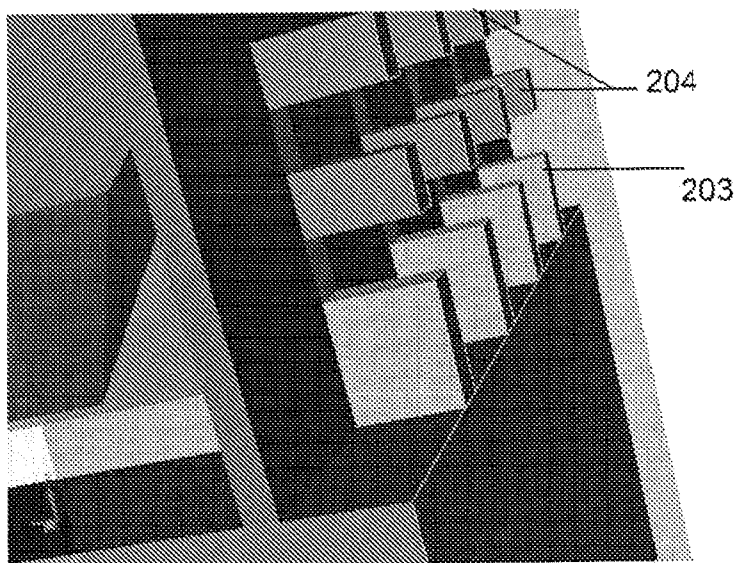


FIG. 2e

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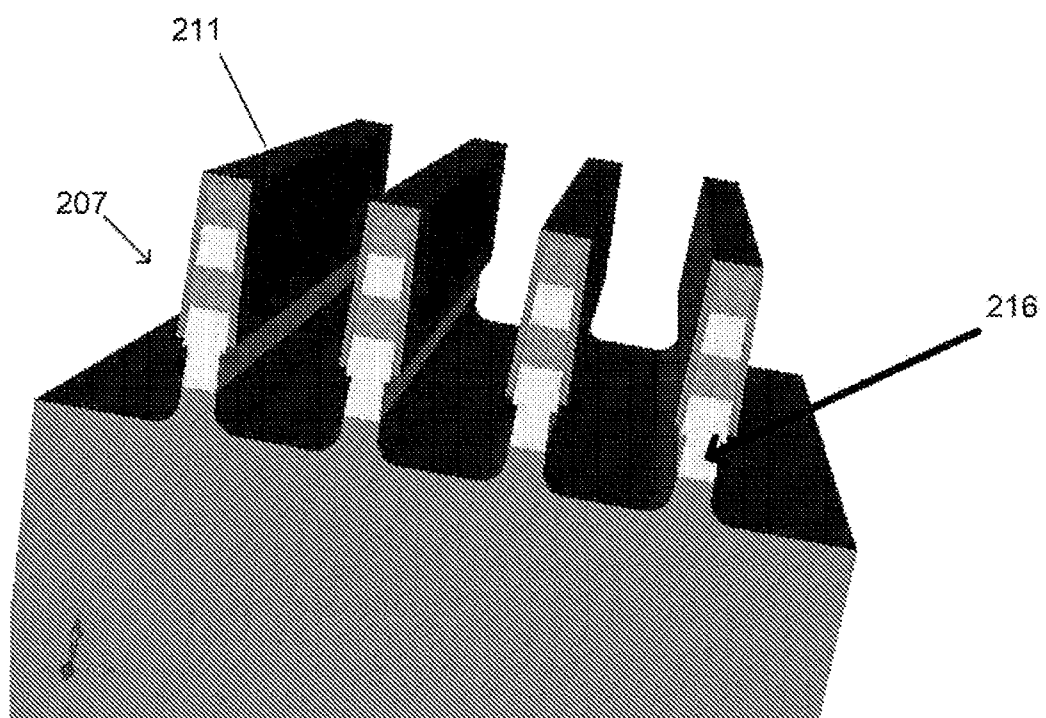


FIG.2f

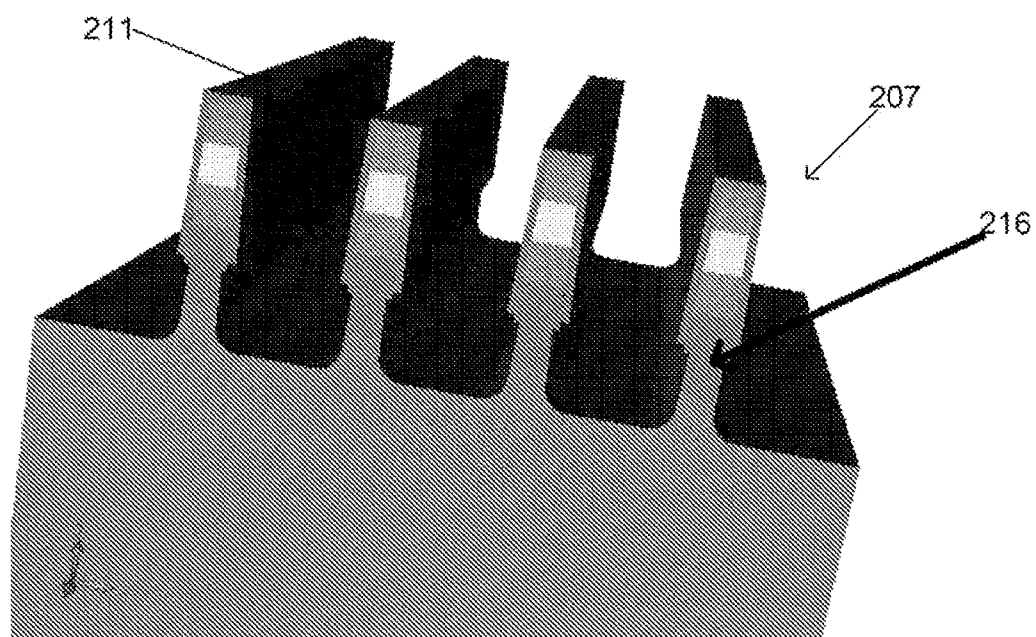


FIG.2g

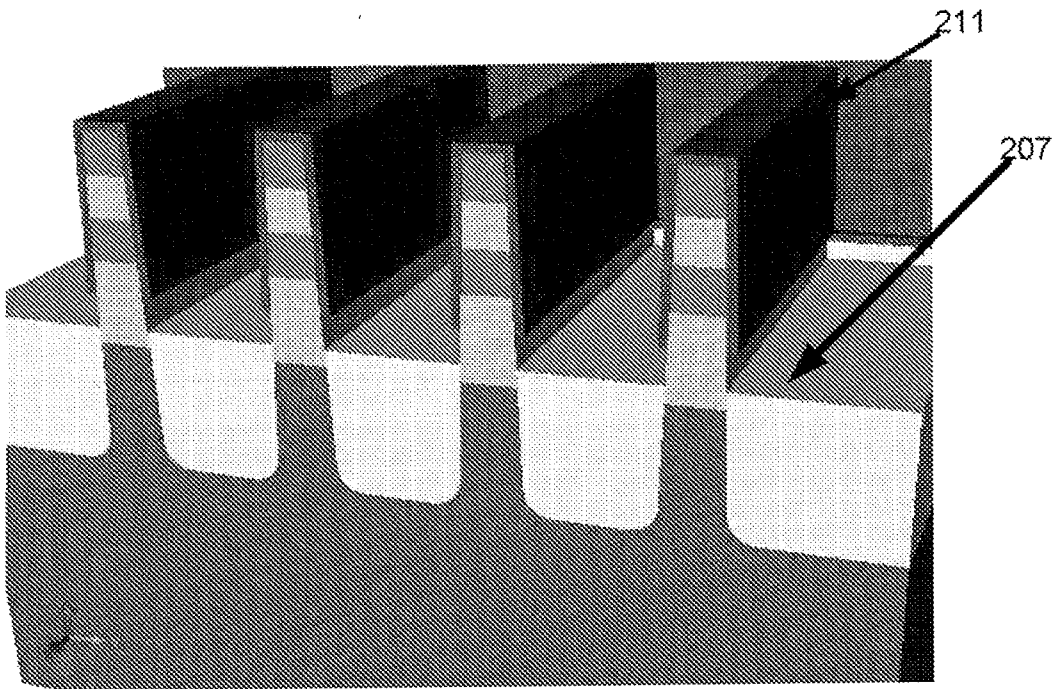


FIG. 2h

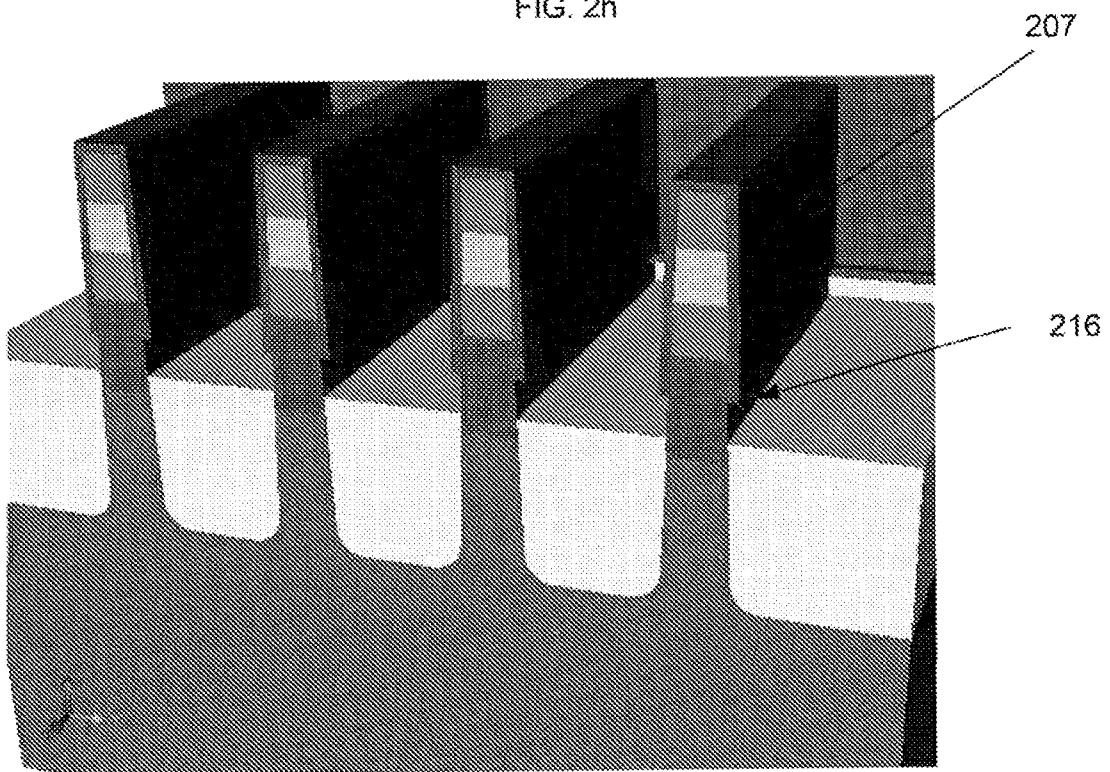


FIG. 2i

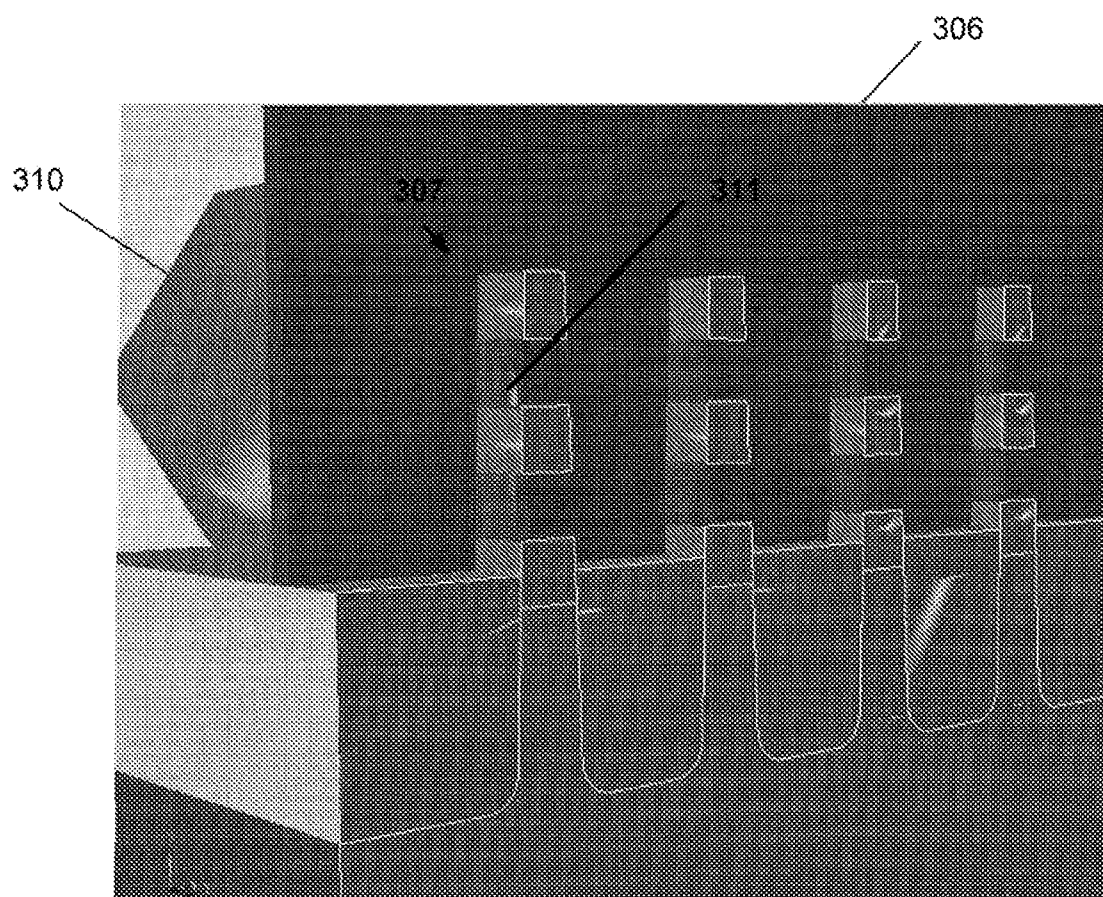


FIG. 3a

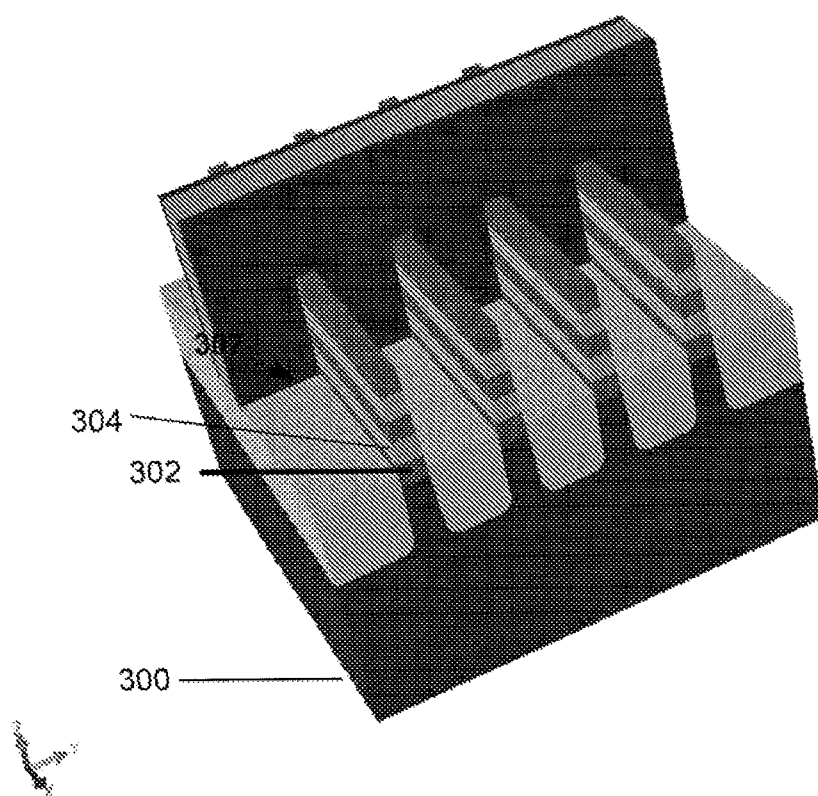


FIG. 3b

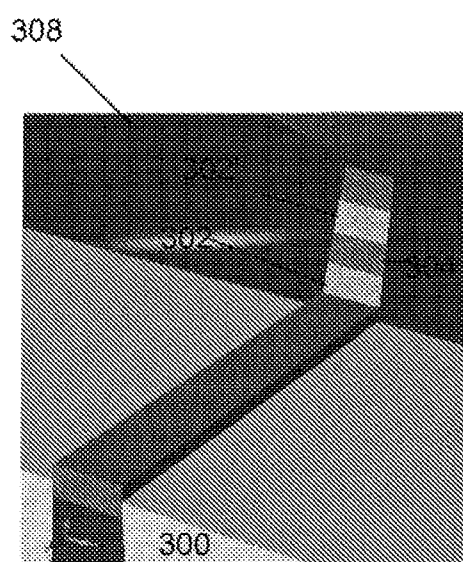


FIG. 3c

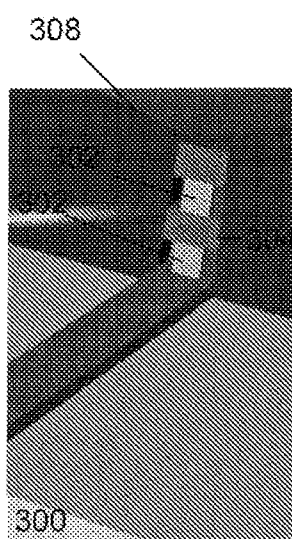


FIG. 3d

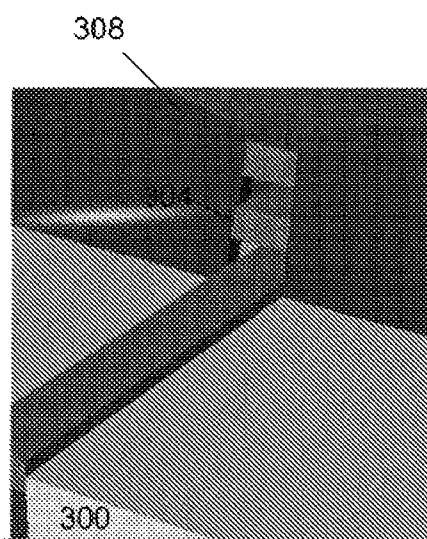


FIG. 3e

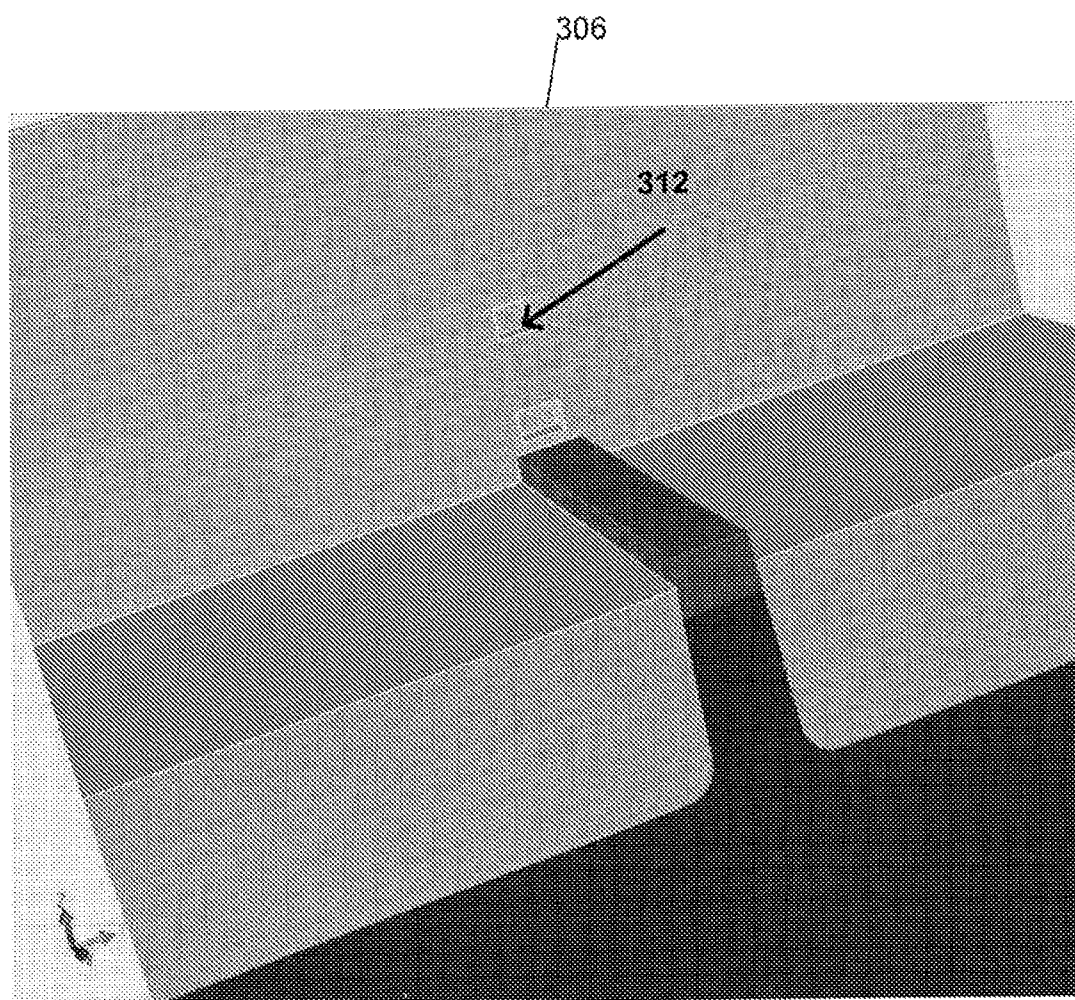


FIG. 3f

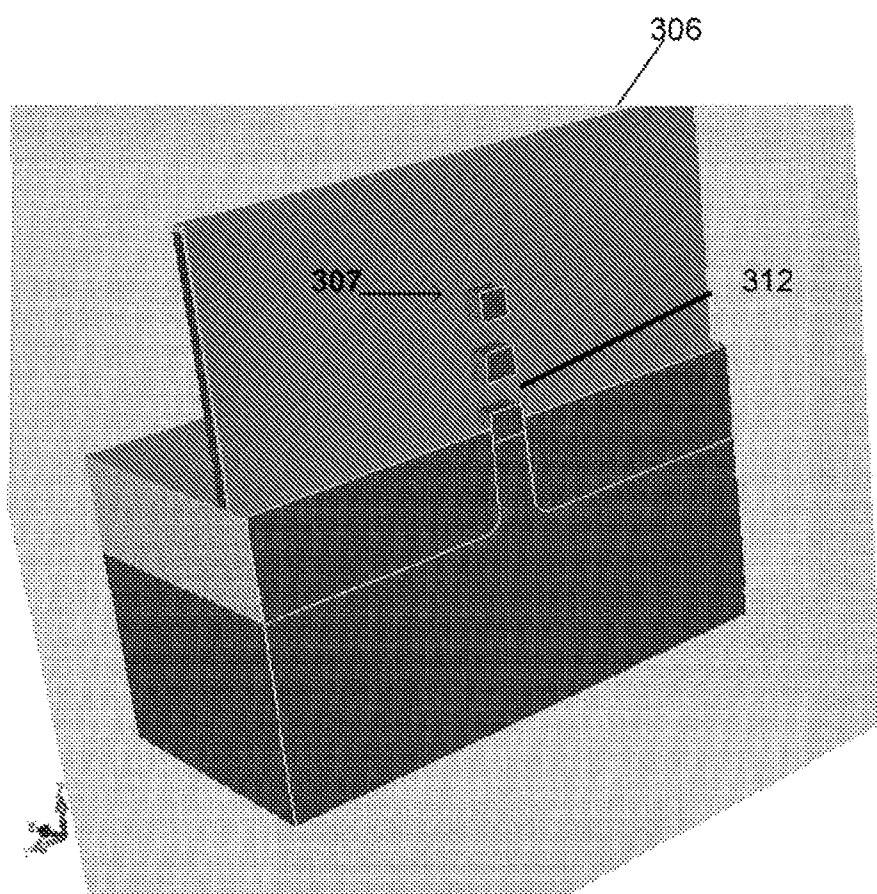


FIG. 3g

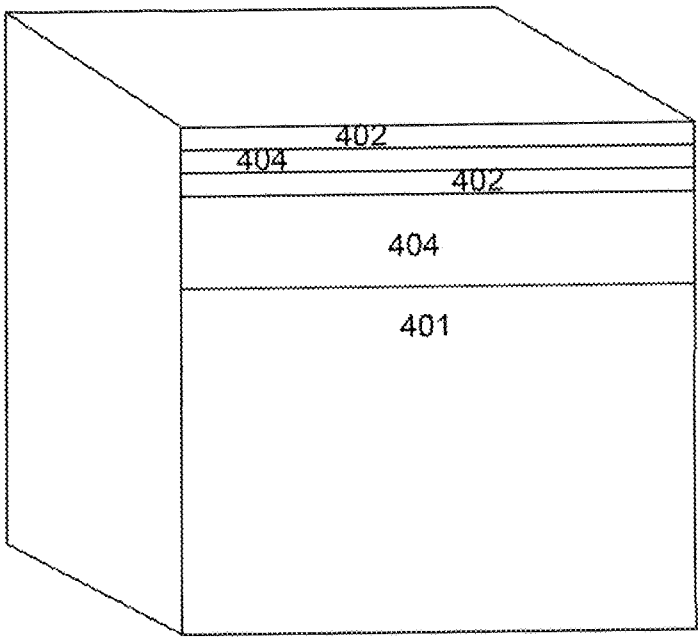


FIG. 4a

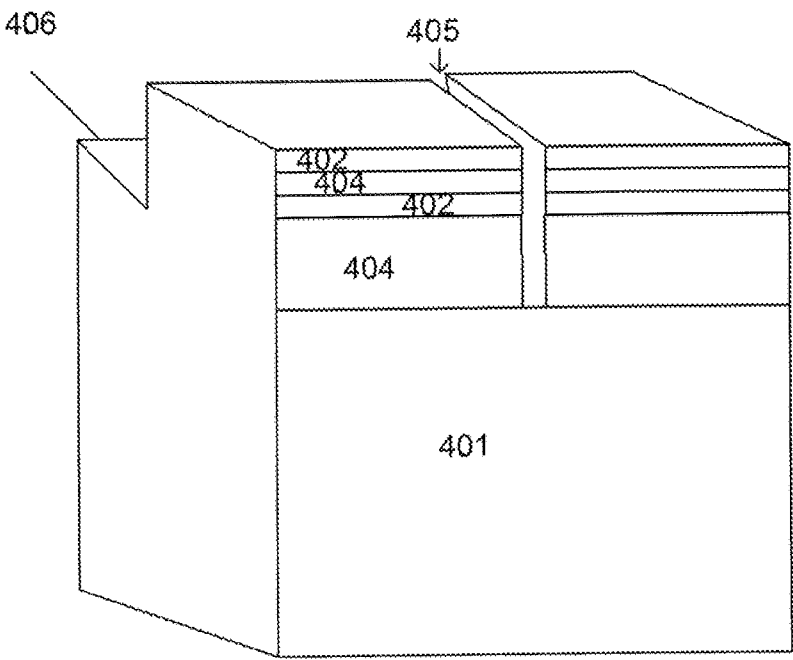


FIG. 4b

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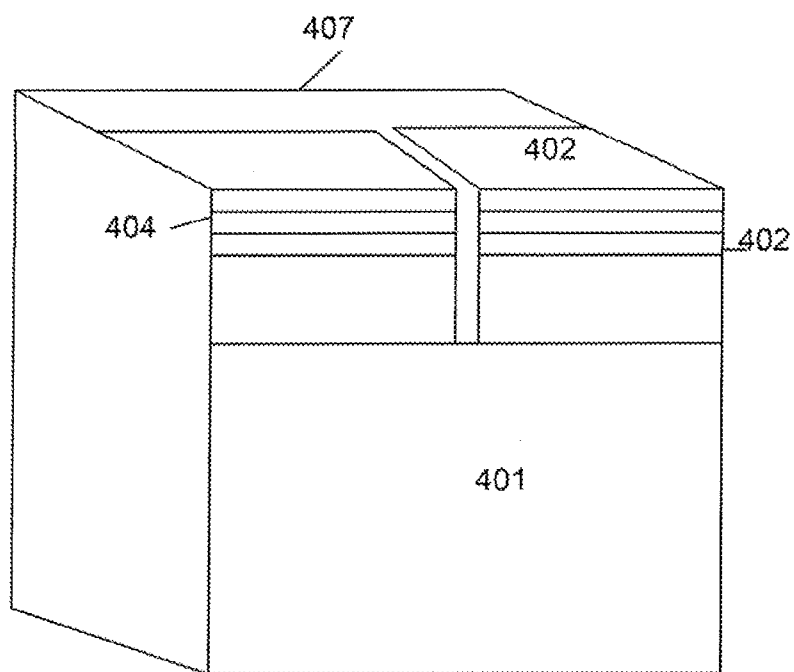


FIG. 4c

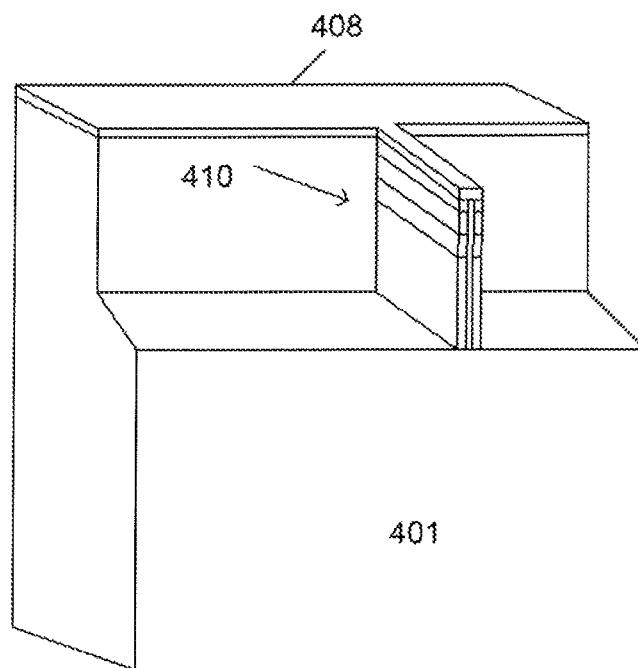


FIG. 4d

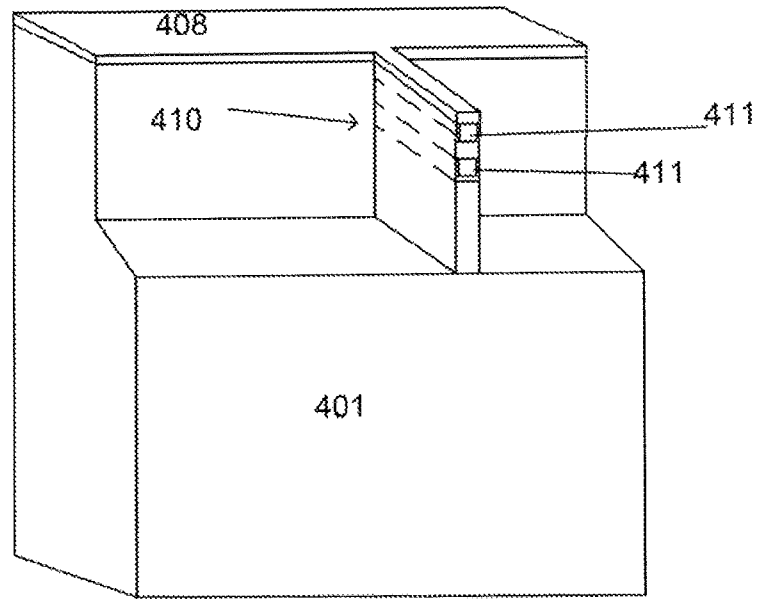


FIG. 4e

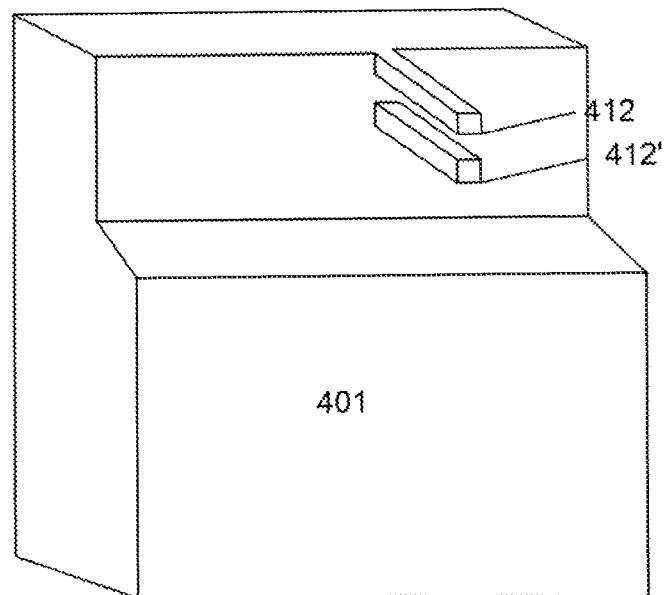


FIG. 4f

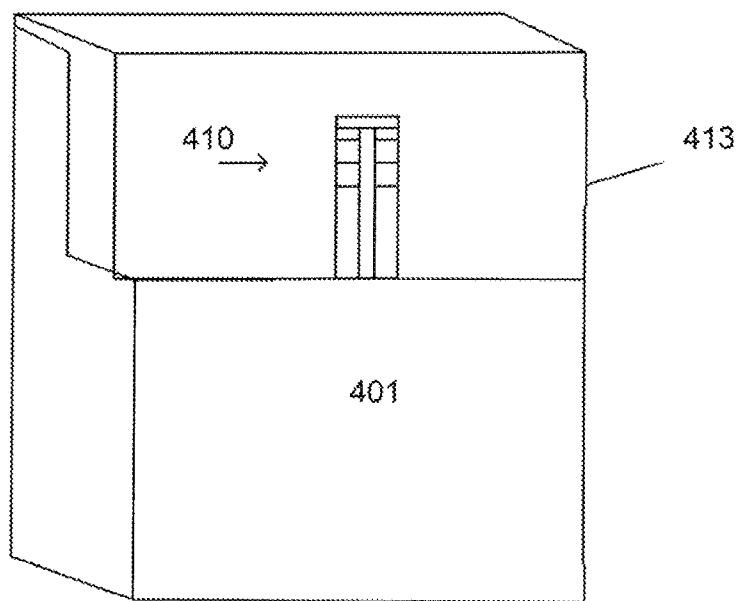


FIG. 4g

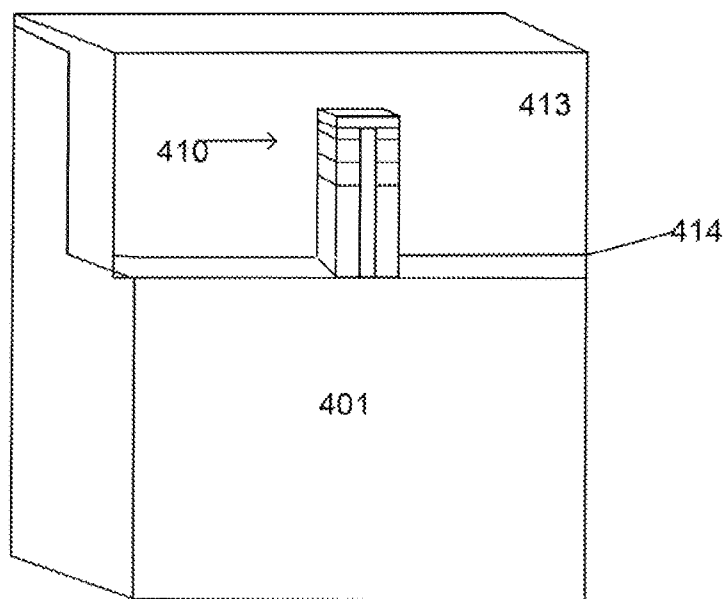


FIG. 4h

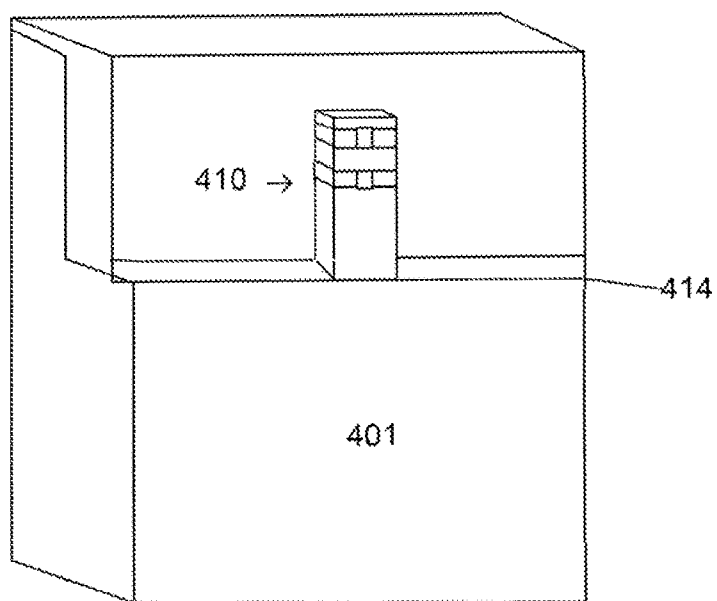


FIG. 4i

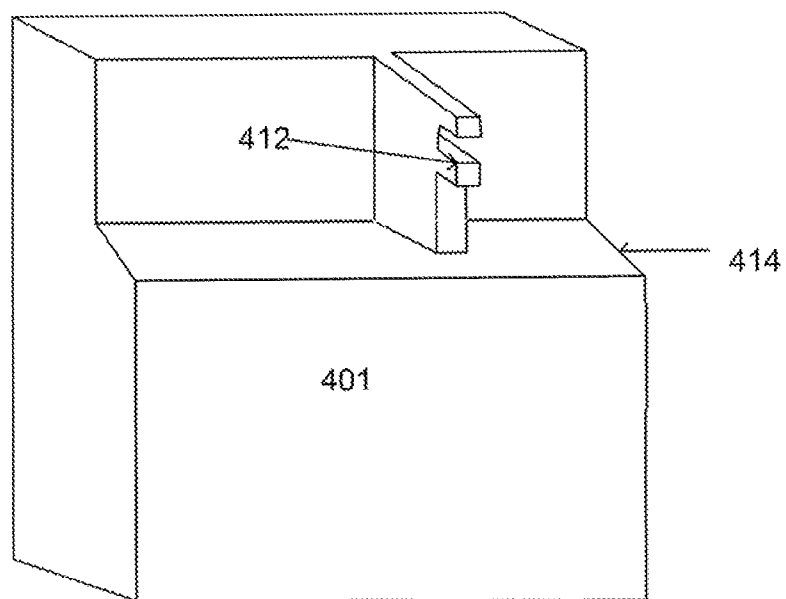


FIG. 4j

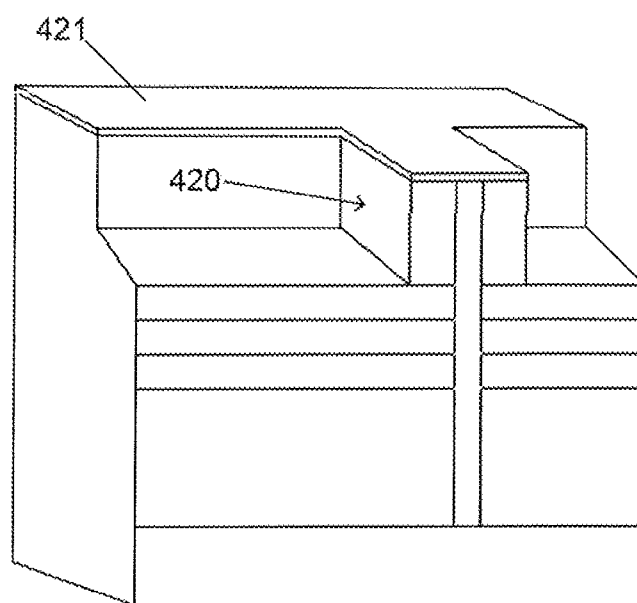


FIG. 4k

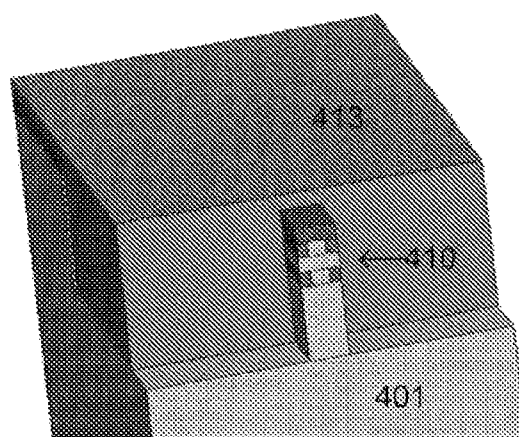


FIG. 4l

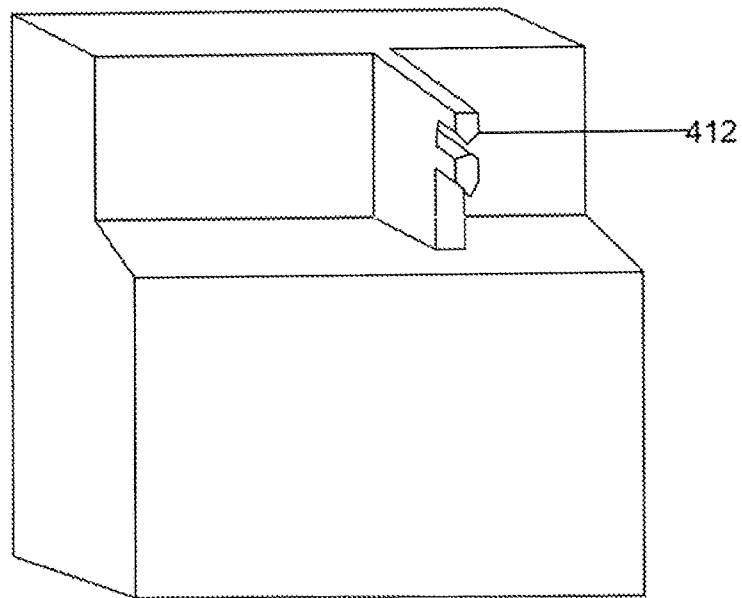


FIG. 4m

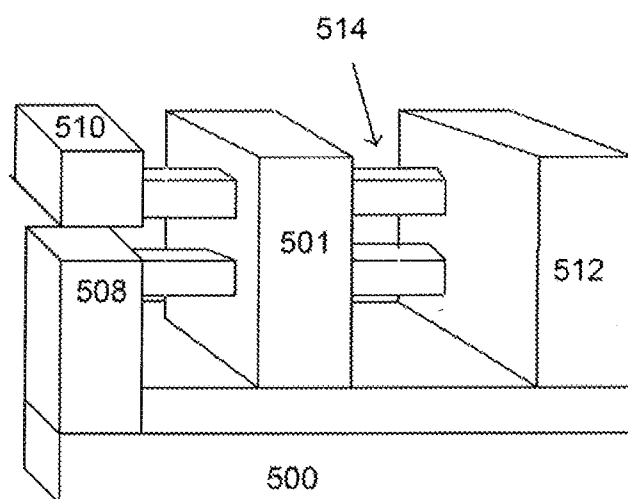


FIG. 5a

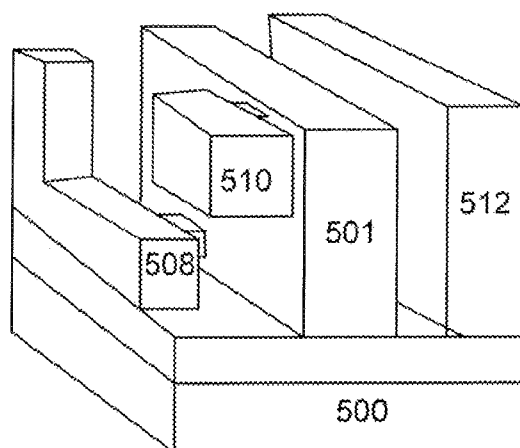


FIG. 5b

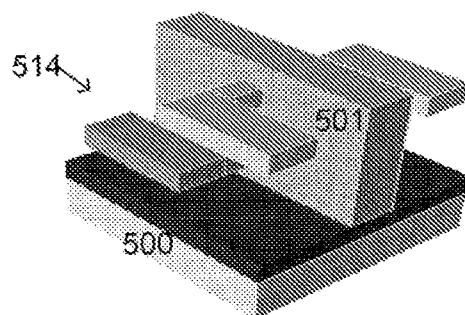


FIG. 5c

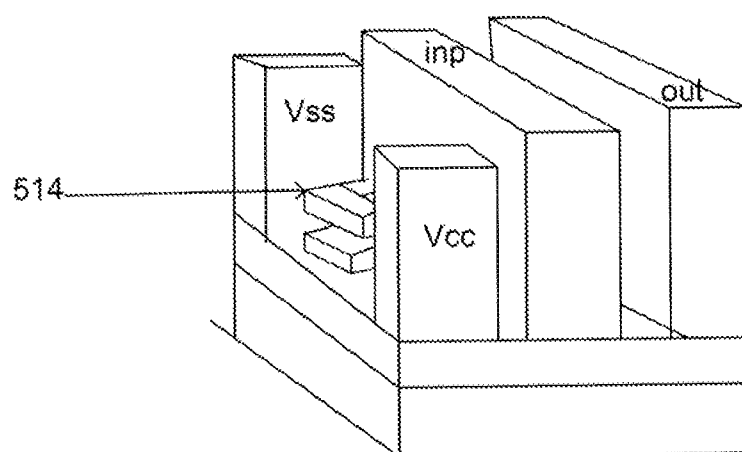


FIG. 5d

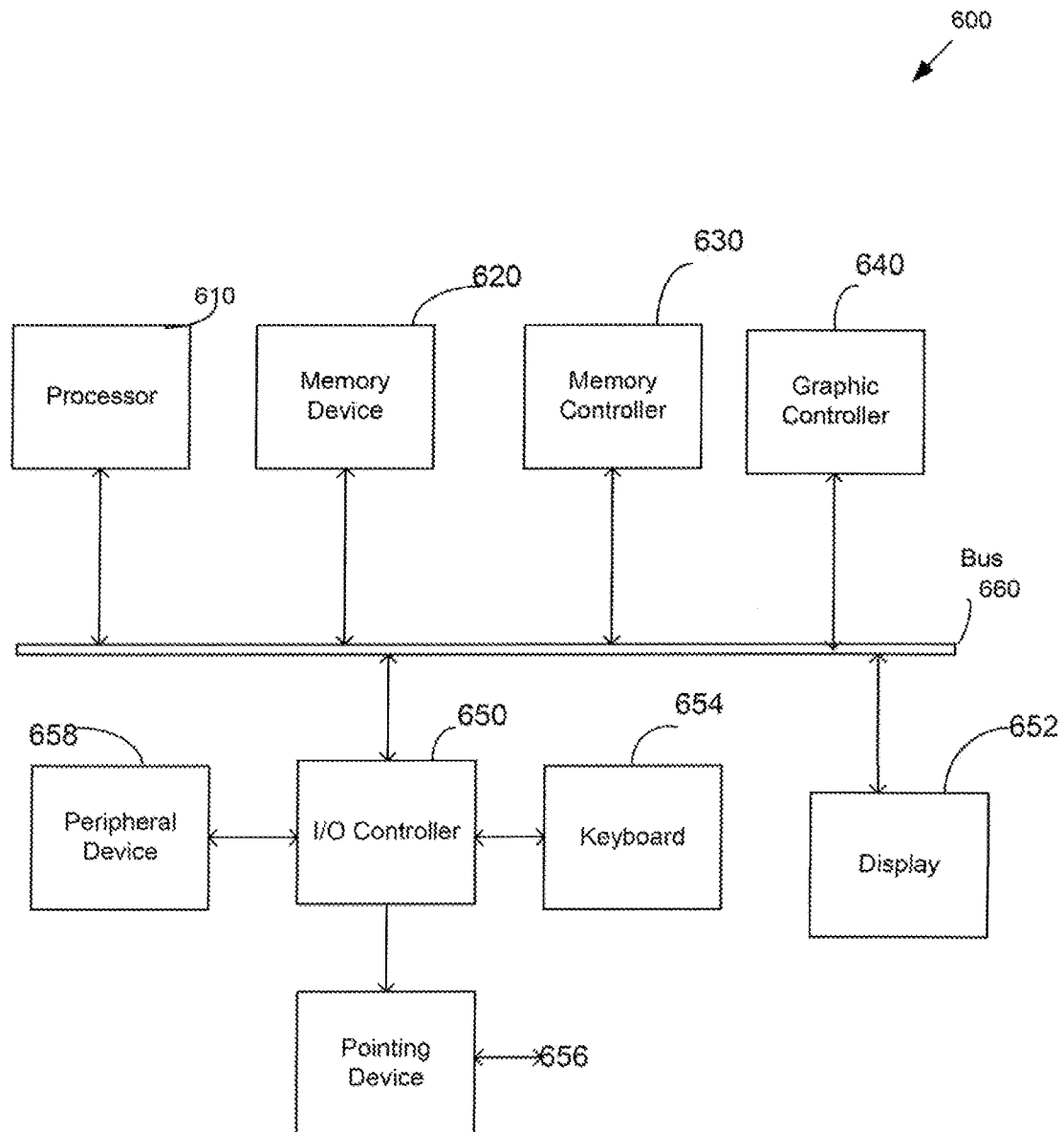


FIG. 6