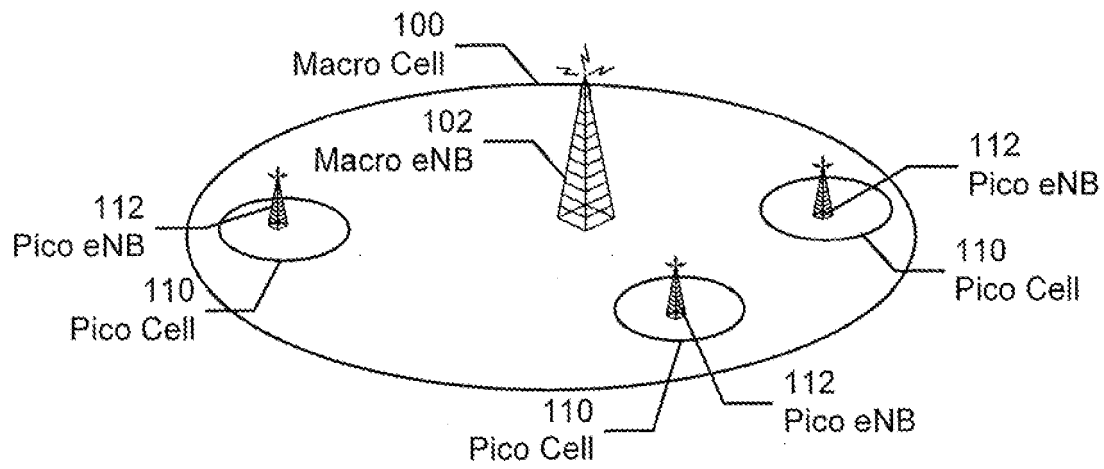
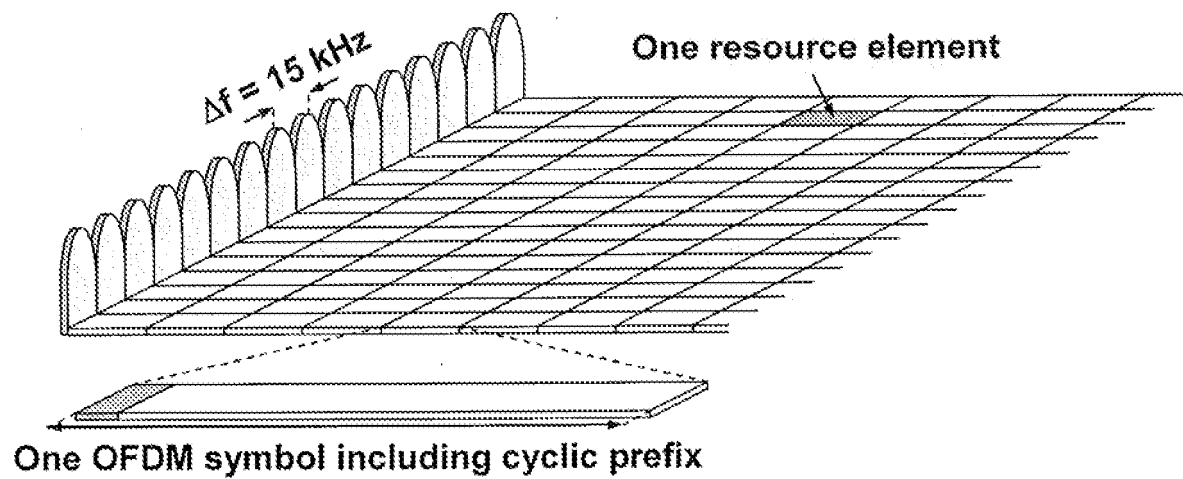
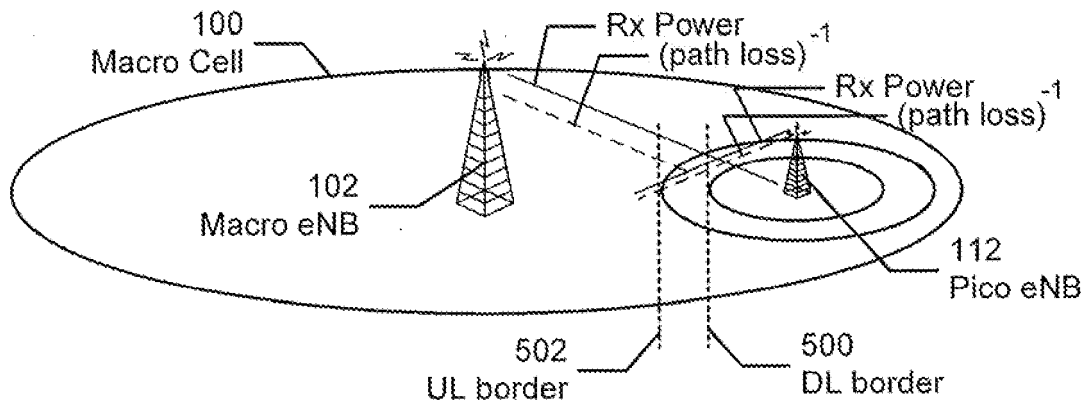
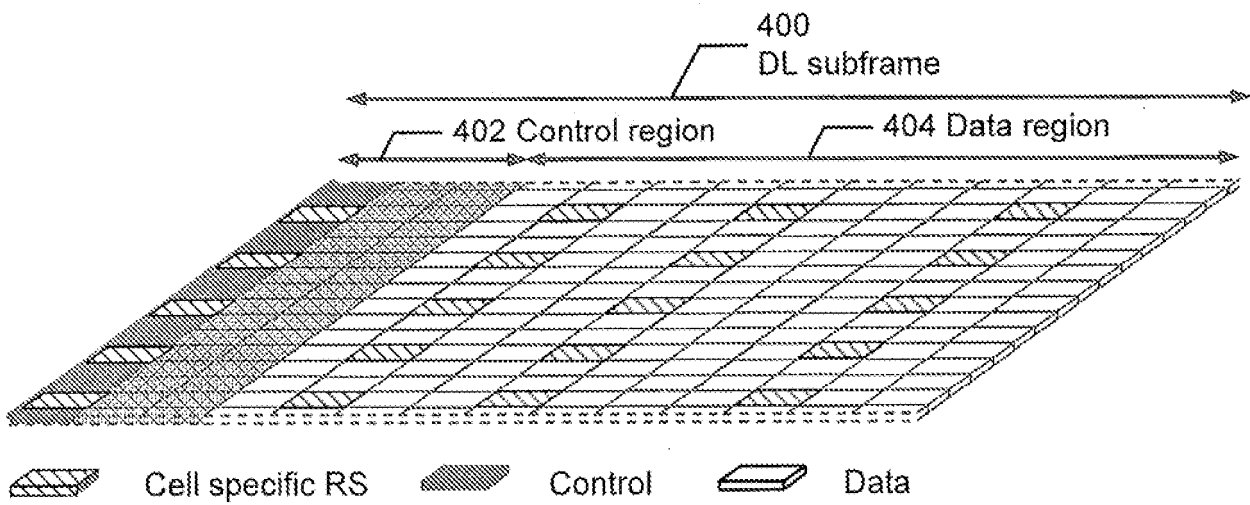
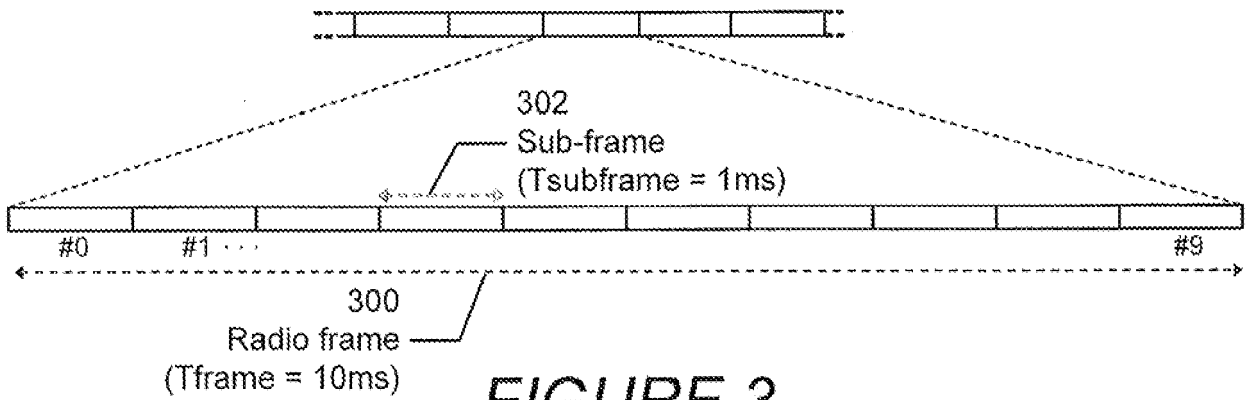
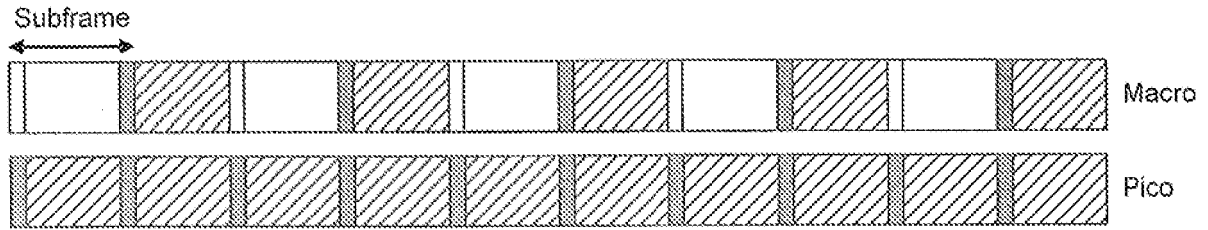
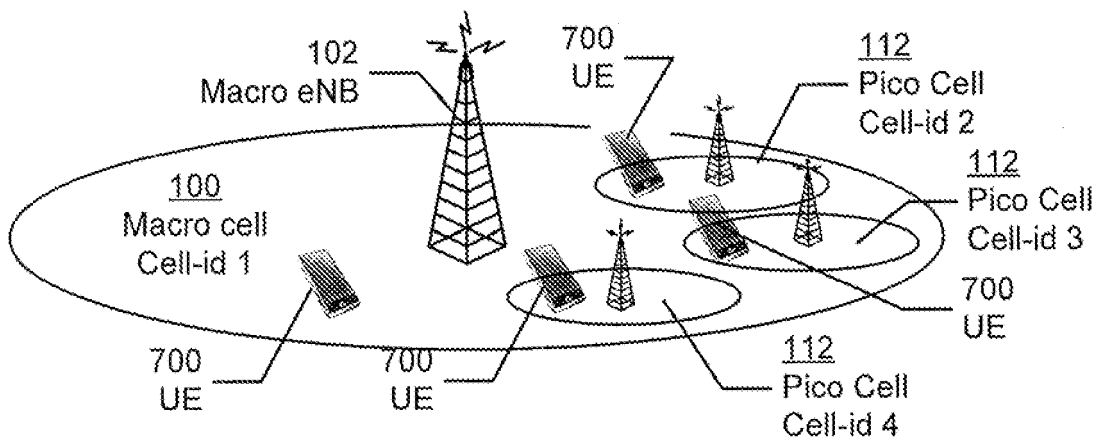
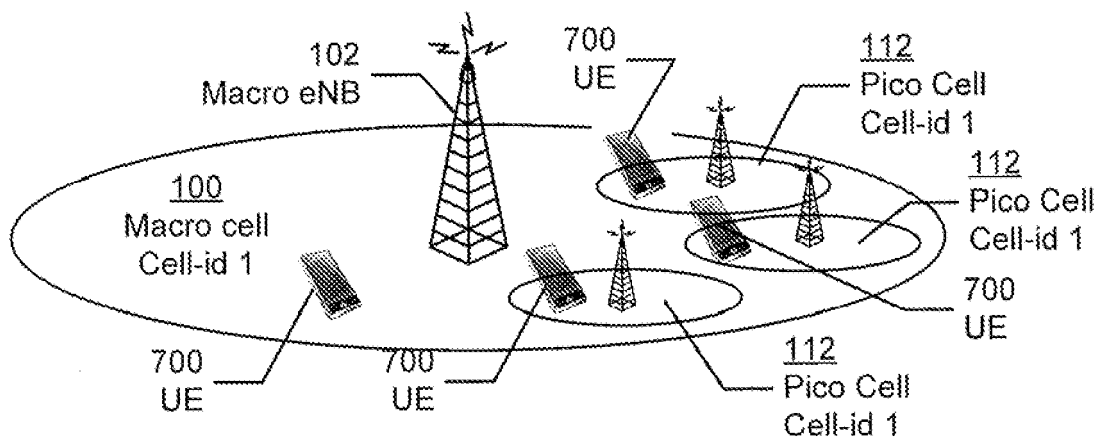


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**FIGURE 1****FIGURE 2**

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**FIGURE 6****FIGURE 7****FIGURE 8**

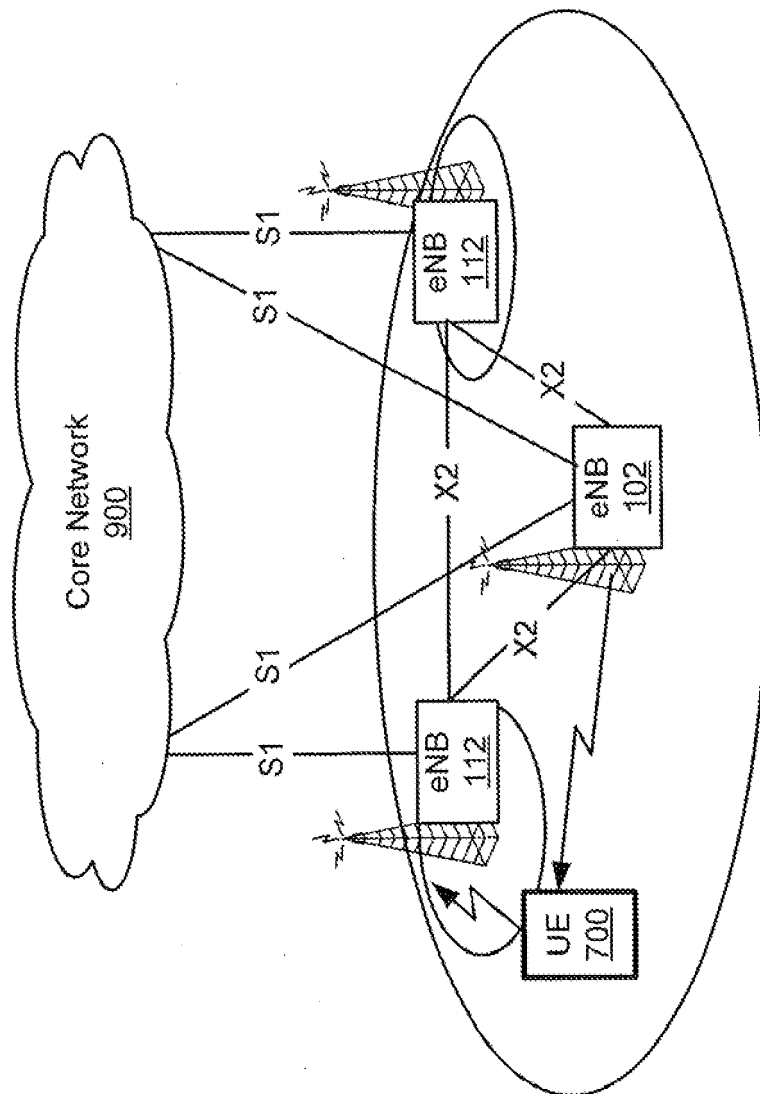
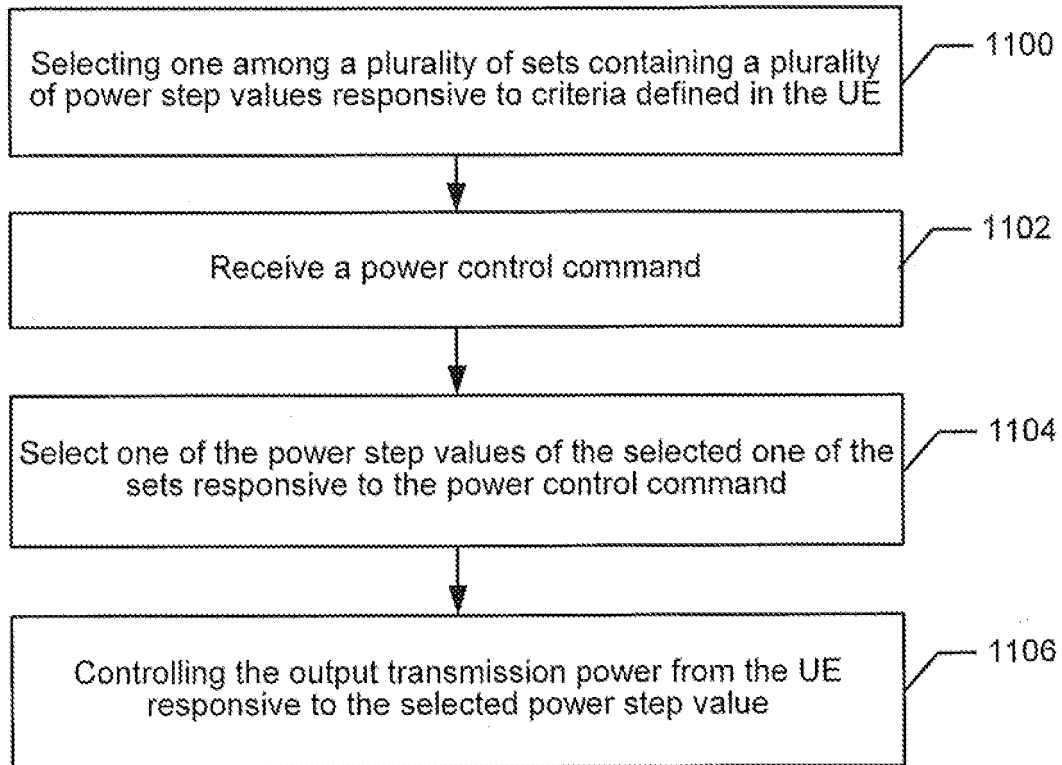
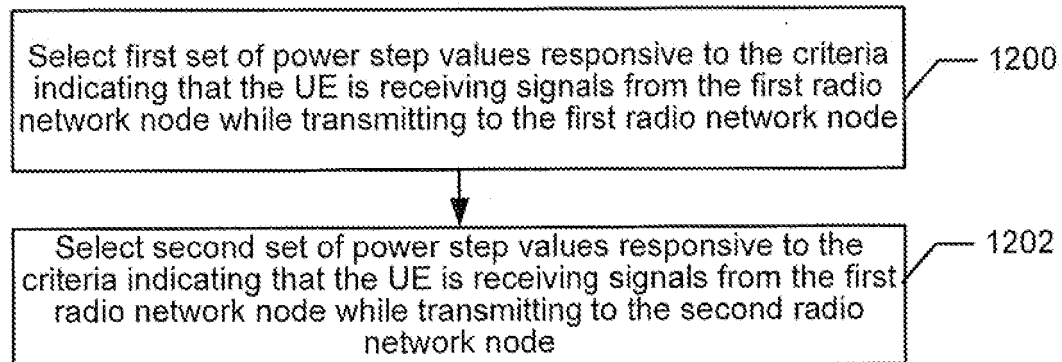


FIGURE 9

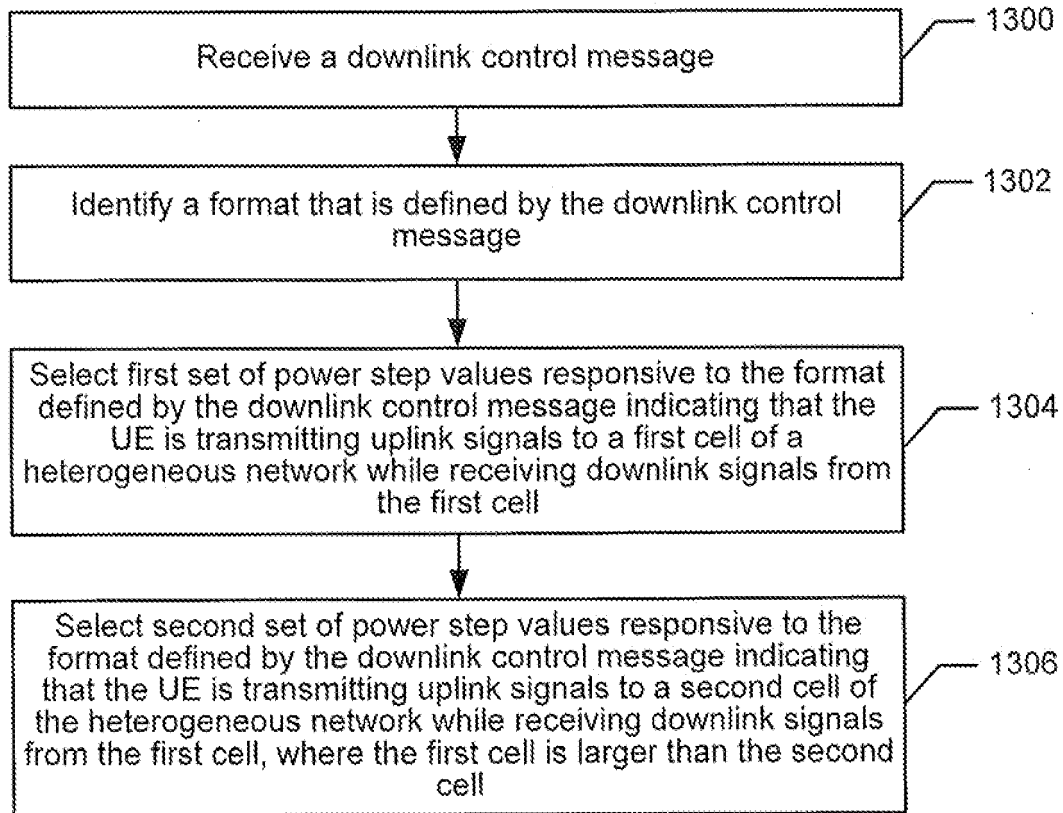
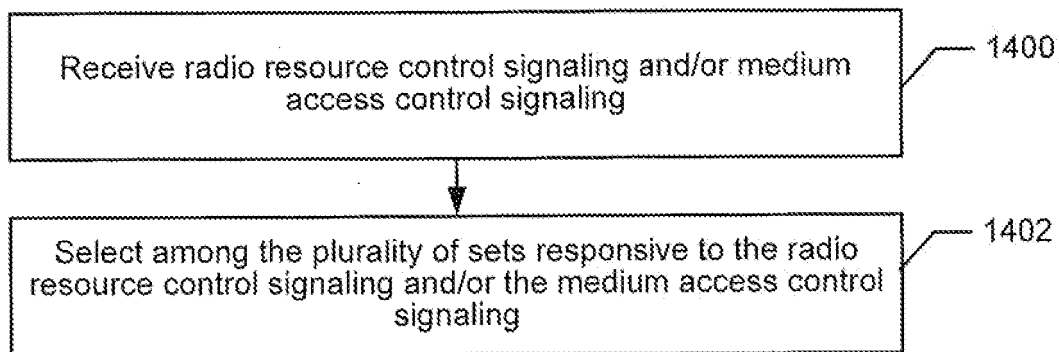
1010		1020a	1020b	1020n	
Power control command value (2 bits)		First Set of power step values C [dB]	Second Set of power step values C [dB]	N'th Set of power step values C [dB]	
0		-1	-3	Value 1	
1		0	0	Value 2	
2		1	5	Value 3	
3		3	7	Value 4	

FIGURE 10

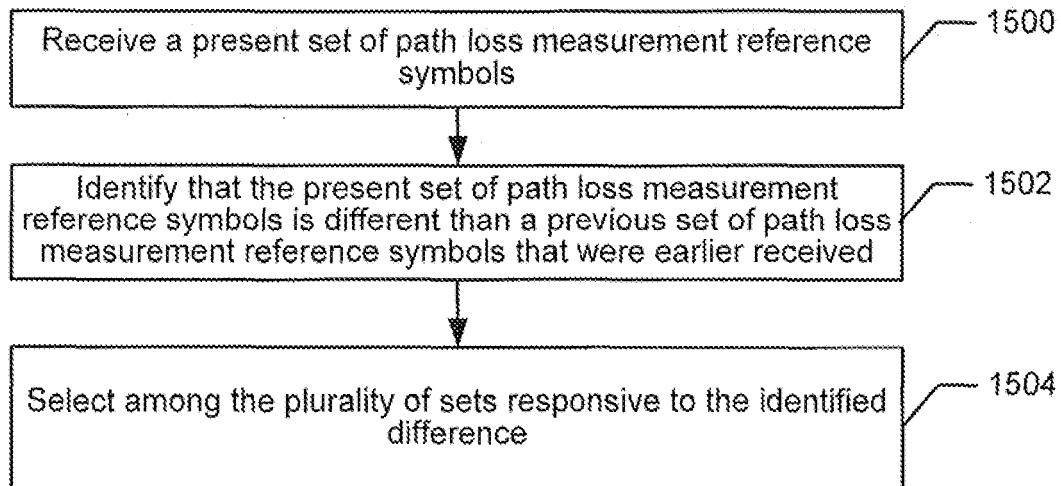
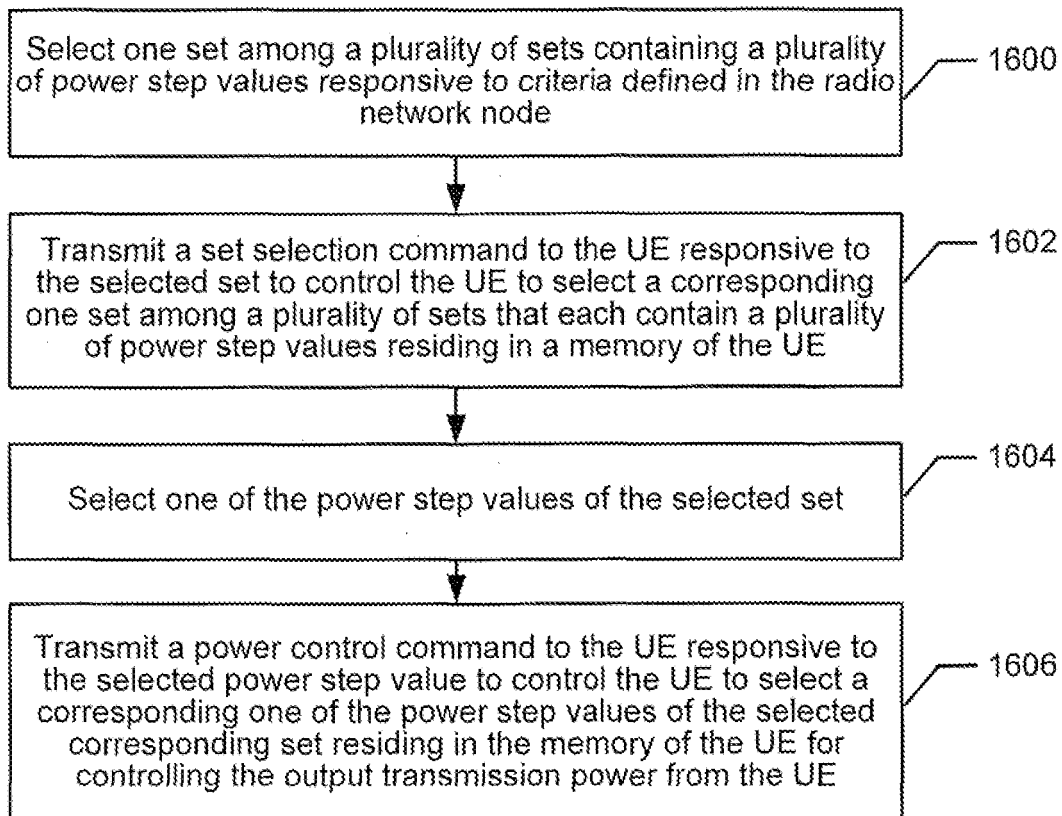
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*FIGURE 11**FIGURE 12*

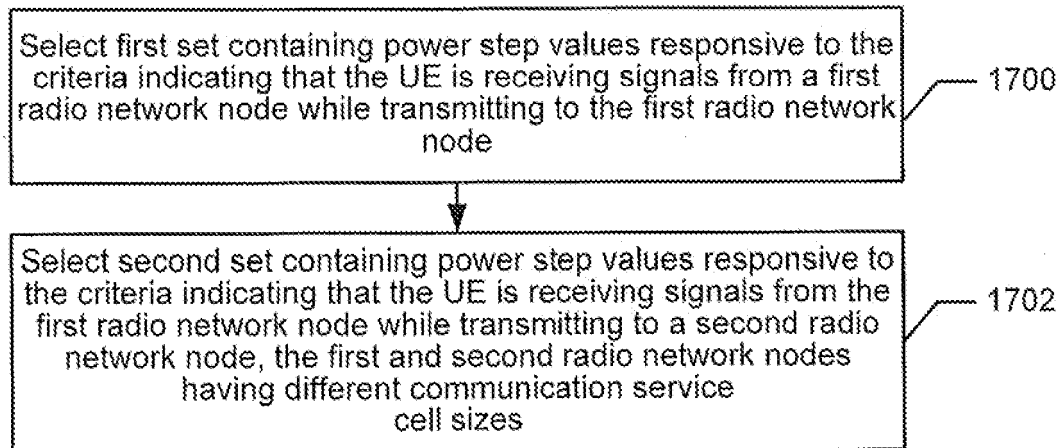
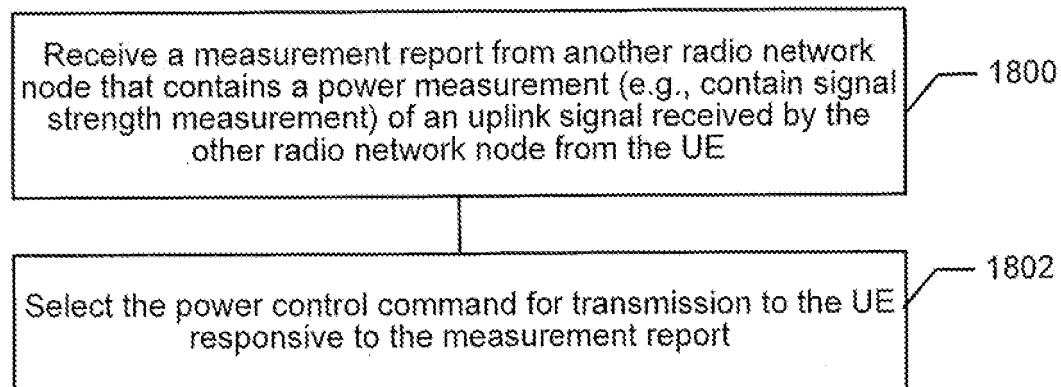
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*FIGURE 13**FIGURE 14*

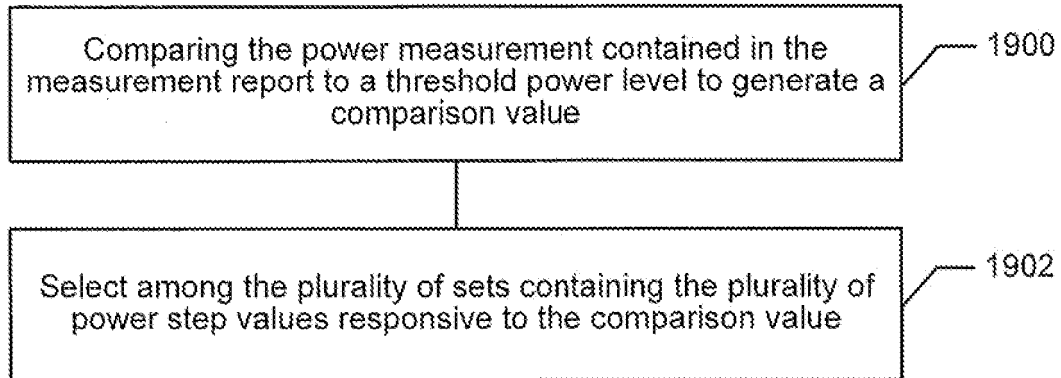
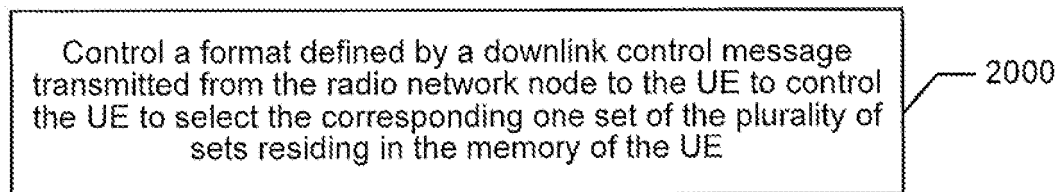
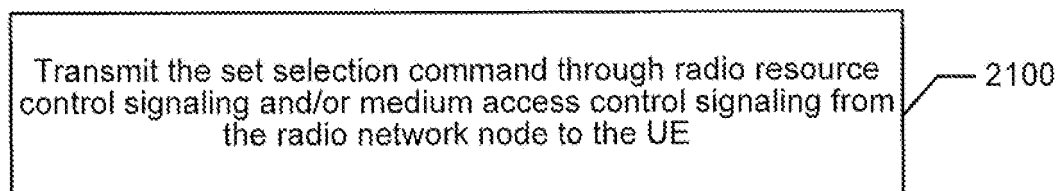
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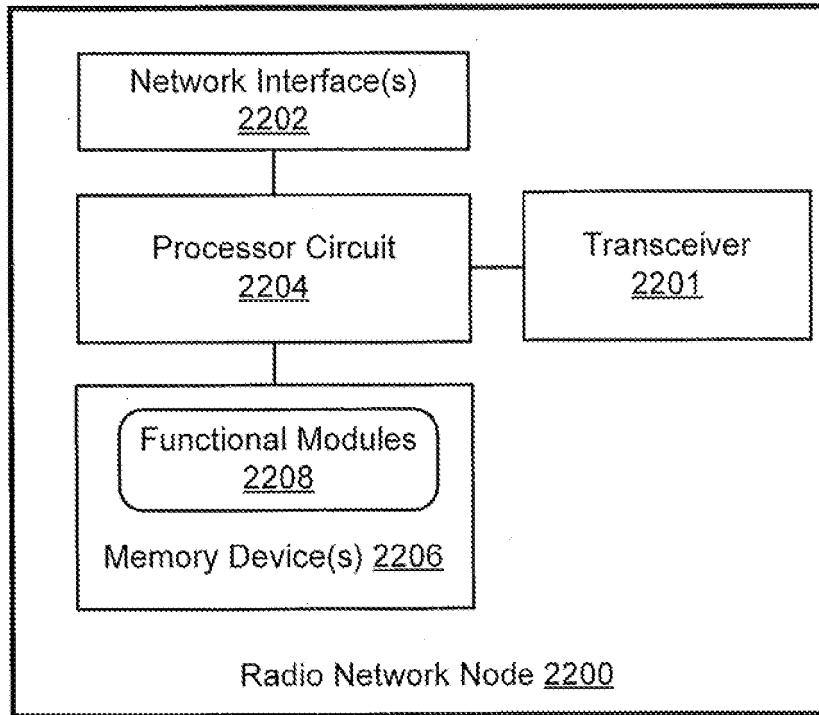
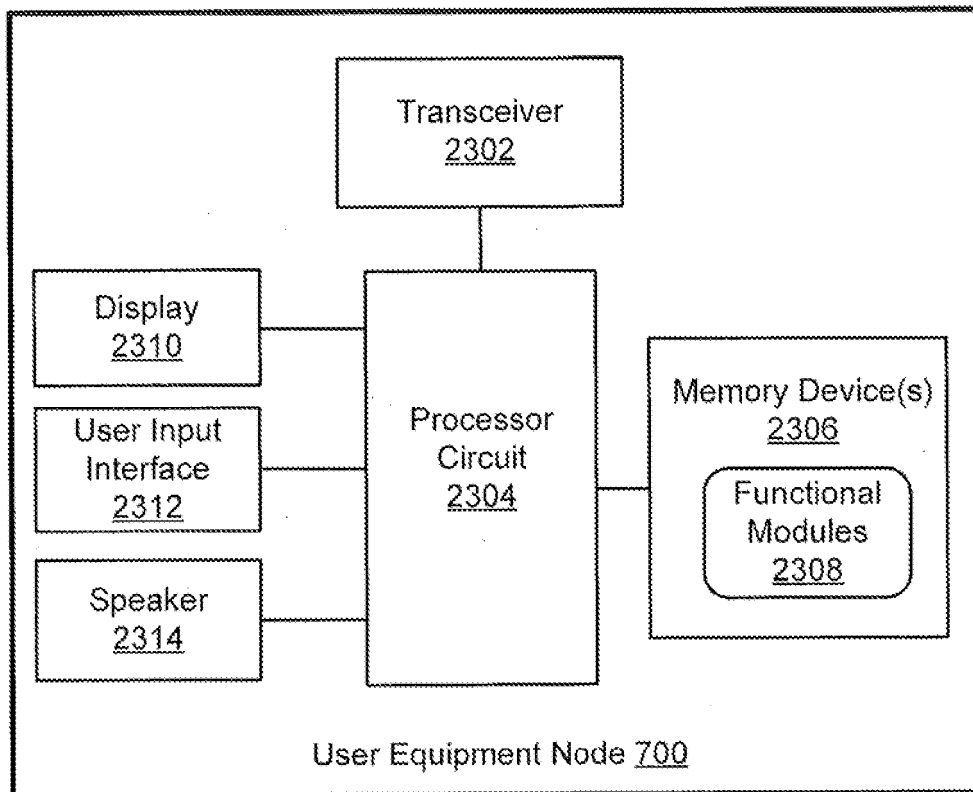
*FIGURE 15**FIGURE 16*

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*FIGURE 17**FIGURE 18*

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*FIGURE 19**FIGURE 20**FIGURE 21*

*FIGURE 22**FIGURE 23*