

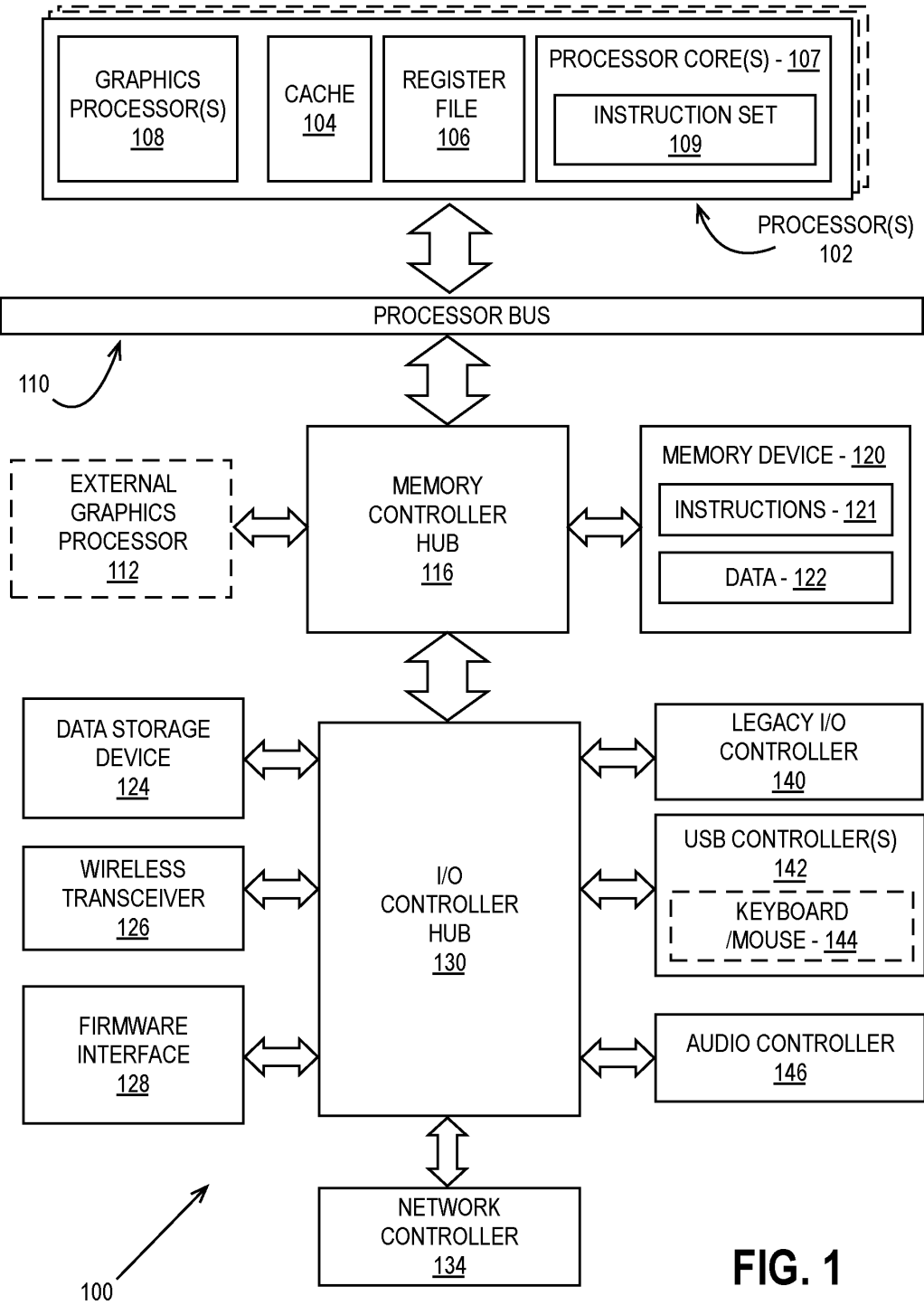


FIG. 1

PROCESSOR 200

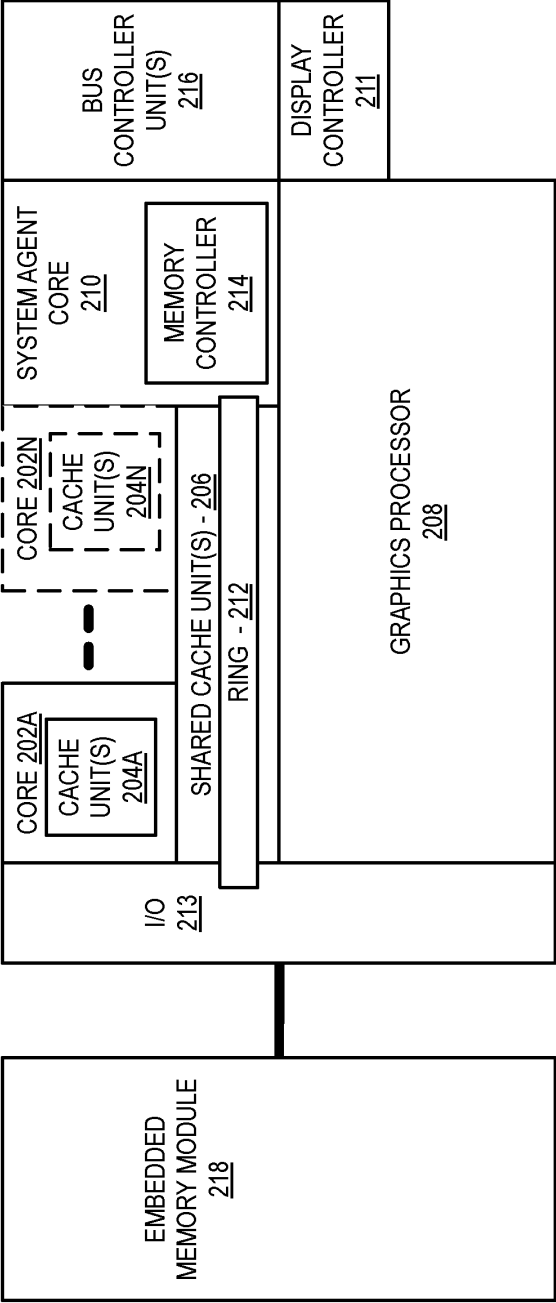
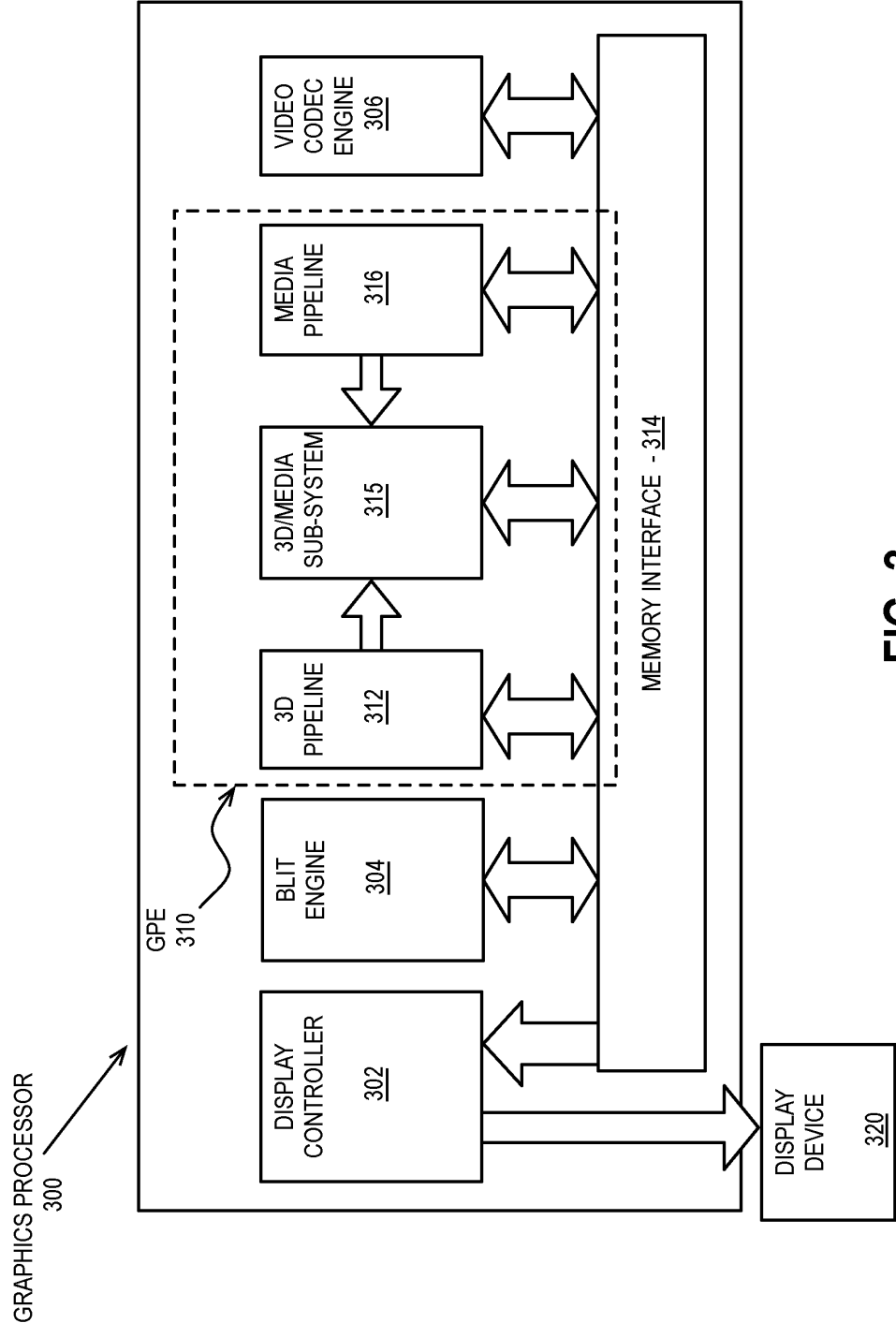


FIG. 2



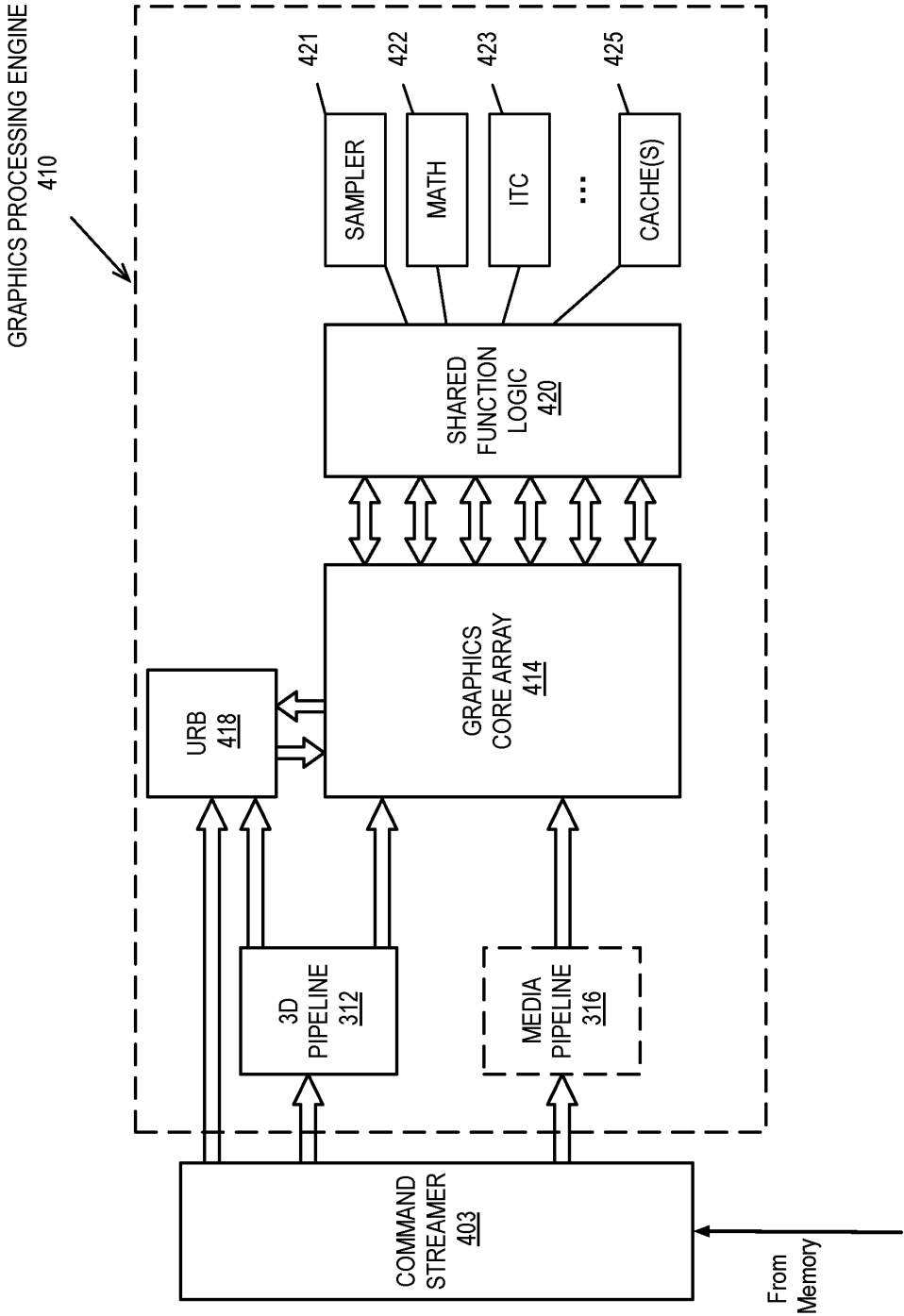


FIG. 4

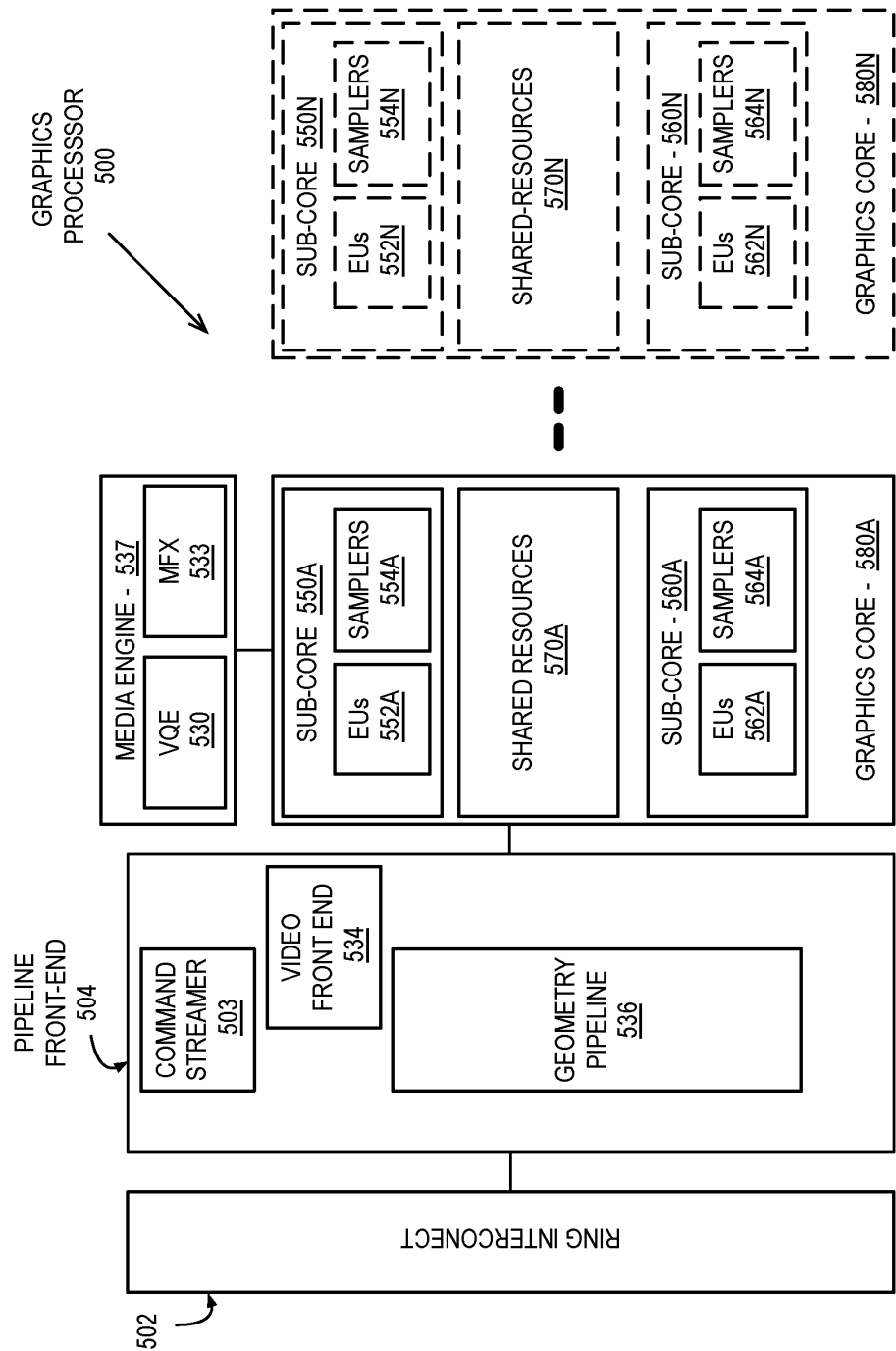


FIG. 5

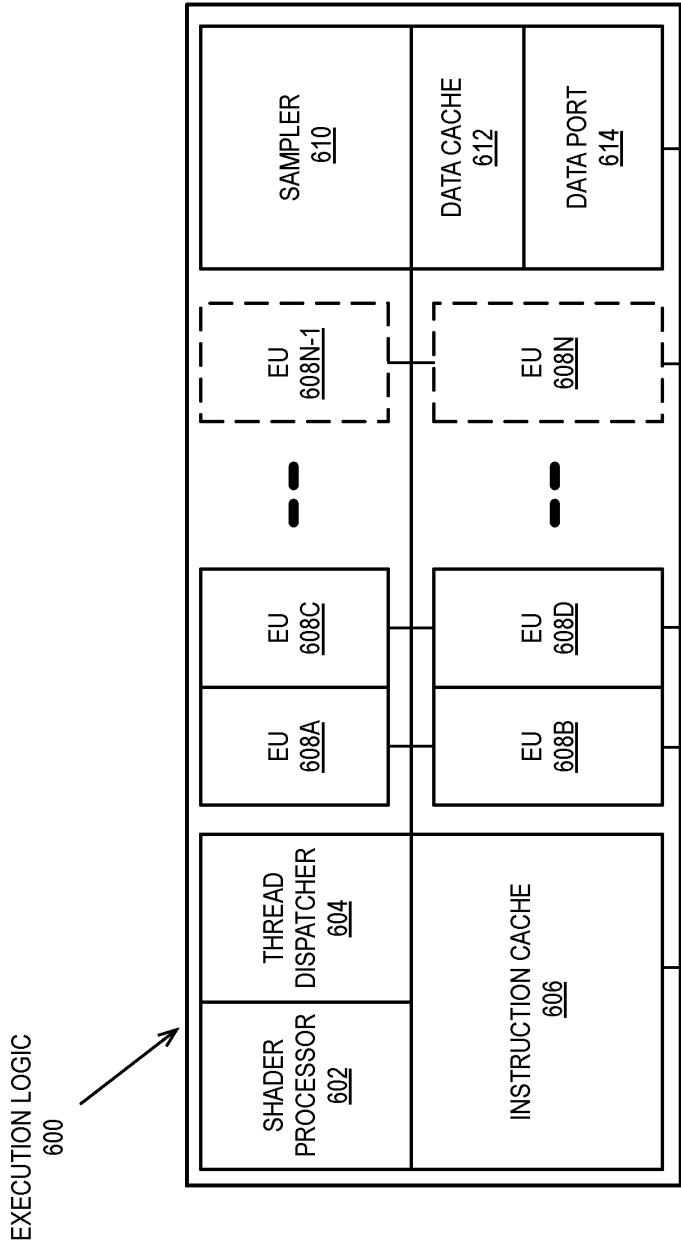


FIG. 6

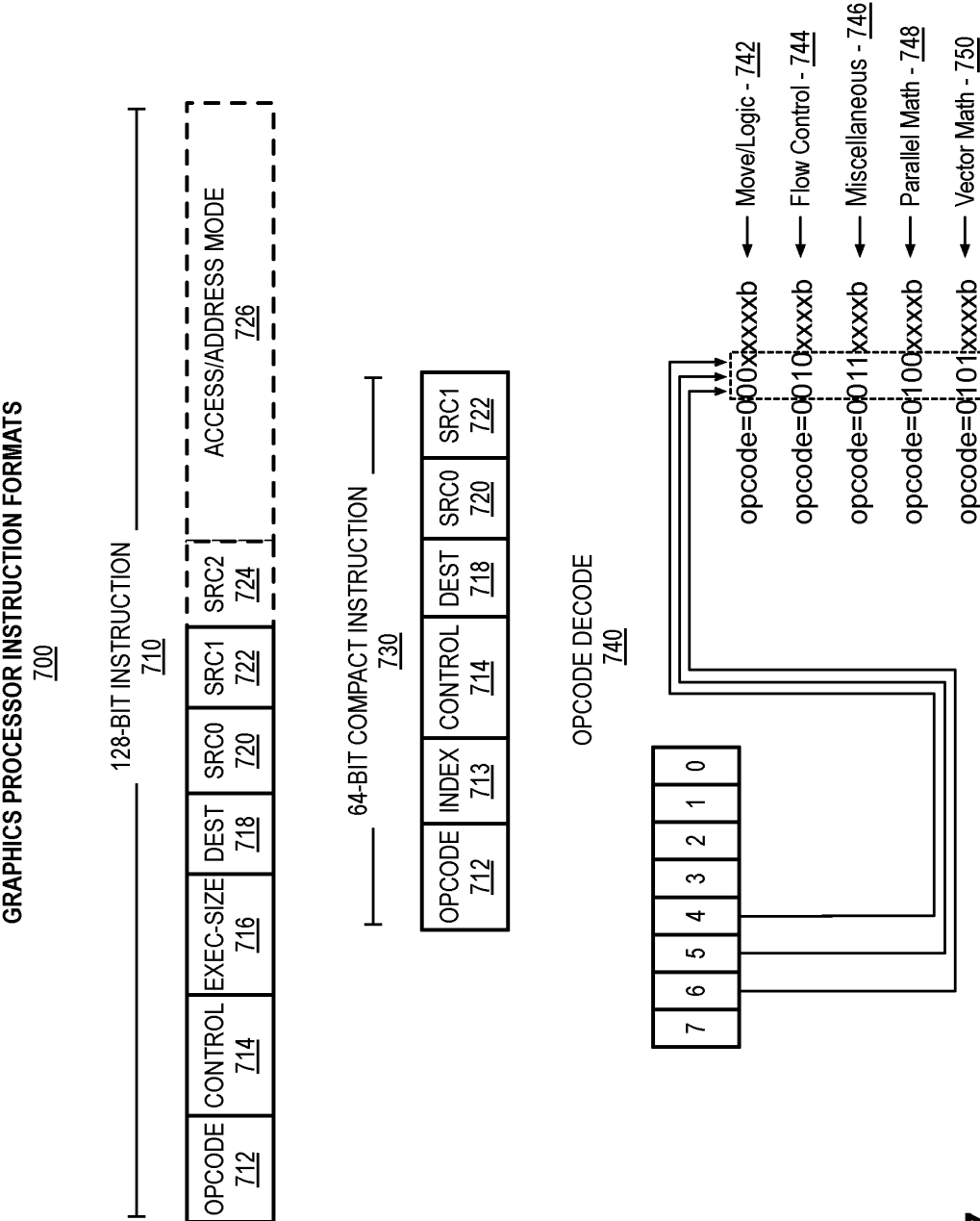


FIG. 7

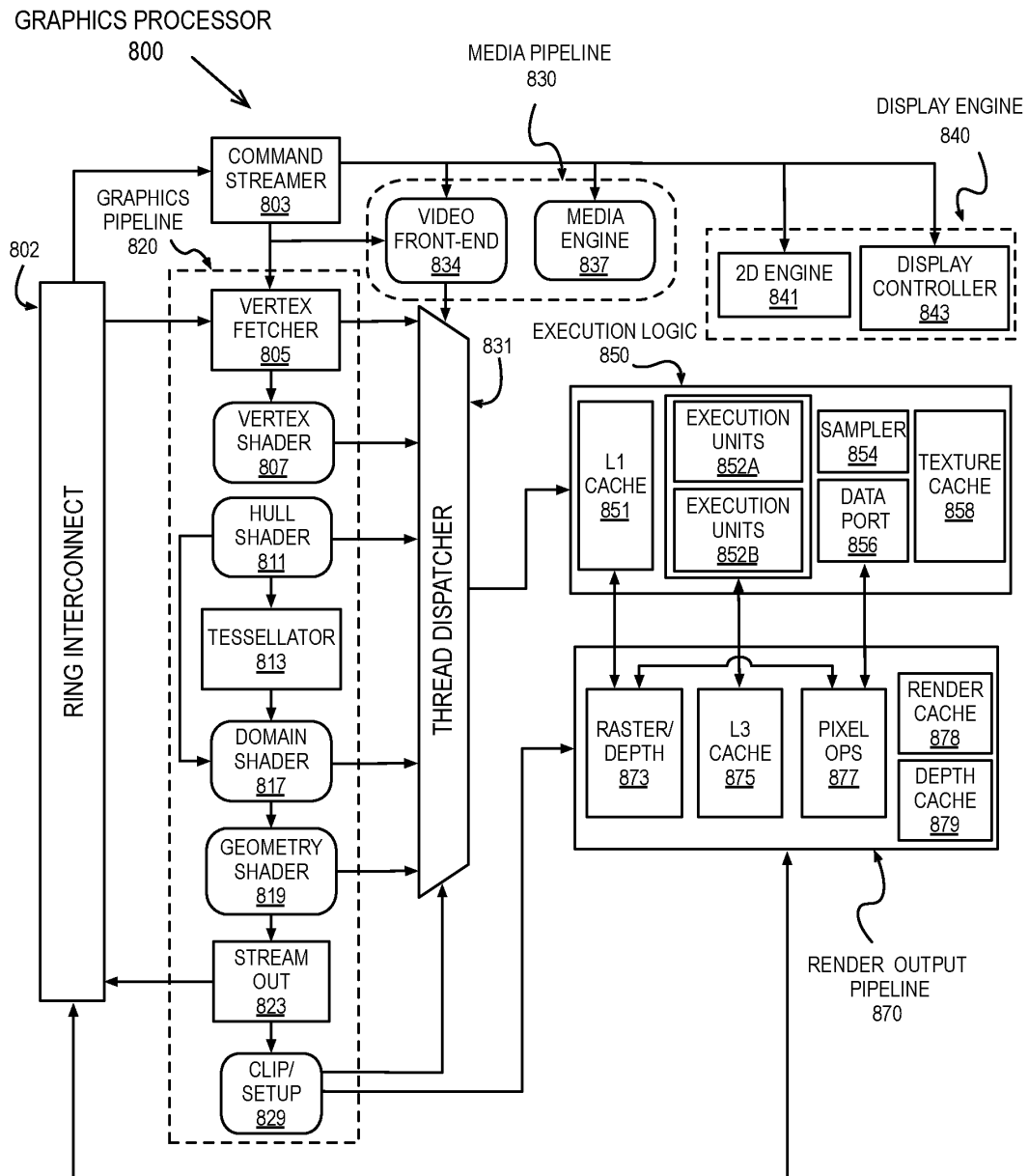


FIG. 8

FIG. 9A GRAPHICS PROCESSOR COMMAND FORMAT

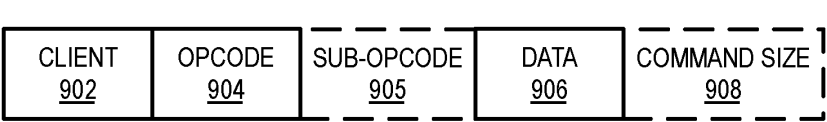
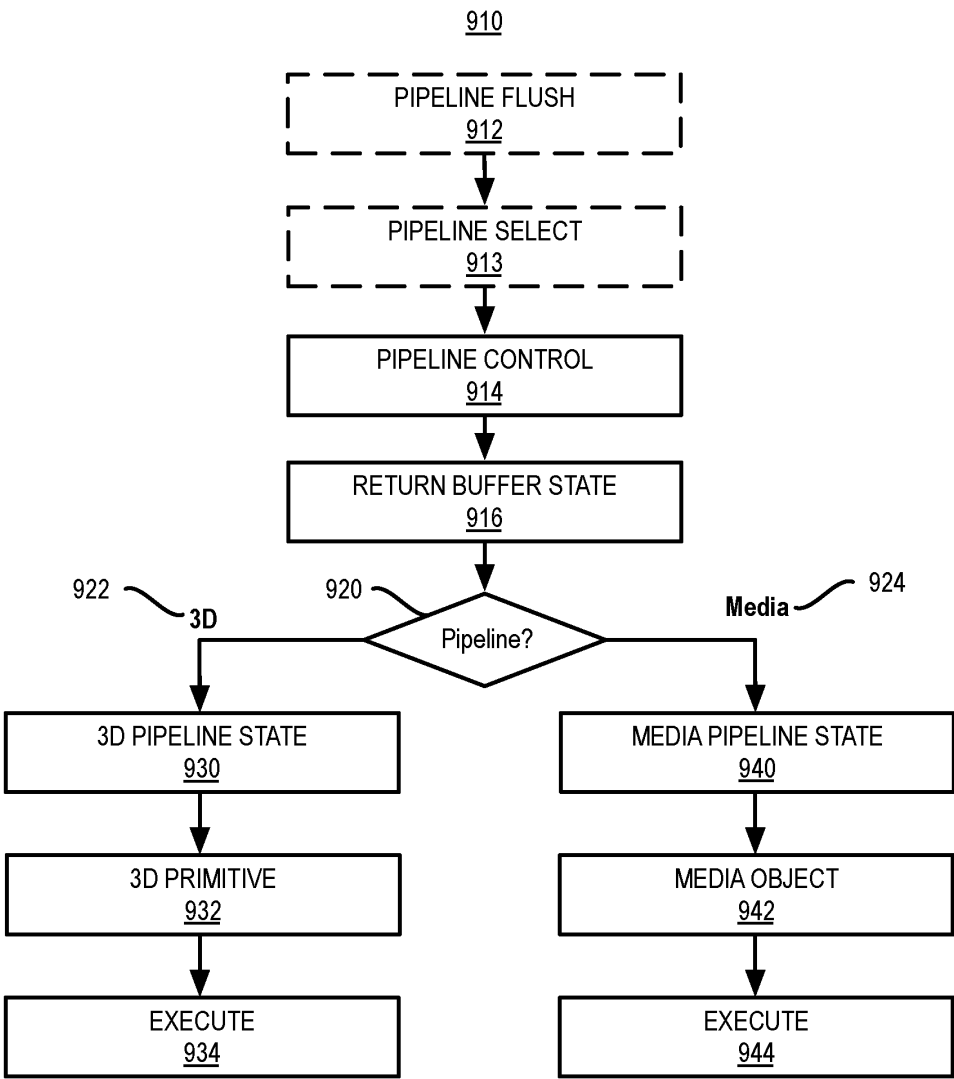


FIG. 9B GRAPHICS PROCESSOR COMMAND SEQUENCE



DATA PROCESSING SYSTEM -1000

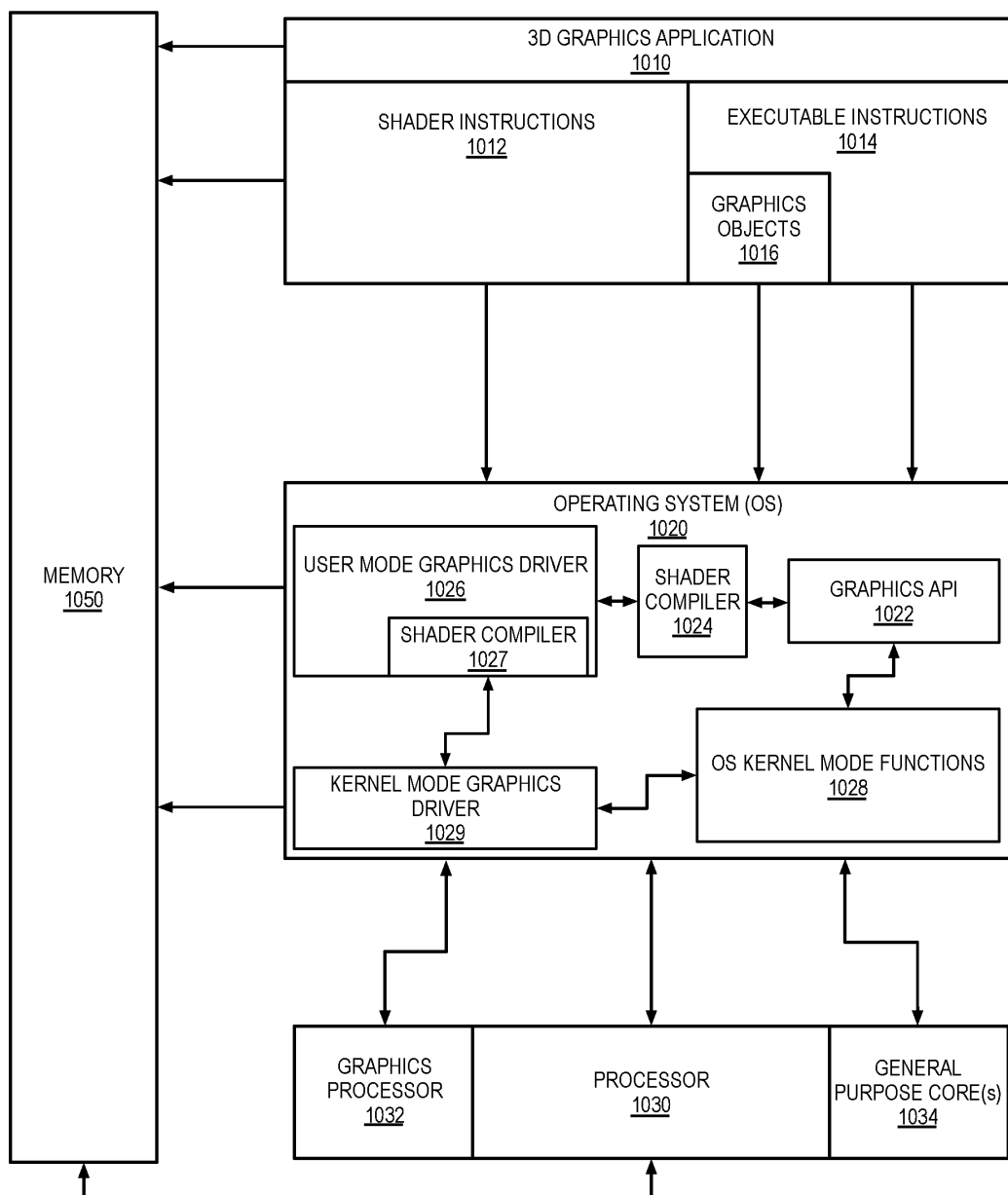


FIG. 10

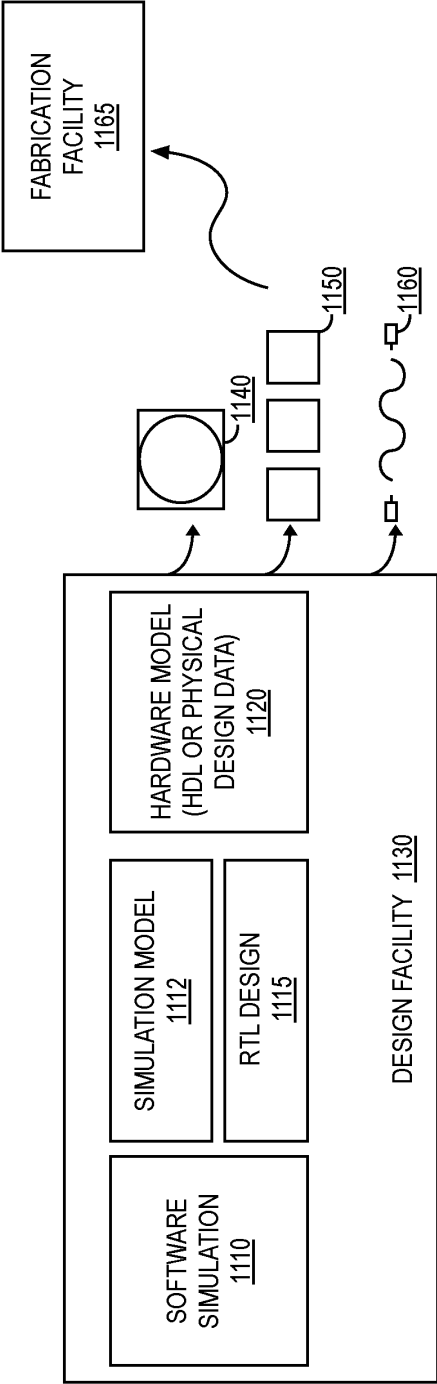


FIG. 11

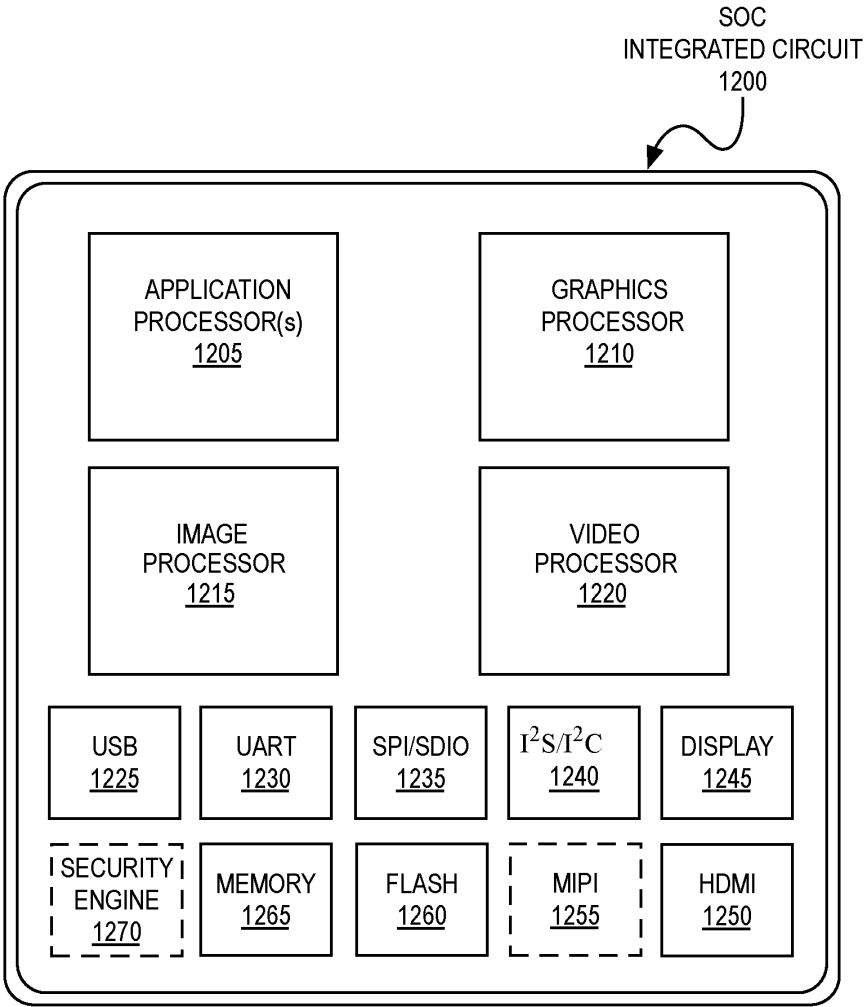


FIG. 12

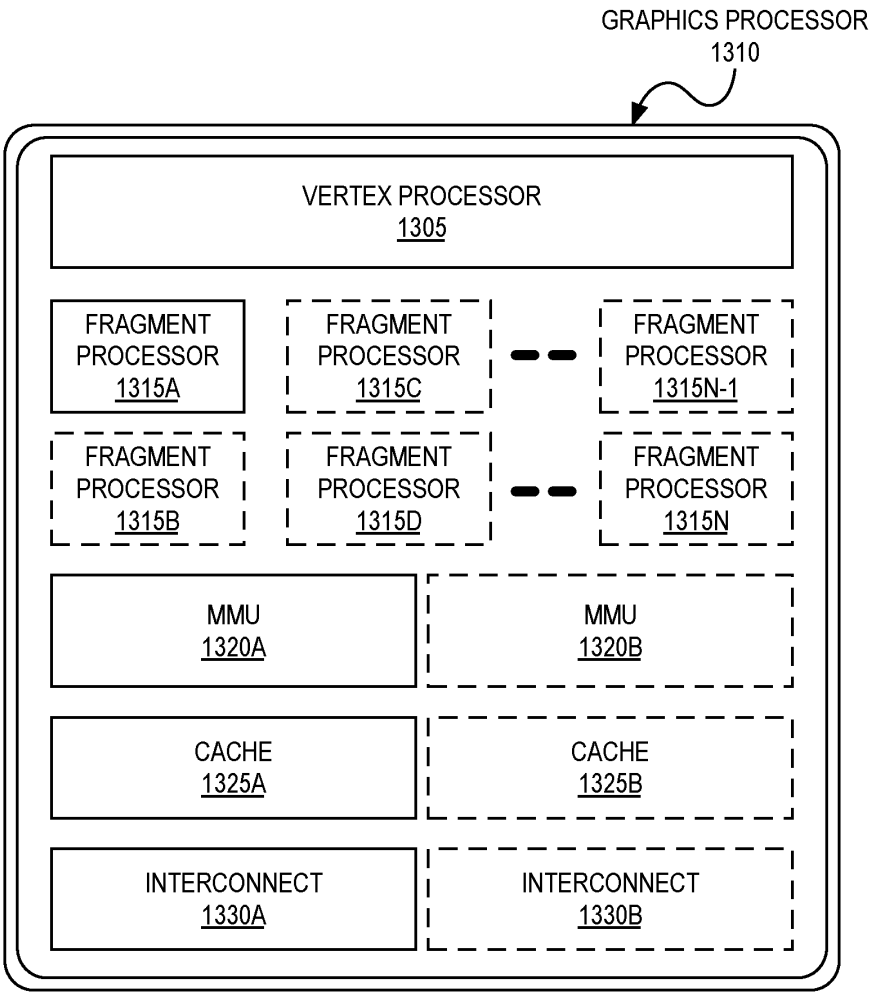


FIG. 13

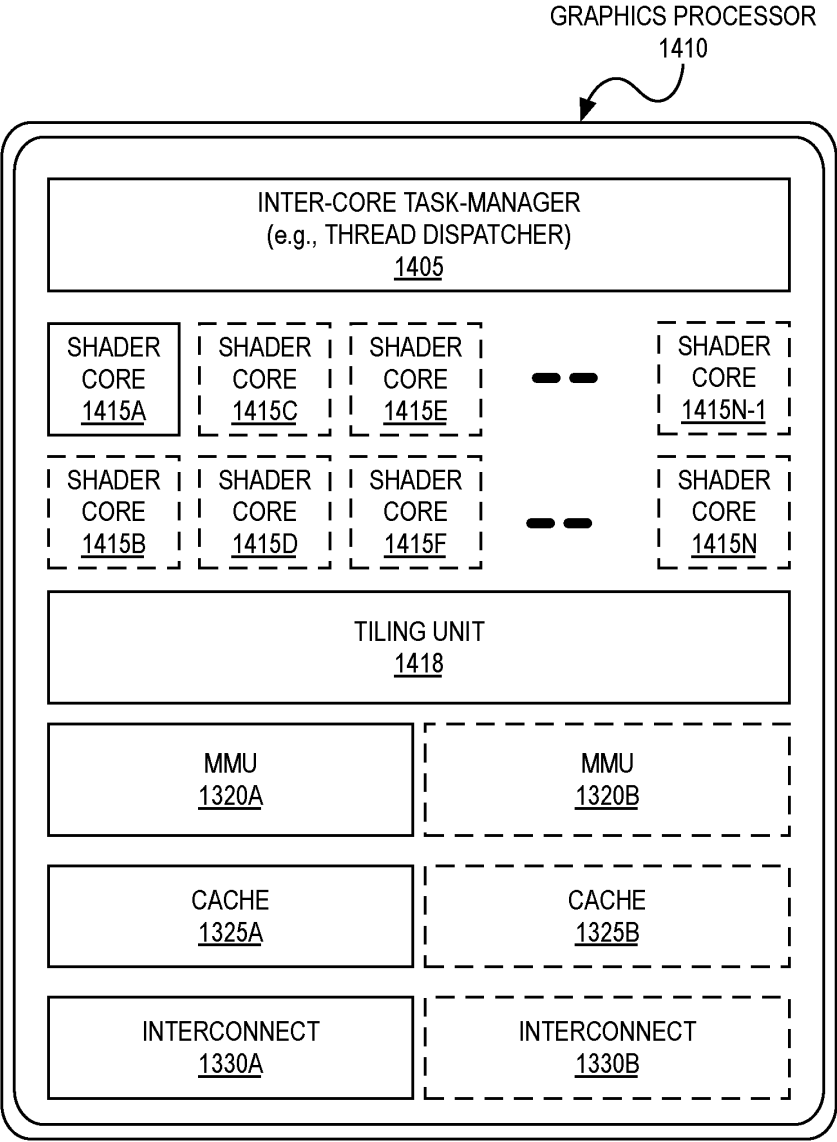


FIG. 14

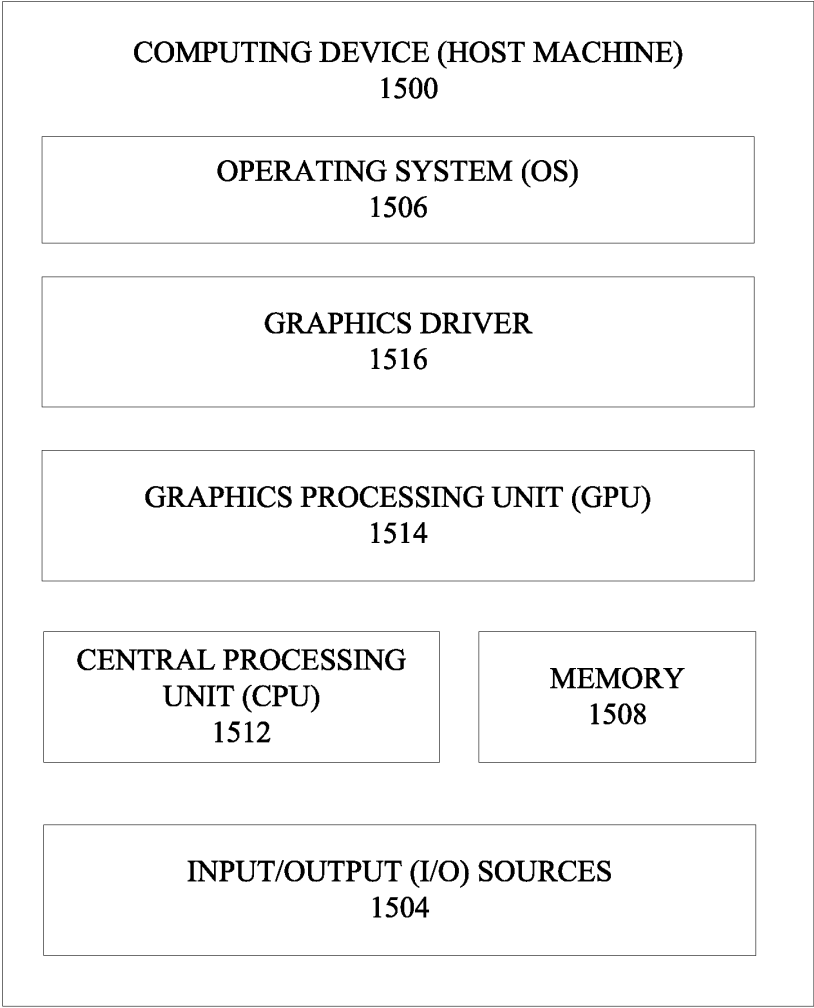


FIG. 15

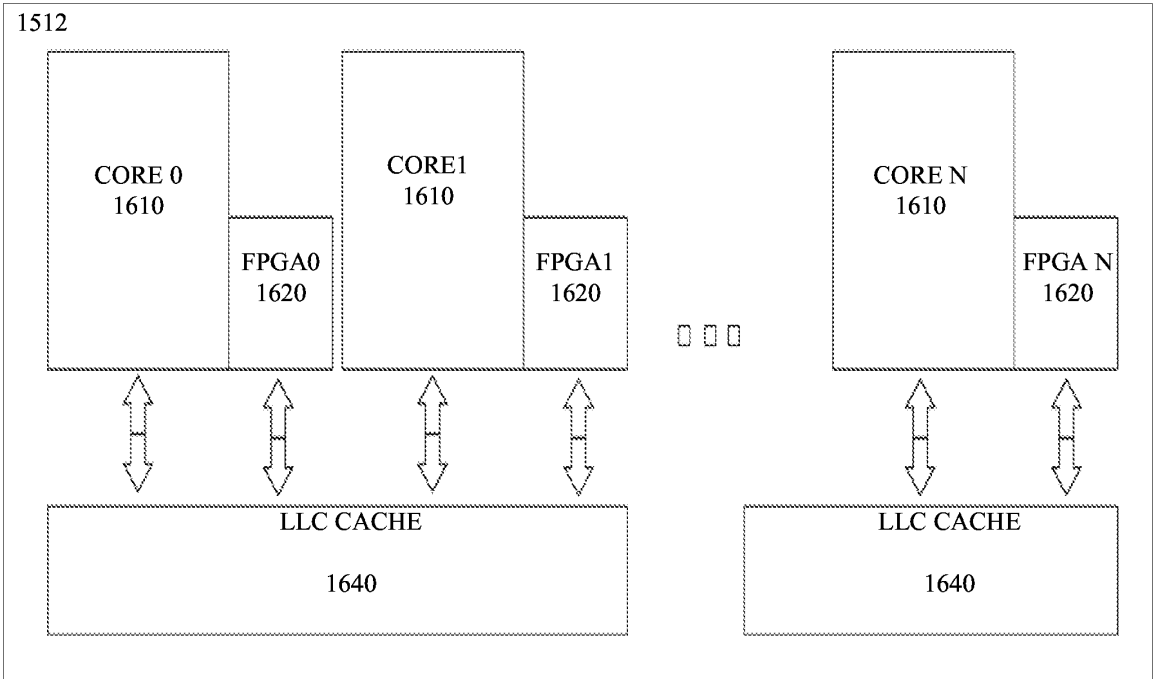


FIG. 16A

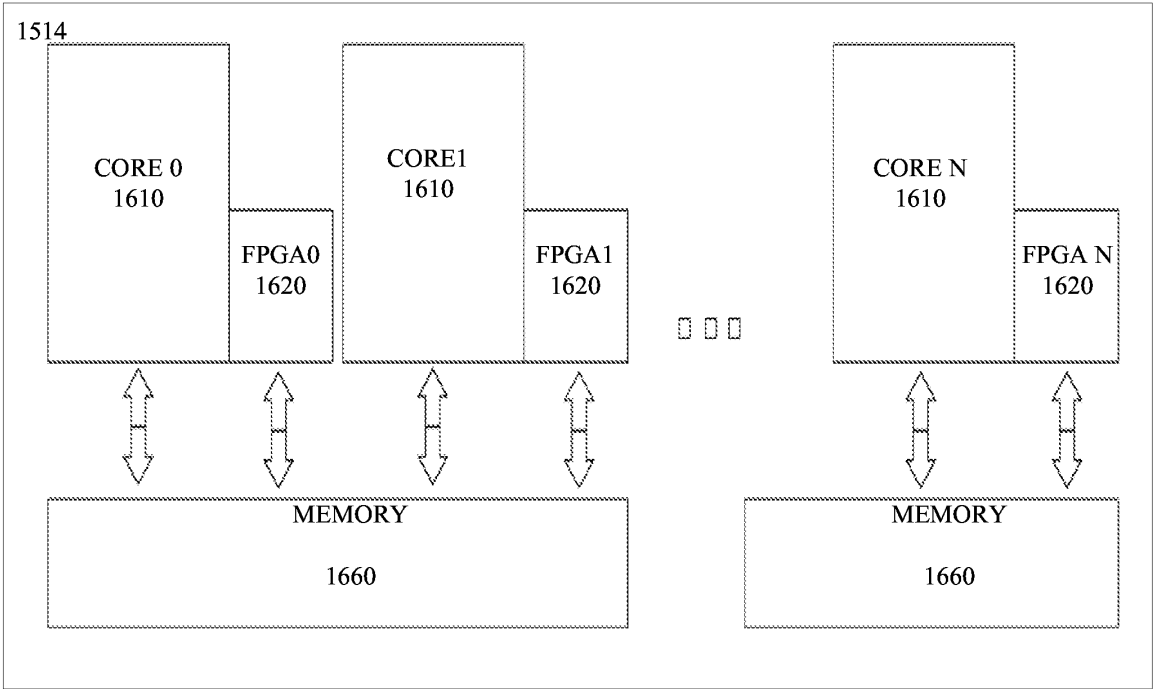


FIG. 16B

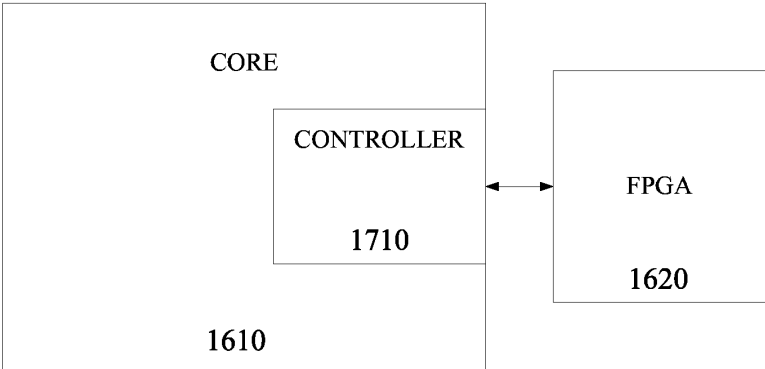


FIG. 17A

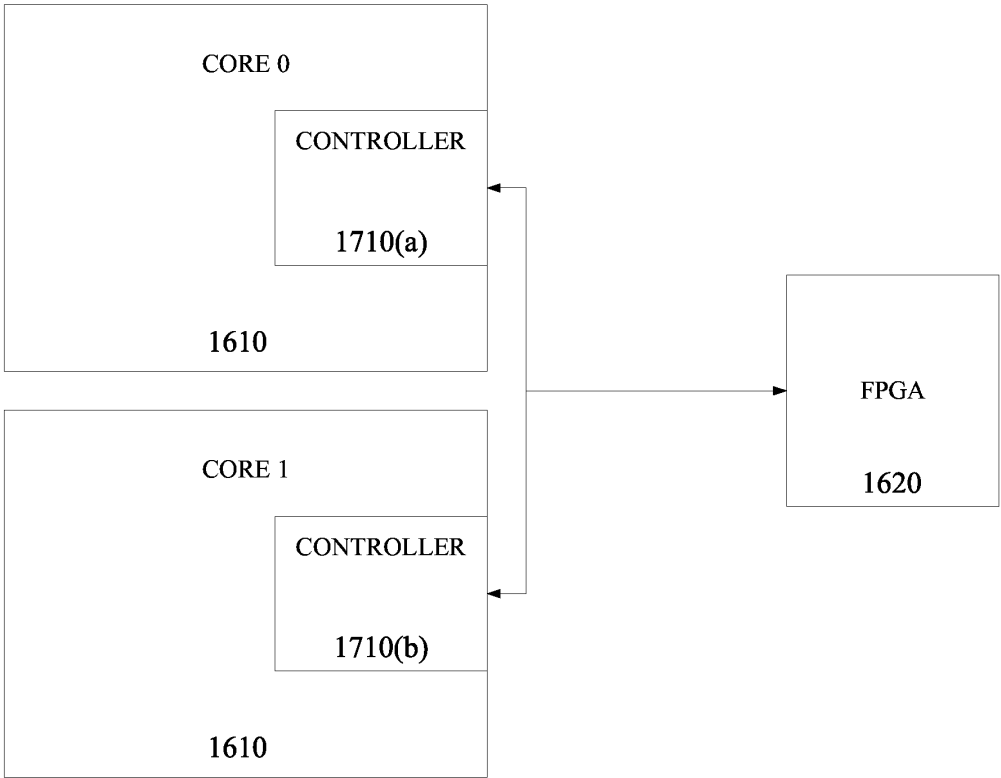


FIG. 17B