$$H = \begin{bmatrix} 0 & 1 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 1 & 1 \\ 0 & 0 & 1 & 0 & 0 & 1 \end{bmatrix}$$

FIG. 1

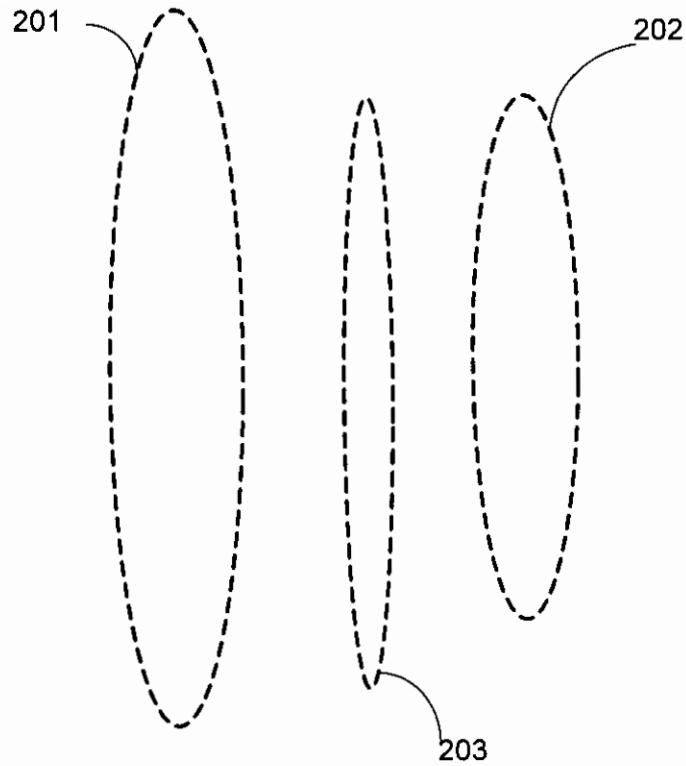


FIG. 2

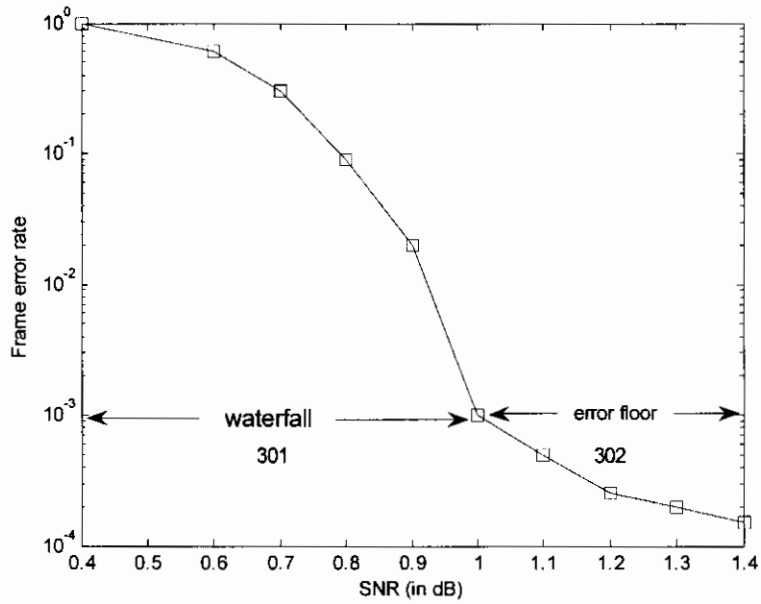

Frame error rate
10^{0}
10^{-1}
10^{-2}
10^{-3}
10^{-4}
waterfall
301
error floor
302
0.4
0.5
0.6
0.7
0.8
0.9
1
1.1
1.2
1.3
1.4
SNR (in dB)

FIG. 3

FIG. 4

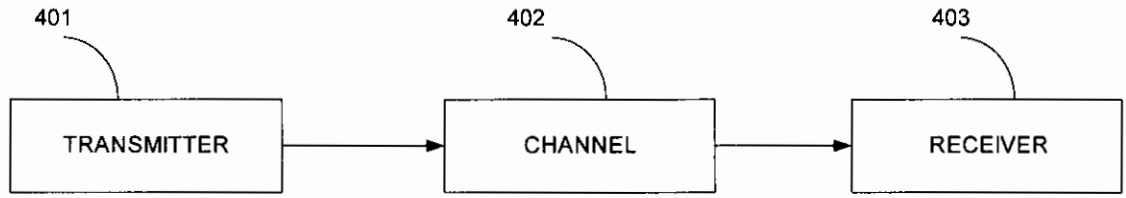
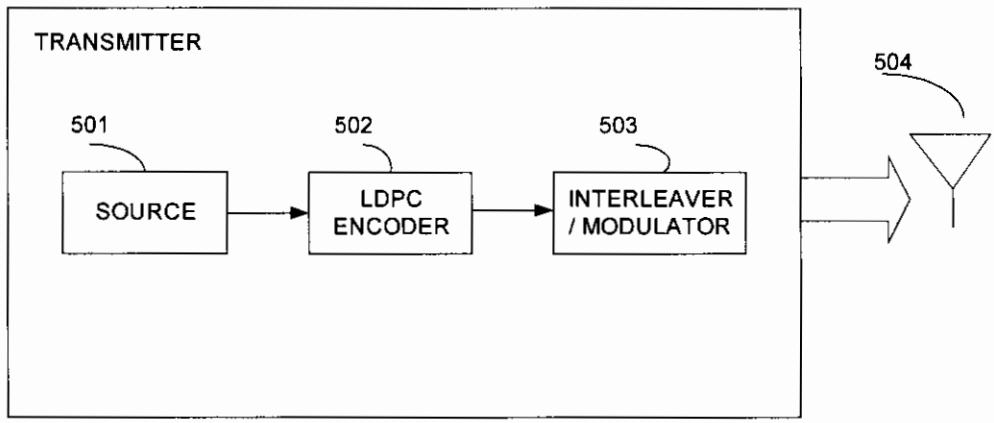


FIG. 5


TRANSMITTER
501
SOURCE
502
LDPC
ENCODER
503
INTERLEAVER
/ MODULATOR
504

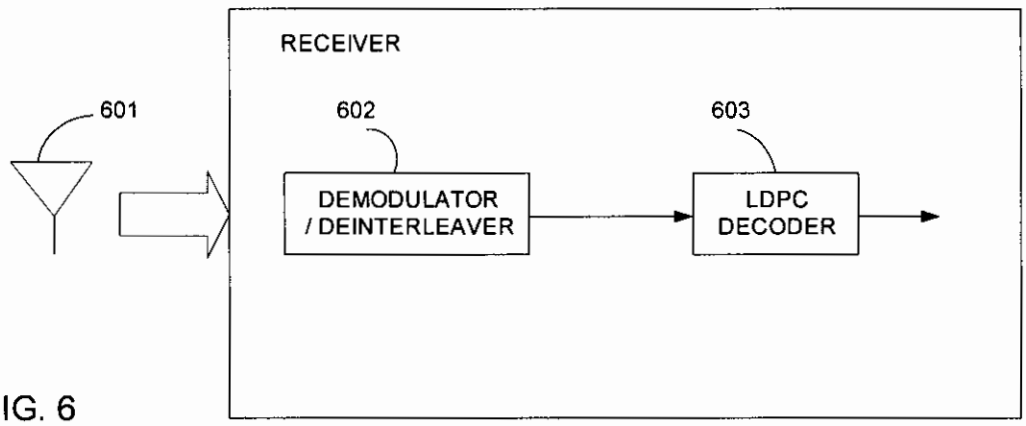

RECEIVER
601
602
DEMODULATOR
/ DEINTERLEAVER
603
LDPC
DECODER

FIG. 6

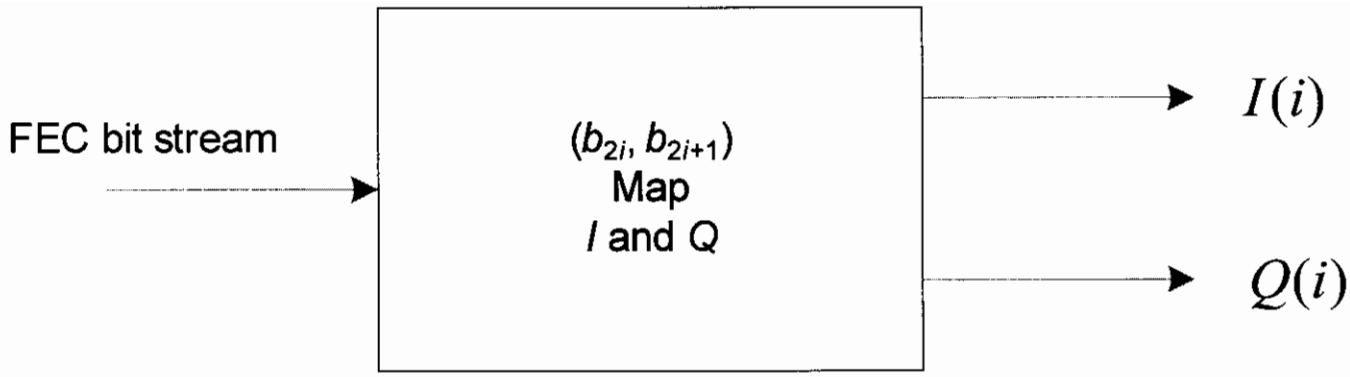


FIG. 7

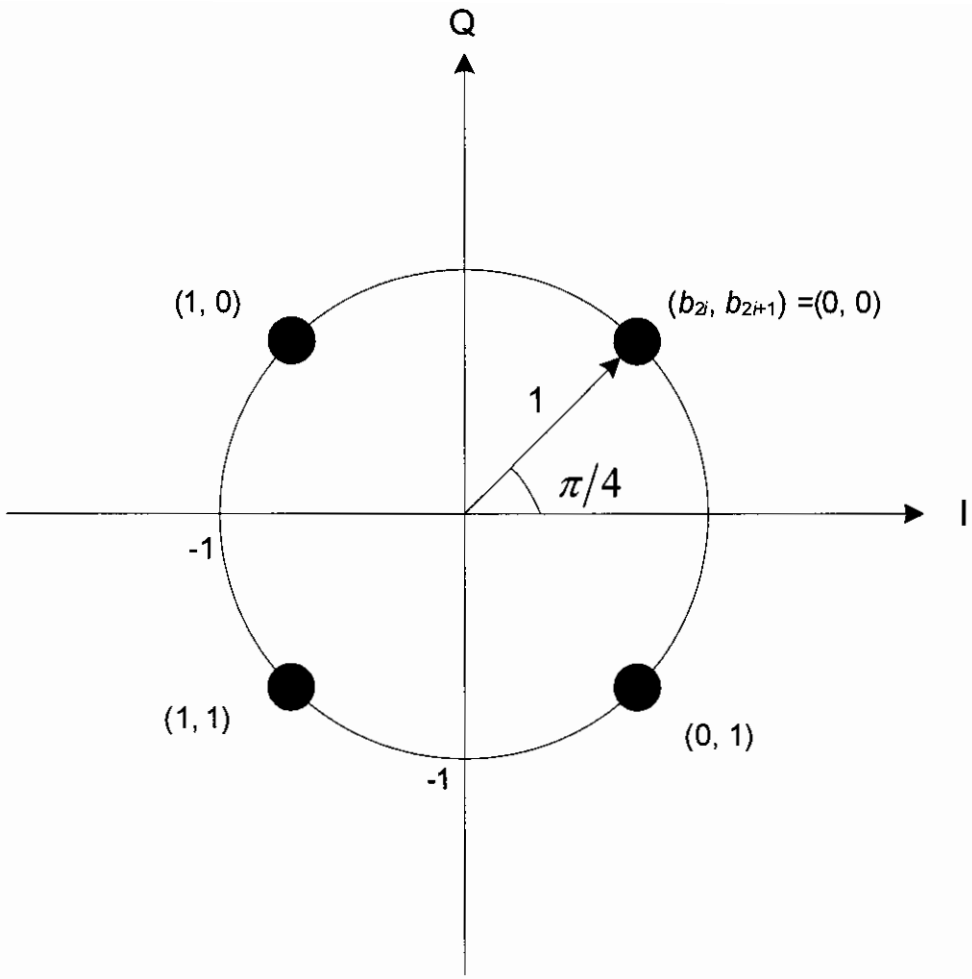


FIG. 8

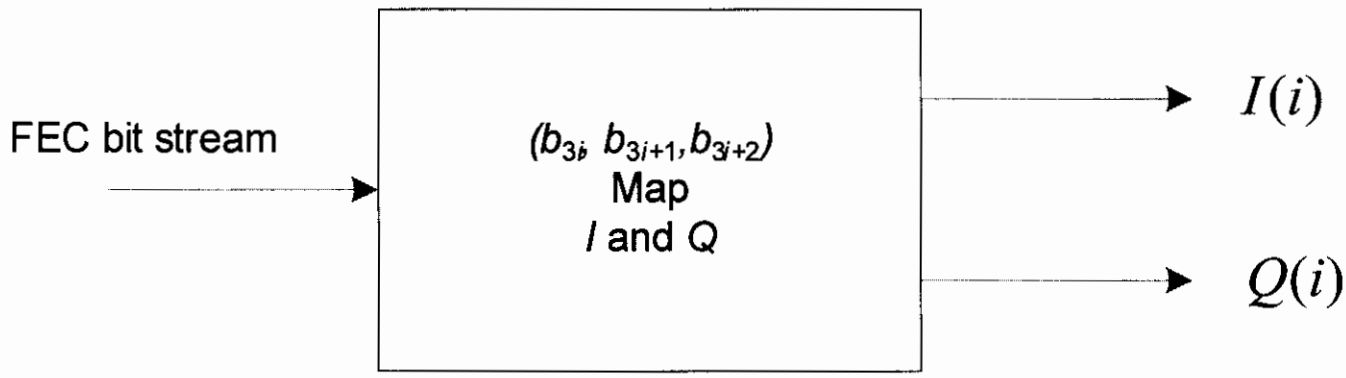


FIG. 9

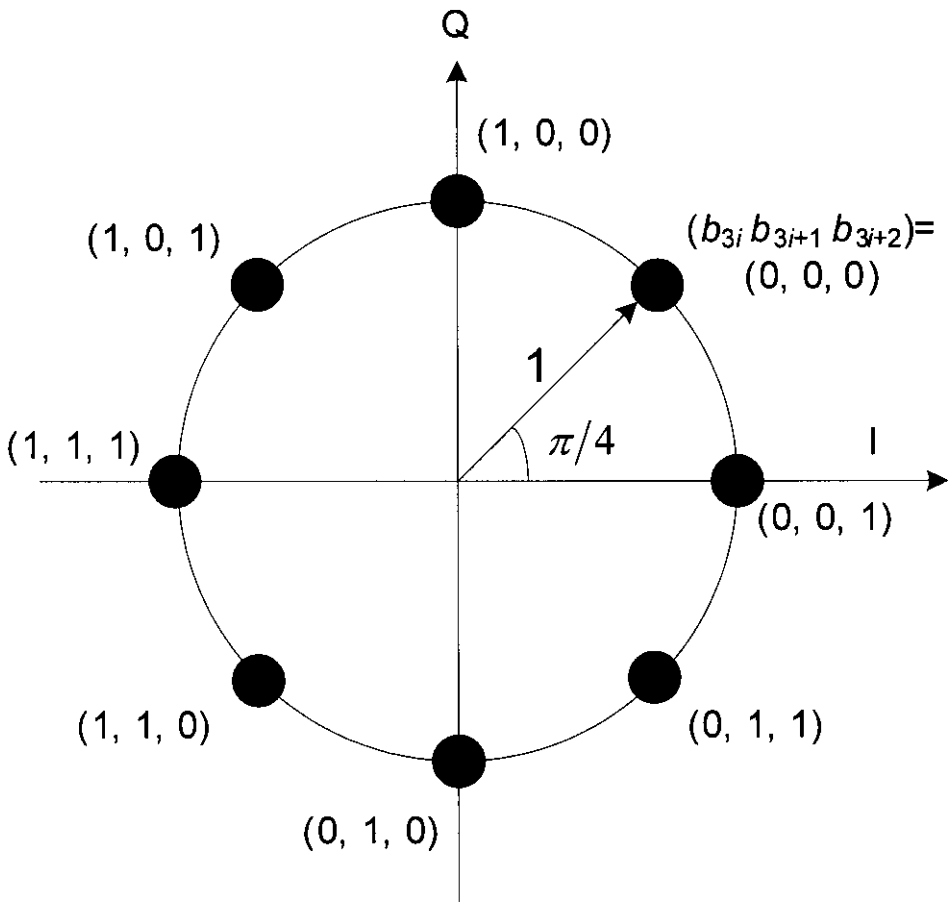


FIG. 10