

ABSTRACT OF THE DISCLOSURE

TITLE OF THE INVENTION

CURRENT COMMAND TYPE PWM INVERTER APPARATUS WITH
LOGIC CIRCUIT FOR GENERATING SWITCHING COMMAND SIGNALS AT
PERIODICAL TIMINGS

In a current command type PWM inverter apparatus including first to sixth main circuit switching power devices for supplying line currents to a three-phase motor, at a periodical state updating first timing, and at least one second timing when any one of first, second and third comparison signals between respective detected line currents and respective line current command signals change after the state updating first timing, a logic circuit generates and outputs first to sixth switching command signals to the main circuit switching power devices so as to minimize errors between respective line current command signals and respective detected line currents.

(Figure 1)