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- (71) Applicant (for US only): **INTEL CORPORATION**
[US/US]; 2200 Mission College Boulevard, Santa Clara,
CA 95052 (US).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **TU, Steven, J.**
[US/US]; 16815 S. 15th Avenue, Phoenix, AZ 85045 (US).
EDIRISOORIYA, Samantha, J. [LK/US]; 640 E. Vinedo
Lane, Tempe, AZ 85284 (US). **JAMIL, Sujat** [BD/US];
1828 W. Enfield Way, Chandler, AZ 85248 (US). **MINER,**
David, E. [US/US]; 1561 S. Karen Dr., Chandler, AZ
85248 (US). **O'BLENESS, R. Frank** [US/US]; 416 E.
Stacy Lane, Tempe, AZ 85284 (US). **NGUYEN, Hang, T.**
[US/US]; 8613 S. Dorsey Lane, Tempe, AZ 85284 (US).
- (74) Agent: **VINCENT, Lester, J.**; Blakely, Sokoloff, Taylor
& Zafman LLP, 12400 Wilshire Boulevard, 7th Floor, Los
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(54) Title: DIRECT PROCESSOR CACHE ACCESS WITHIN A SYSTEM HAVING A COHERENT MULTI-PROCESSOR PROTOCOL

(57) Abstract: Methods and apparatuses for pushing data from a system agent to a cache memory.

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