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(54) Title: OPTIMIZED EXECUTION OF STATICALLY STRONGLY PREDICTED BRANCH INSTRUCTIONS

(57) Abstract

A microprocessor is disclosed. The microprocessor includes a branch prediction table (BPT) that has at least one branch entry. The at least one branch entry includes a prediction field to indicate whether a branch is predicted taken. The at least one branch entry also includes a history register that stores history information. Moreover, the BPT includes a prediction update logic that updates the prediction field and the history register except when a branch is strongly predicted statically.

