

SCAN VELOCITY MODULATION CIRCUIT

ABSTRACT

A television receiver includes a scan velocity modulation circuit (42, 44, L1). An amplitude correction circuit (R1-R8, C1, C2, Q1) couples vertical and horizontal parabola signals to the scan velocity modulation circuit so as to vary scan velocity modulation with displacement of beam position from the center of the screen. The parabola signals may be derived from a pincushion correction circuit (24) of the television receiver, for example, from a diode modulator. The position-indicating parabolas may control amplifier (44) gain within the scan velocity modulation circuit, output level limiting, or the amplitude of the differentiated luminance signal (YSVM) applied to the amplifier to provide scan velocity modulation reduction away from screen center.

FIGURE 1