

WHAT IS CLAIMED IS:

1. An apparatus provided in a communications device, said apparatus comprising at least one processor and at least one memory including computer code
5 for one or more programs, the at least one memory and the computer code configured, with the at least one processor, to cause the apparatus at least to:
determine that at least one of a plurality of logical channels configured for packet duplication is to be deactivated; and
determine an allocation of one or more cells associated with the at least one
10 deactivated logical channel to one or more active logical channels of said plurality of logical channels configured for packet duplication.
2. The apparatus as claimed in claim 1, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause
15 the apparatus at least to determine the allocation using one or more preconfigured rules.
3. The apparatus as claimed in any preceding claim, wherein the at least one memory and the computer program code are configured to, with the at least one
20 processor, cause the apparatus at least to receive the one or more preconfigured rules from a base station.
4. The apparatus as claimed in any preceding claim, wherein the at least one memory and the computer program code are configured to, with the at least one
25 processor, cause the apparatus at least to allocate the one or more cells associated with the deactivated logical channel to a primary logical channel of the plurality of logical channels configured for packet duplication.
5. The apparatus as claimed in any of claims 1 to 3, wherein the at least one
30 memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more cells associated with the deactivated logical channel to one or more active logical channels in dependence on a logical channel index of at least one of said plurality of logical channels configured for packet duplication.

6. The apparatus as claimed in claim in any of claims 1 to 3 or 5, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more cells associated with the deactivated logical channel to:

5 an active logical channel with a highest logical channel index;
an active logical channel with a lowest logical channel index;
an active logical channel with a next highest logical channel index to that of the deactivated logical channel; or

10 an active logical channel with a next lowest logical channel index to that of the deactivated logical channel.

7. The apparatus as claimed in any of claims 1 to 3, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more cells associated with the deactivated logical channel to one or more active logical channels in dependence on activity associated with one or more active logical channels of said plurality of logical channels configured for packet duplication.

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8. The apparatus as claimed in any of claims 1 to 3 or 7, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more or more cells associated with the deactivated logical channel to:

20 an active logical channel associated with a most queued buffer;
an active logical channel associated with a least queued buffer;
an active logical channel associated with a highest sequence number of a packet; or

25 an active logical channel associated with a lowest sequence number of a packet.

9. The apparatus as claimed in any of claims 1 to 3, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more or more cells

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associated with the deactivated logical channel to one or more active logical channels in dependence on a predefined mapping between said logical channels.

10. The apparatus as claimed in any of claims 1 to 3, wherein the at least one
5 memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more or more cells associated with the deactivated logical channel to one or more active logical channels in dependence on a number of cells associated with one or more active logical channels of said plurality of logical channels configured for packet duplication.

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11. The apparatus as claimed in any of claims 1 to 3 or 10, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more or more cells associated with the deactivated logical channel to:

15 an active logical channel associated with highest number of allowed cells; or
an active logical channel associated with a lowest number of allowed cells.

12. The apparatus as claimed in any of claims 1 to 3, wherein the at least one memory and the computer program code are configured to, with the at least one
20 processor, cause the apparatus at least to allocate the one or more cells associated with the deactivated logical channel to one or more active logical channels in dependence on a number of carriers in a given band associated with one or more active logical channels of said plurality of logical channels configured for packet duplication.

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13. The apparatus as claimed in any of claims 1 to 3 or 12, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more or more cells associated with the deactivated logical channel to:

30 an active logical channel associated with most number of carriers; or
an active logical channel associated with a lowest number of carriers.

14. The apparatus as claimed in any of claims 1 to 3, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more or more cells associated with the deactivated logical channel to one or more active logical channels in dependence one or more of the following: the logical channel priority; the prioritized bit rate; and the channel access priority class.

15. The apparatus as claimed in any preceding claim, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to allocate the one or more cells associated with the deactivated logical channel to an active logical channel associated with a same cell group as the deactivated logical channel.

16. The apparatus as claimed in any preceding claim, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to receive information indicating that one or more of the deactivated logical channels is to be activated and reallocating the one or more cells previously associated with that respective logical channel to that respective logical channel.

17. The apparatus as claimed in any preceding claim, wherein the at least one memory and the computer program code are configured to, with the at least one processor, cause the apparatus at least to receive information indicating that at least one of a plurality of logical channels configured for packet duplication is to be deactivated and in response thereto determine that the respective logical channel is to be deactivated.

18. An apparatus provided in a communications device, said apparatus comprising means for:

determining that at least one of a plurality of logical channels configured for packet duplication is to be deactivated; and

determining an allocation of one or more cells associated with the at least one deactivated logical channel to one or more active logical channels of said plurality of logical channels configured for packet duplication.

19. A method performed by an apparatus provided in a communications device, said method comprising:

5 determining that at least one of a plurality of logical channels configured for packet duplication is to be deactivated; and

determining an allocation of one or more cells associated with the at least one deactivated logical channel to one or more active logical channels of said plurality of logical channels configured for packet duplication.

10 20. The method as claimed in claim 19, comprising receiving the one or more preconfigured rules from a base station.

21. The method as claimed in claim 19 or 20, comprising allocating the one or more cells associated with the deactivated logical channel to a primary logical
15 channel of the plurality of logical channels configured for packet duplication.

22. The method as claimed in claim 19 or 20, comprising allocating the one or more cells associated with the deactivated logical channel to one or more active logical channels in dependence on a logical channel index of at least one of said
20 plurality of logical channels configured for packet duplication.

23. The method as claimed in claim 19 or 20, comprising allocating the one or more cells associated with the deactivated logical channel to one or more active logical channels in dependence on activity associated with one or more active logical
25 channels of said plurality of logical channels configured for packet duplication.

24. The method as claimed in claim 19 or 20, comprising allocating the one or more or more cells associated with the deactivated logical channel to one or more active logical channels in dependence on a predefined mapping between said logical
30 channels.

25. The method as claimed in claim 19 or 20, comprising allocating the one or more or more cells associated with the deactivated logical channel to one or more active logical channels in dependence on a number of cells associated with one or

more active logical channels of said plurality of logical channels configured for packet duplication.

26. The method as claimed in claim 19 or 20, comprising allocating the one or
5 more cells associated with the deactivated logical channel to one or more active logical channels in dependence on a number of carriers in a given band associated with one or more active logical channels of said plurality of logical channels configured for packet duplication.

10 27. The method as claimed in claim 19 or 20, comprising allocating the one or more or more cells associated with the deactivated logical channel to one or more active logical channels in dependence one or more of the following: the logical channel priority; the prioritized bit rate; and the channel access priority class.

15 28. The method as claimed in any of claims 19 to 27, comprising allocating the one or more cells associated with the deactivated logical channel to an active logical channel associated with a same cell group as the deactivated logical channel.

20 29. The method as claimed in any of claims 19 to 28, comprising receiving information indicating that one or more of the deactivated logical channels is to be activated and reallocating the one or more cells previously associated with that respective logical channel to that respective logical channel.

25 30. The method as claimed in any of claims 19 to 29, comprising receiving information indicating that at least one of a plurality of logical channels configured for packet duplication is to be deactivated and in response thereto determine that the respective logical channel is to be deactivated.

30 31. A non-transitory computer readable medium comprising program instructions stored thereon which when run on at least one processor cause performing of the method of:

determining that at least one of a plurality of logical channels configured for packet duplication is to be deactivated; and

determining an allocation of one or more cells associated with the at least one deactivated logical channel to one or more active logical channels of said plurality of logical channels configured for packet duplication.