



(51) **International Patent Classification:**
H03K 5/135 (2006.01) *G06F 1/08* (2006.01)

(21) **International Application Number:**
PCT/GB2012/050110

(22) **International Filing Date:**
19 January 2012 (19.01.2012)

(25) **Filing Language:** English

(26) **Publication Language:** English

(30) **Priority Data:**
1100986.7 20 January 2011 (20.01.2011) GB

(71) **Applicant** (*for all designated States except US*): **XNORD-IC SEMICONDUCTOR ASA** [NO/NO]; Otto Nielsen veg 12, N-7004 Trondheim (NO).

(71) **Applicant** (*for MG only*): **WILSON, Timothy James** [GB/GB]; Dehns, St. Brides House, 10 Salisbury Square, London EC4Y 8JD (GB).

(72) **Inventors; and**

(75) **Inventors/Applicants** (*for US only*): **BRUSET, Ola** [NO/NO]; Trofetvegen 7, N-7081 Sjetnemarka (NO). **VEDAL, Tor Øyvind** [NO/NO]; Einerveien 23, N-7227 Gimse (NO).

(74) **Agent:** DEHNS; St Bride's House, 10 Salisbury Square, London EC4Y 8JD (GB).

(81) Designated States (*unless otherwise indicated, for every kind of national protection available*): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: LOW-POWER OSCILLATOR

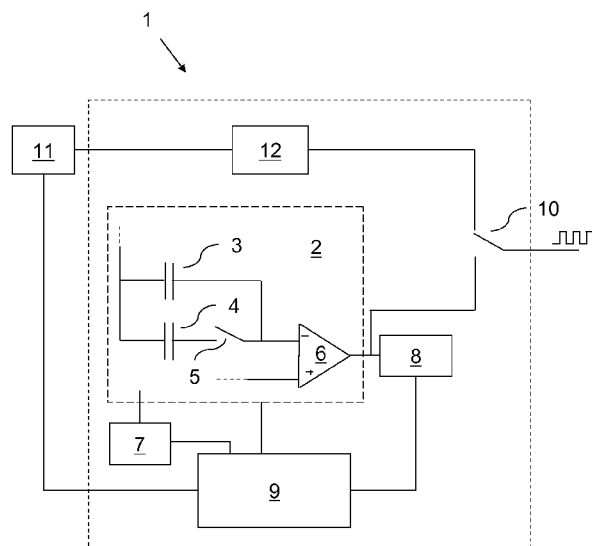


Figure 1

(57) Abstract: An integrated oscillator circuit comprises an oscillator configured to be switched between a first frequency and a second frequency. A switching circuit receives an input representing a target frequency and switches the oscillator between the first and second frequencies at intervals determined by the input, so as to cause the average output frequency of the oscillator to approximate the target frequency.



Published:

- *without international search report and to be republished
upon receipt of that report (Rule 48.2(g))*