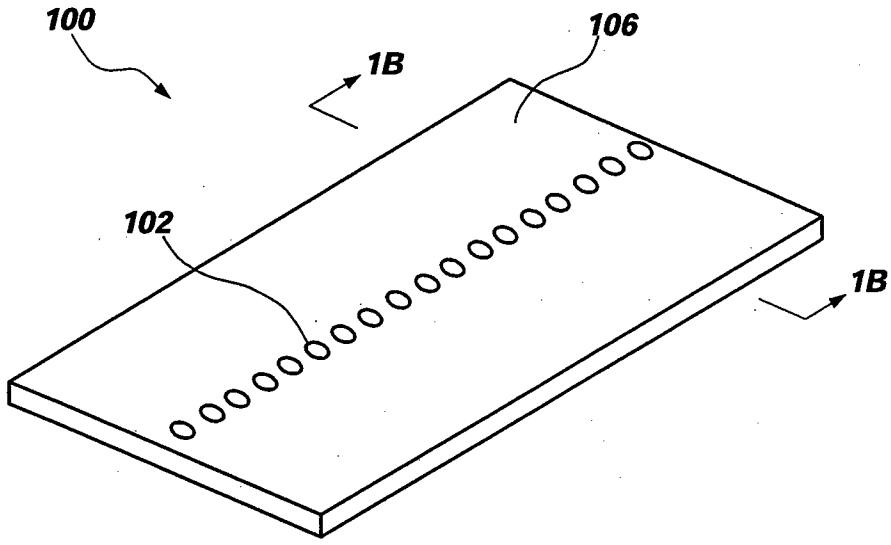


FIG. 1A

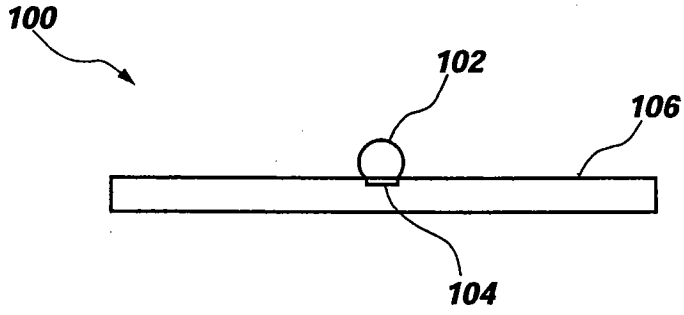


FIG. 1B

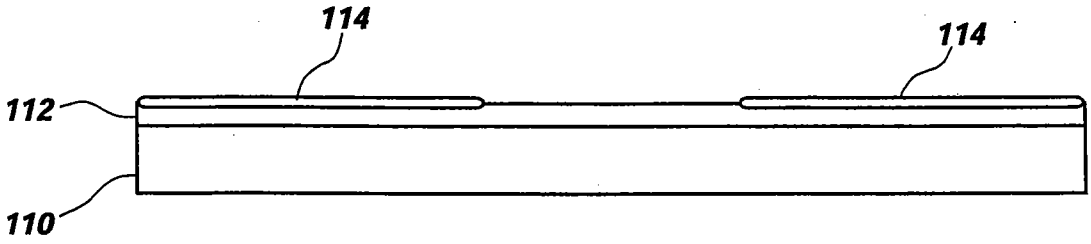


FIG. 2A

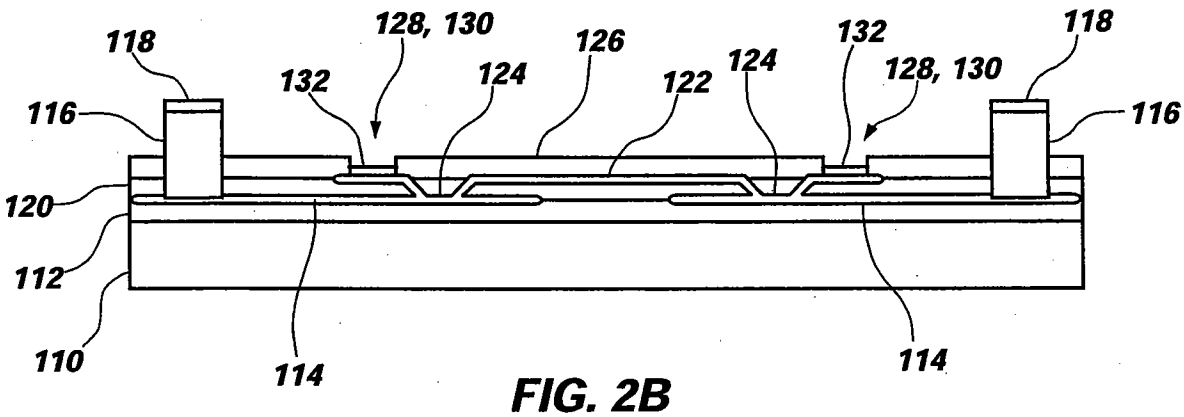


FIG. 2B

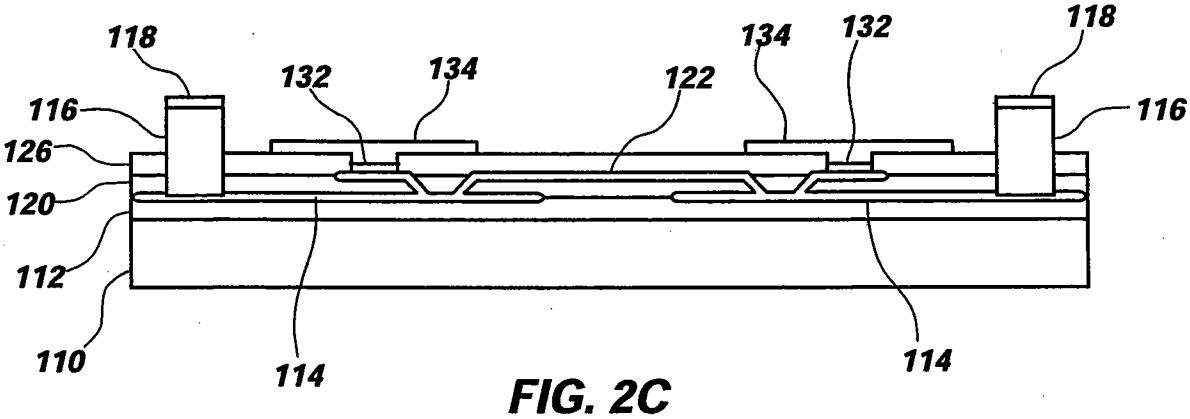


FIG. 2C

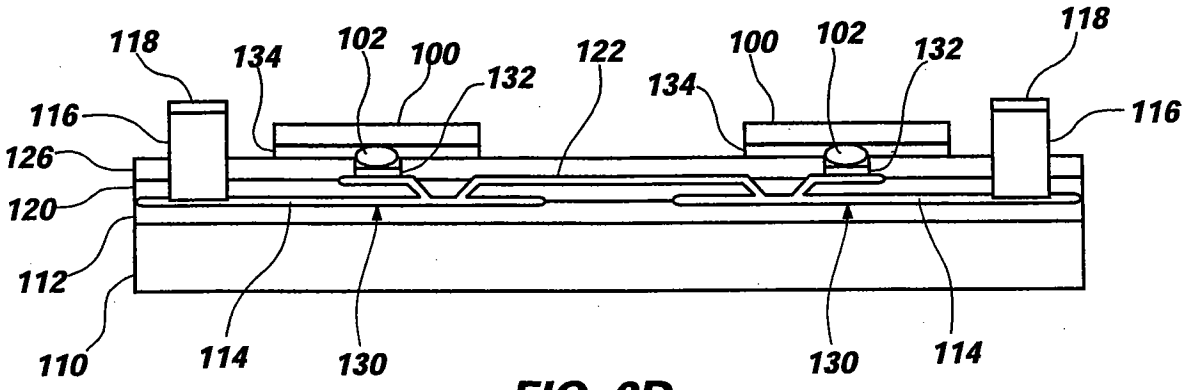


FIG. 2D

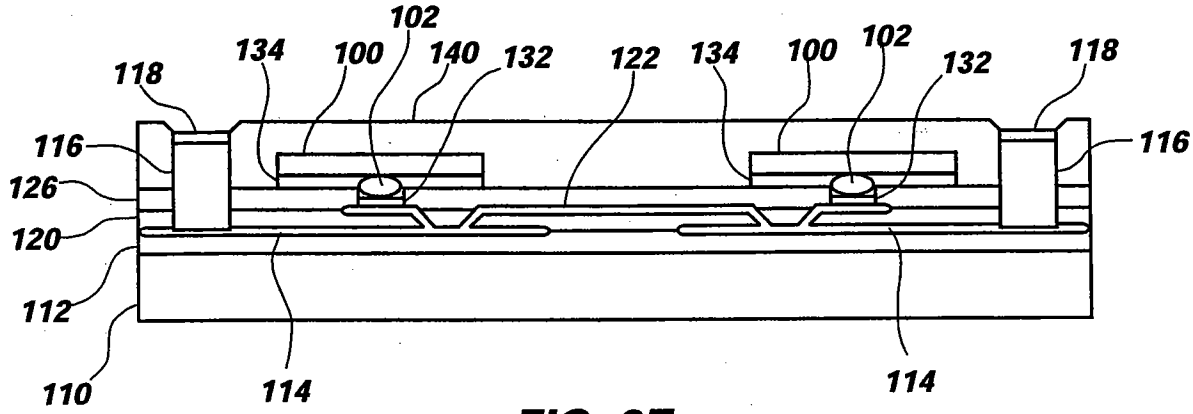


FIG. 2E

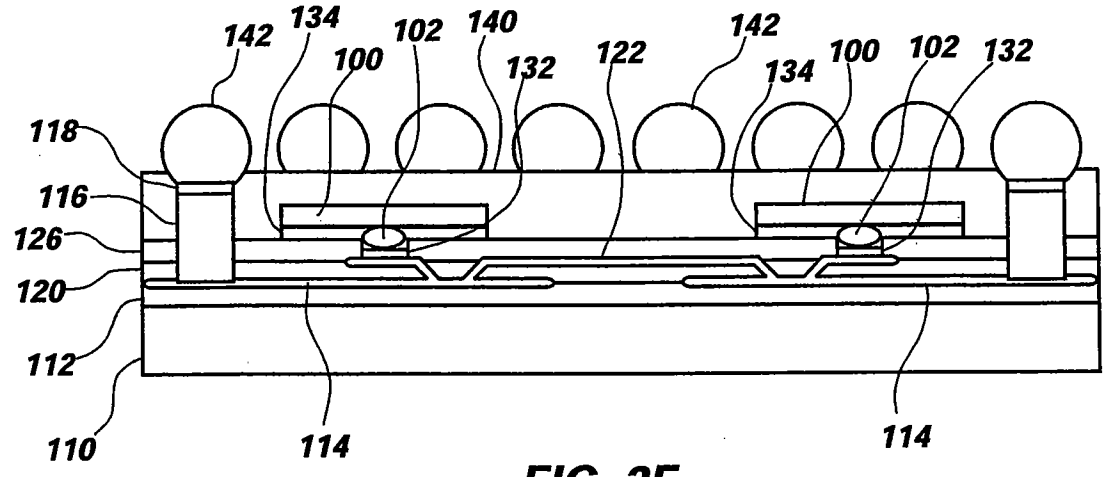


FIG. 2F

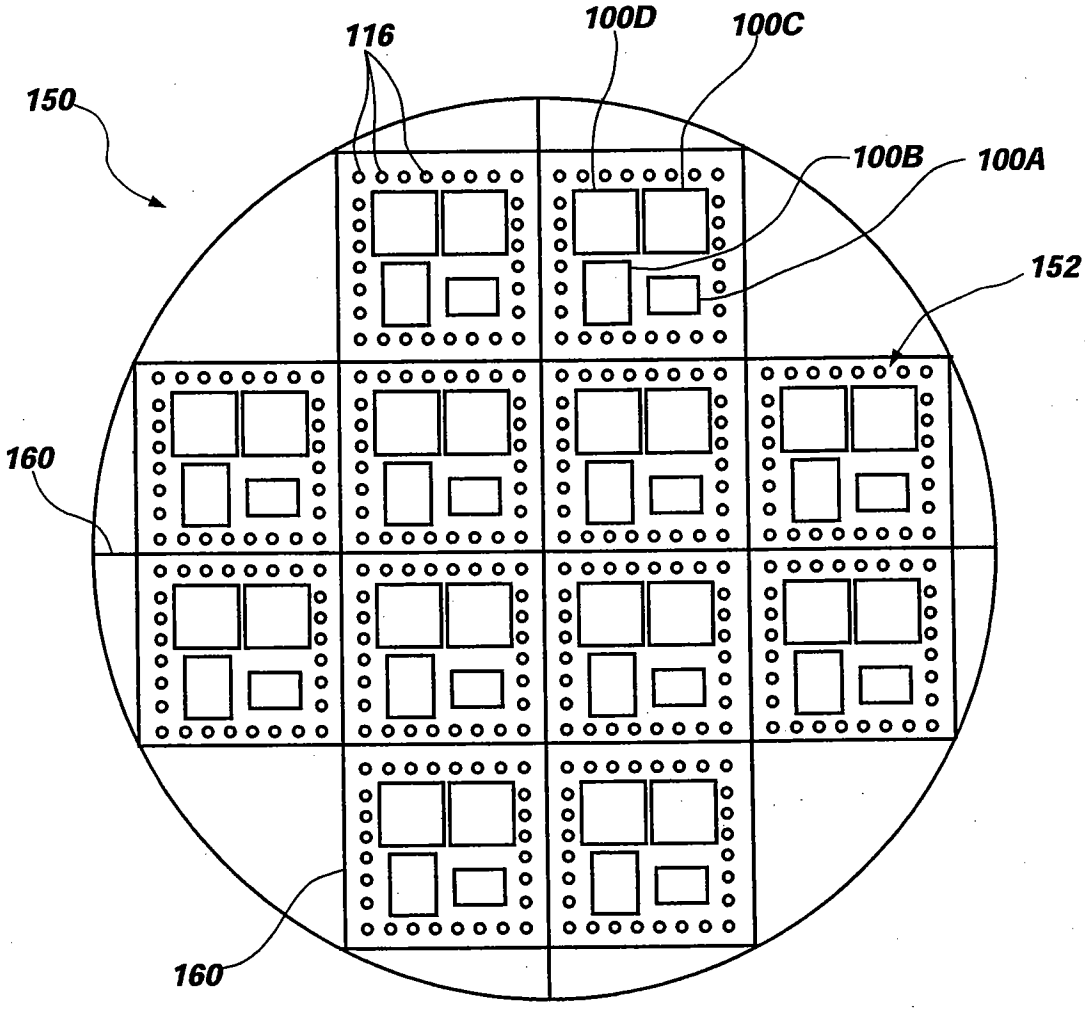


FIG. 3

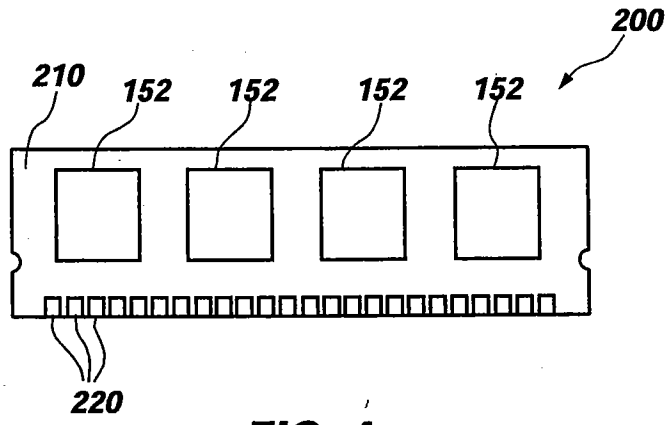

200
210
152
152
152
152
220

FIG. 4

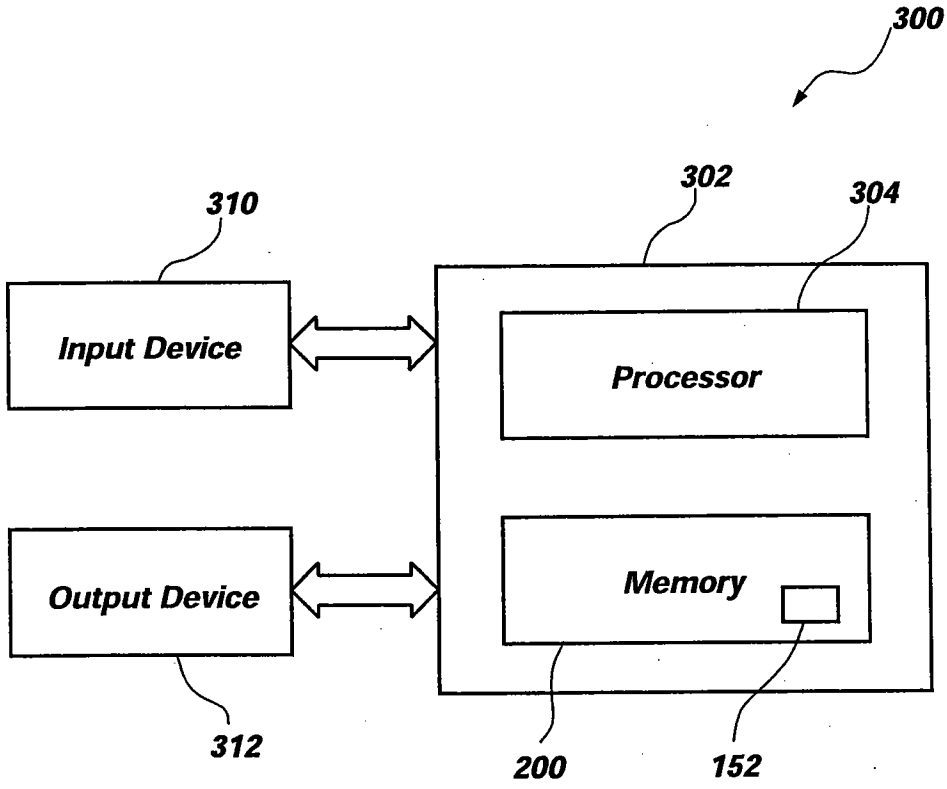

300
310
302
304
Input Device
Processor
Output Device
Memory
312
200
152

FIG. 5