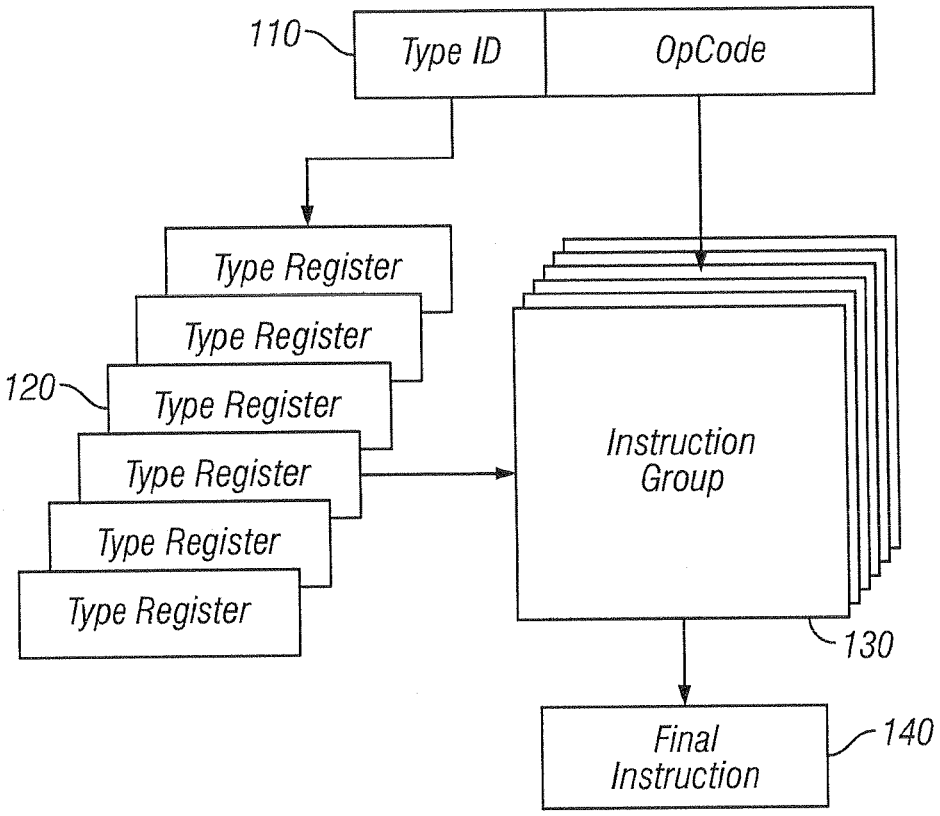

110
Type ID
OpCode
Type Register
Type Register
120
Type Register
Type Register
Type Register
Type Register
Instruction Group
130
Final Instruction
140

FIG. 1

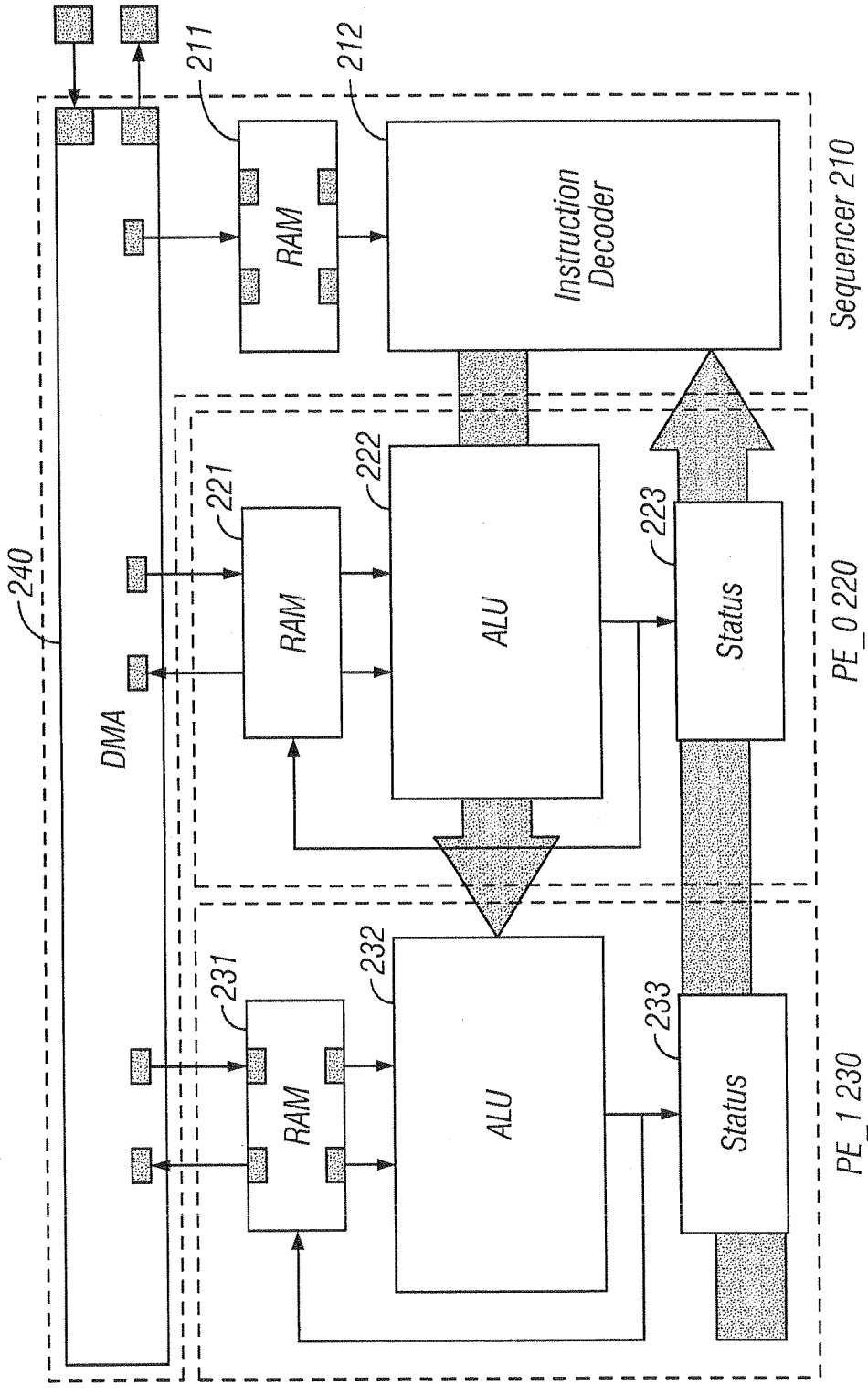

211
212
RAM
Instruction Decoder
Sequencer 210
240
DMA
221
222
223
RAM
ALU
Status
PE_0 220
231
232
233
RAM
ALU
Status
PE_1 230

FIG. 2

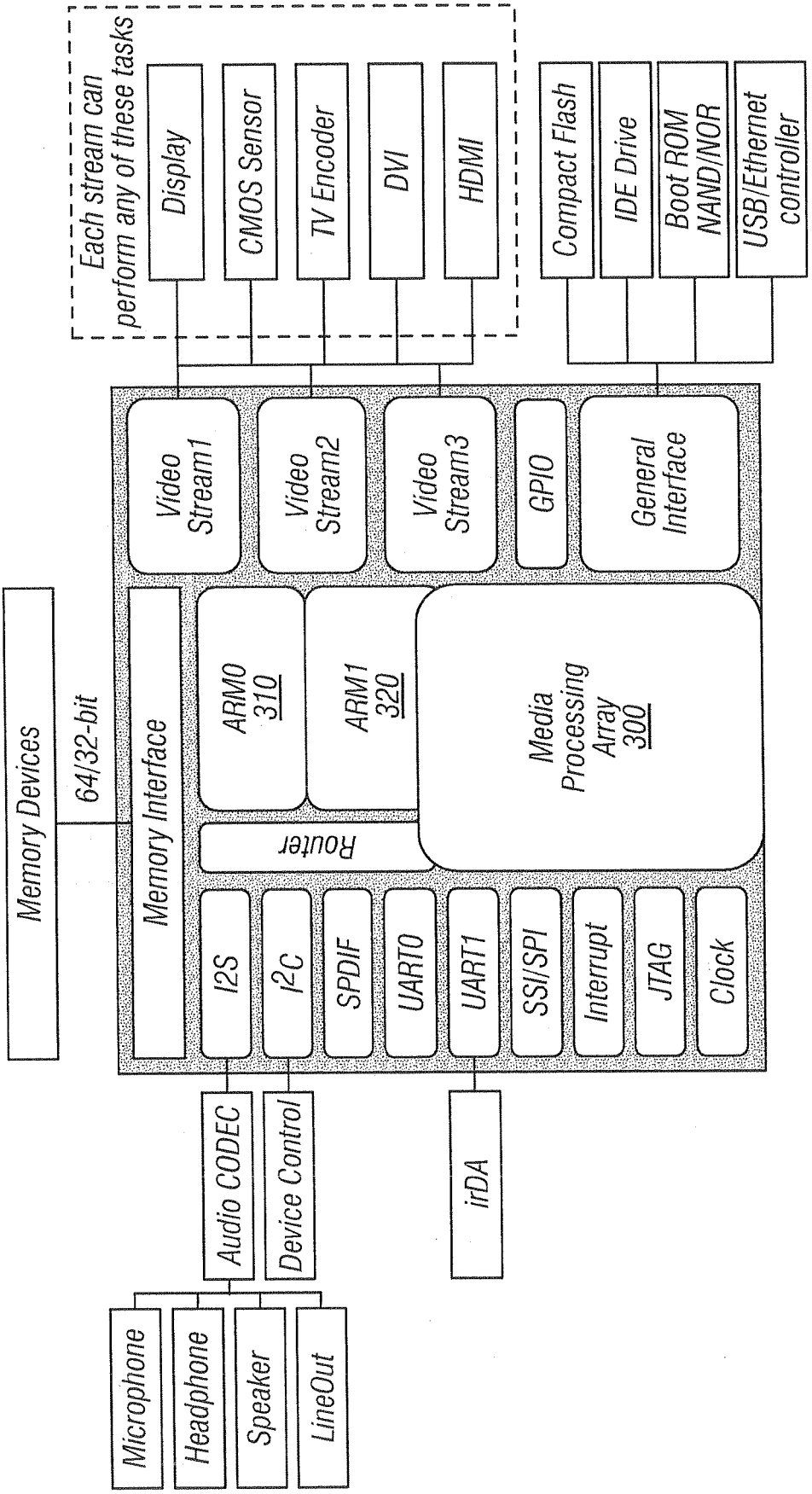

Each stream can perform any of these tasks
Display
CMOS Sensor
TV Encoder
DVI
HDMI
Compact Flash
IDE Drive
Boot ROM NAND/NOR
USB/Ethernet controller
Video Stream1
Video Stream2
Video Stream3
GPIO
General Interface
Memory Devices
64/32-bit
Memory Interface
ARM0 310
ARM1 320
Media Processing Array 300
Router
I2S
I2C
SPDIF
UART0
UART1
SSI/SPI
Interrupt
JTAG
Clock
Audio CODEC
Device Control
irDA
Microphone
Headphone
Speaker
LineOut

FIG. 3