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## ABSTRACT OF THE DISCLOSURE

### ACTIVE MATRIX DISPLAY DEVICE AND ITS DRIVING METHOD

To restrict a potential oscillation in a video line caused by a high speed sampling rate, the active matrix display device is comprised of gate lines X in row, signal lines Y in column and liquid crystal pixels LC of matrix arranged at each of the crossing points of both lines. The V driver 1 scans in sequence each of the gate lines X and selects the liquid crystal pixels LC in one line for every one horizontal period. The H driver 4 performs a sampling of the video signal VSIG for each of the signal lines Y and performs a writing of the video signal VSIG in the liquid crystal pixels LC in one selected line within one horizontal period. The precharging means 5 supplies the predetermined precharging signal VPS to each of the signal lines Y just before writing the video signal VSIG for the liquid crystal pixels LC in one line. With such an arrangement as above, it is possible to reduce the charging or discharging amount in each of the signal lines Y when the video signal VSIG is sampled and further to restrict the potential oscillation in the video line 2.

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