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(21) International Application Number: PCT/JP99/03772 (22) International Filing Date: 13 July 1999 (13.07.99) (30) Priority Data: 10/201591 16 July 1998 (16.07.98) JP (71) Applicant: MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. [JP/JP]; 1006, Oaza Kadoma, Kadoma-shi, Osaka 571-8501 (JP). (72) Inventors: NIO, Yutaka; 5-10-8-1212, Sagisu, Fukushima-ku, Osaka-shi, Osaka 553-0002 (JP). OKUMURA, Naoji; 2-13-55-311, Nishishoji, Minoo-shi, Osaka 562-0003 (JP). TERAJ, Katsumi; 1-10-6, Arakawa, Higashiosaka-shi, Osaka 577-0843 (JP). TANAKA, Kazuto; 2-2-6-604, Niono Hama, Otsu-shi, Shiga 520-0801 (JP). OKAMOTO, Satoshi; 1-4-40-407, Nonakaminami, Yodogawa-ku, Osaka-shi, Osaka 532-0022 (JP). FUJITA, Masaaki; 6-26-5, Magami-cho, Takatsuki-shi, Osaka 569-1121 (JP). MIYATA, Minoru; 11-1-705, Shimizu, Suita-shi, Osaka 565-0805 (JP). (74) Agent: FUKUSHIMA, Yoshito; 8th Floor, Daido Seimei Esaka Daini Bldg., 23-5, Esaka-cho 1-chome, Suita-shi, Osaka 564-0063 (JP).		(81) Designated States: AU, CN, KR, SG, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>With international search report.</i>

(54) Title: VIDEO DISPLAY APPARATUS AND VIDEO DISPLAY METHOD

(57) Abstract

A video signal of an interlaced scanning system having 525 scanning lines and a vertical scanning frequency of 60 Hz is converted to a video signal having 1050 scanning lines and a vertical scanning frequency of 120 Hz, for displaying an image by bidirectional scanning. A vertical synchronizing signal is subjected to offset processing by a $\frac{1}{2}$ horizontal scanning period when an odd field is started, thereby keeping interlaced relation between odd and even fields. The vertical synchronizing signal is subjected to offset processing by a $\frac{1}{2}$ horizontal scanning period every frame, so that the scanning direction for each scanning line is reversed every frame.

