

ASSOCIATIVE MEMORY DEVICE

ABSTRACT OF THE DISCLOSURE

An associative memory support for a data processing system includes an associative memory device containing n -cells. A controller is provided for issuing an instruction to the associative memory device. A clock device outputs a synchronizing clock signal that includes a predetermined number of clock cycles per second, the clock device outputting the synchronizing clock signal to the associative memory device and the controller. The controller globally communicates the instruction to all of the n -cells simultaneously, within one of the clock cycles and the instruction is applied equally to each of the n -cells.