

FIG. 1

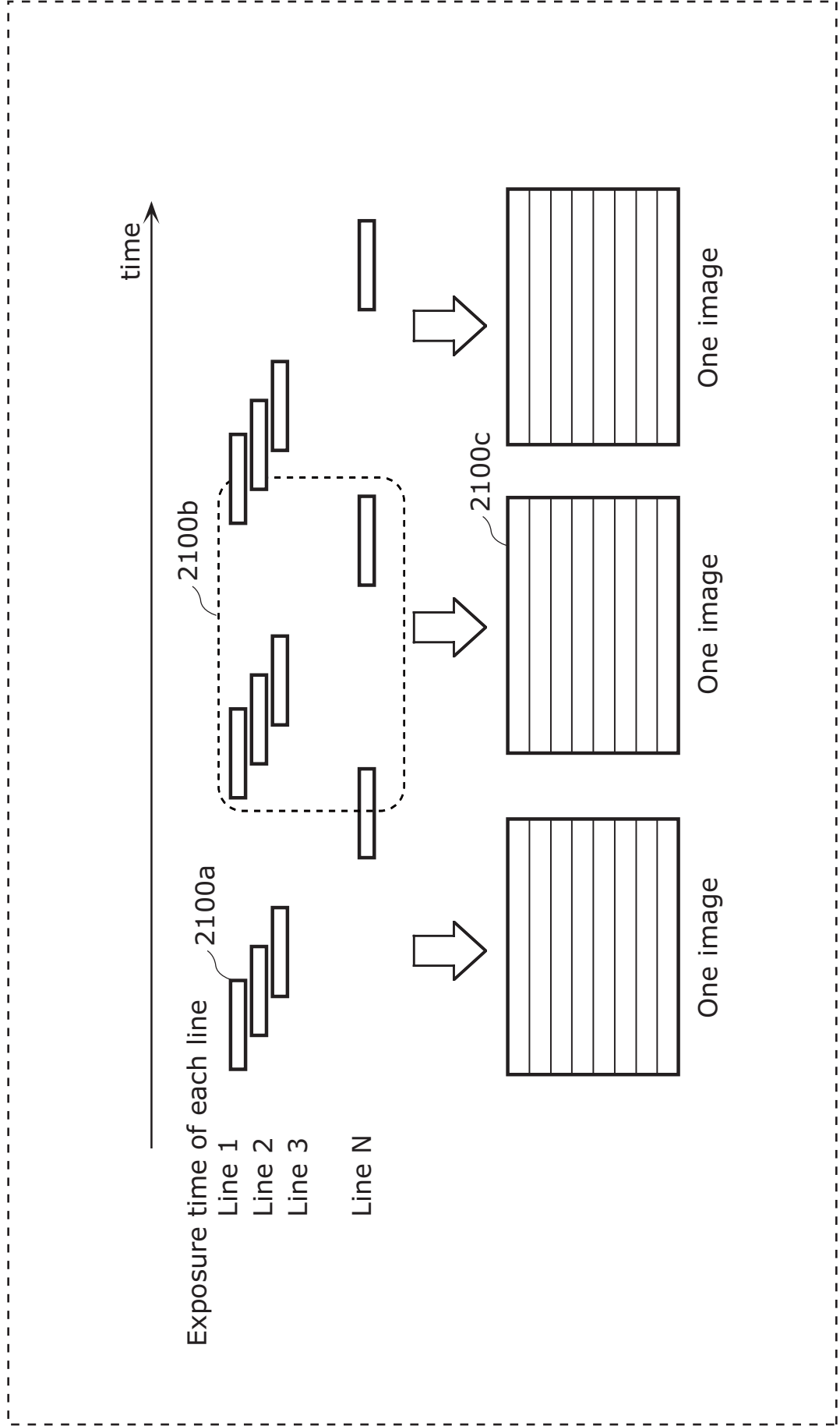


FIG. 2

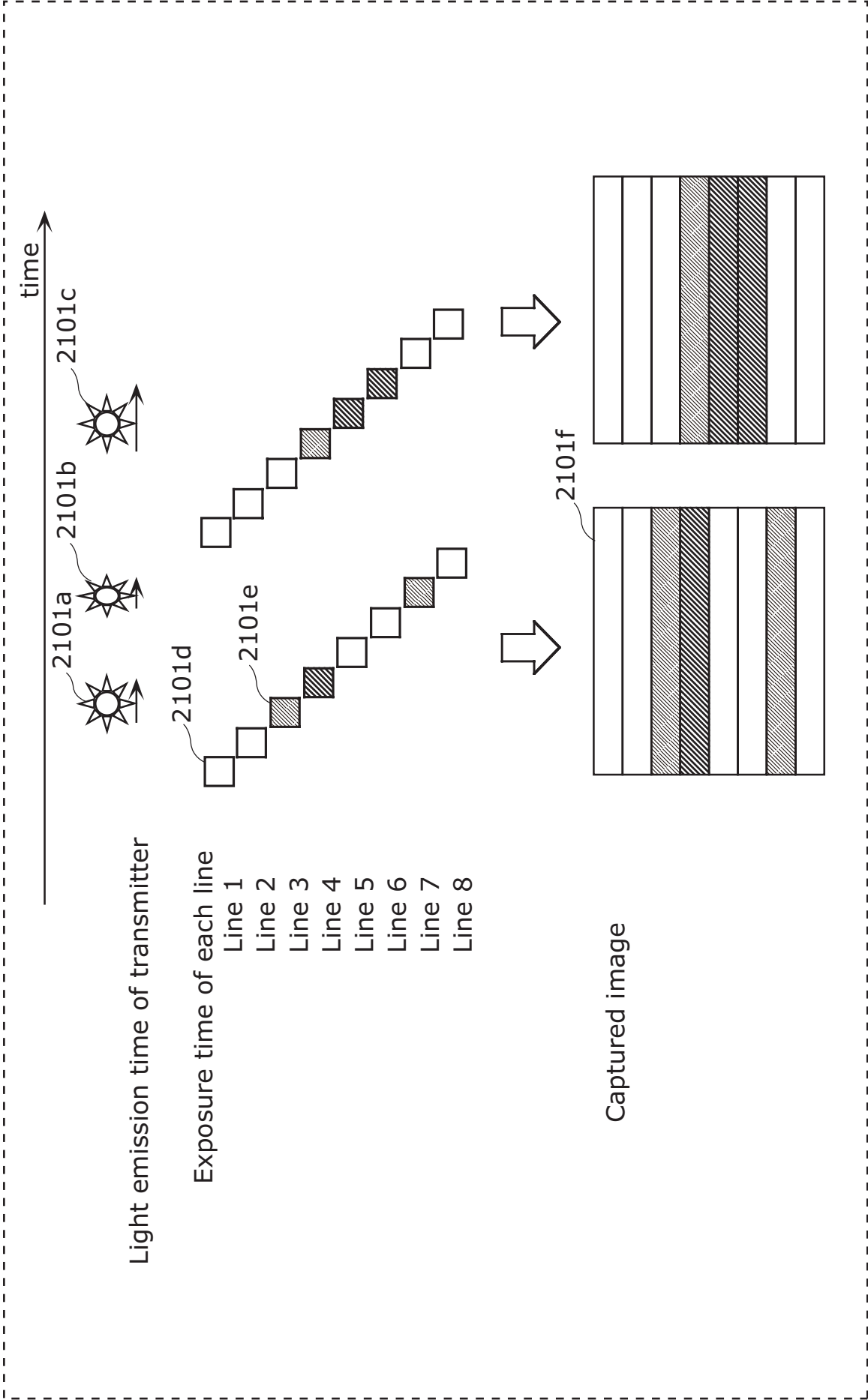


FIG. 3

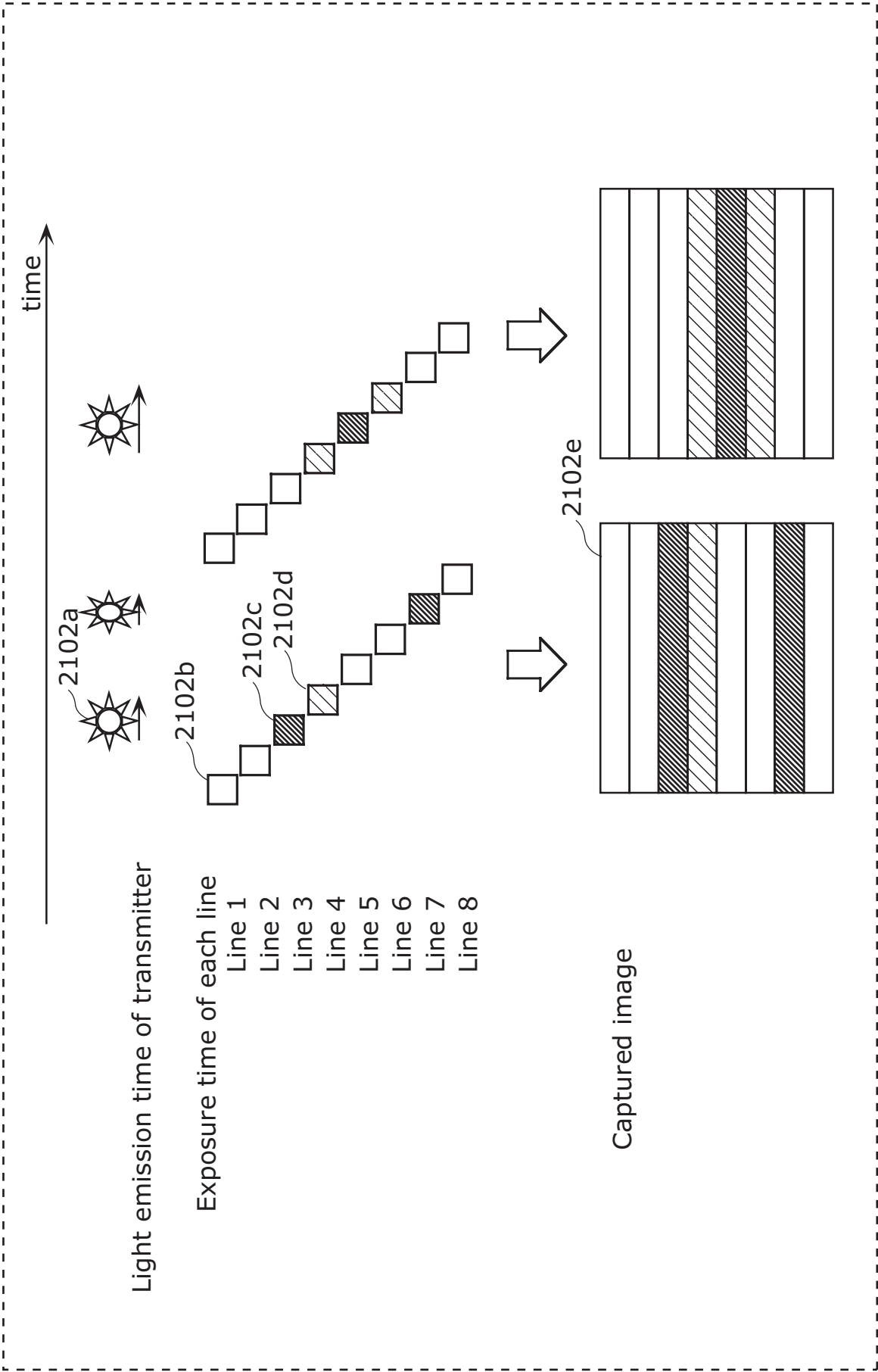


FIG. 4

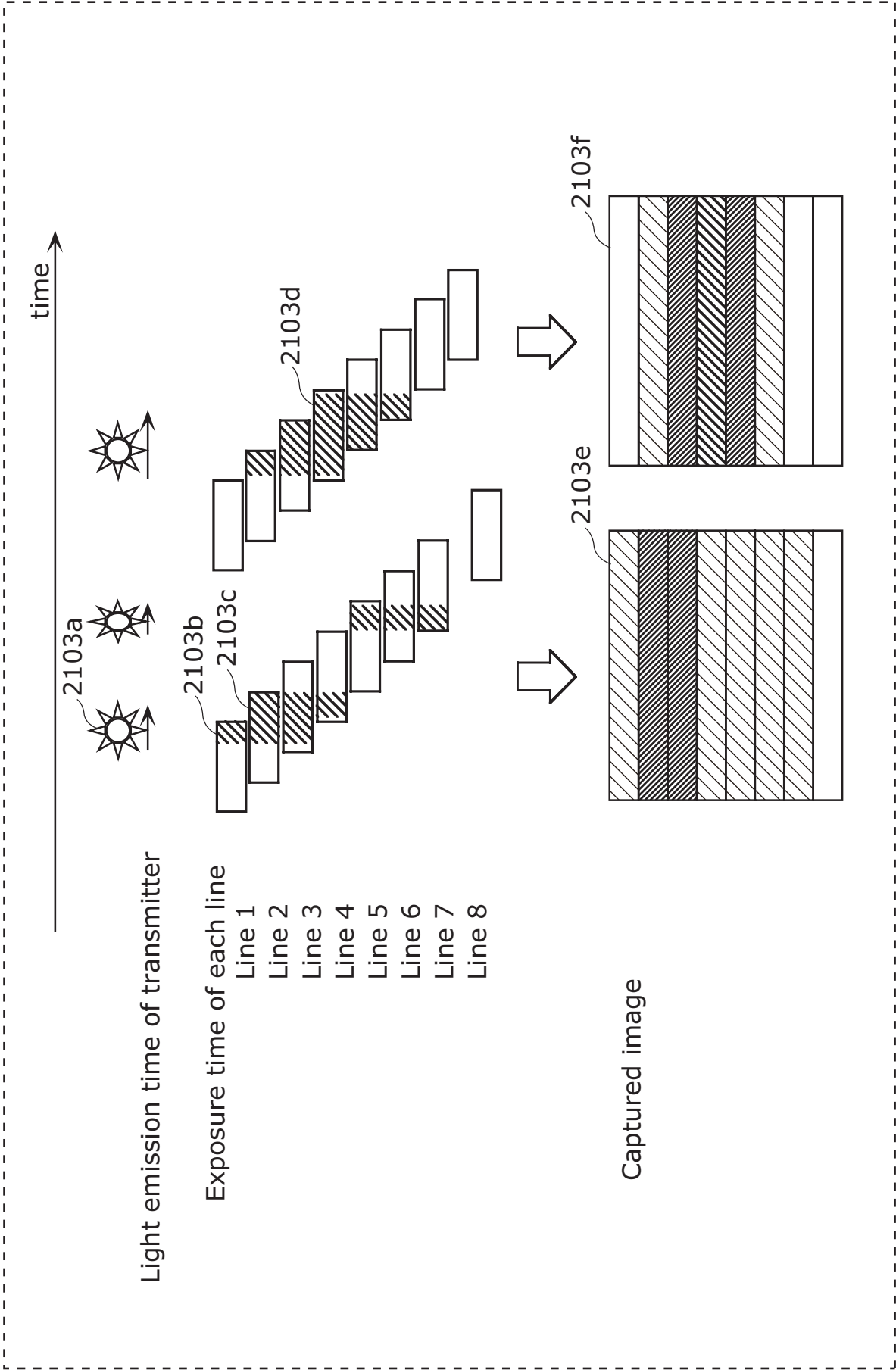


FIG. 5A

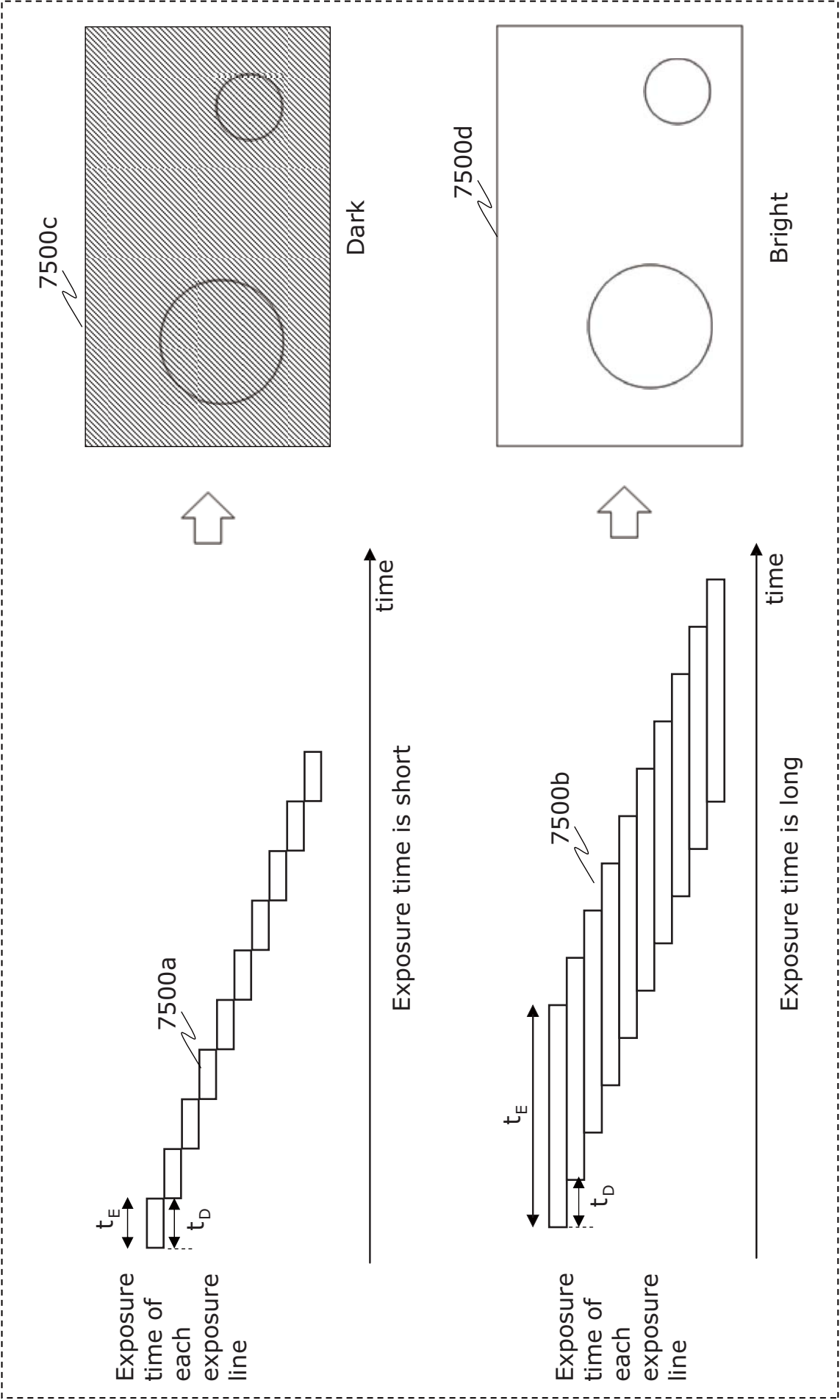


FIG. 5B

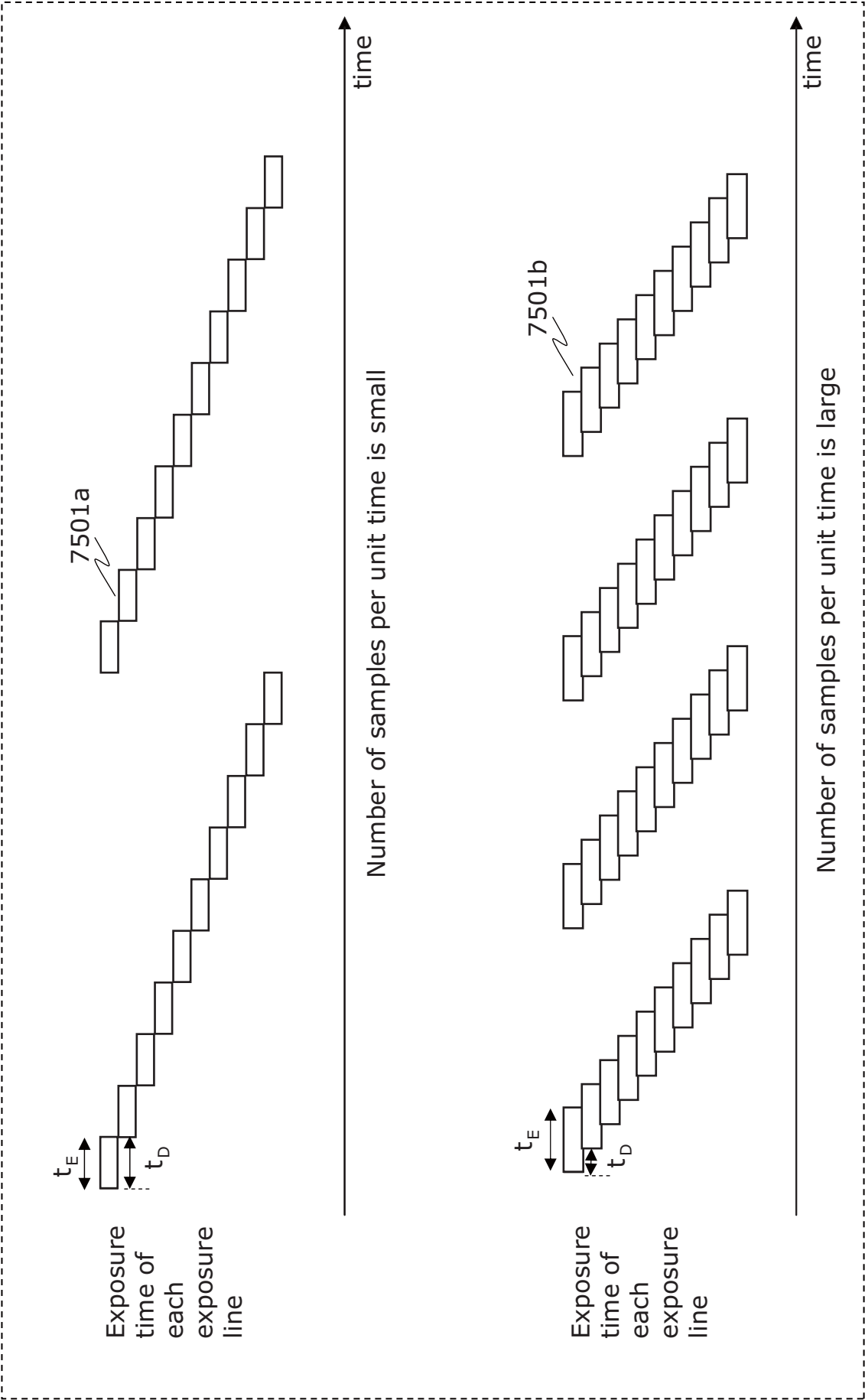


FIG. 5C

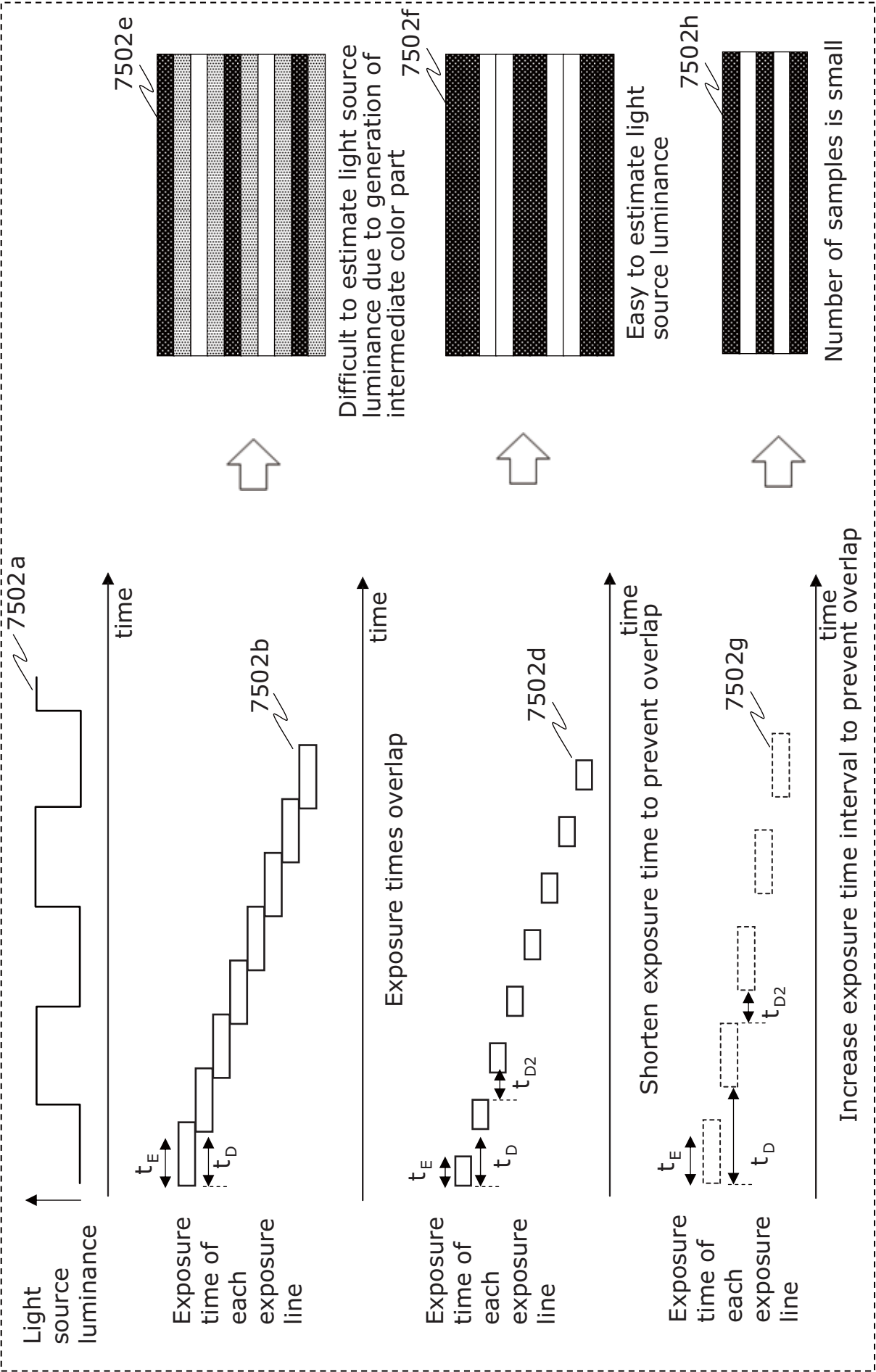


FIG. 5D

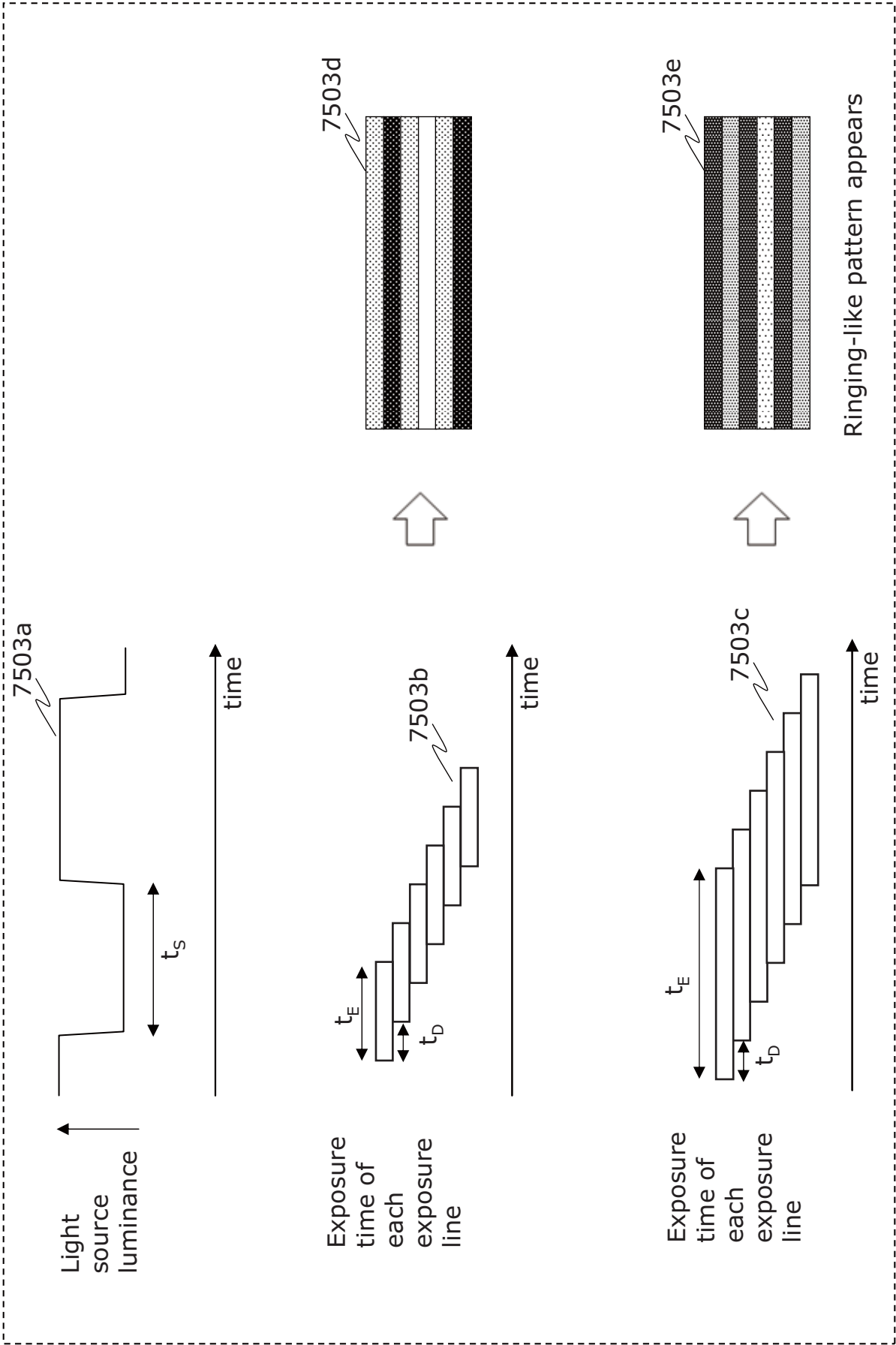


FIG. 5E

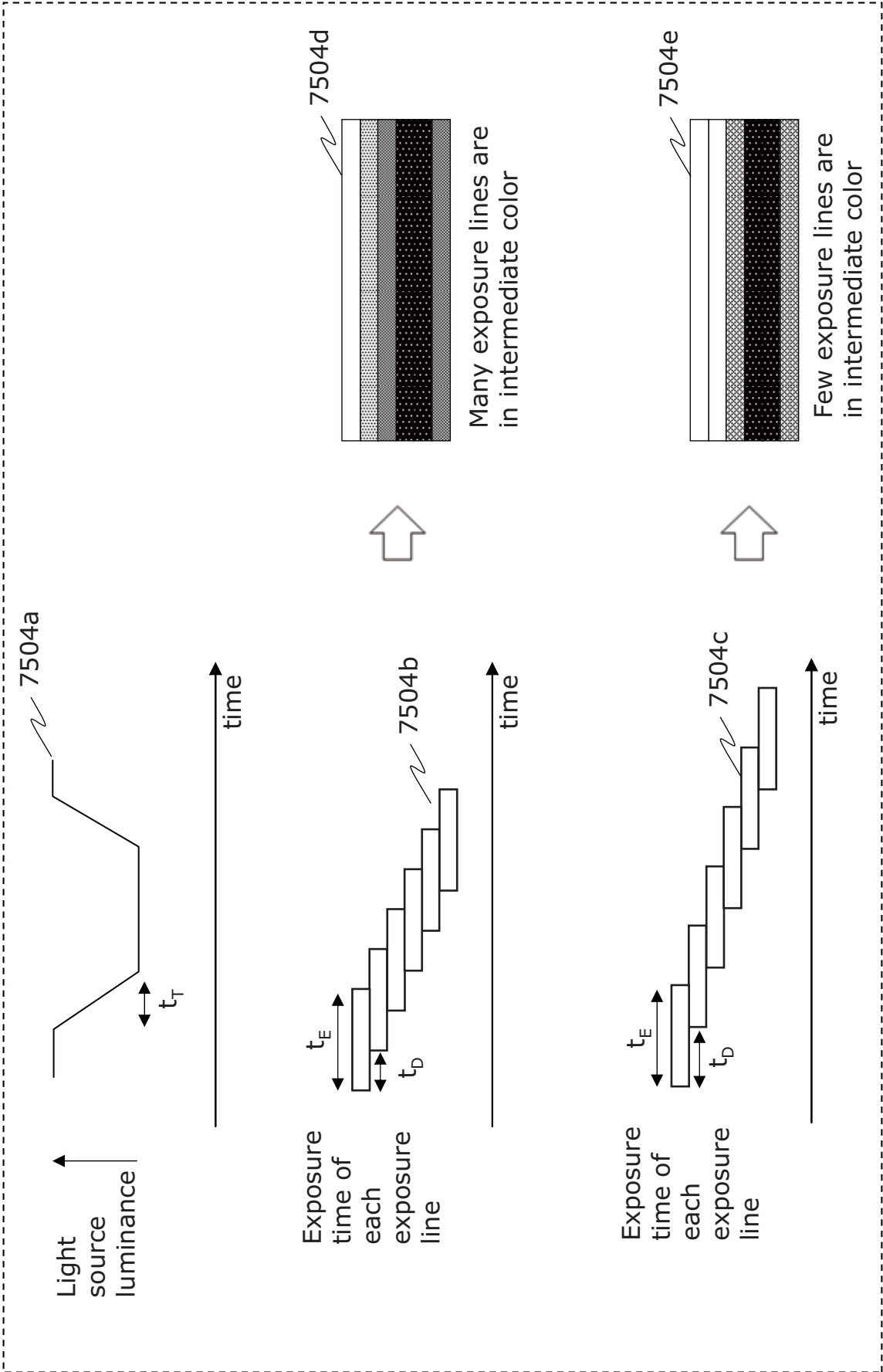


FIG. 5F

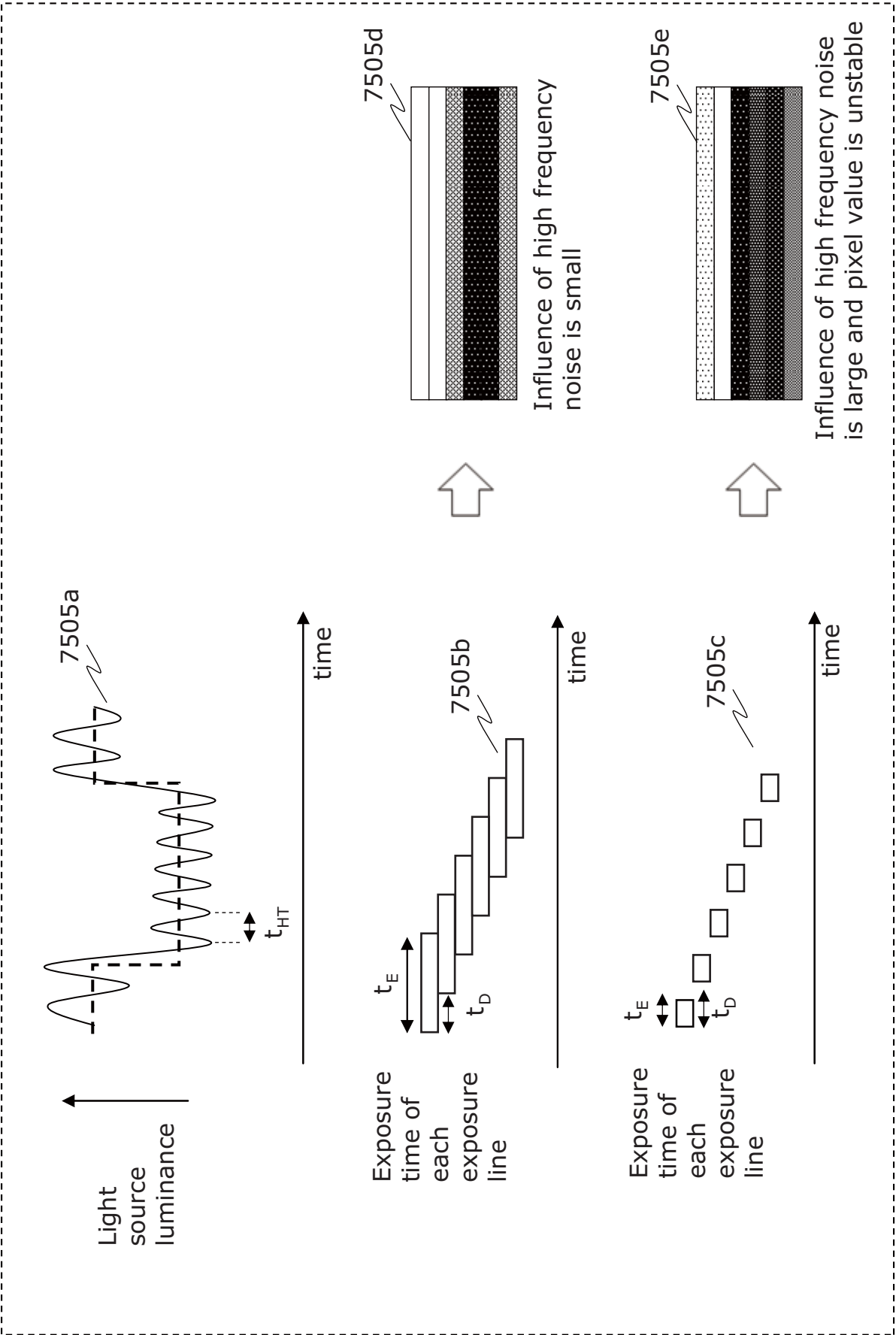


FIG. 5G

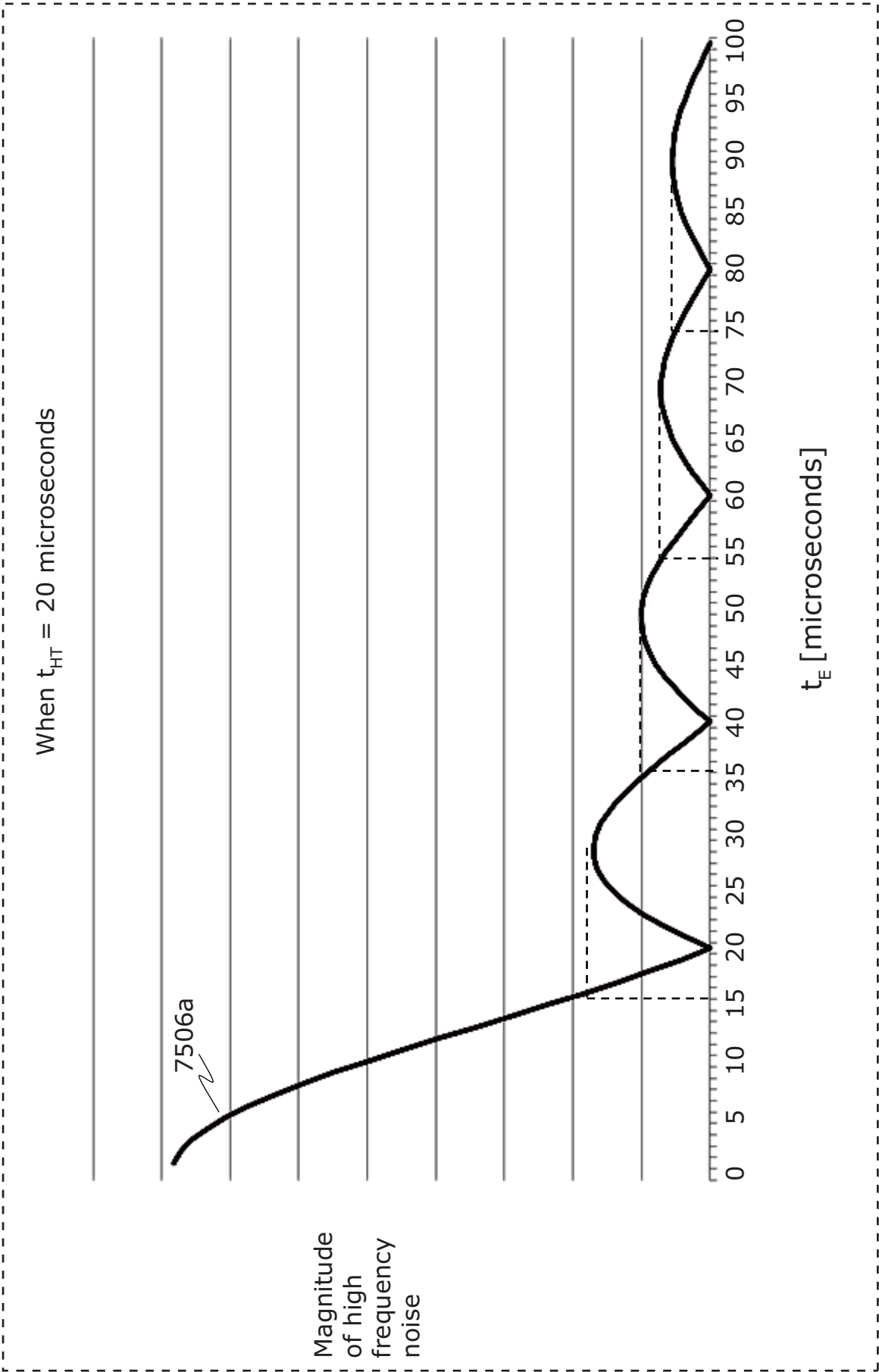


FIG. 5H

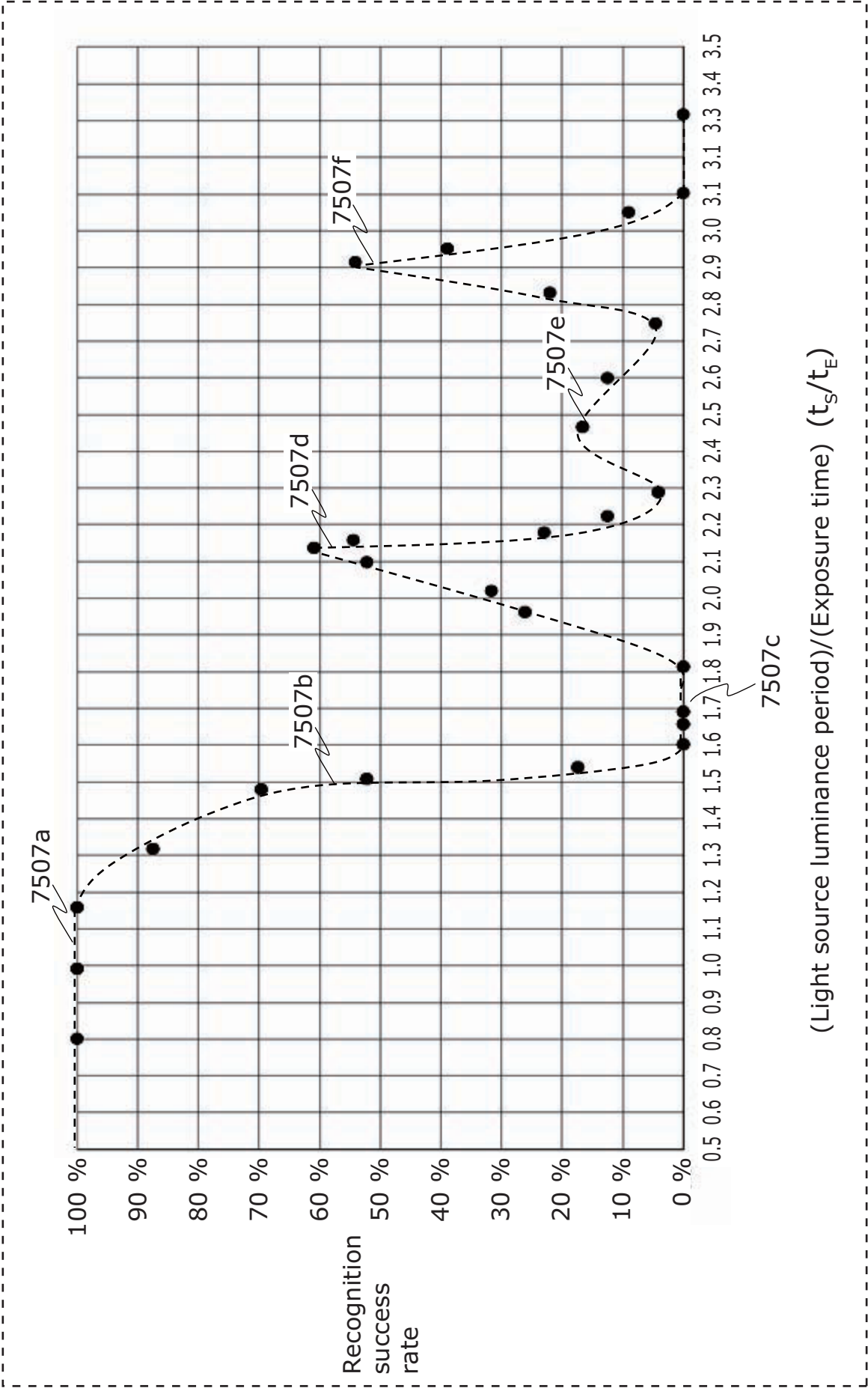


FIG. 6A

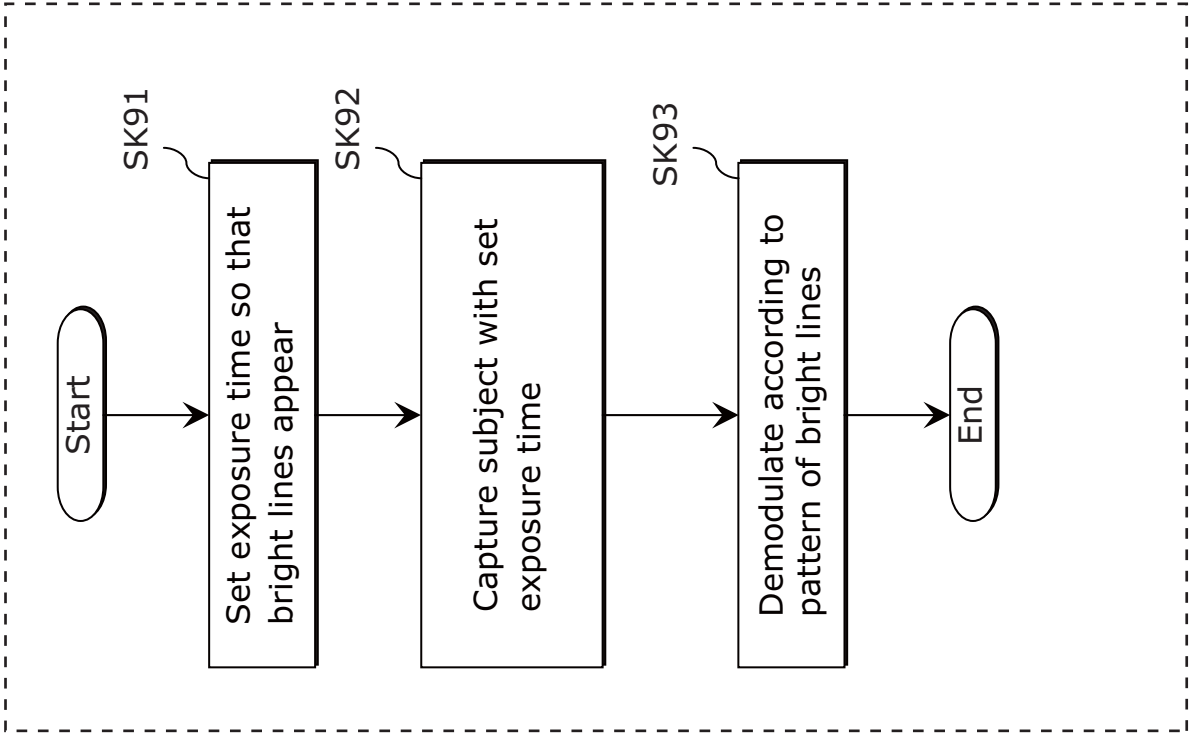


FIG. 6B

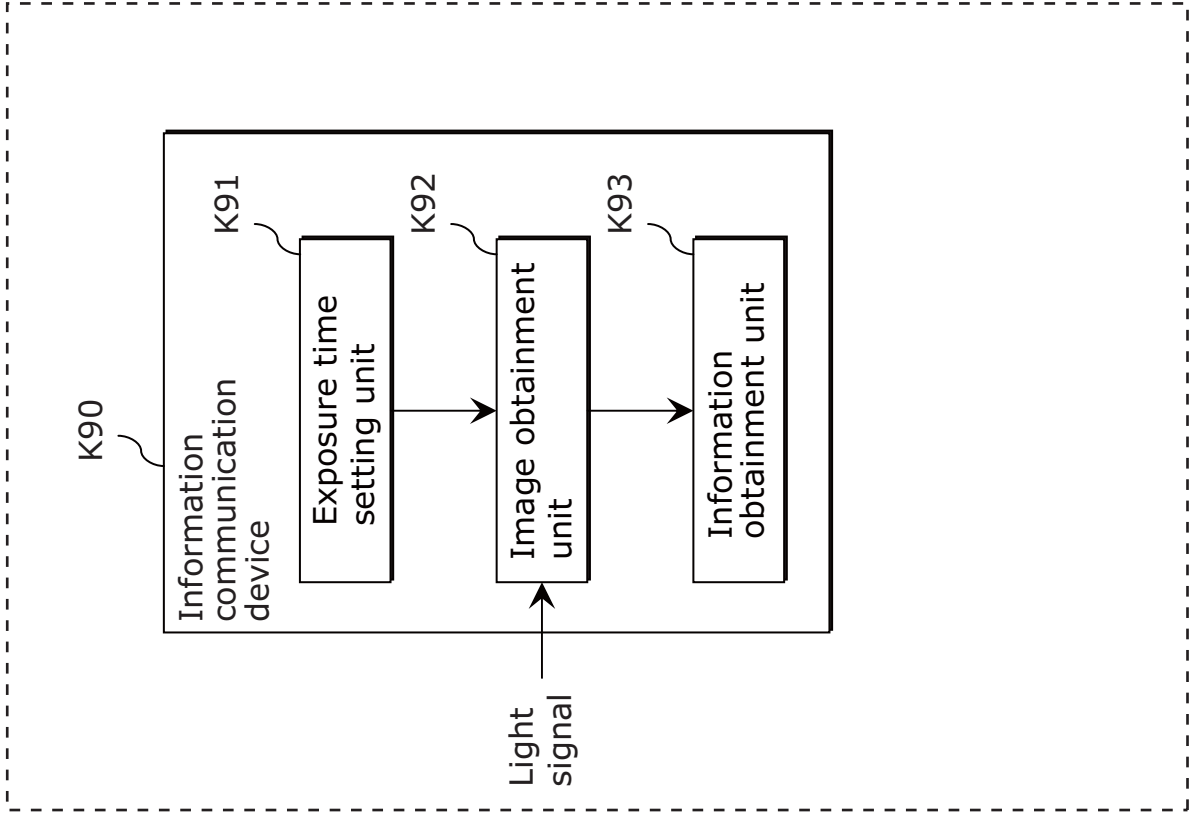


FIG. 7

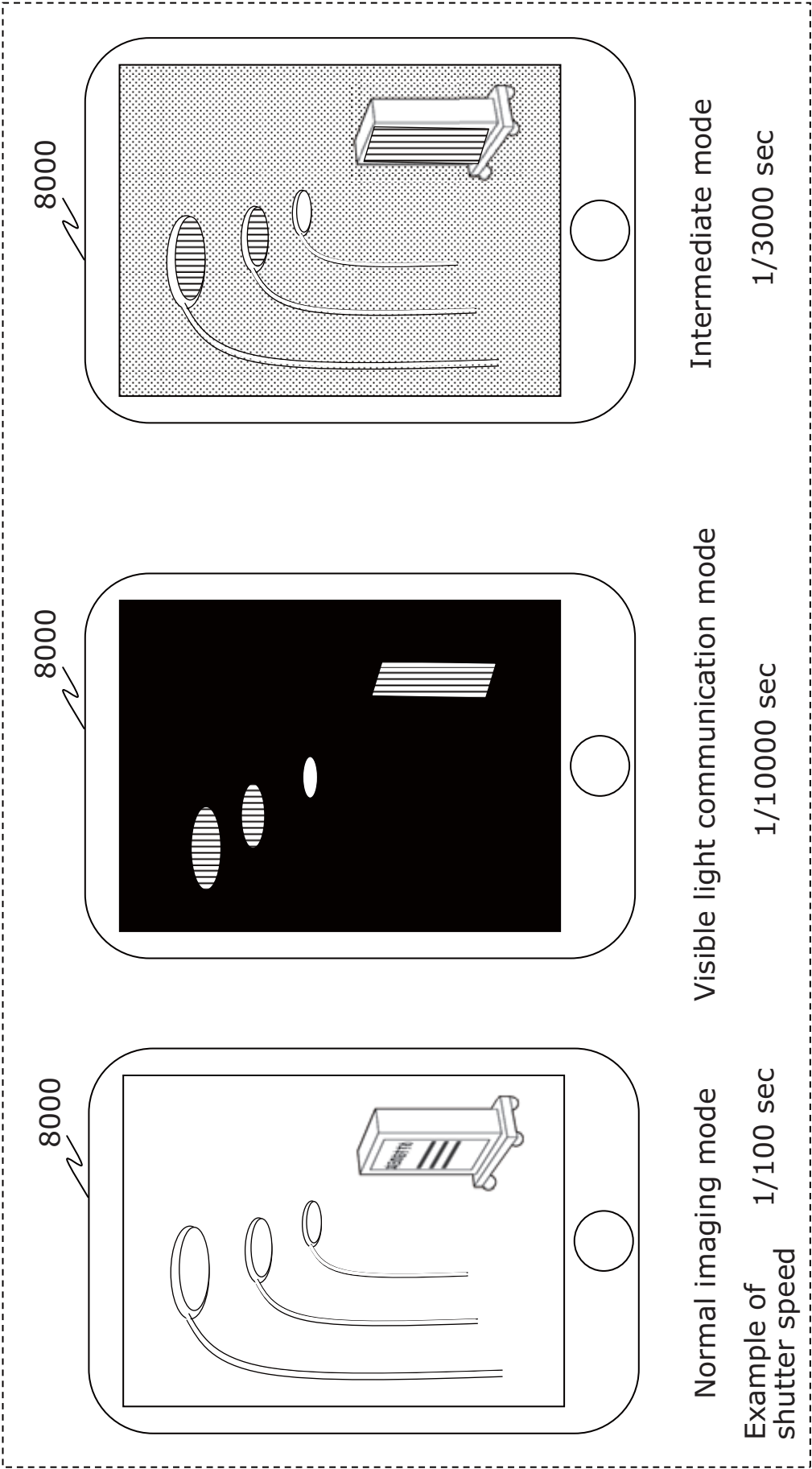


FIG. 8

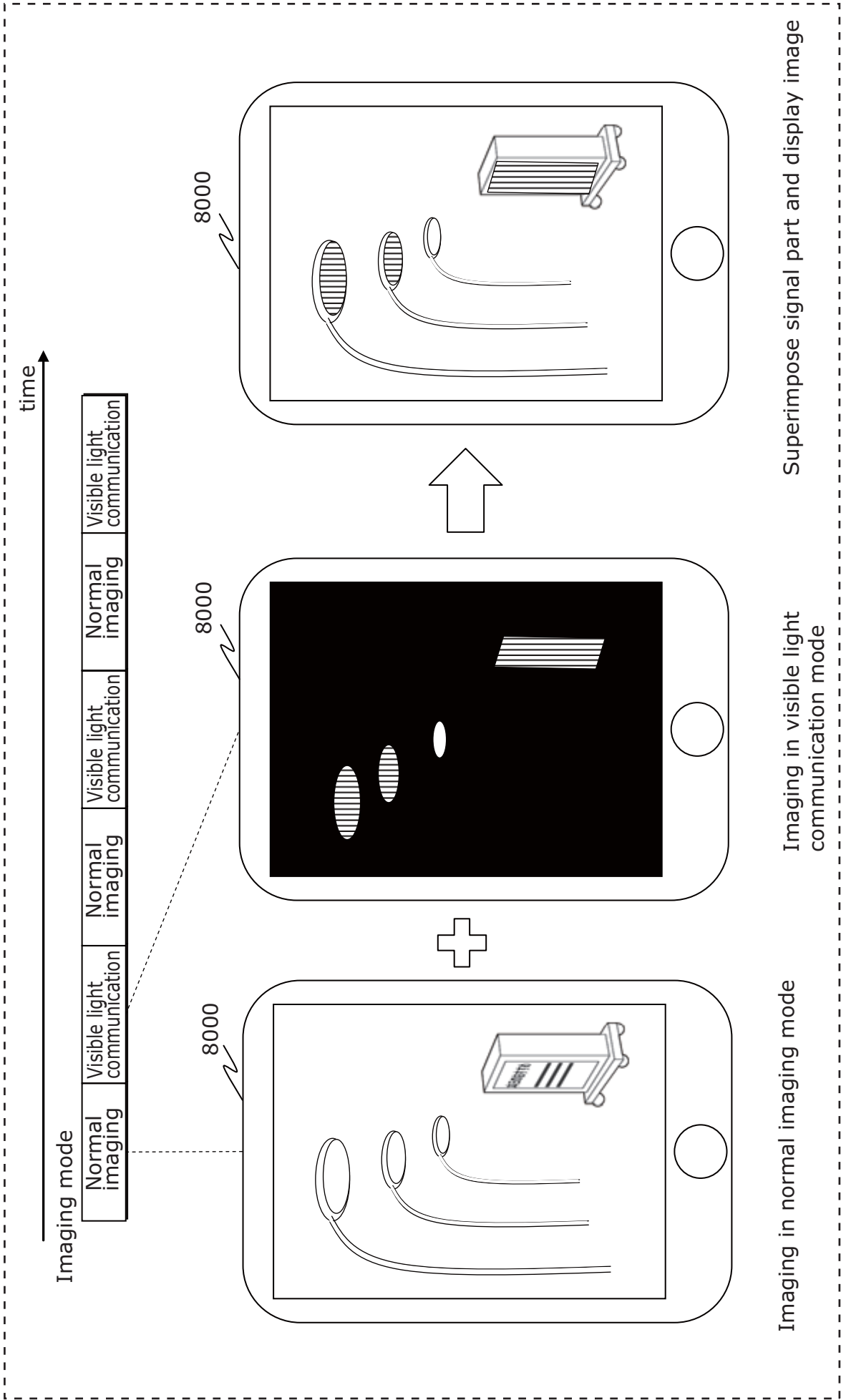


FIG. 9

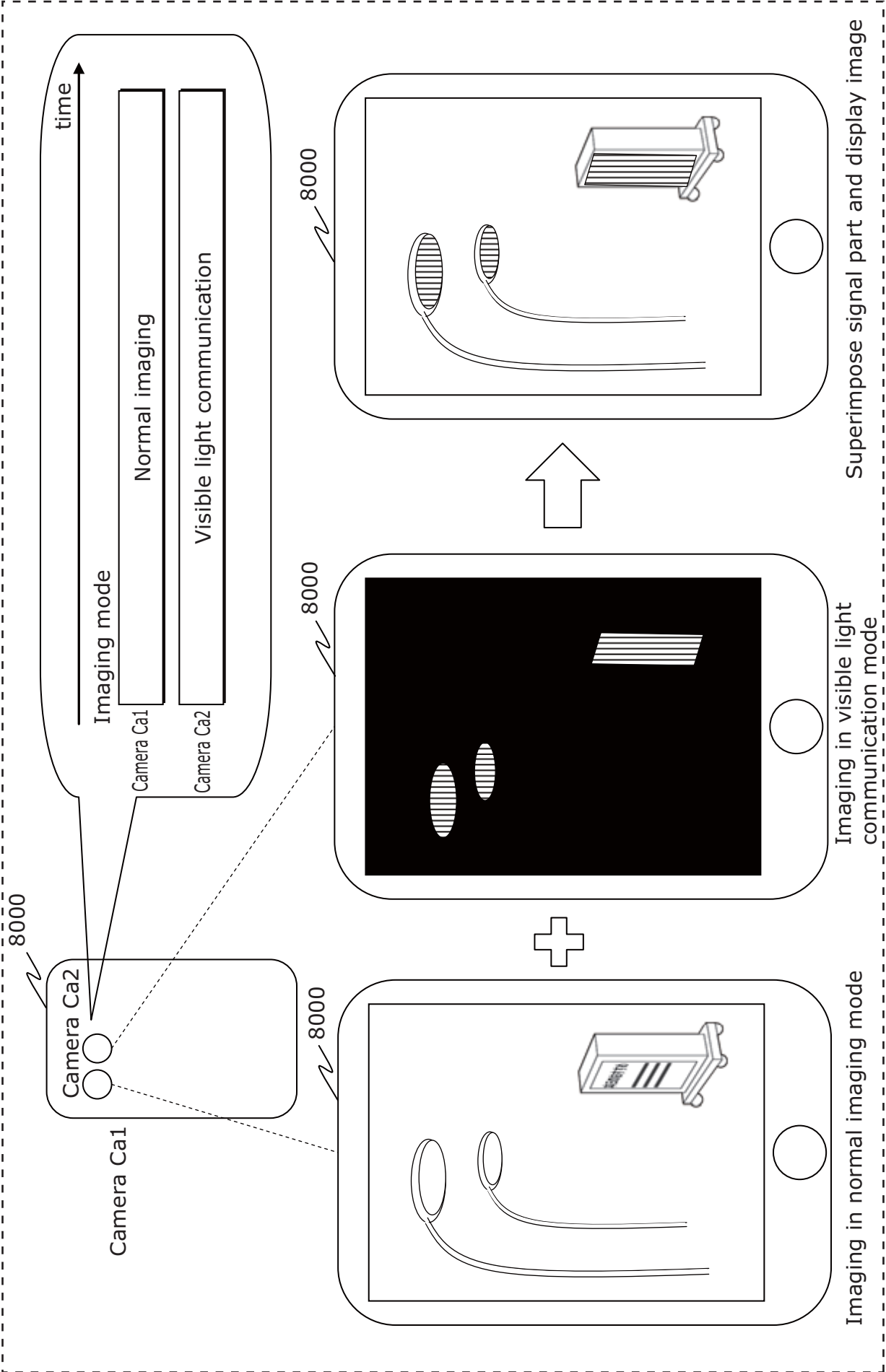


FIG. 10A

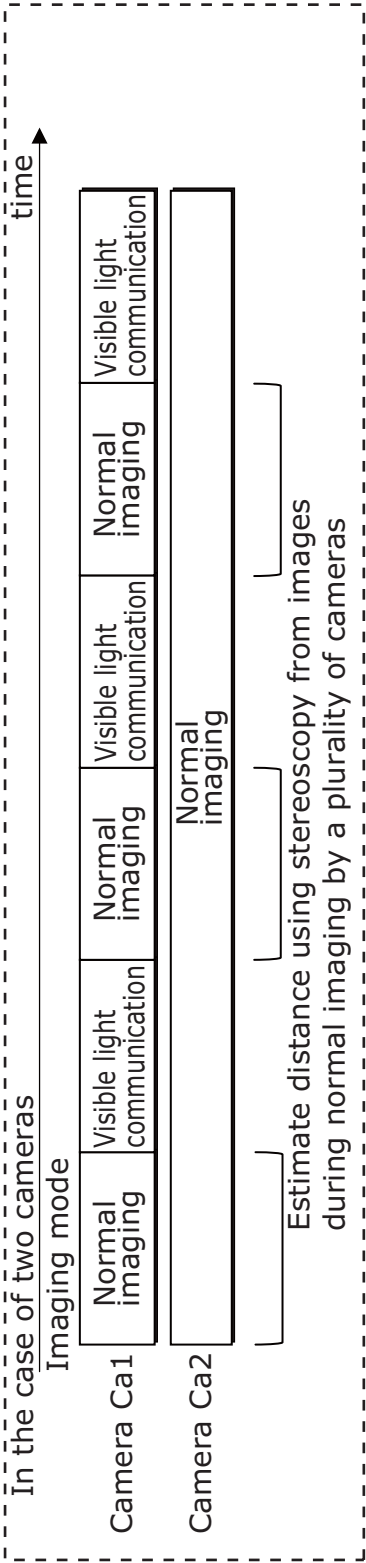


FIG. 10B

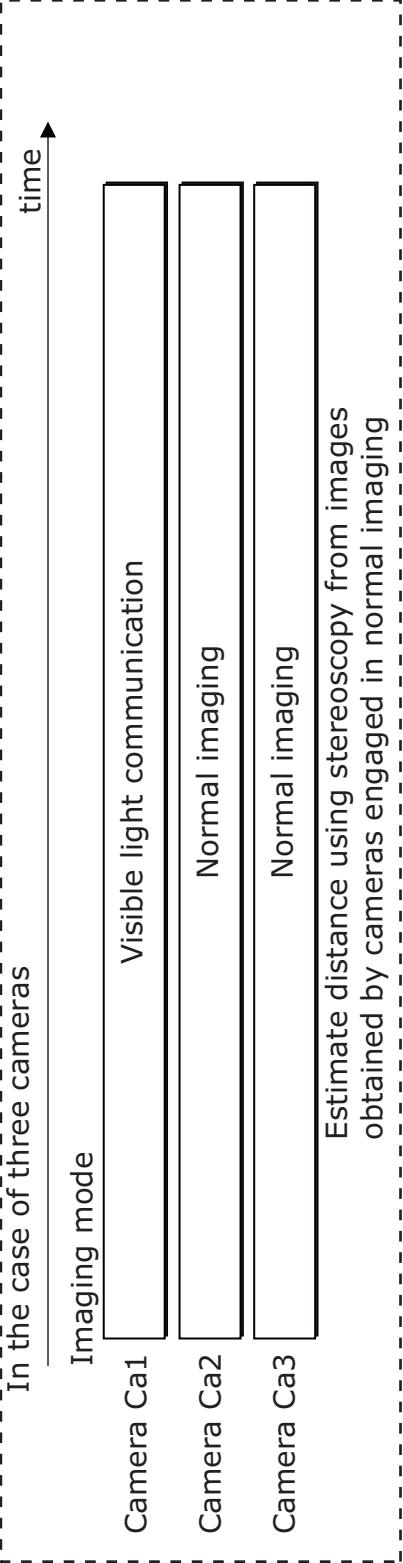


FIG. 10C

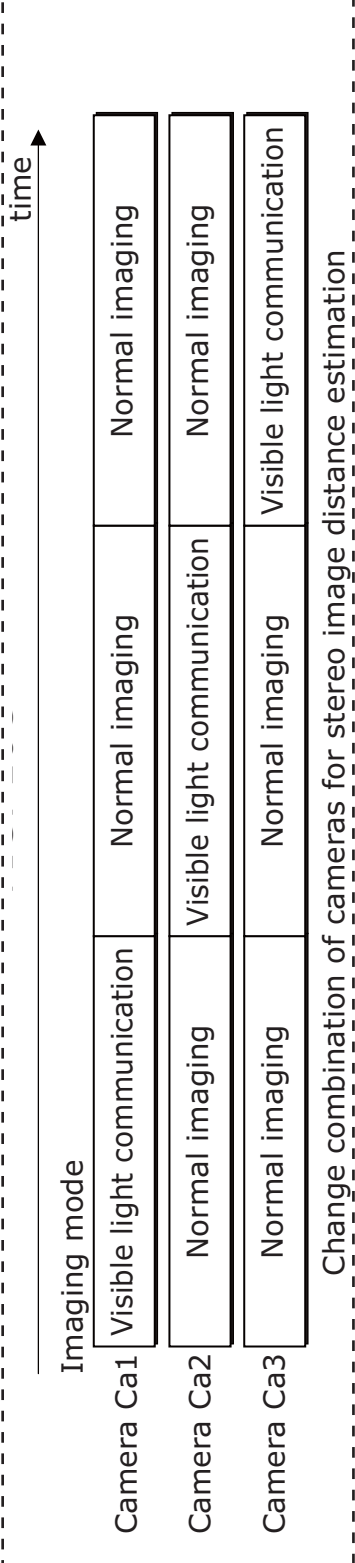


FIG. 11A

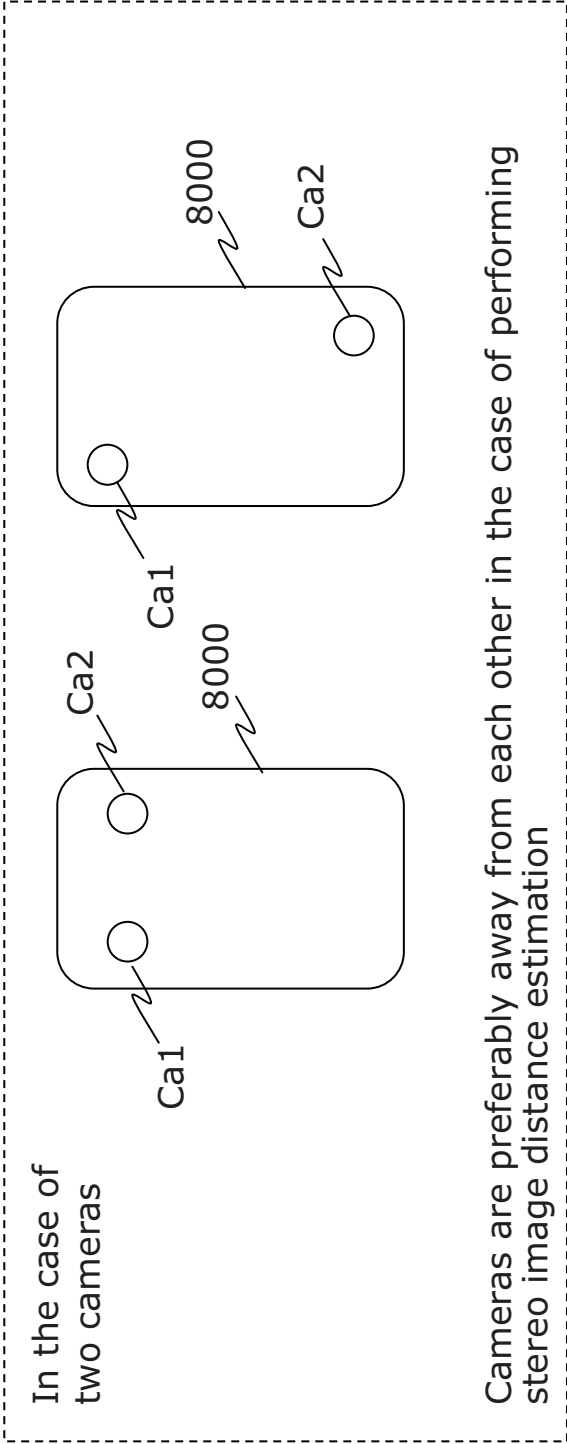


FIG. 11B

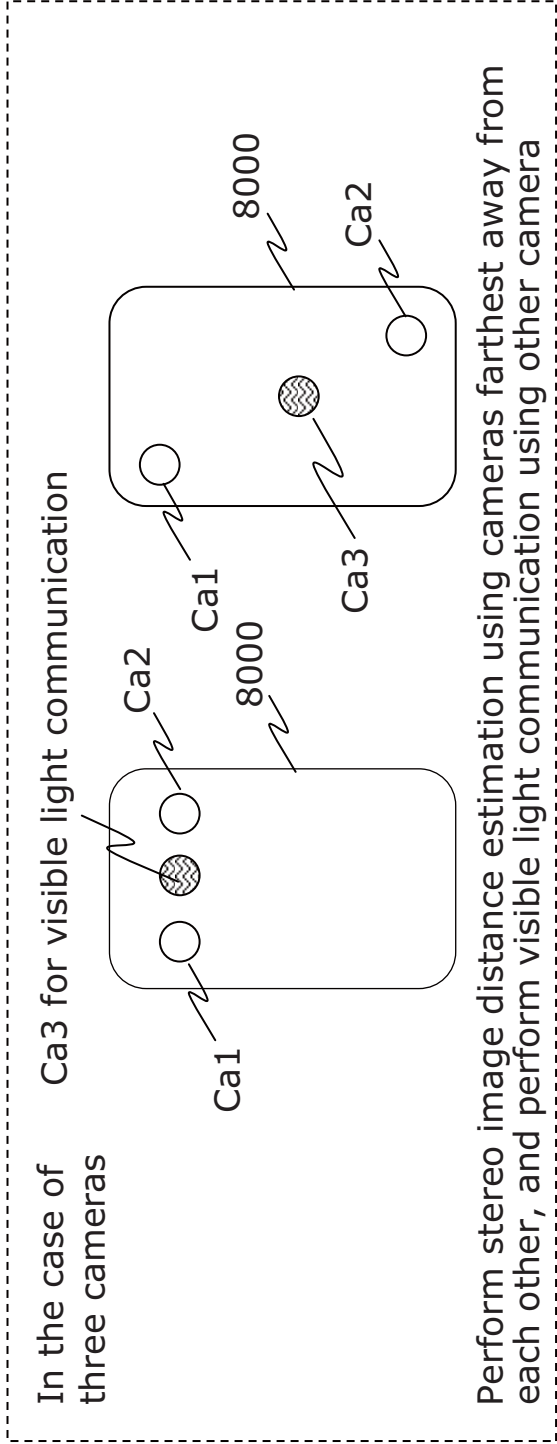


FIG. 12

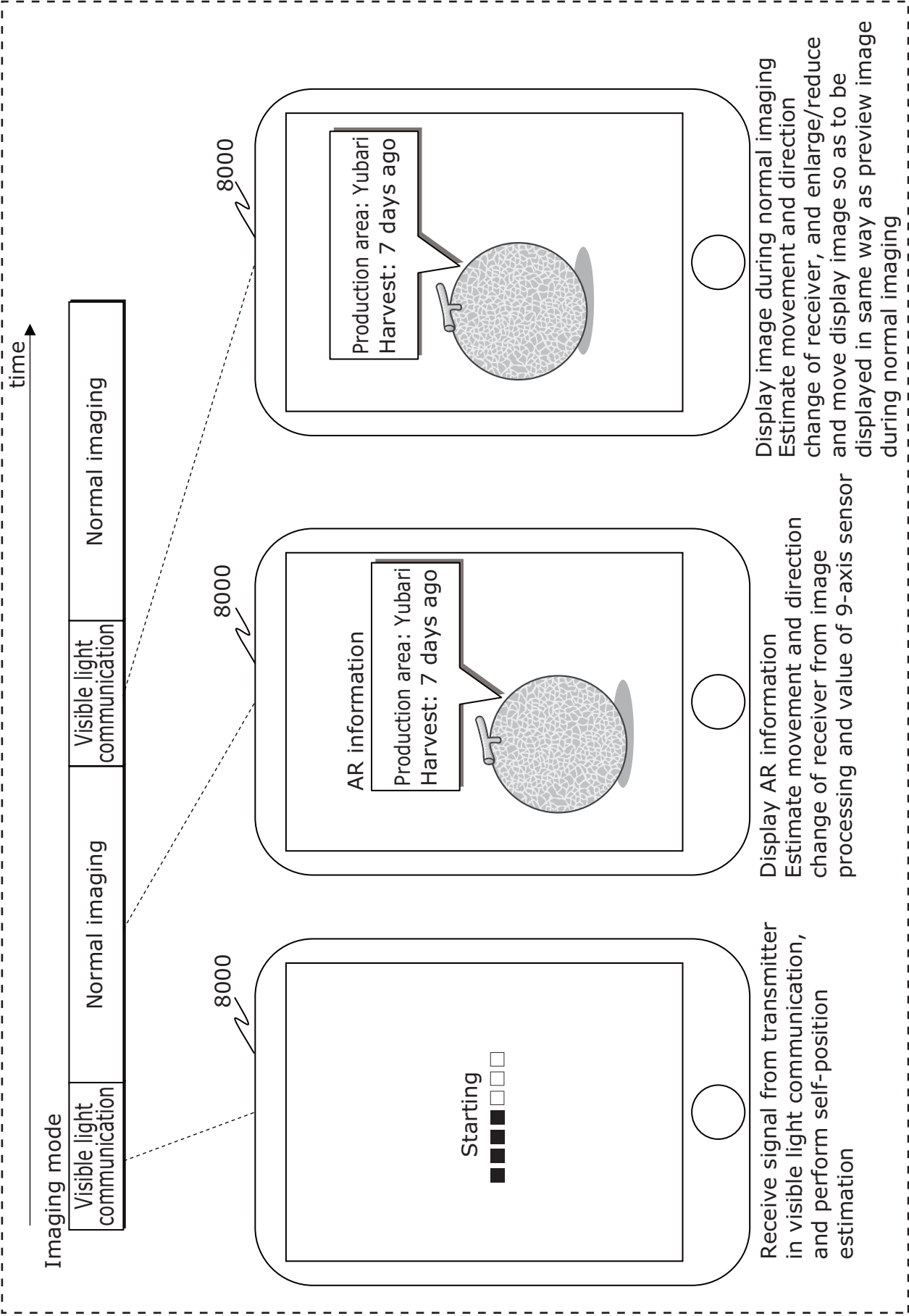


FIG. 13

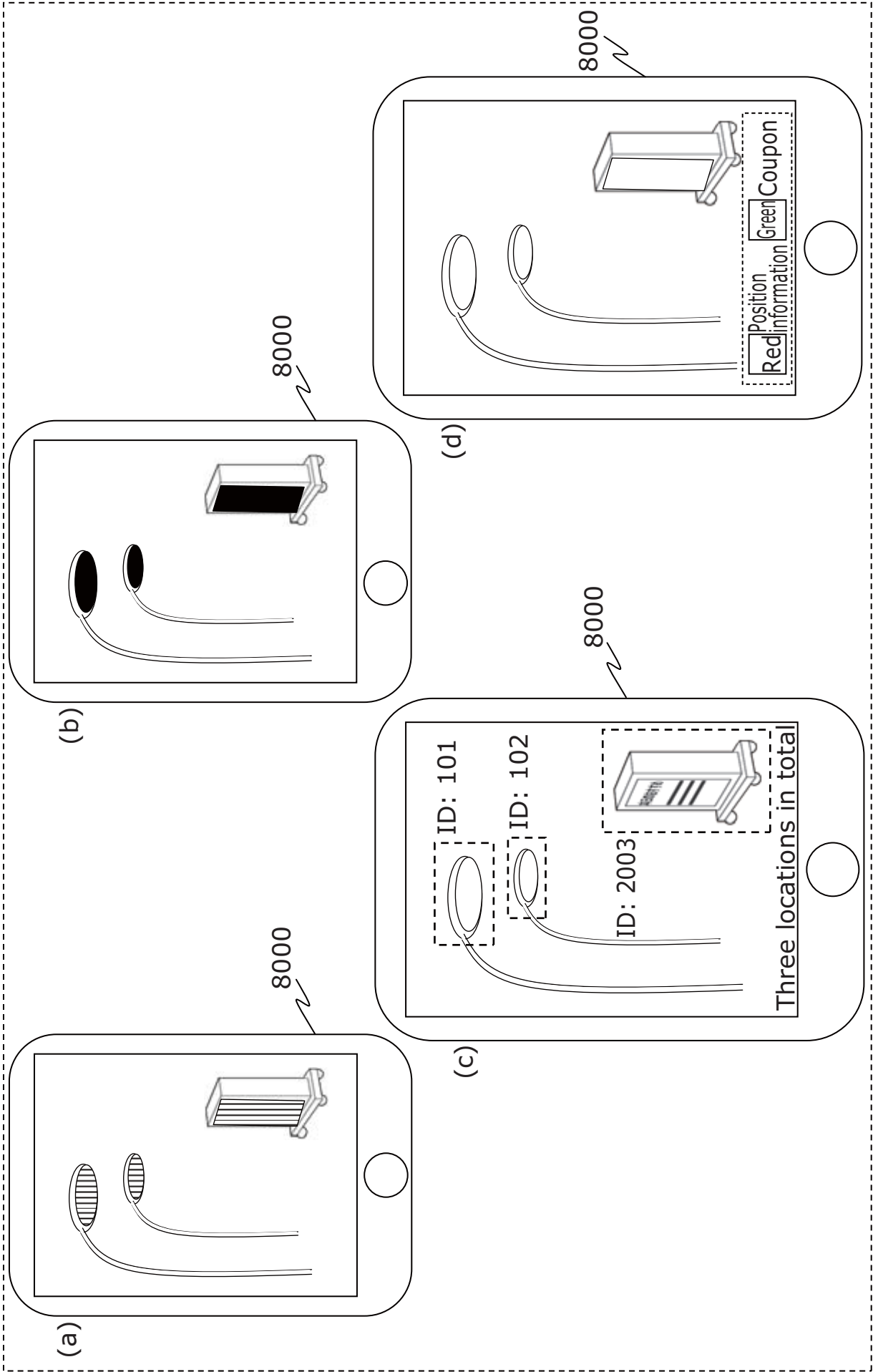
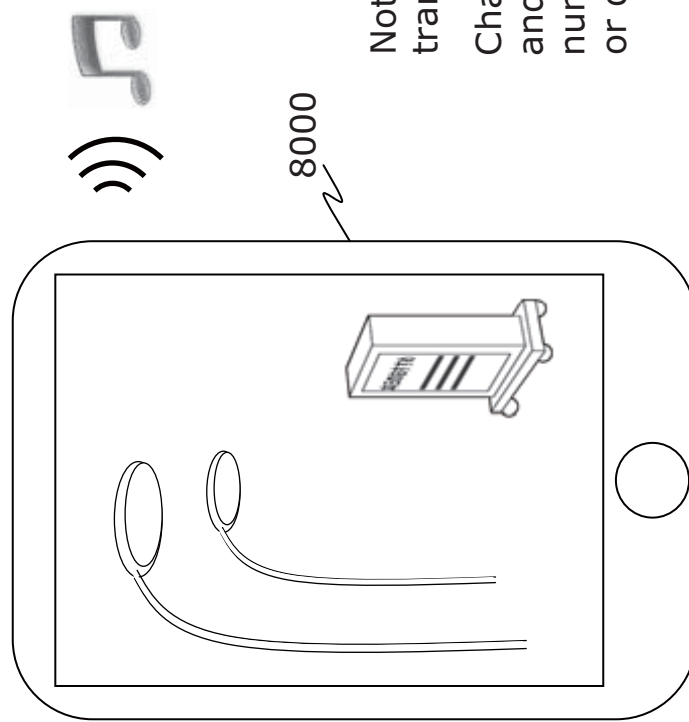


FIG. 14



Notify by sound upon discovery of transmitter and upon signal reception

Change sound type, reproduction time, and number of reproductions according to number of transmitters or type of received or display information

FIG. 15

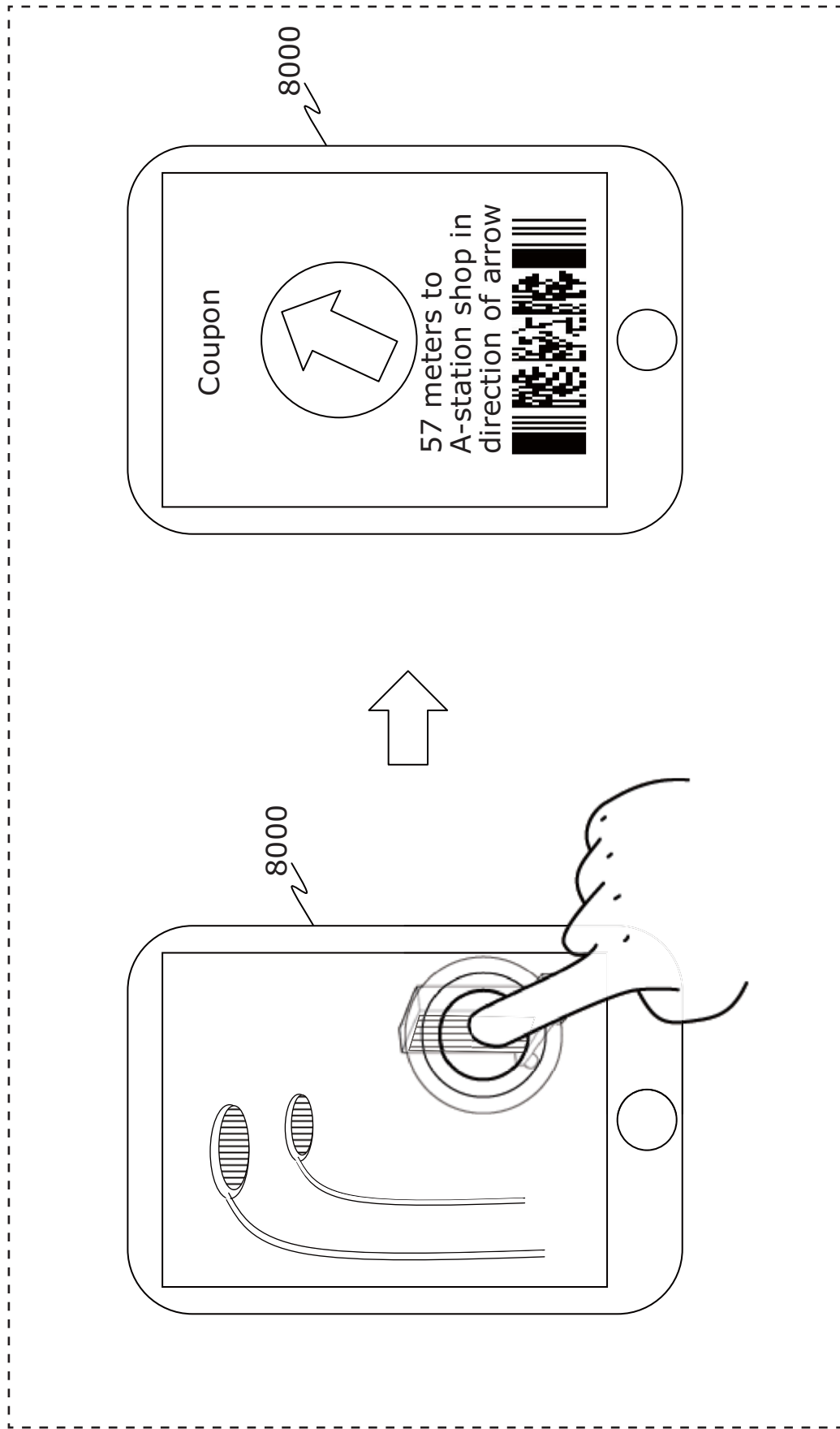


FIG. 16

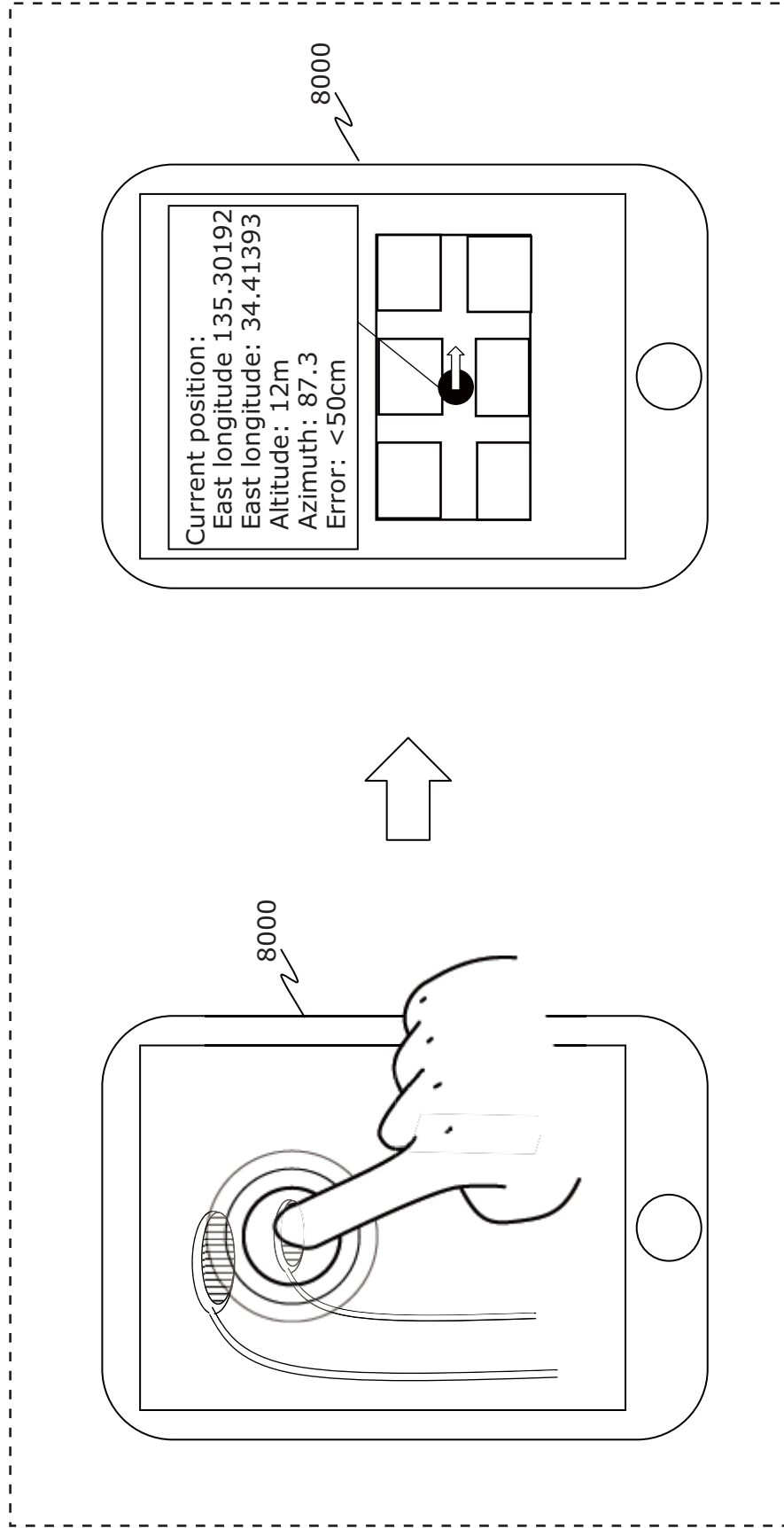


FIG. 17

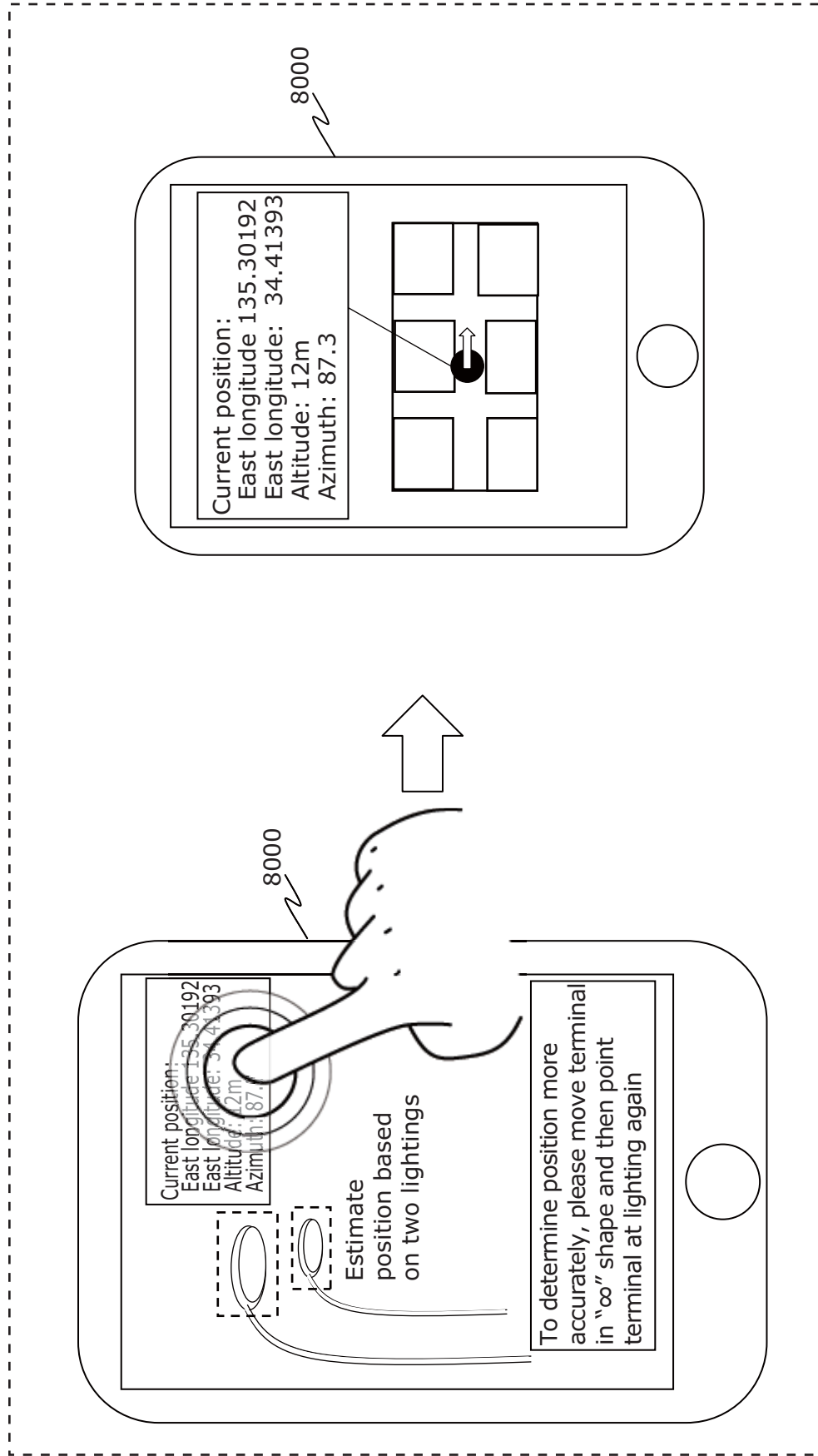


FIG. 18

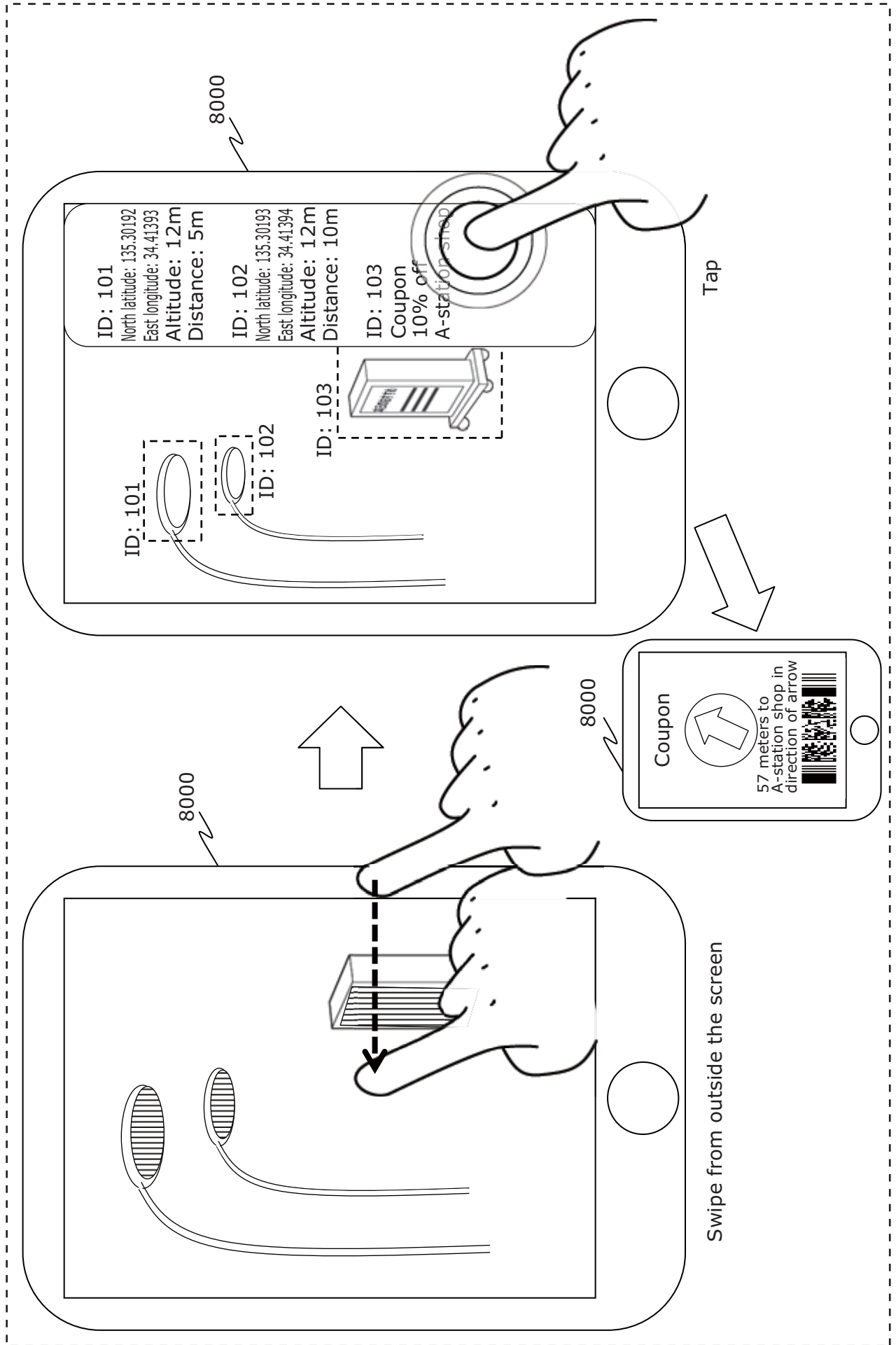


FIG. 19

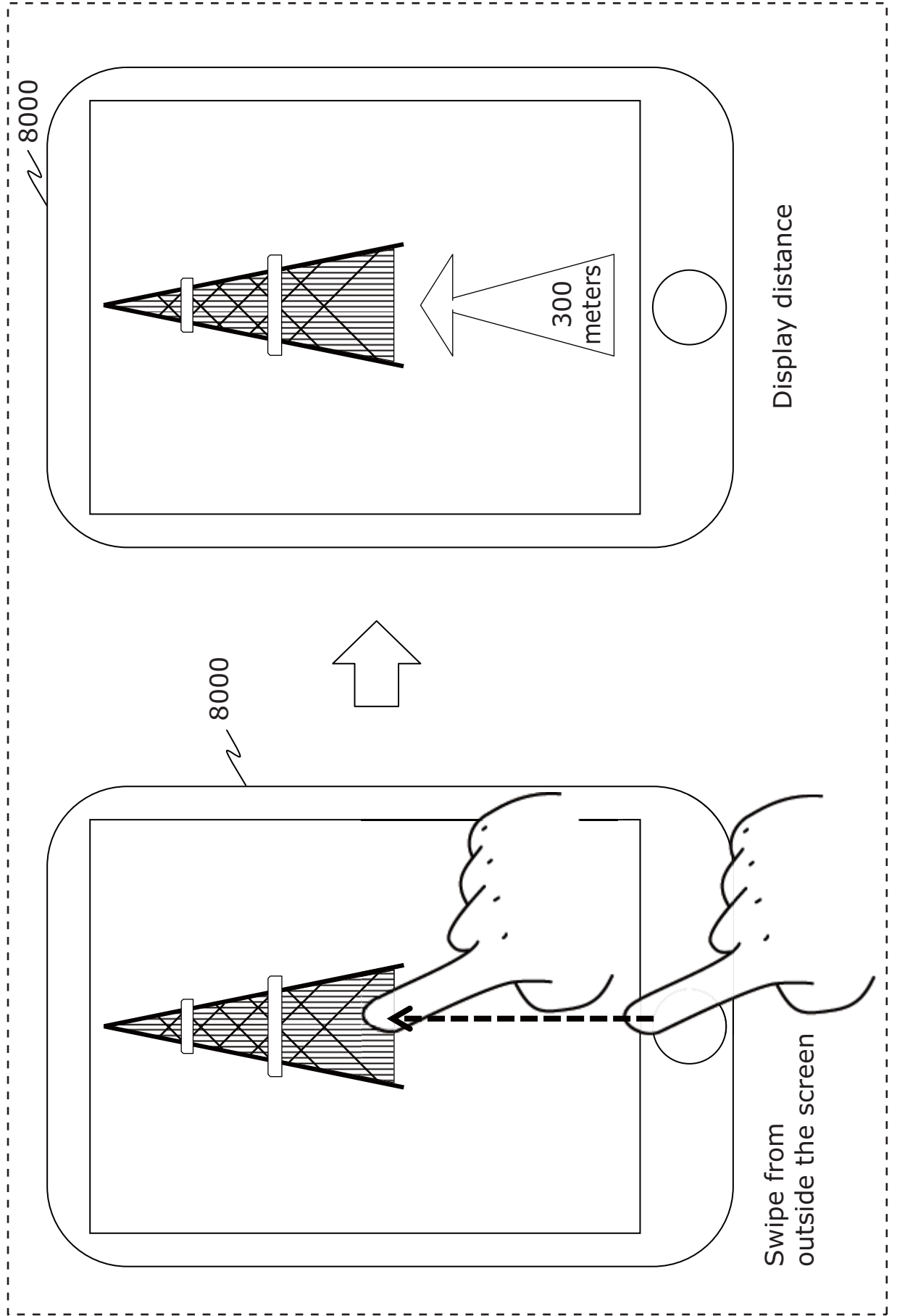


FIG. 20

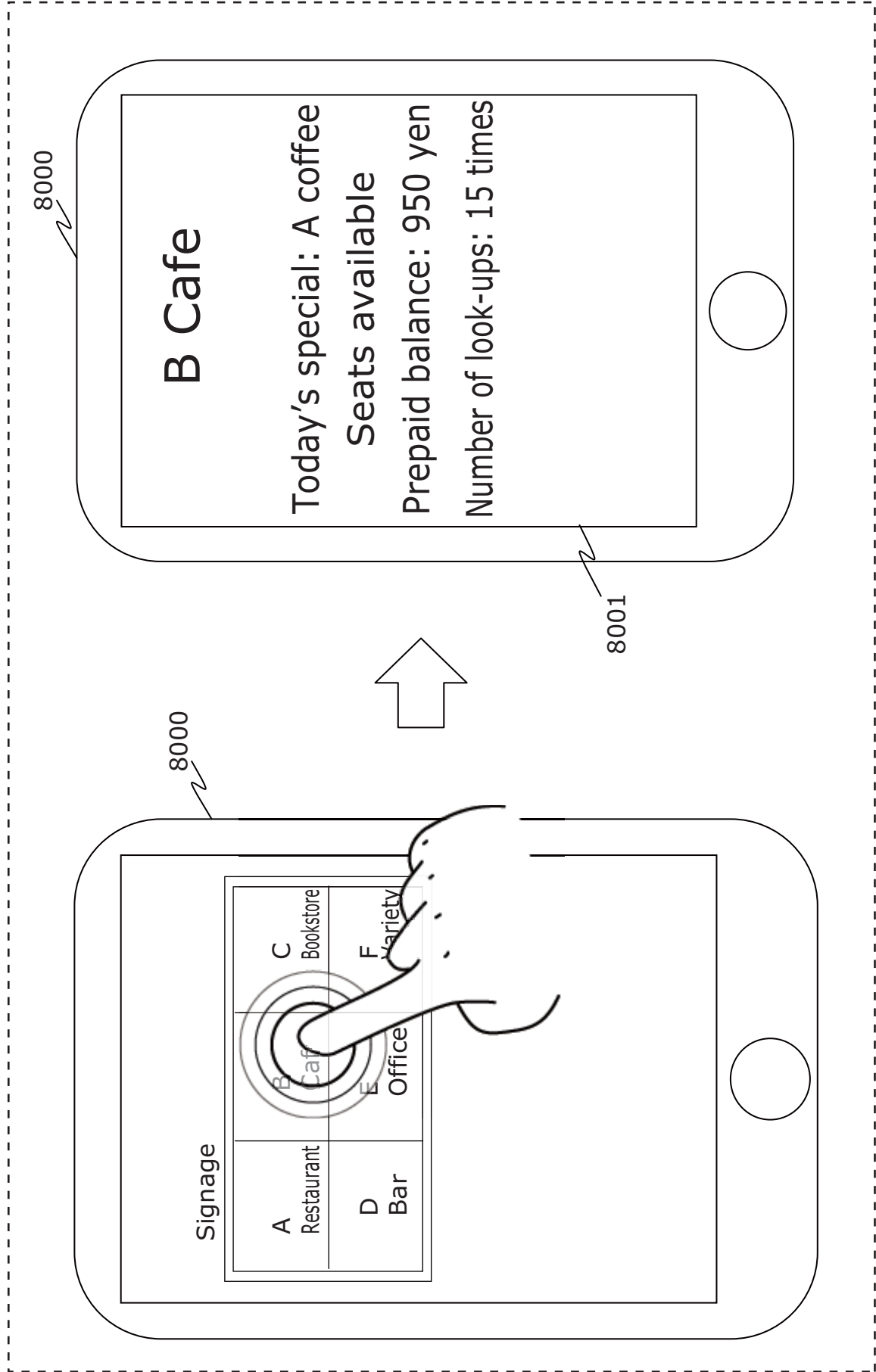


FIG. 21

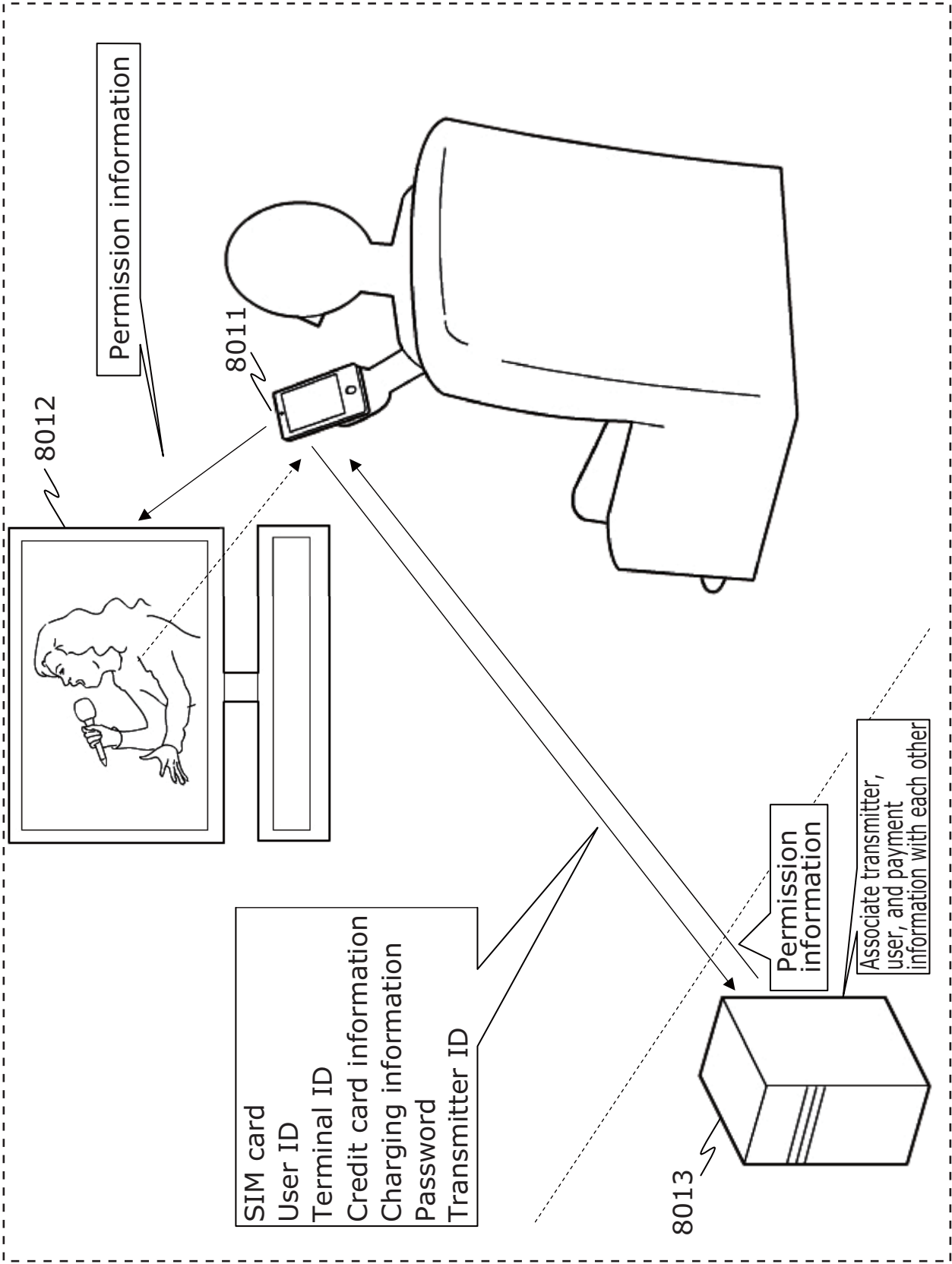


FIG. 22

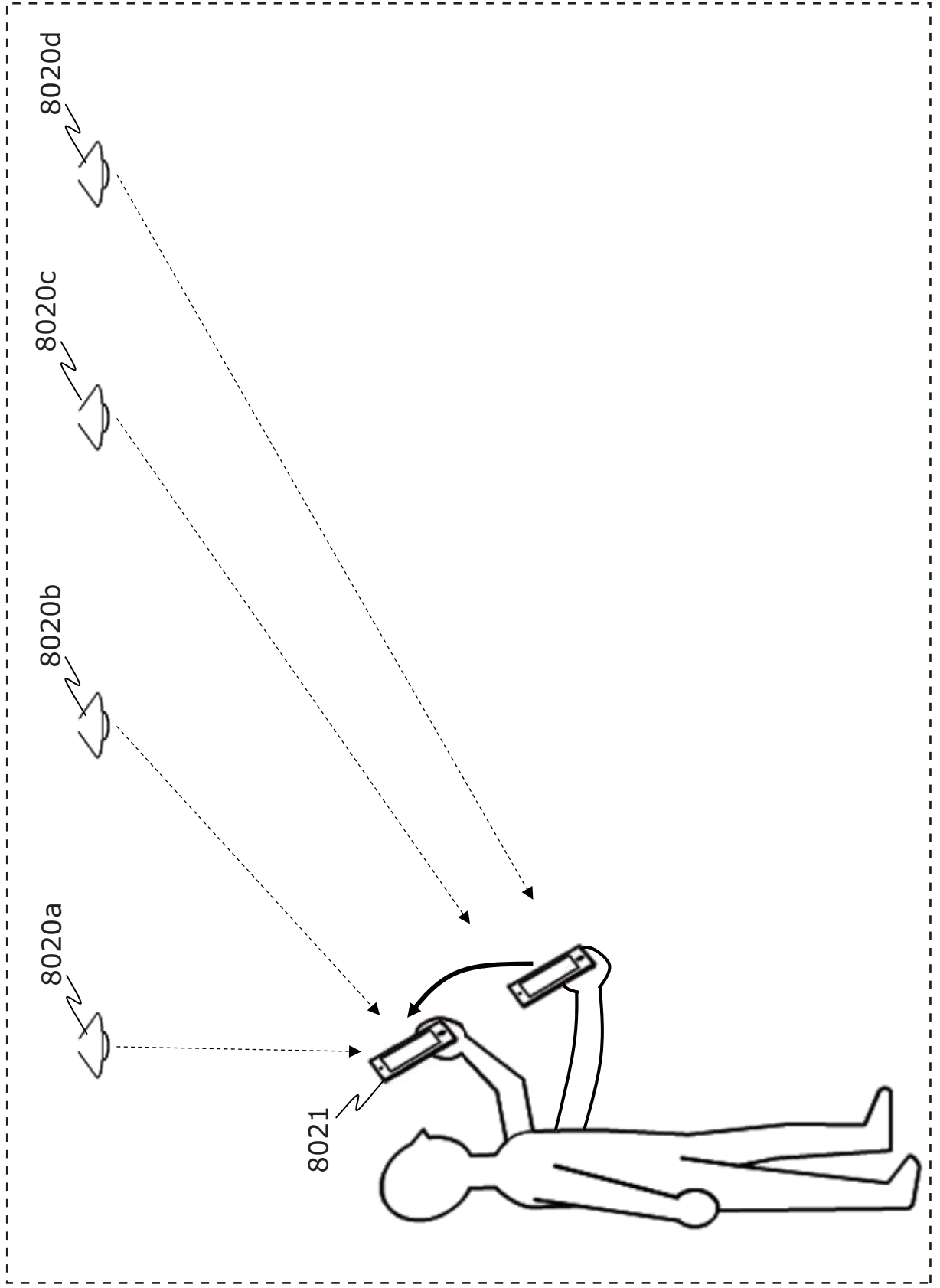


FIG. 23

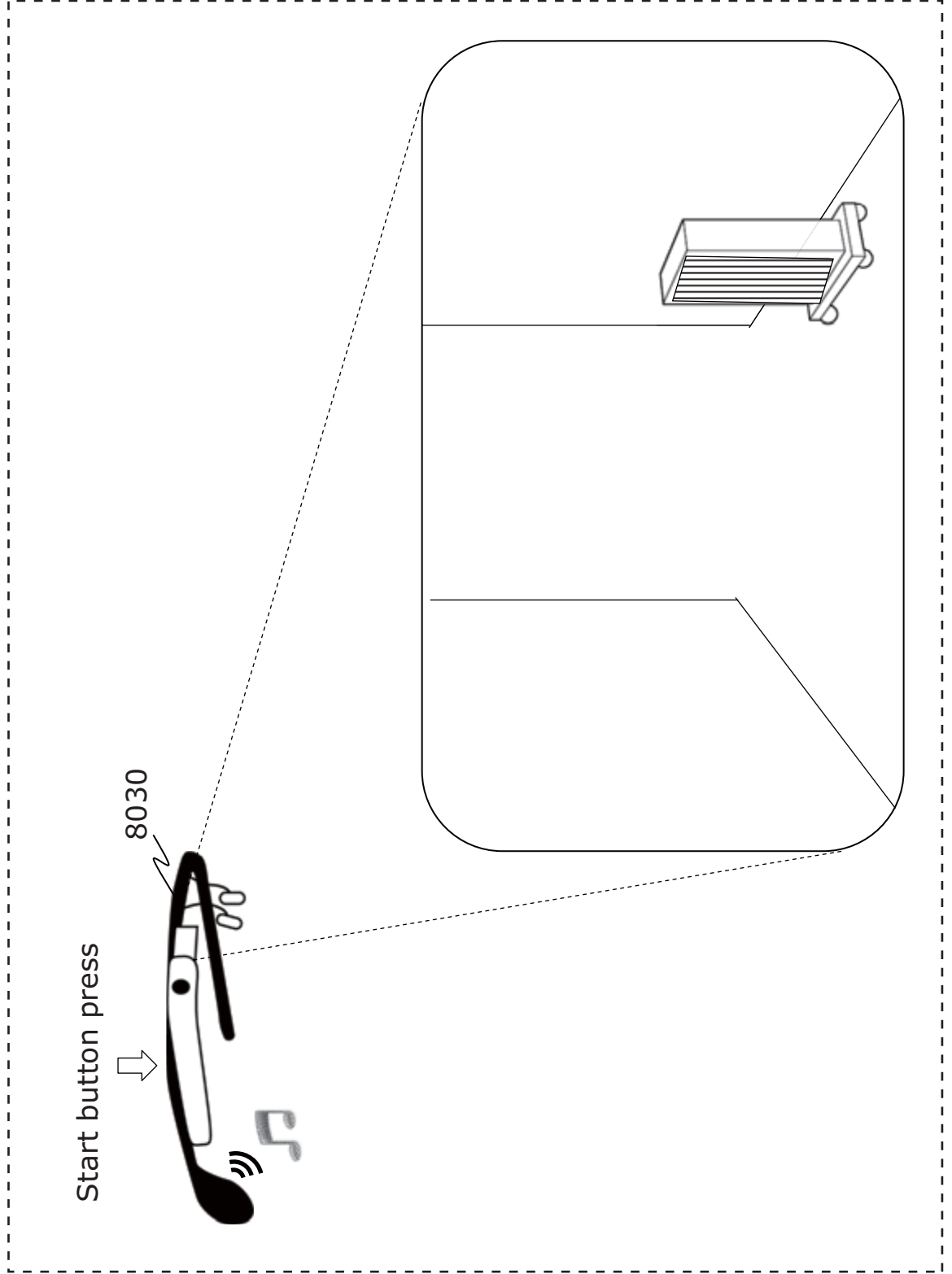


FIG. 24

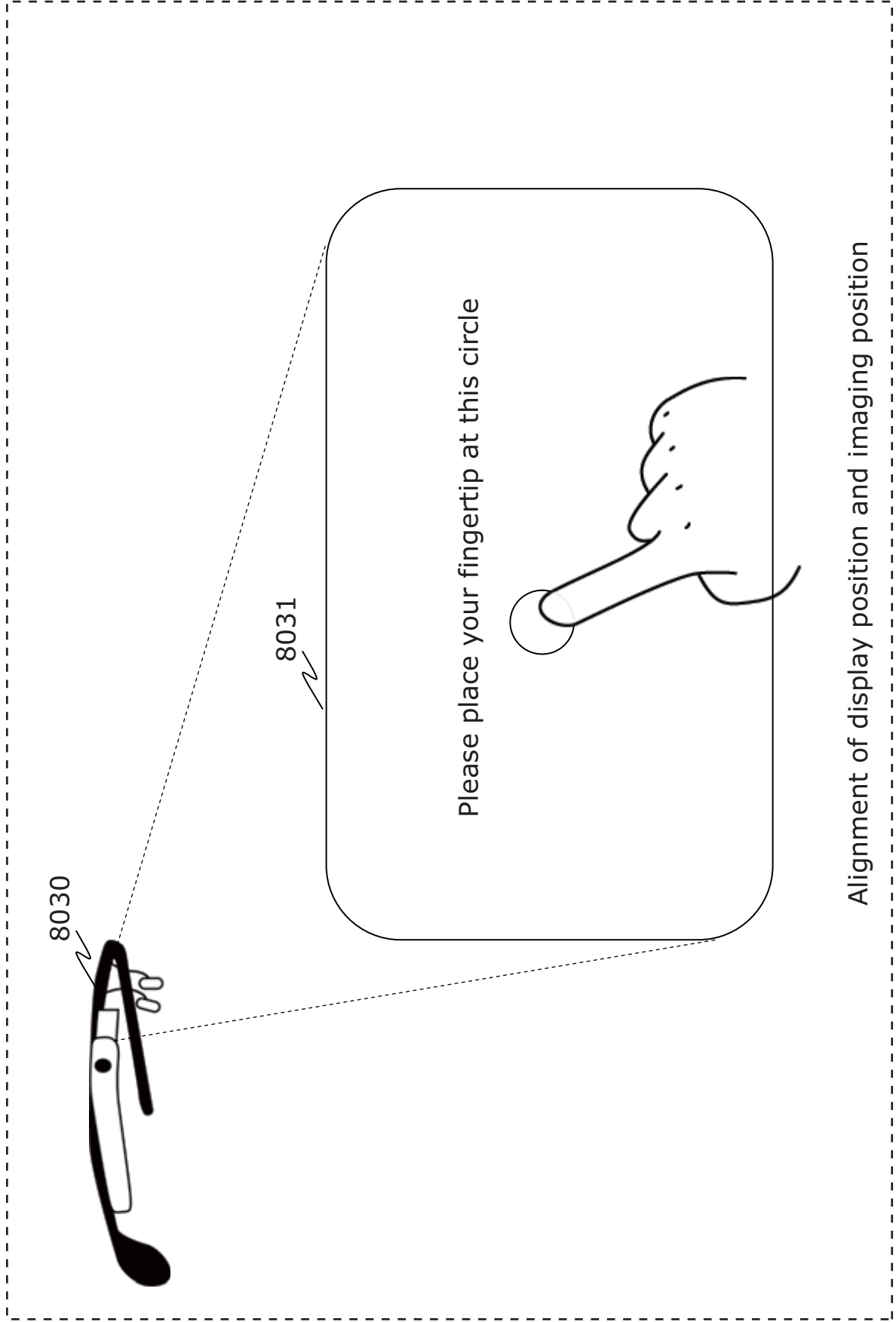


FIG. 25

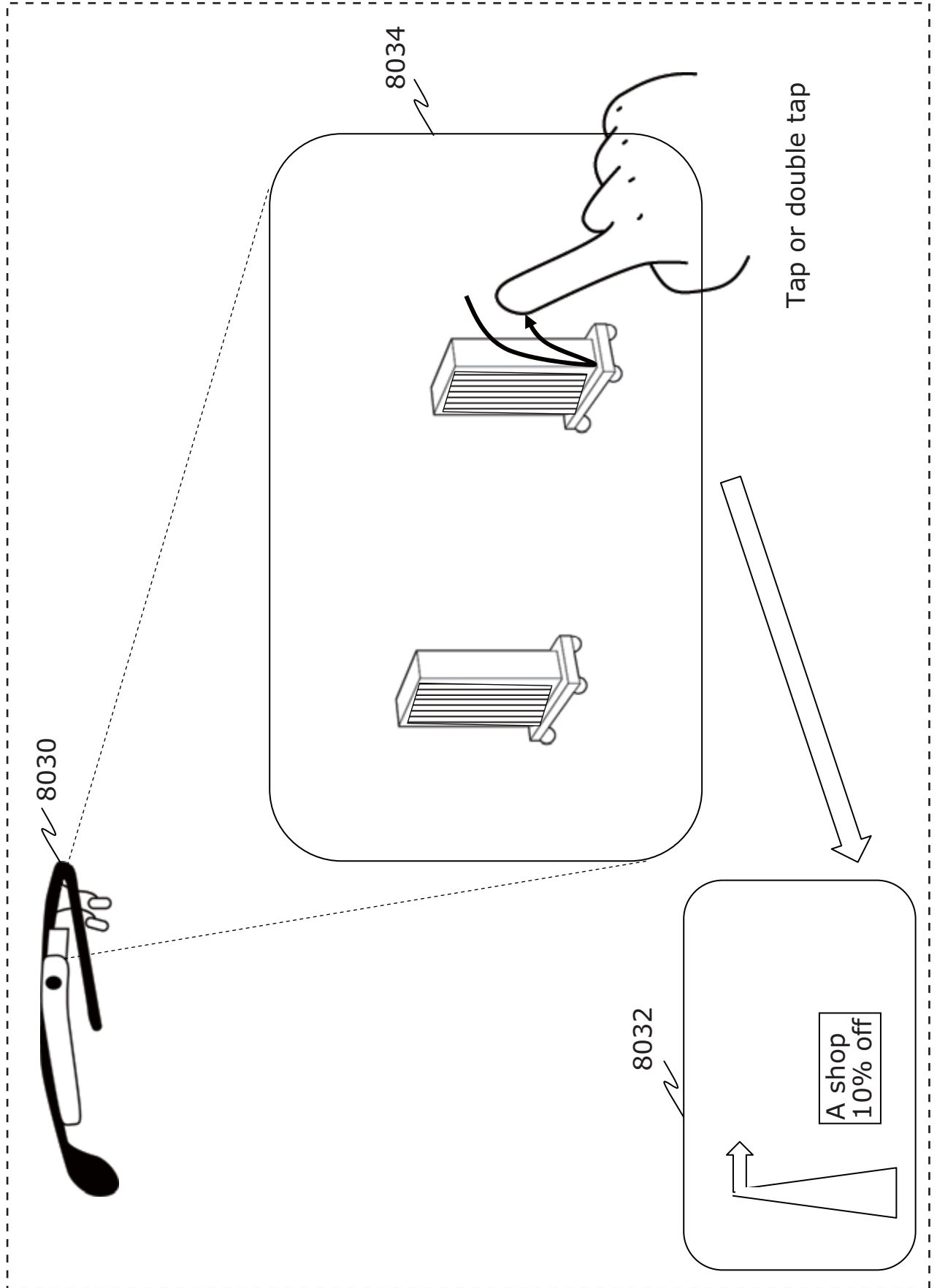


FIG. 26

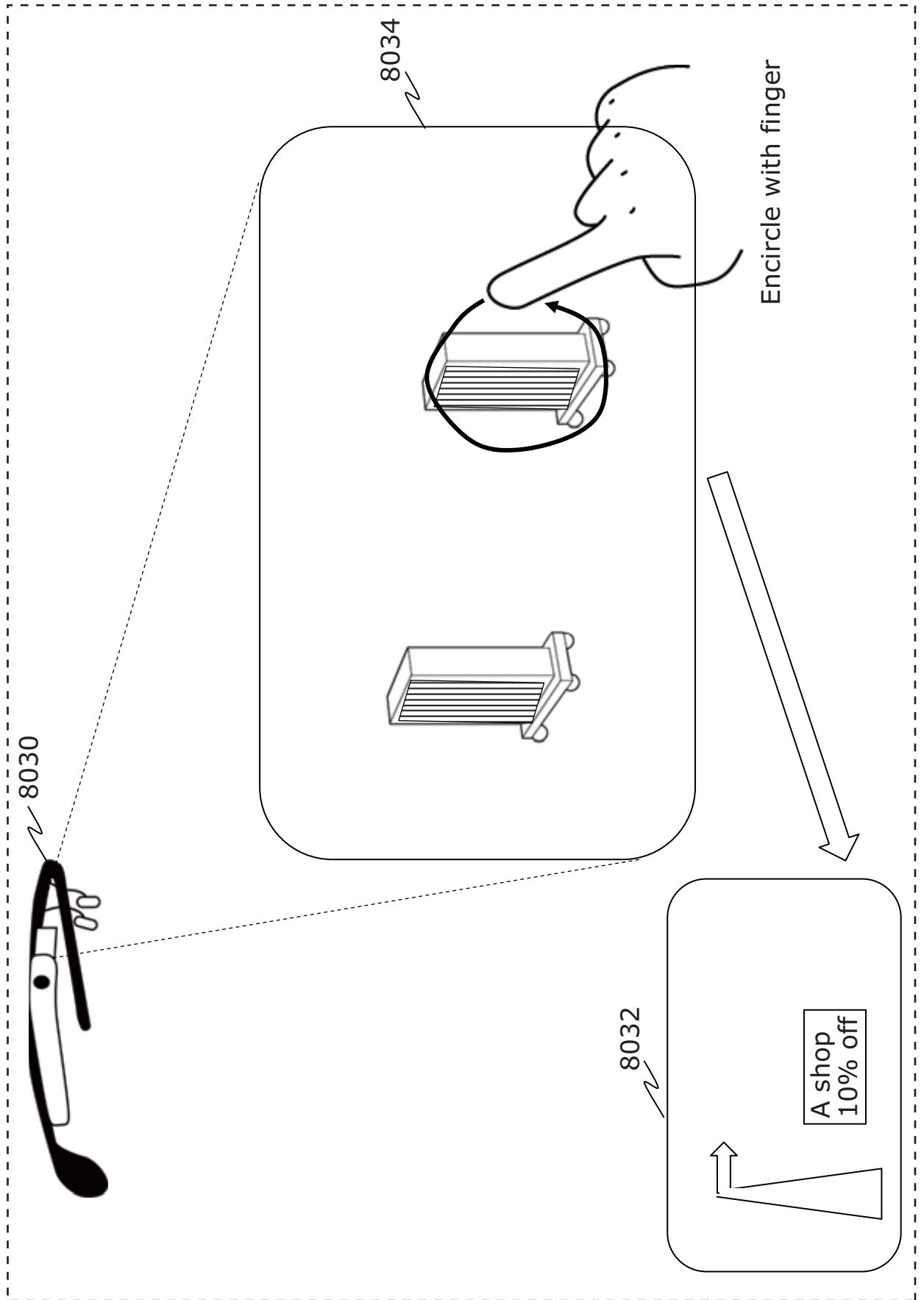


FIG. 27

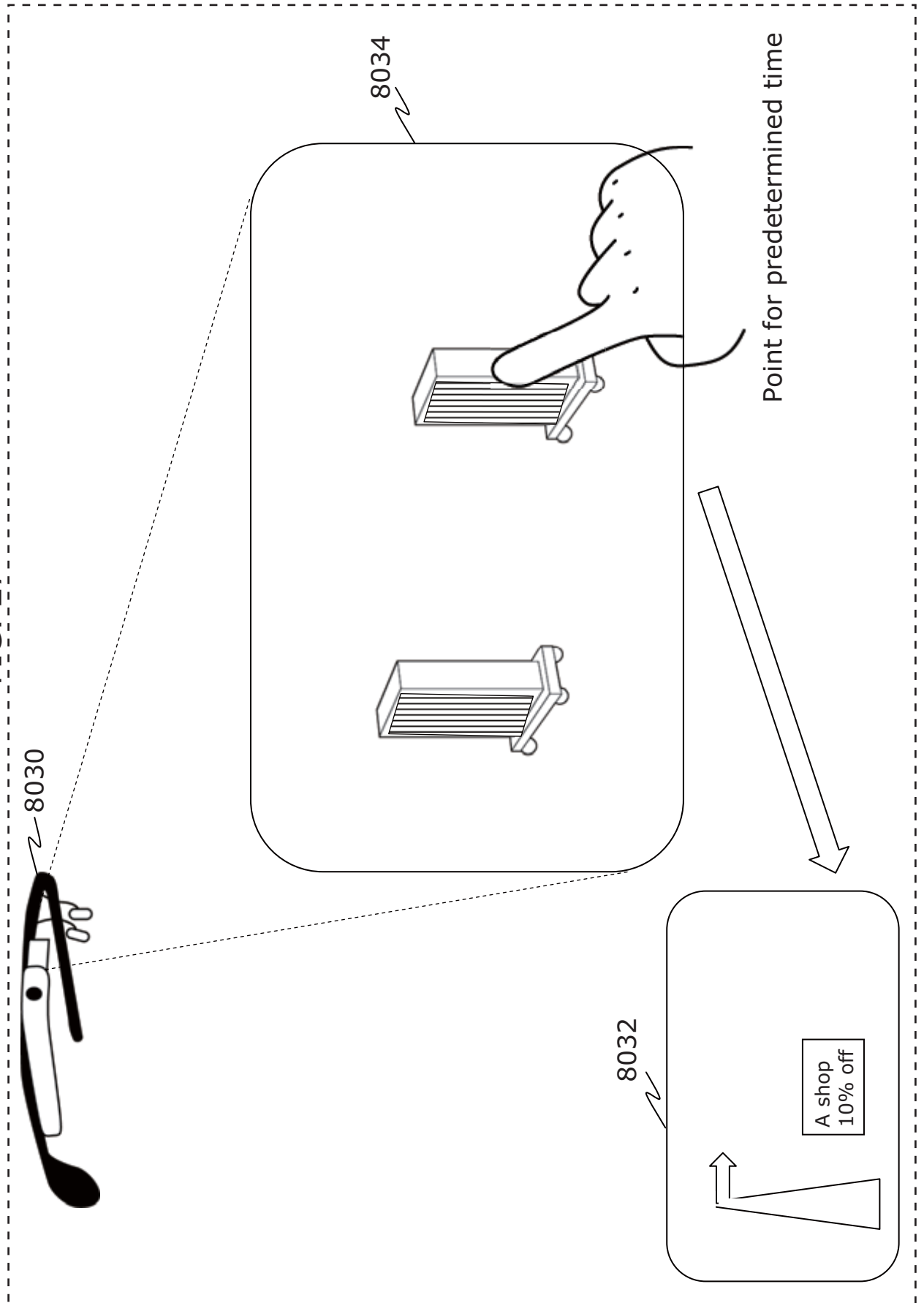


FIG. 28

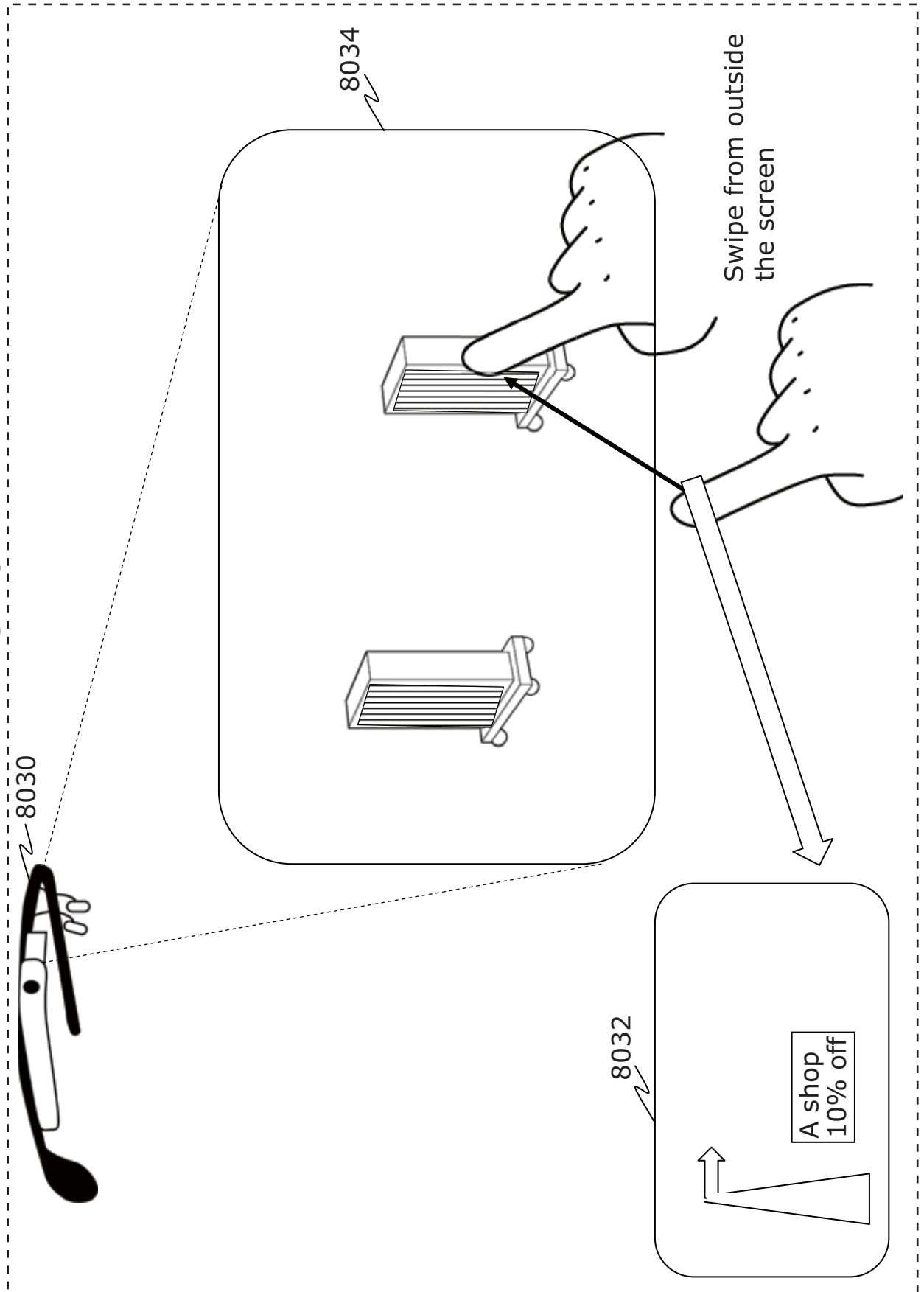


FIG. 29

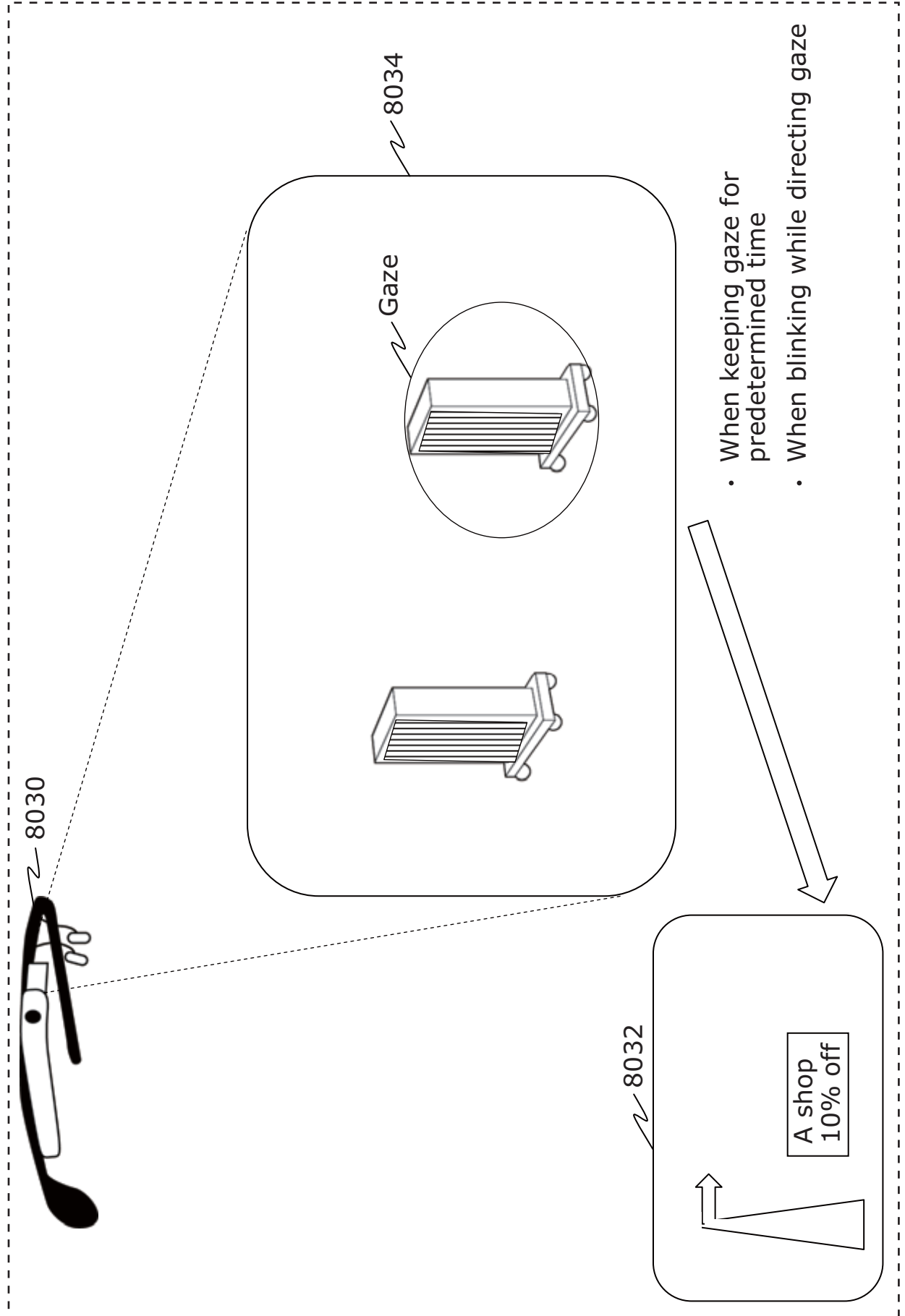


FIG. 30

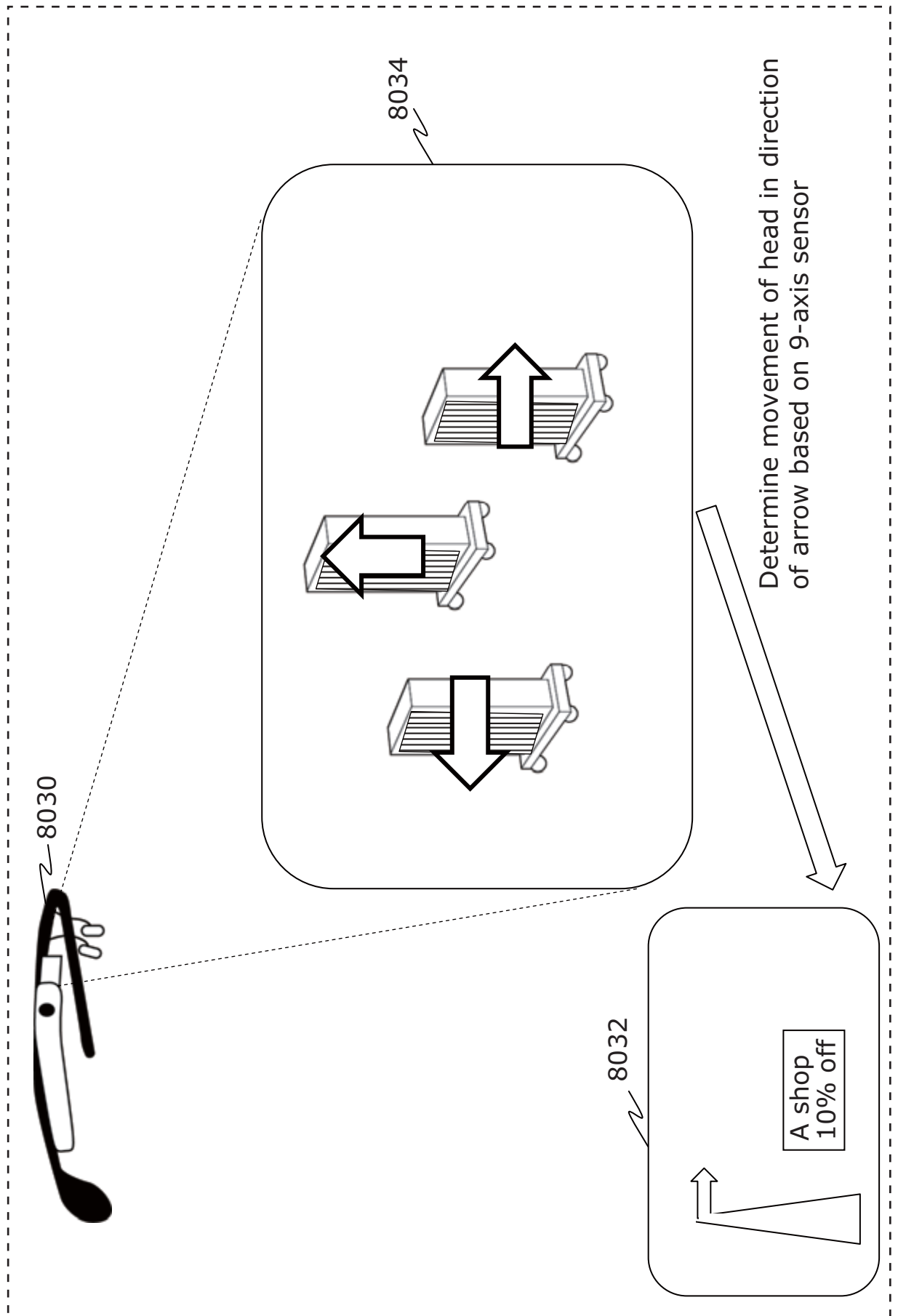


FIG. 31A

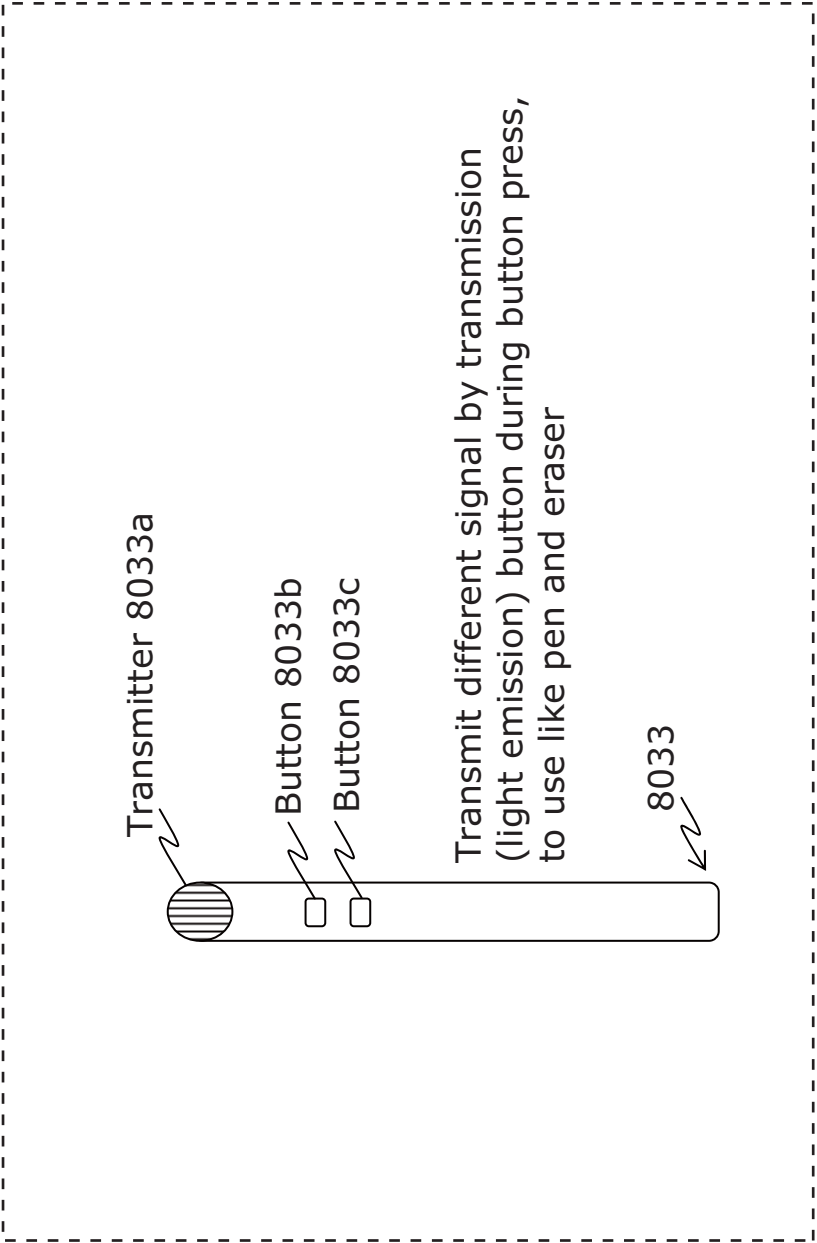


FIG. 31B

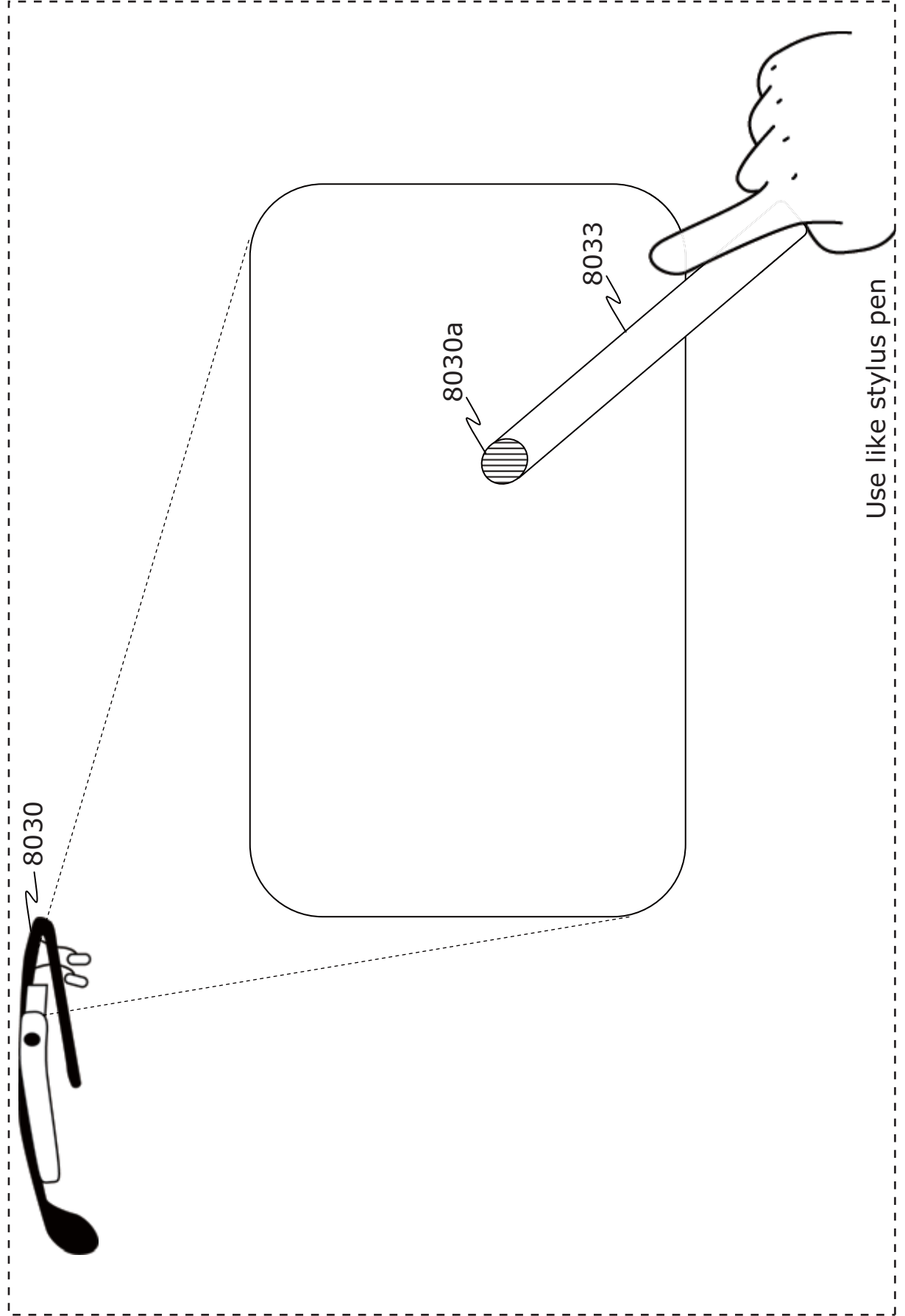


FIG. 32

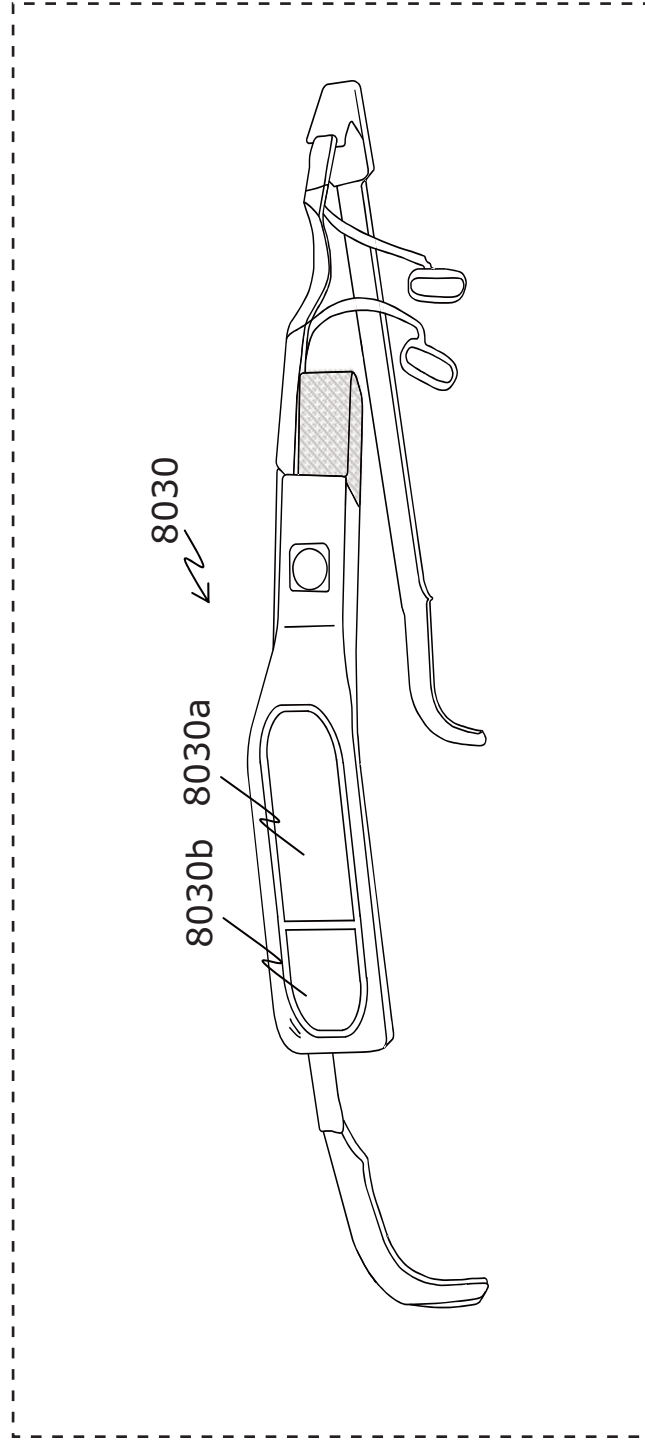


FIG. 33

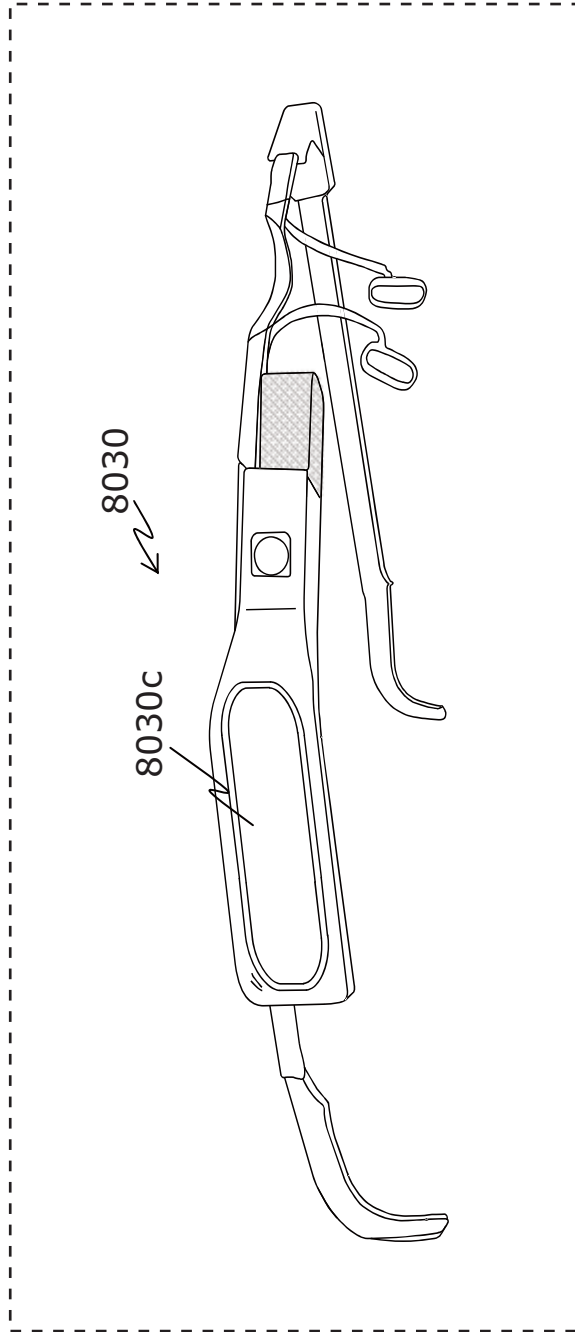


FIG. 34

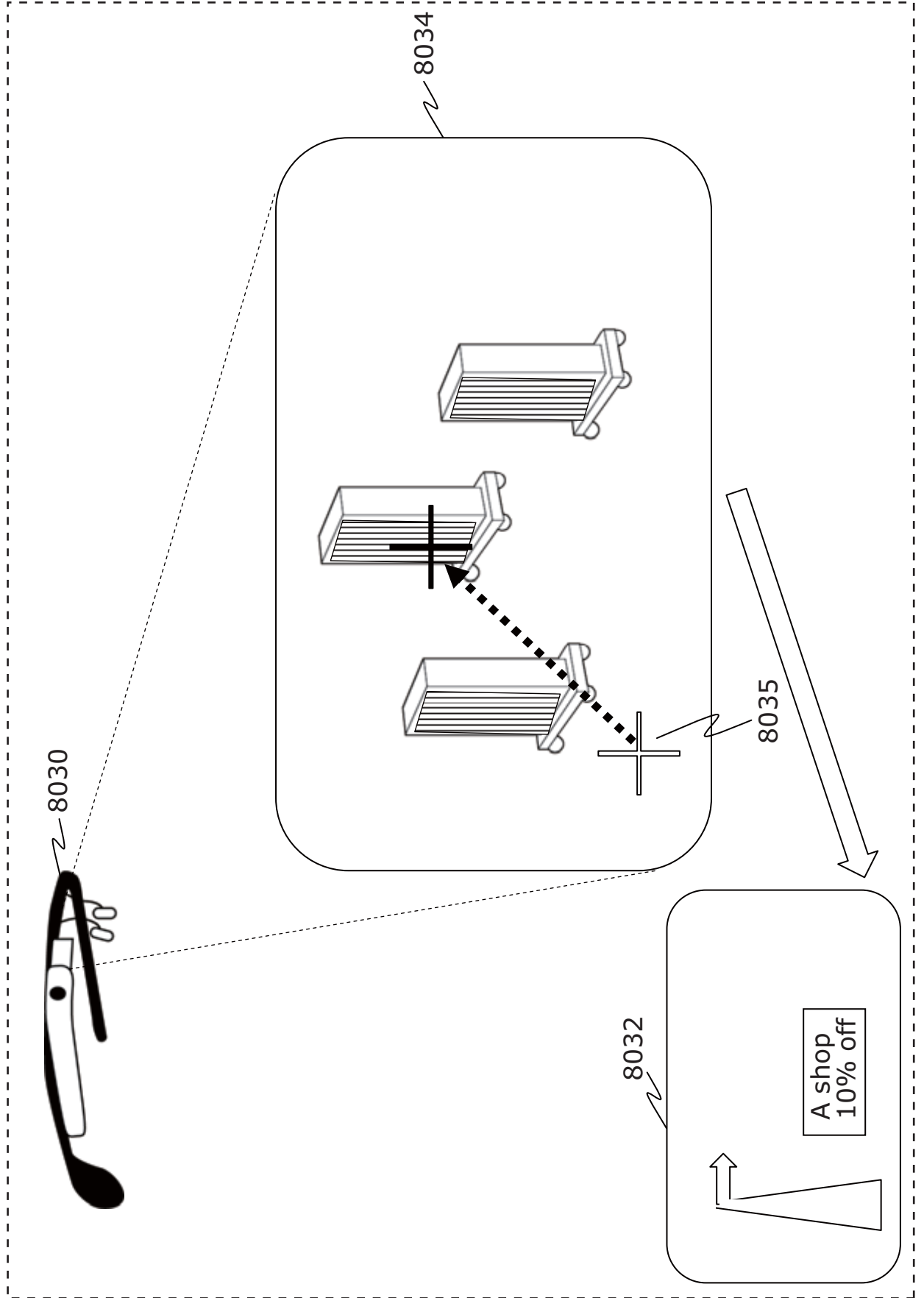


FIG. 35A

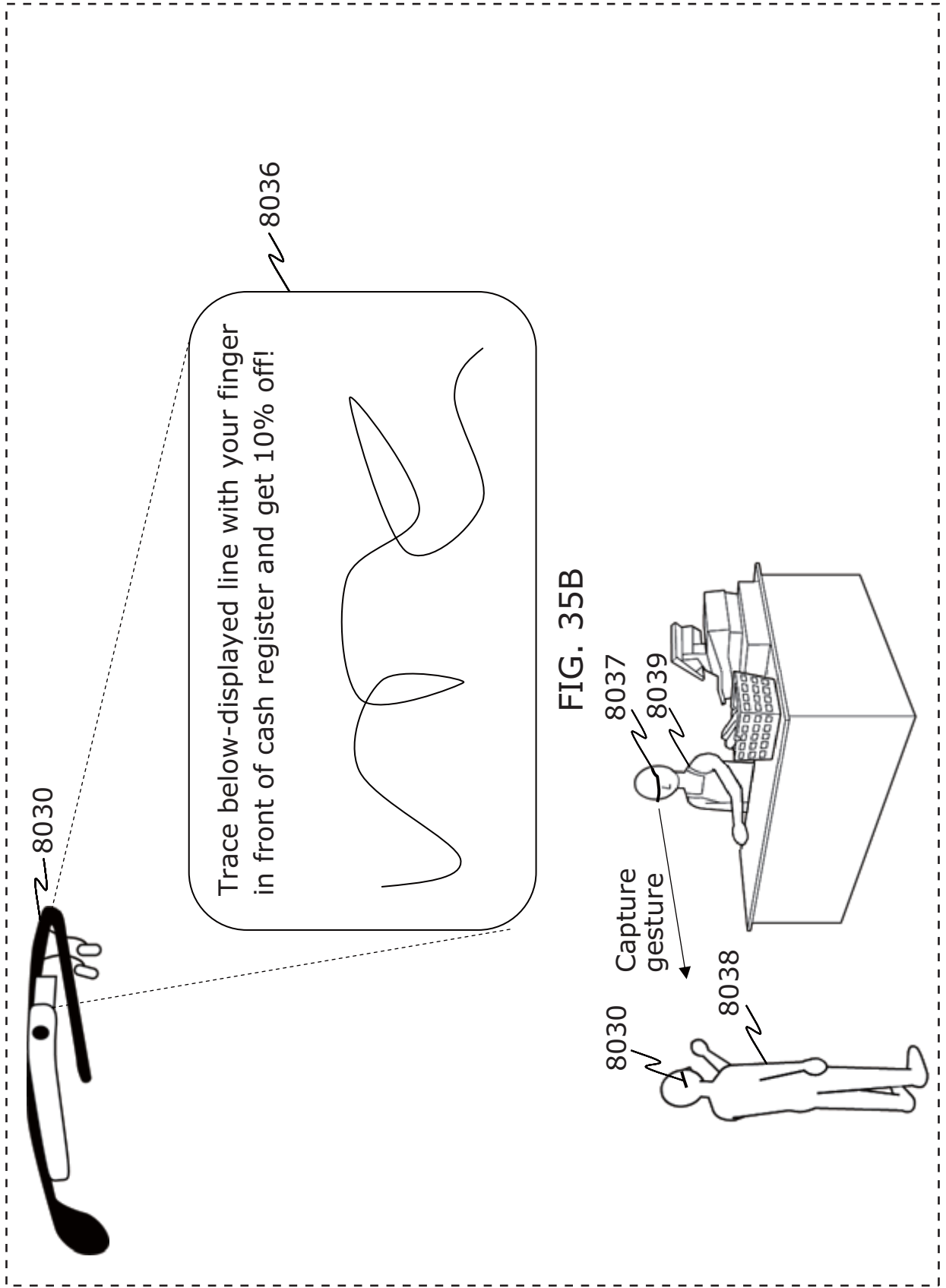


FIG. 36A

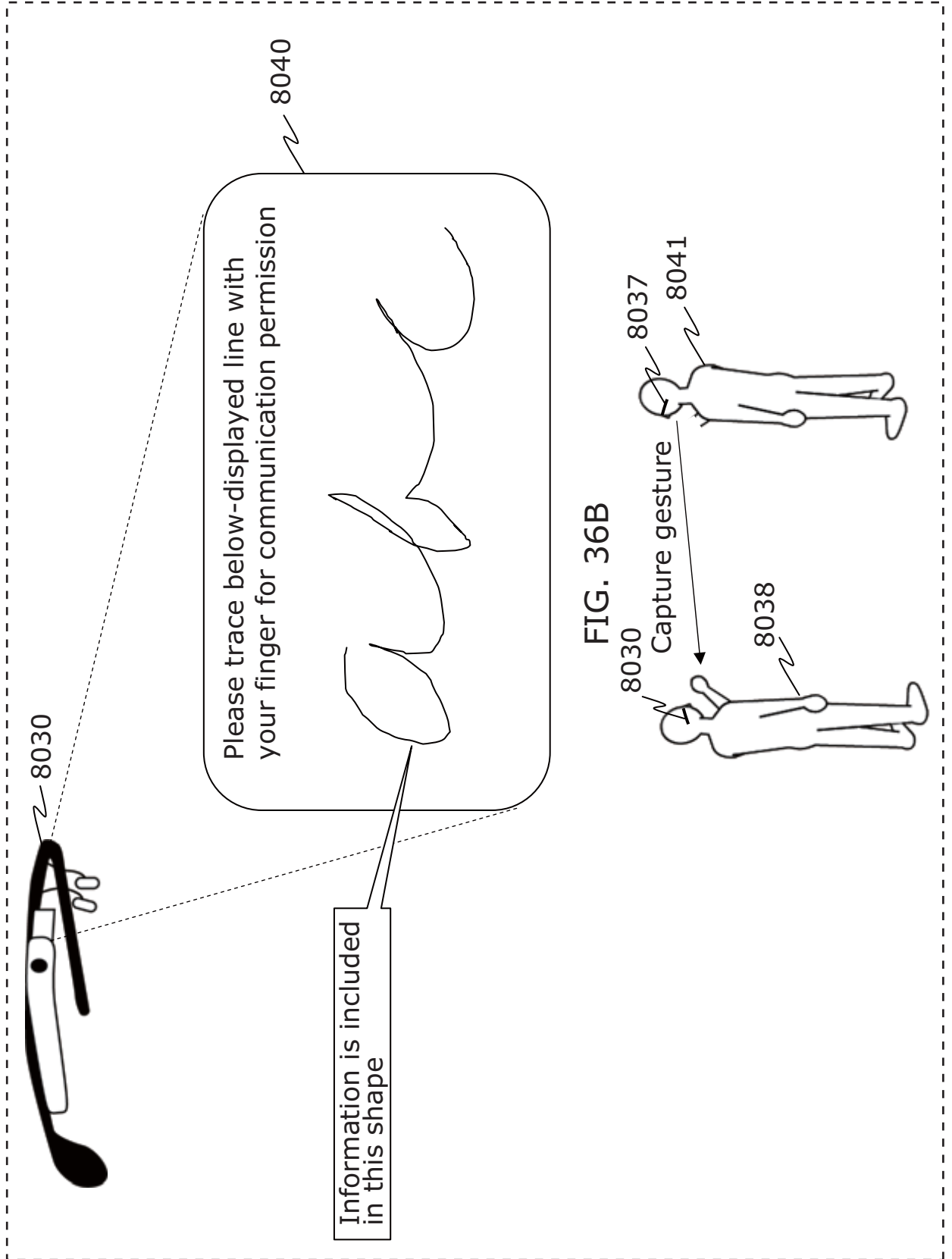


FIG. 36B

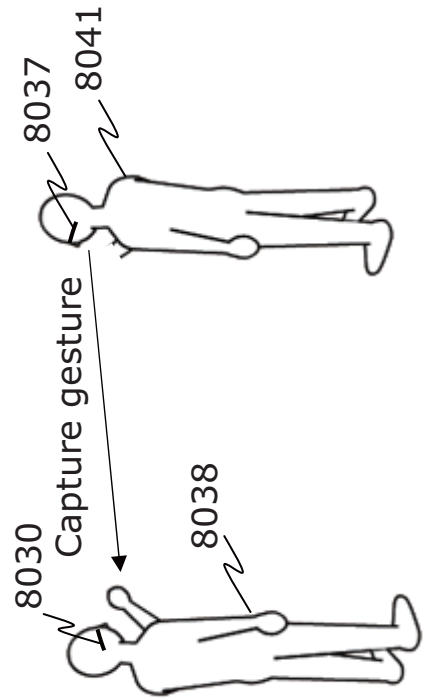


FIG. 37A

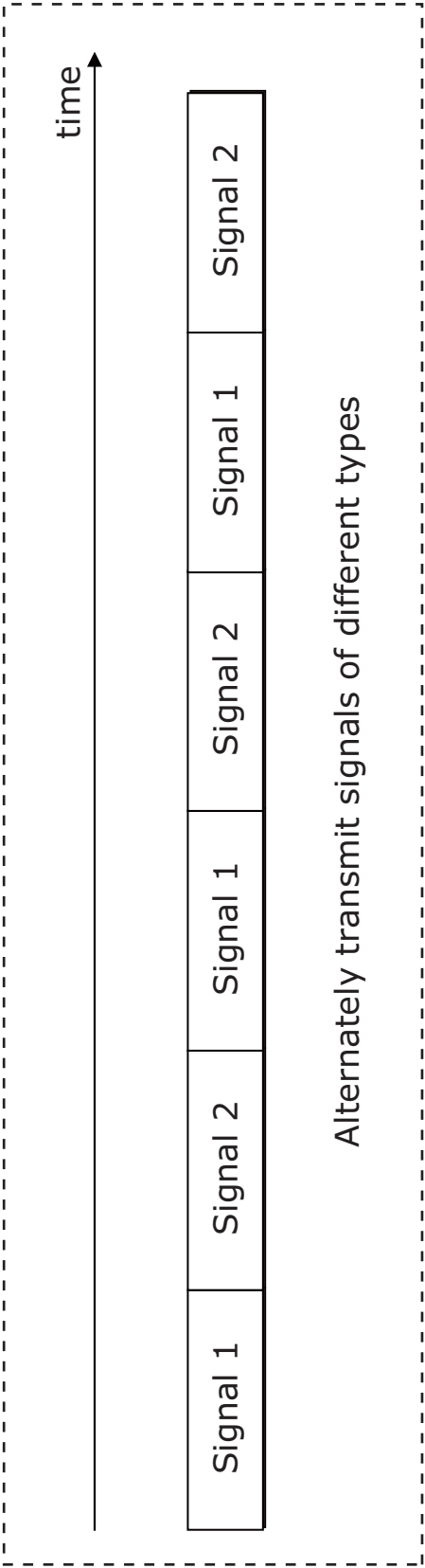


FIG. 37B

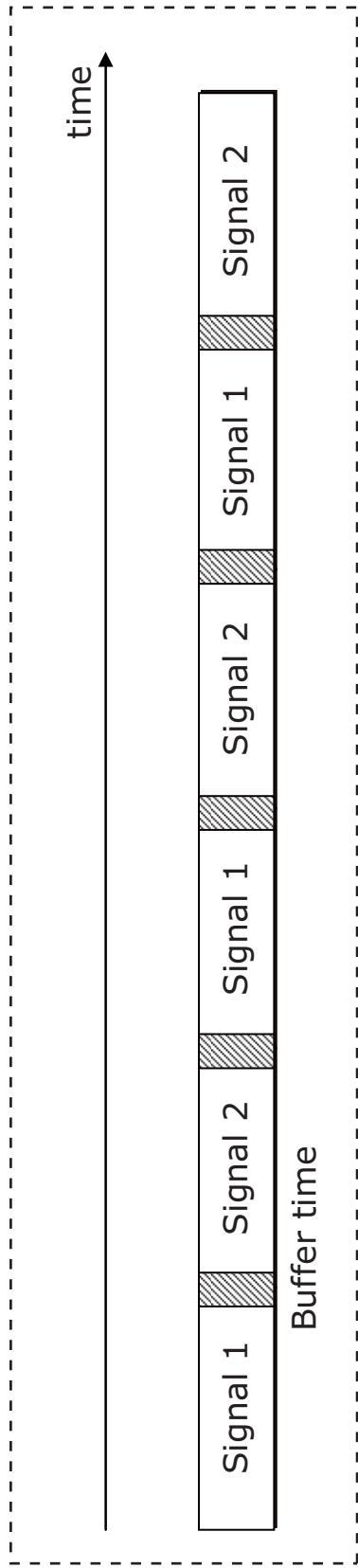


FIG. 38

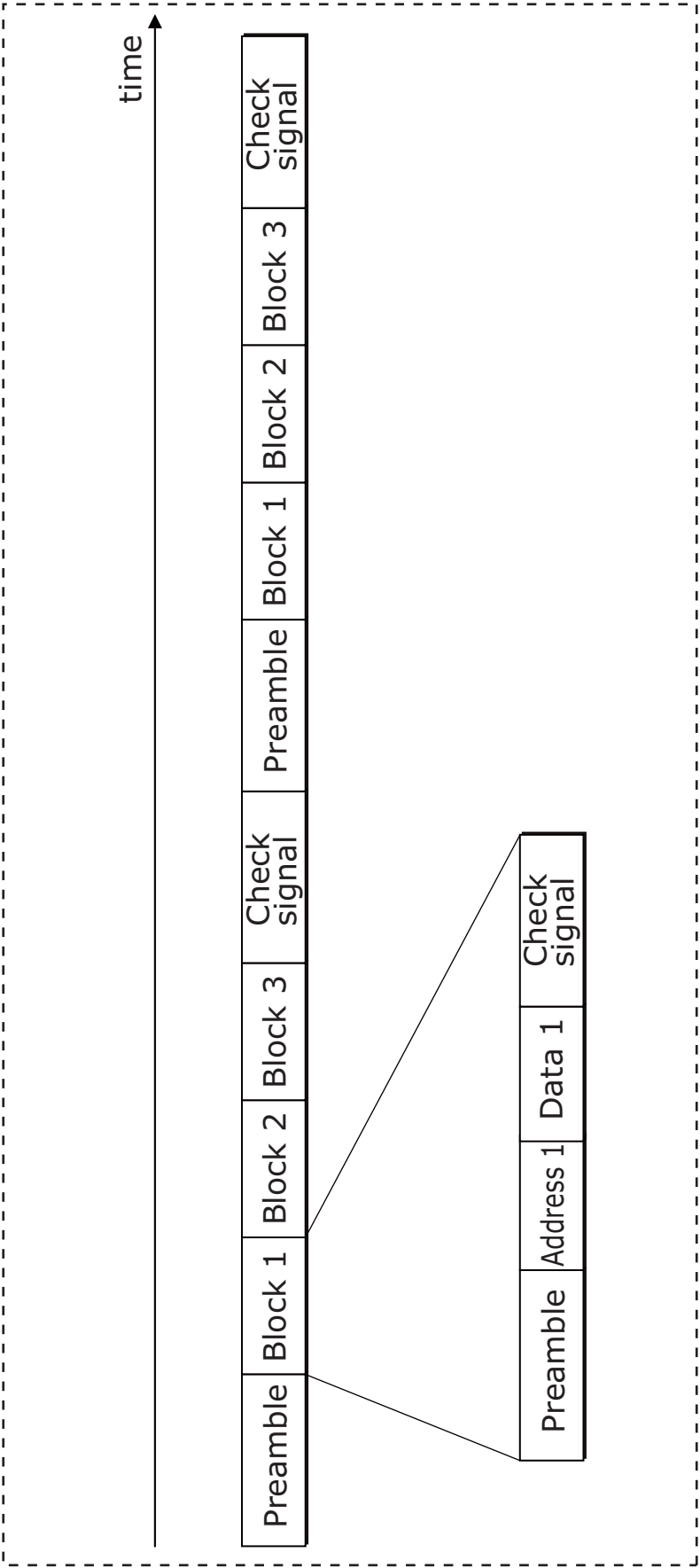


FIG. 39

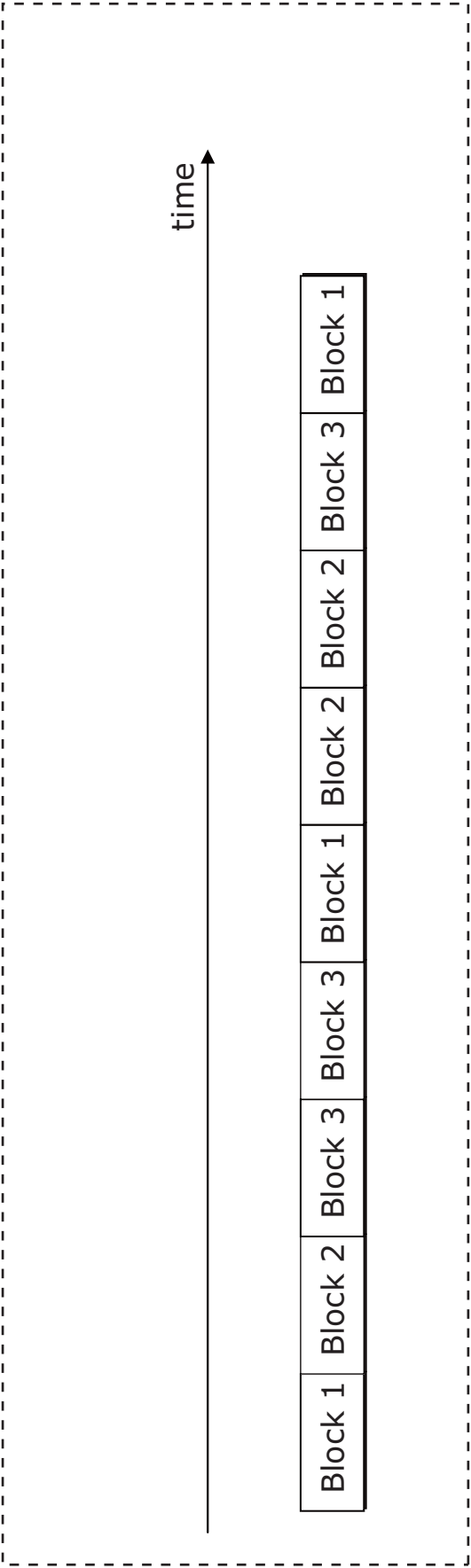


FIG. 40

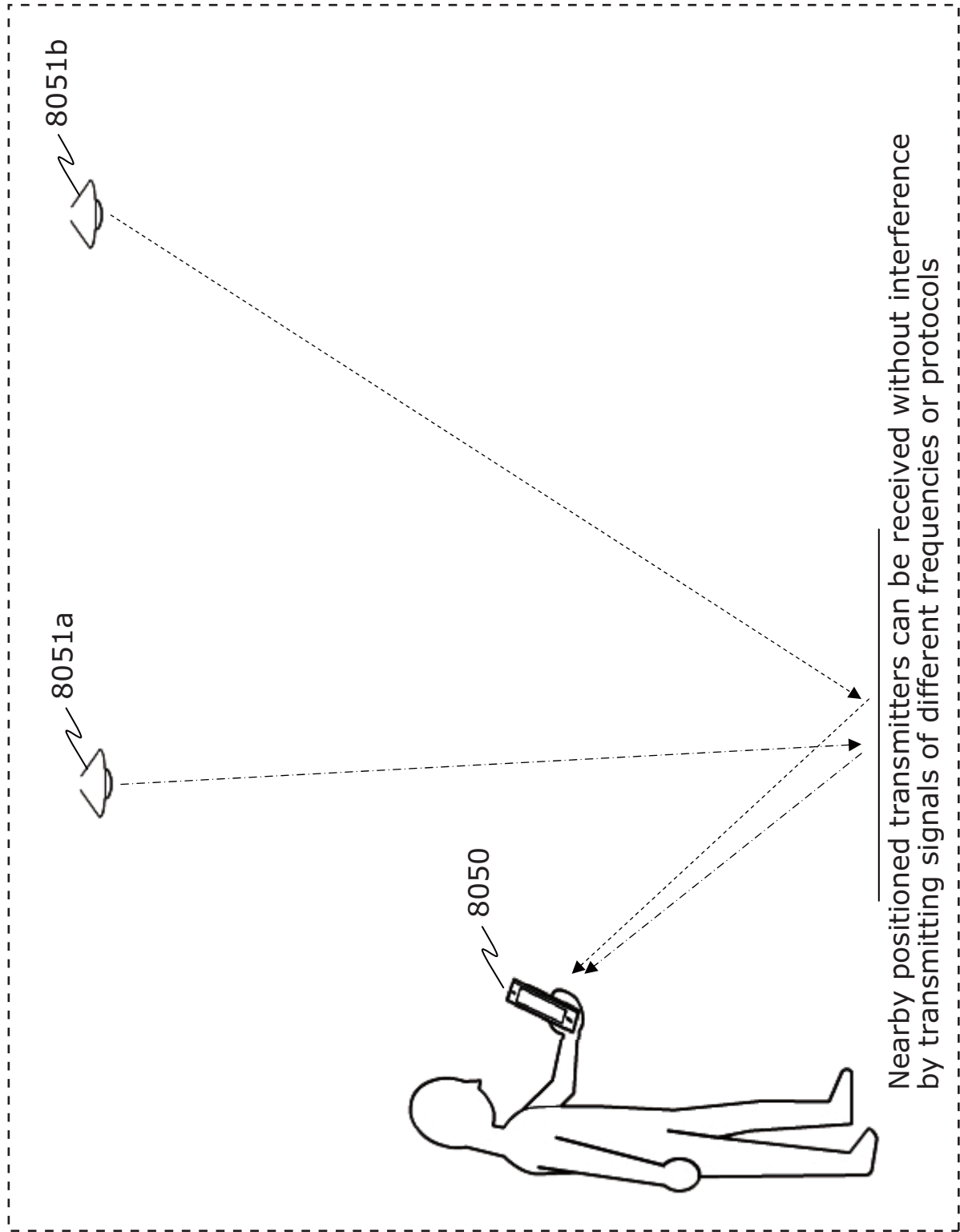


FIG. 41

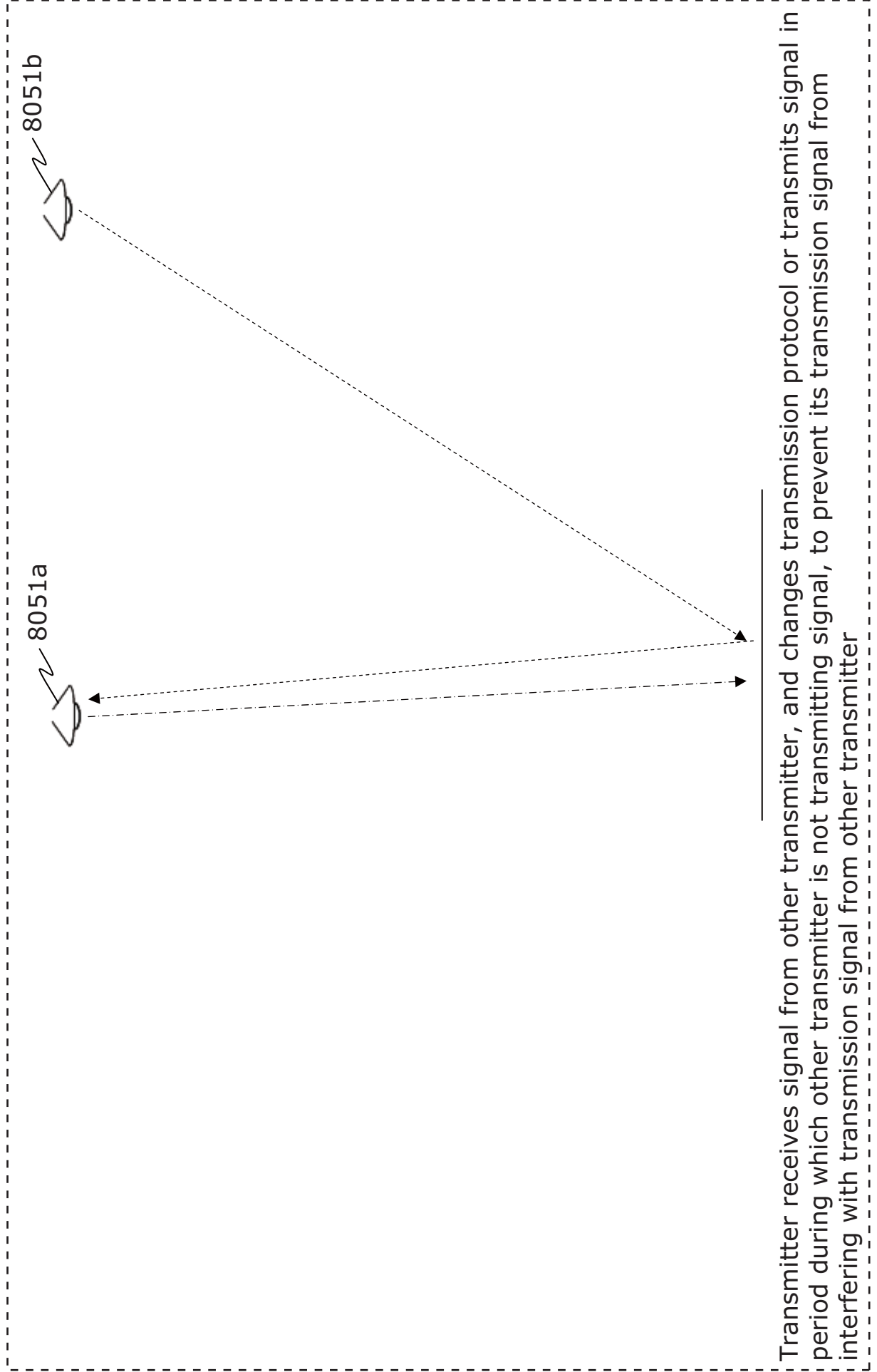


FIG. 42

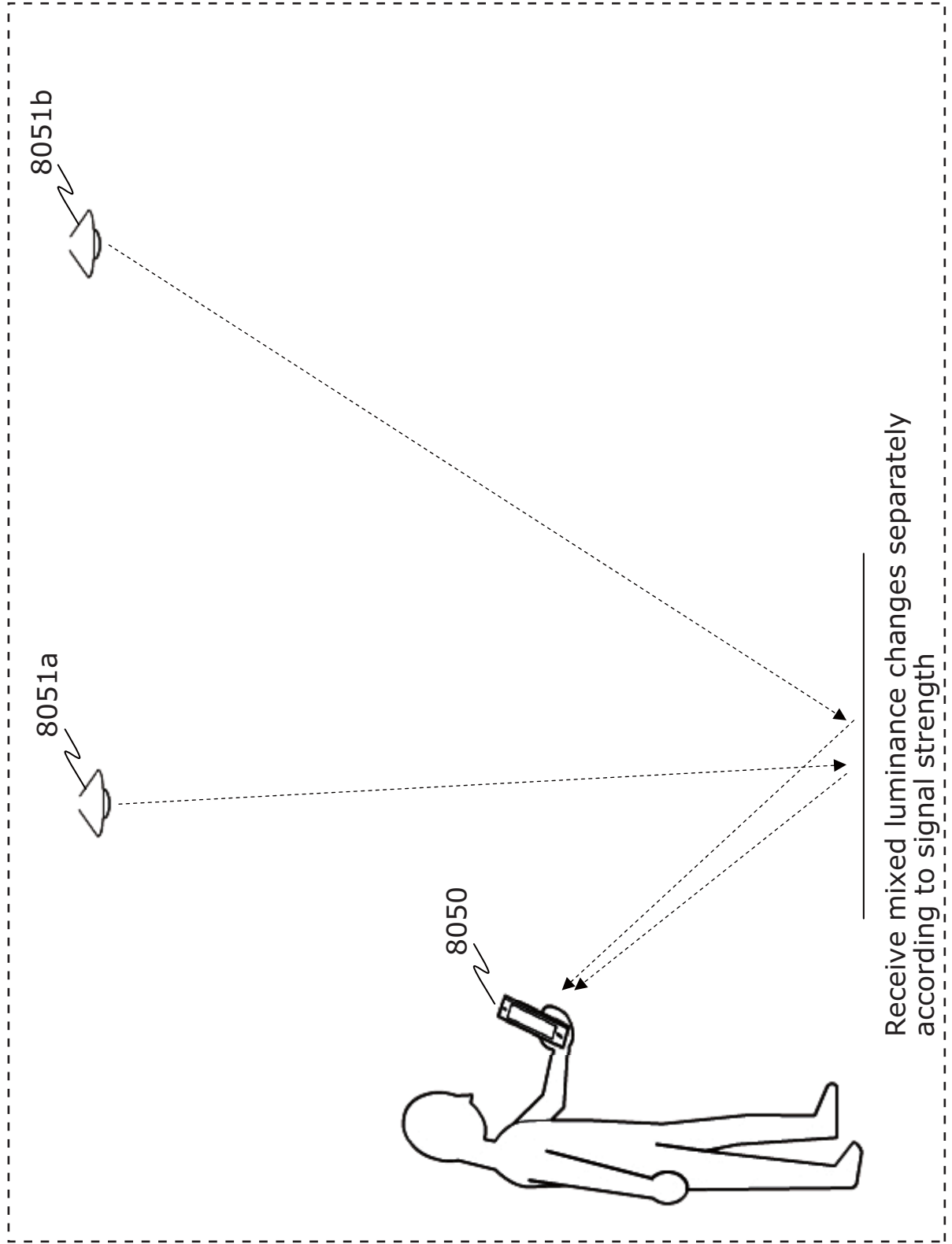


FIG. 43

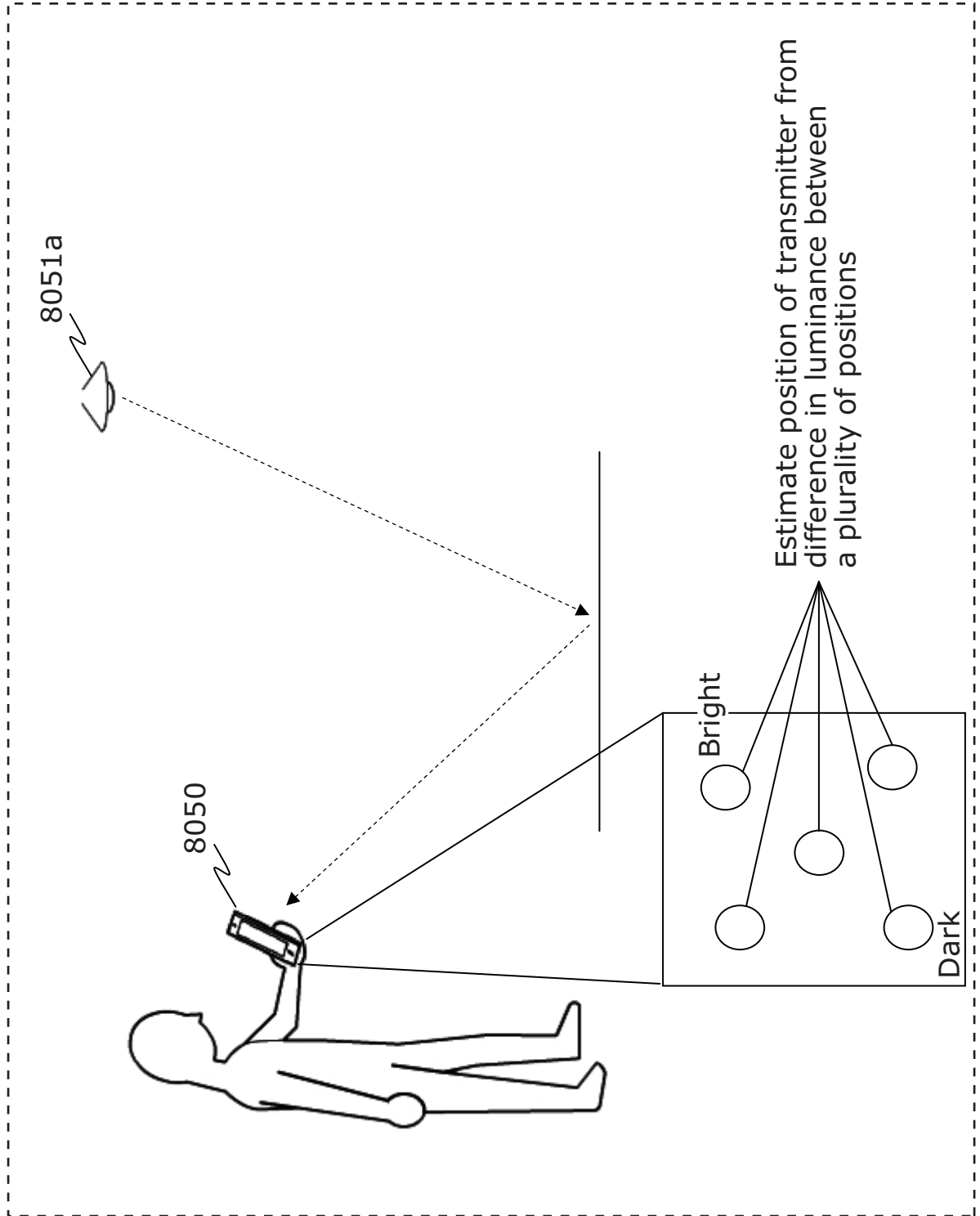


FIG. 44

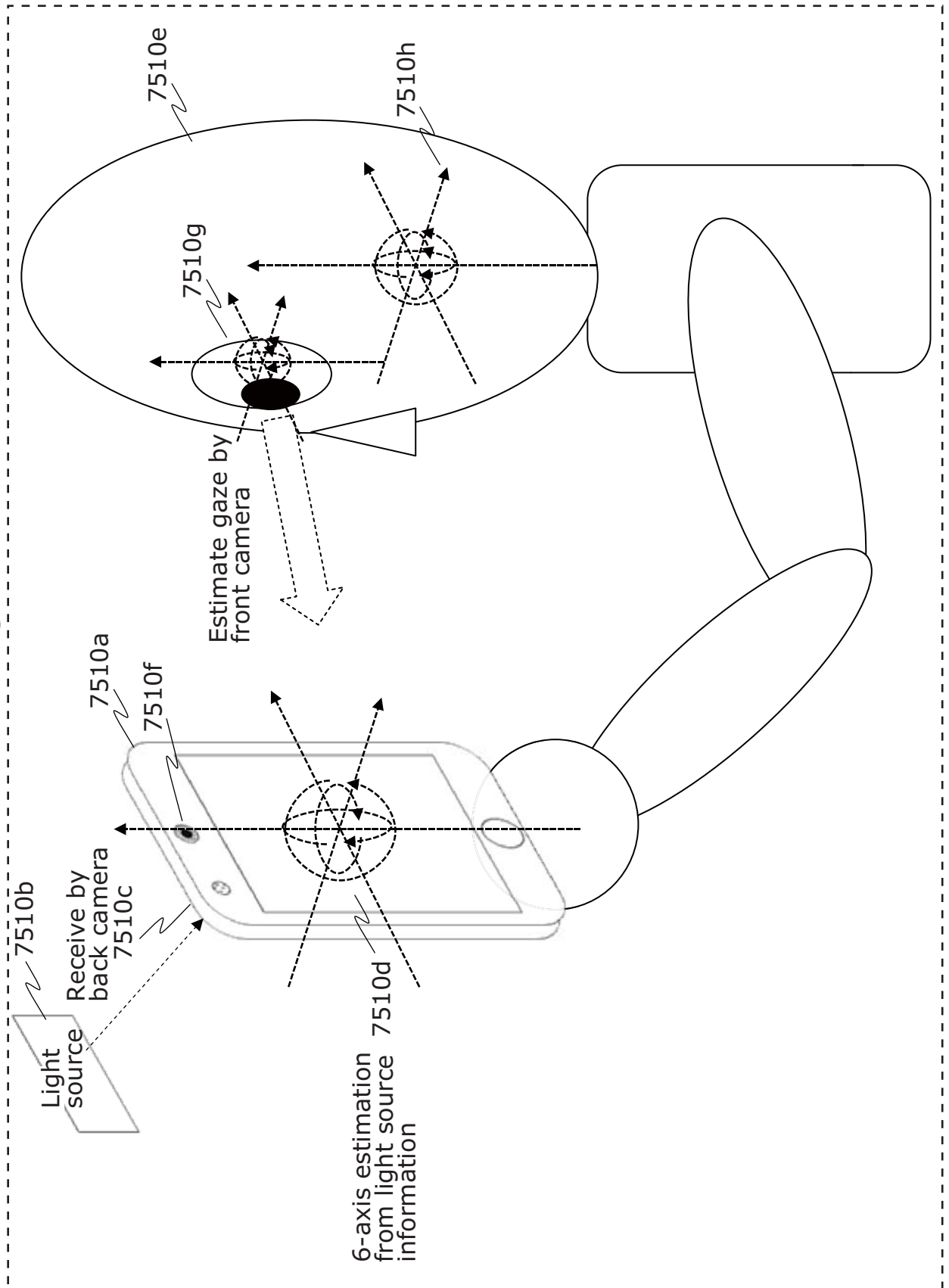


FIG. 45

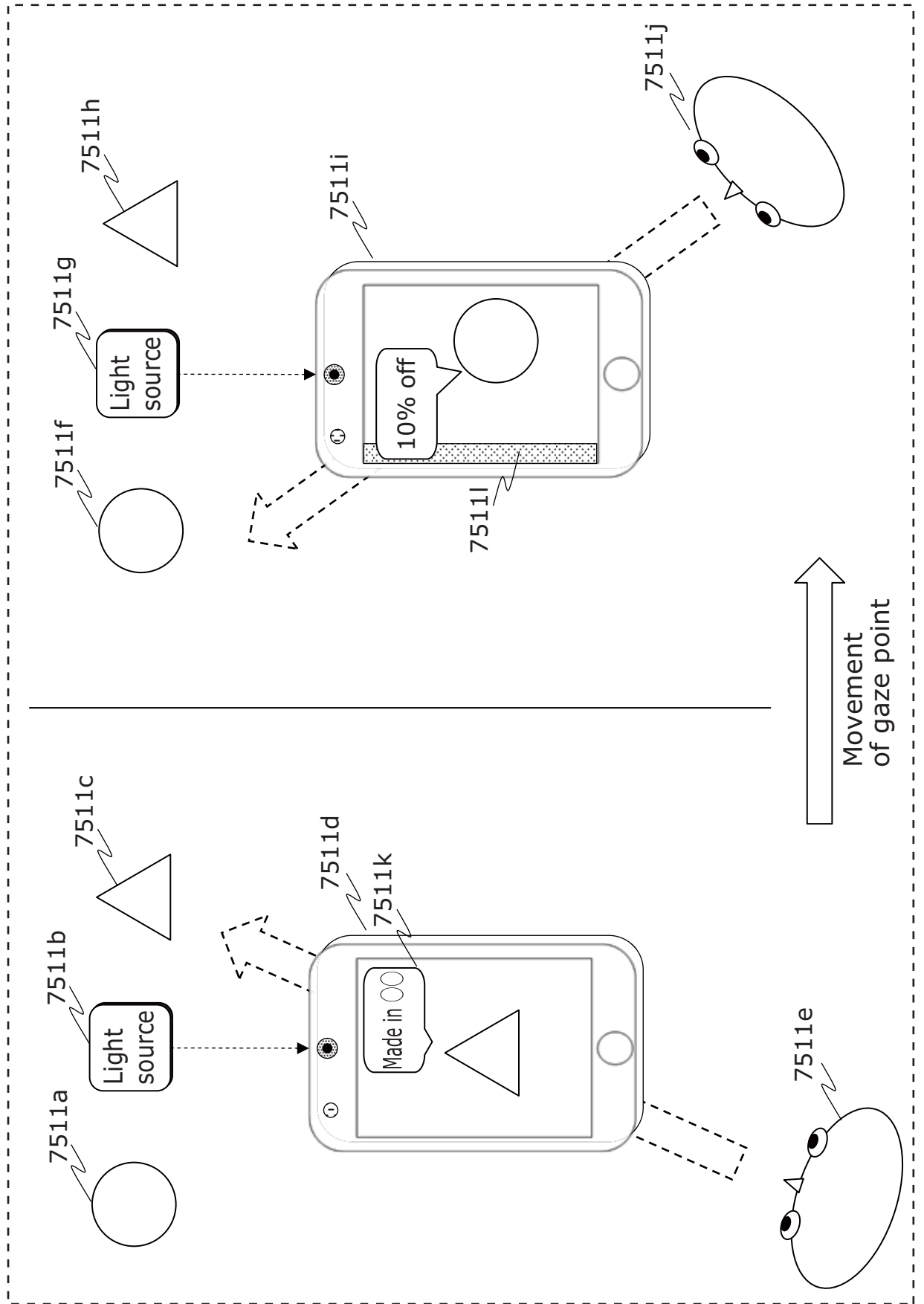


FIG. 46

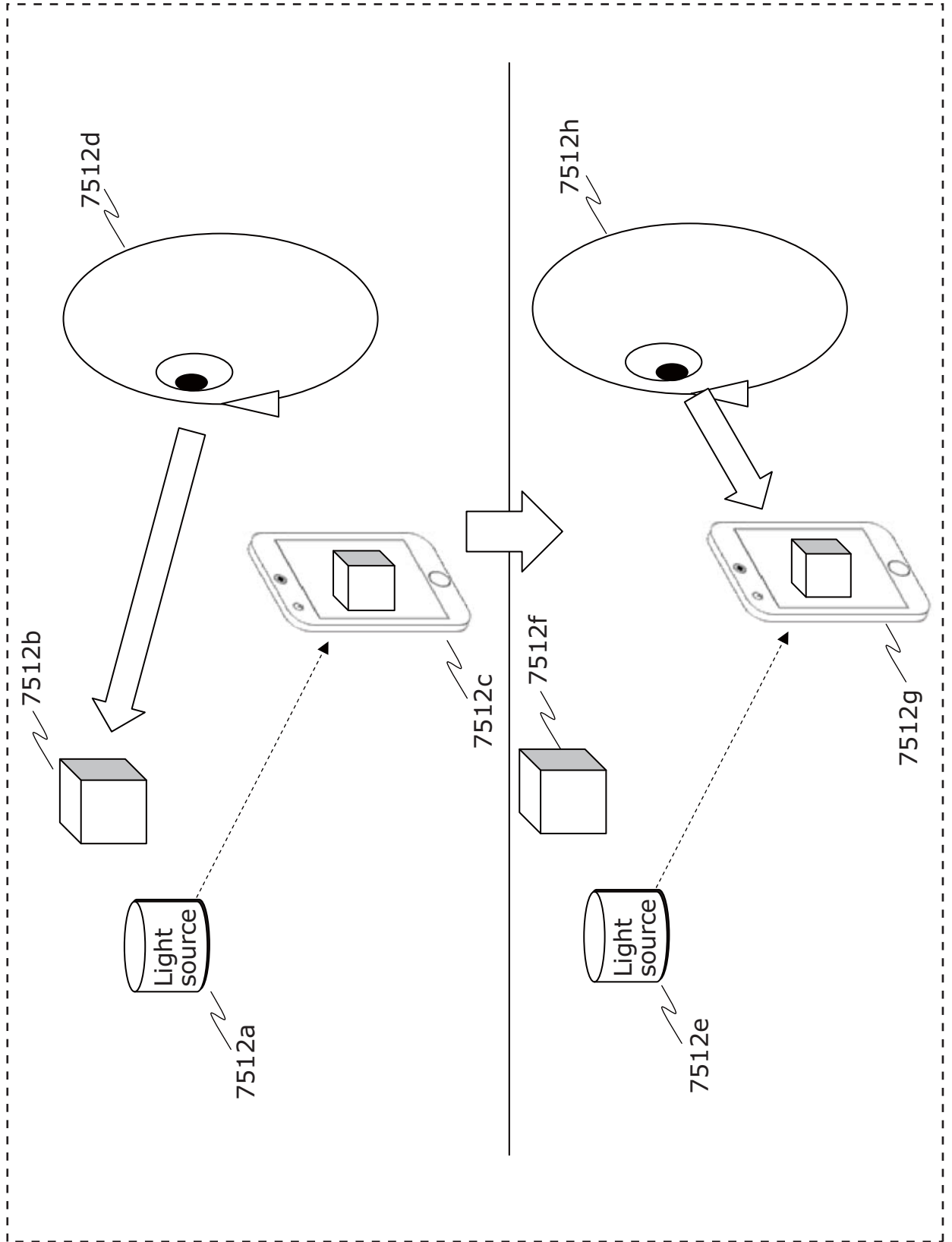


FIG. 47

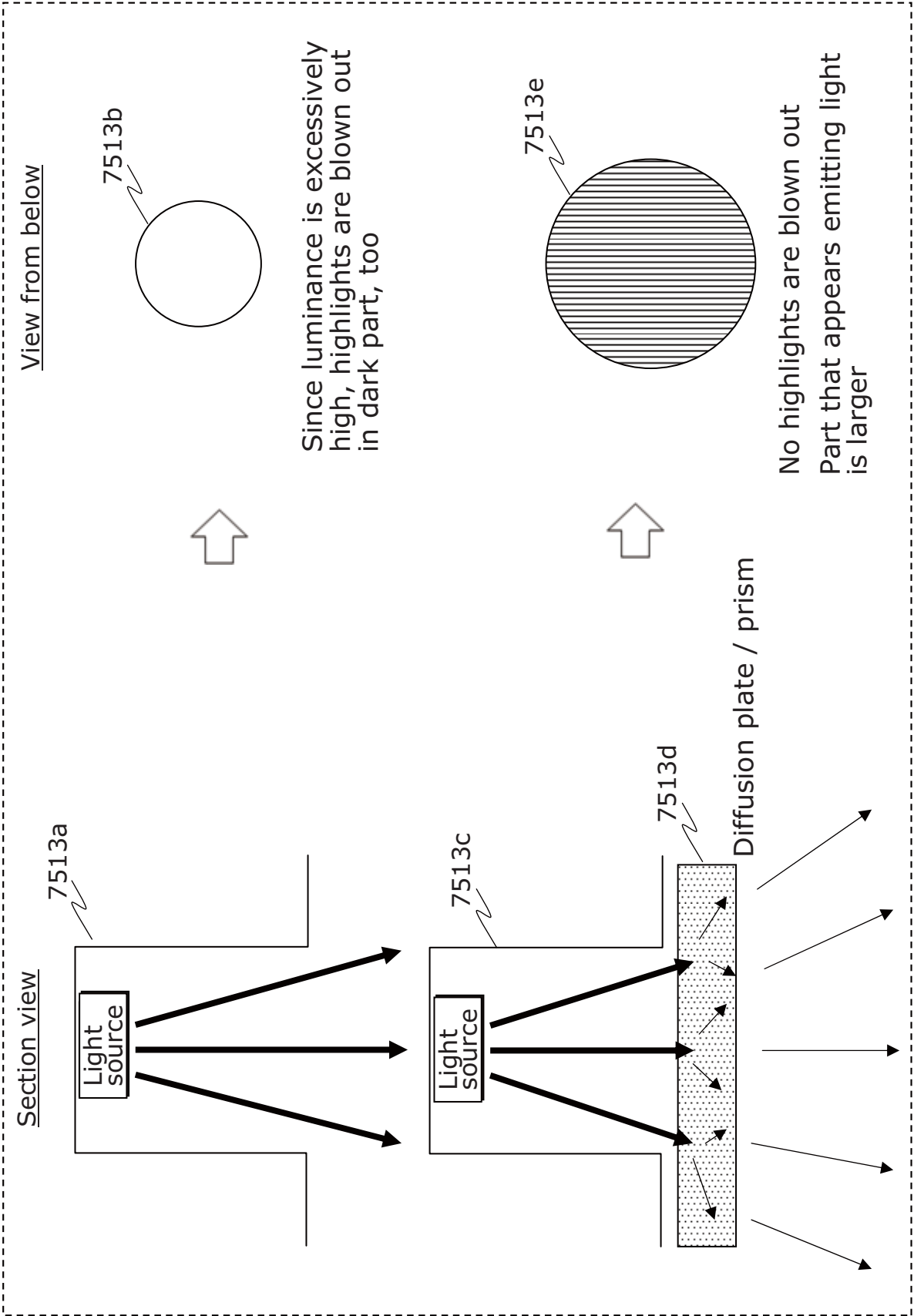


FIG. 48

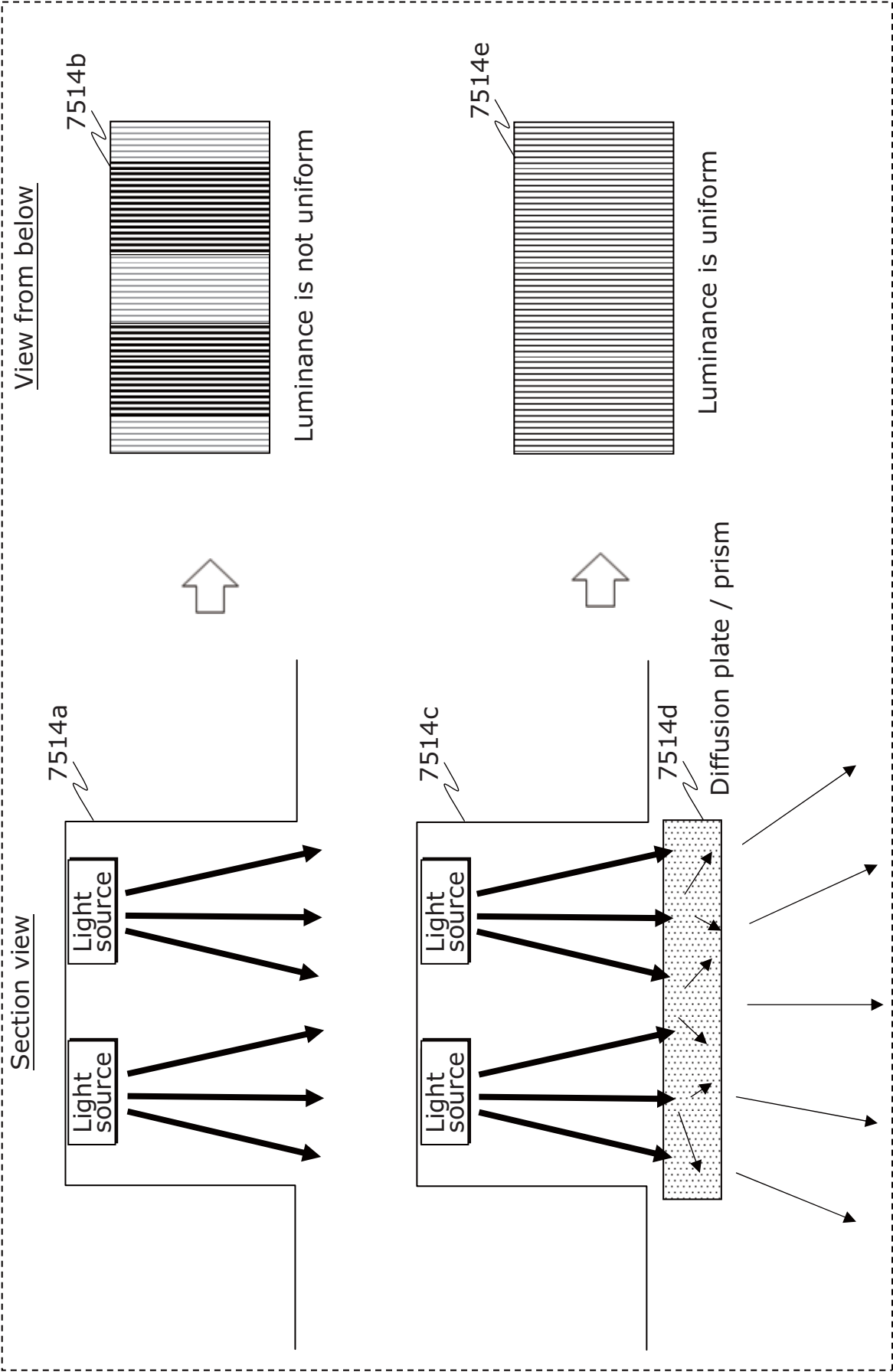


FIG. 49

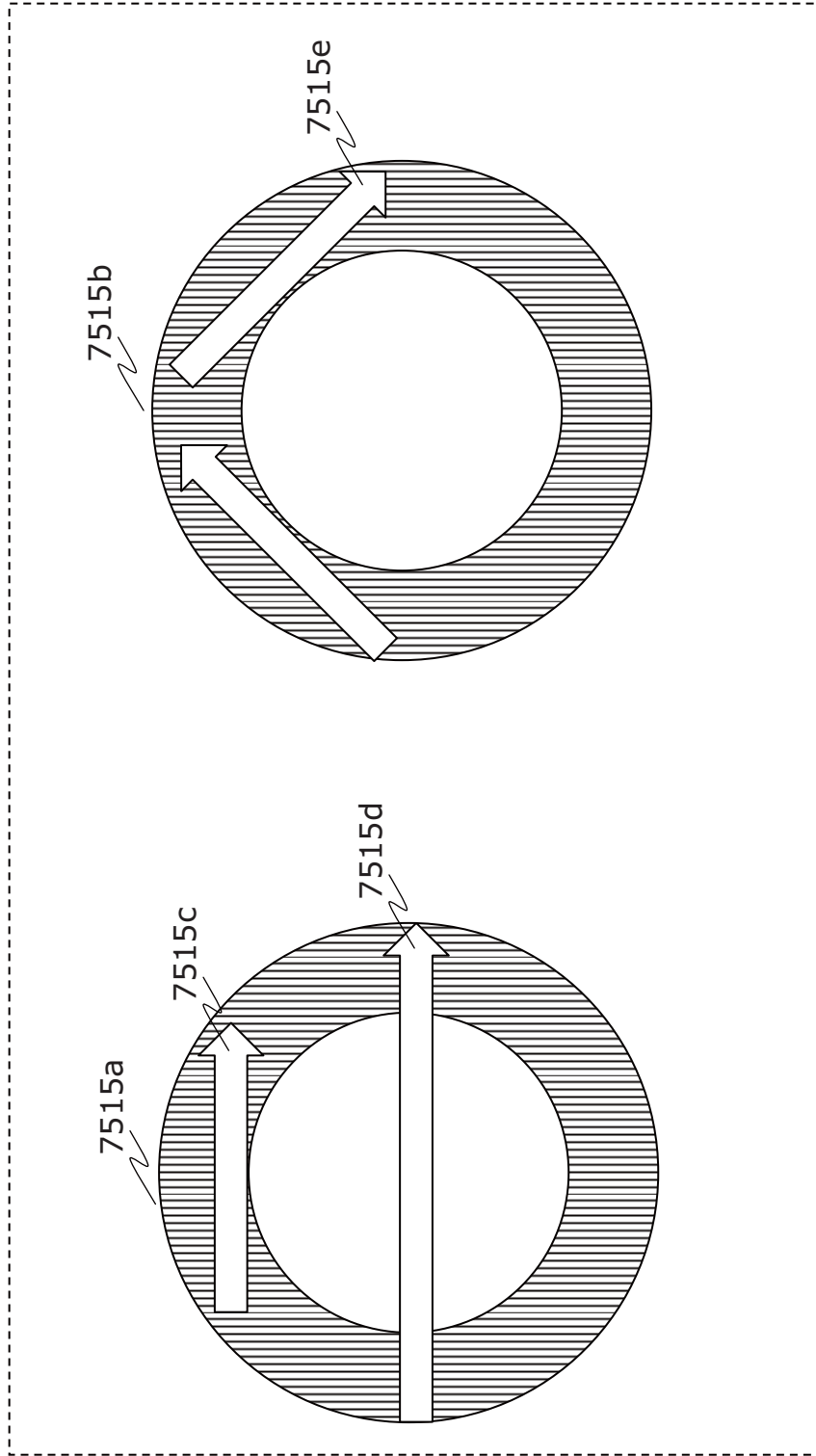


FIG. 50

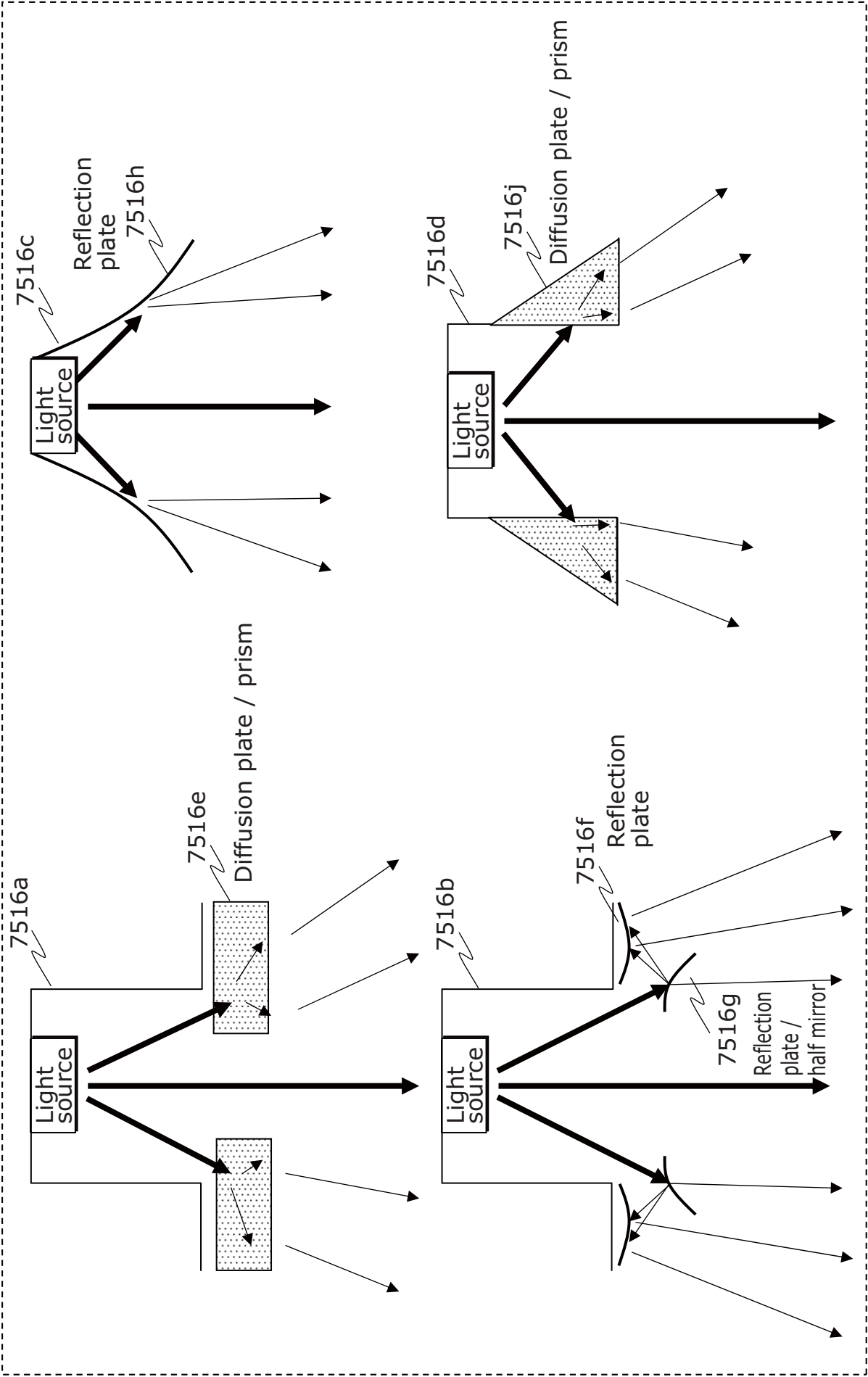


FIG. 51

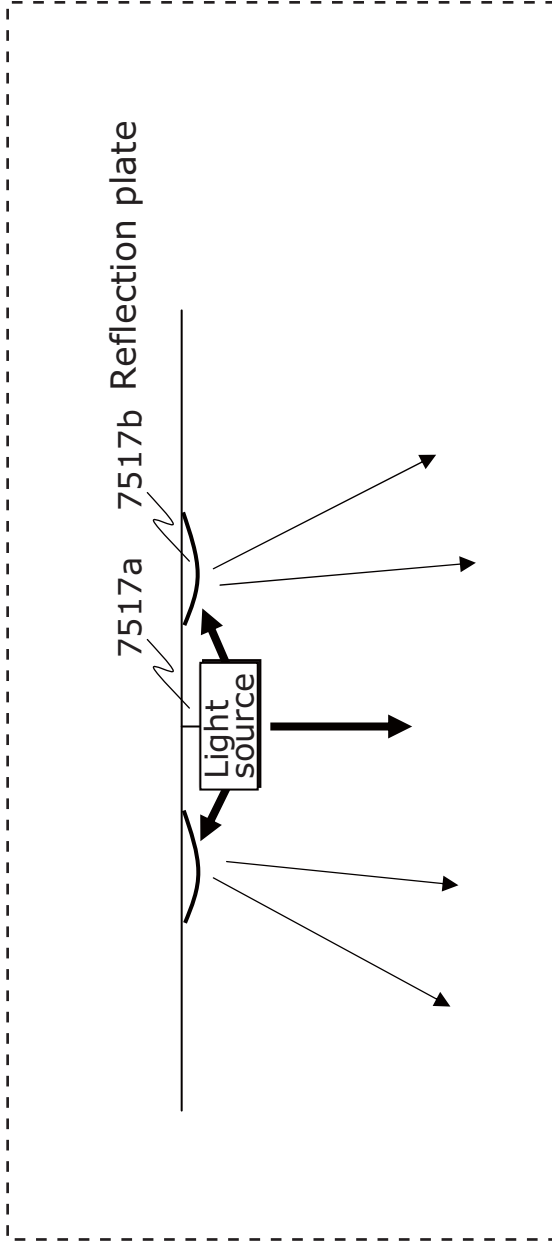


FIG. 52

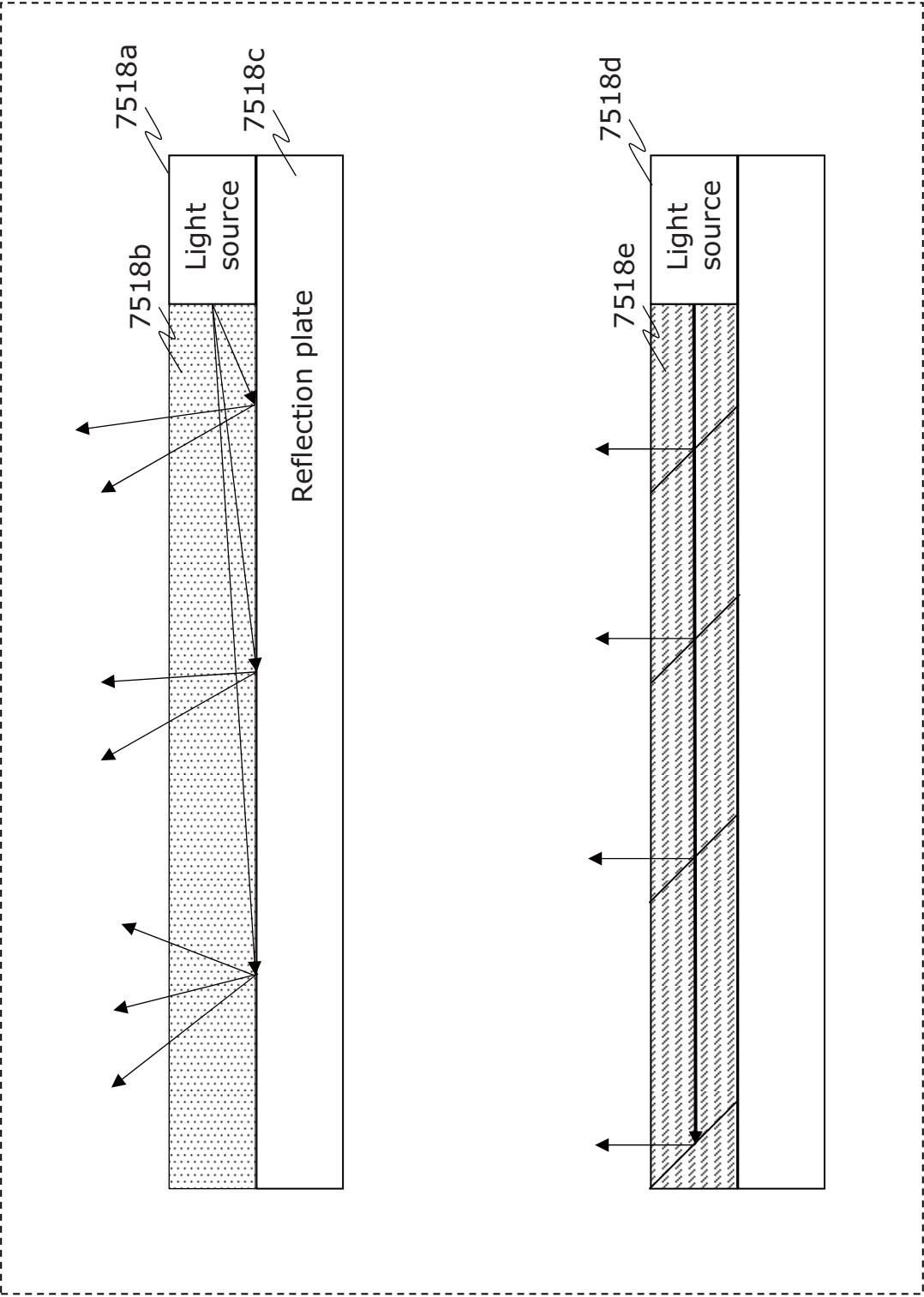


FIG. 53

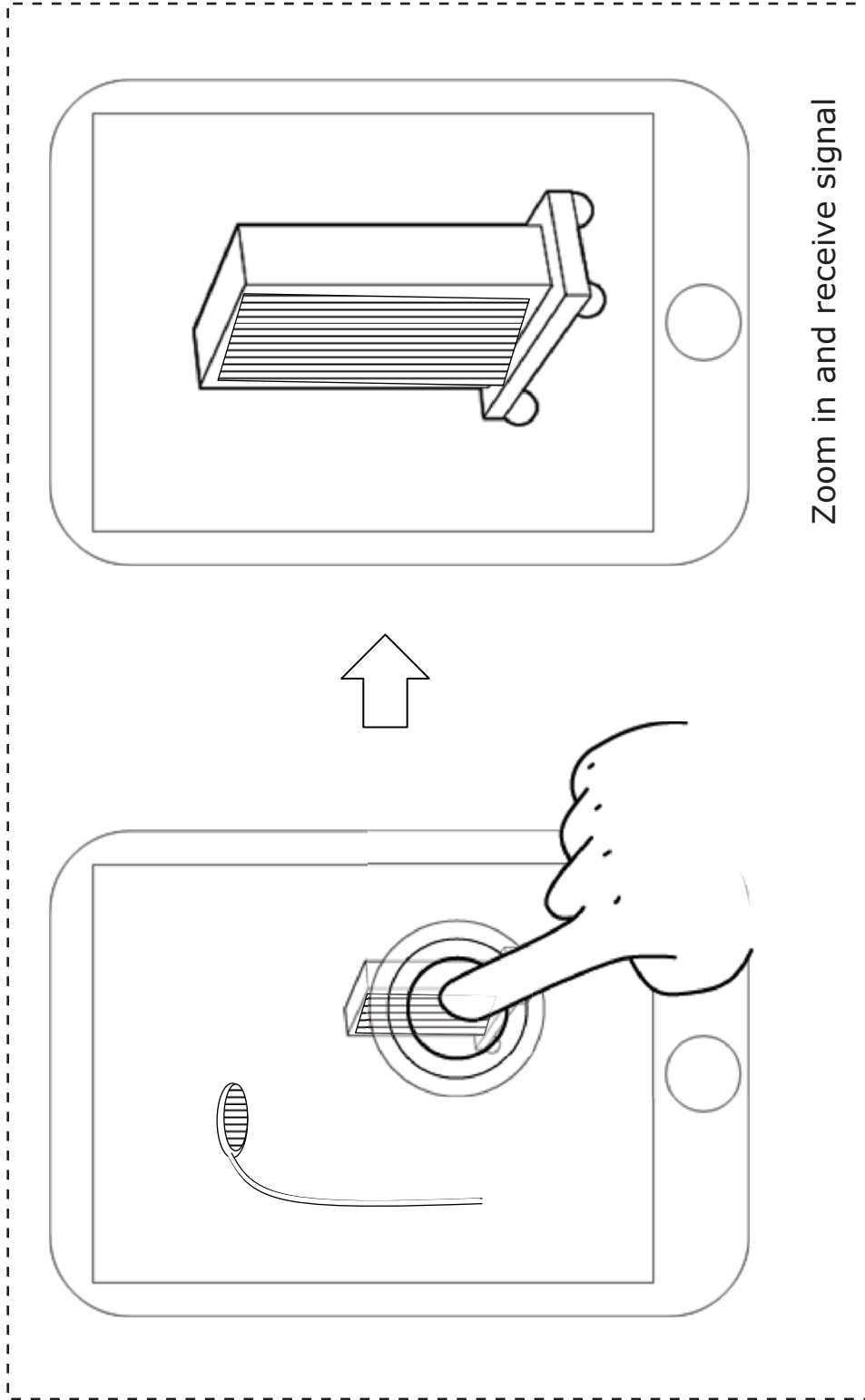


FIG. 54

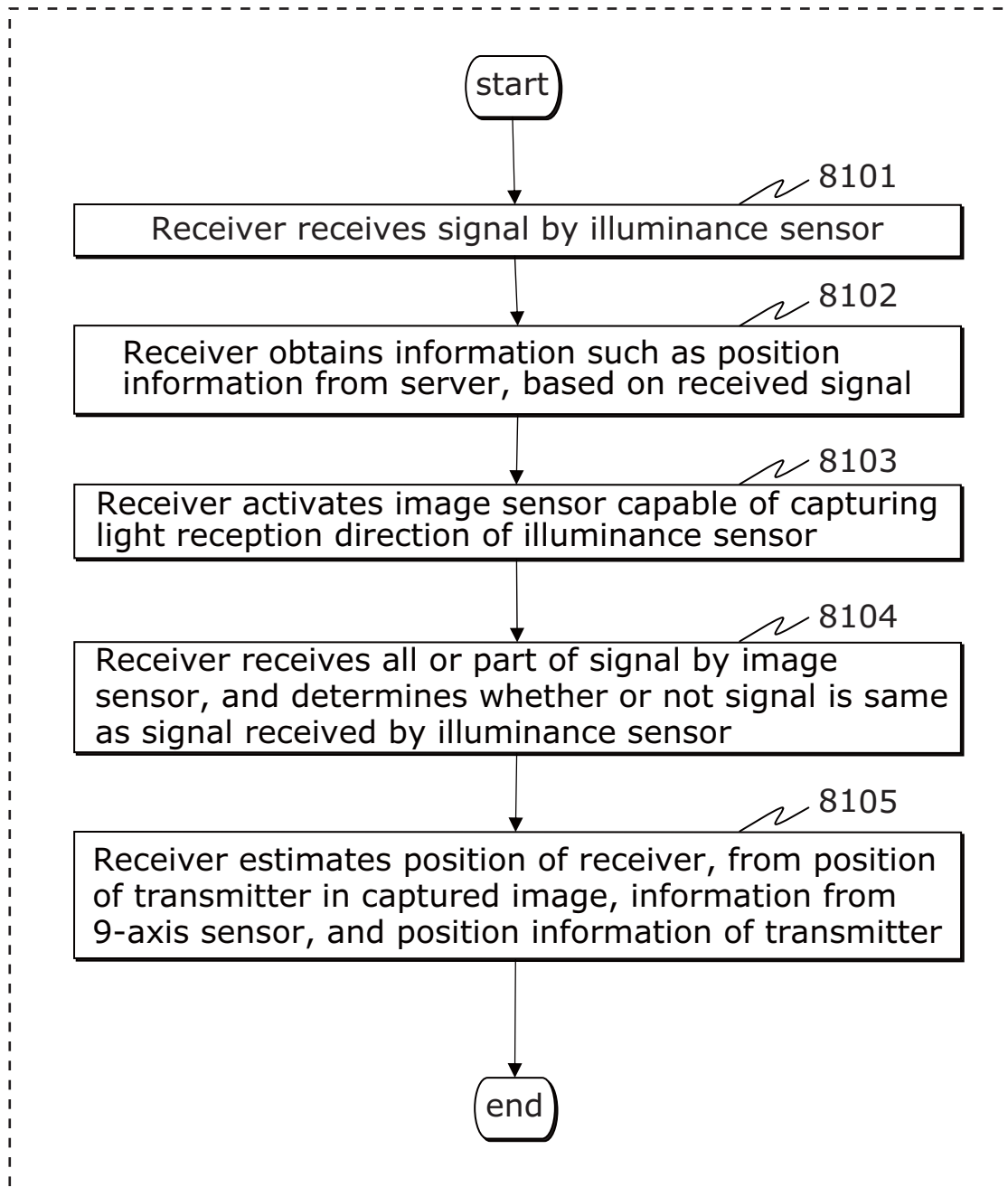


FIG. 55

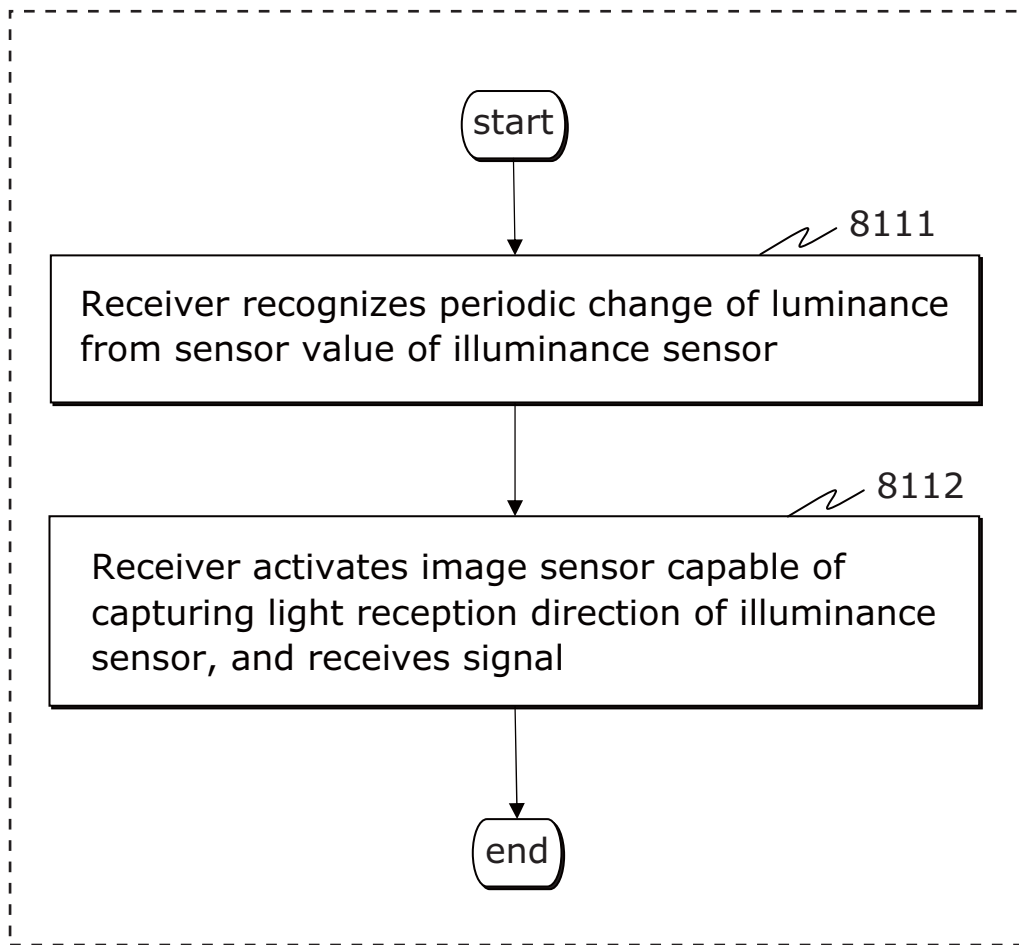


FIG. 56A

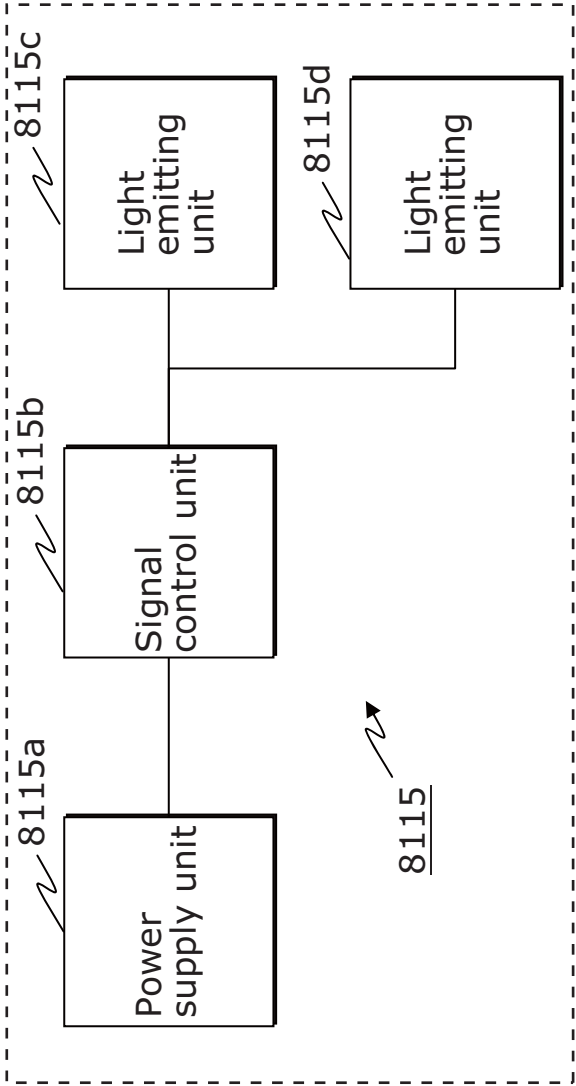


FIG. 56B

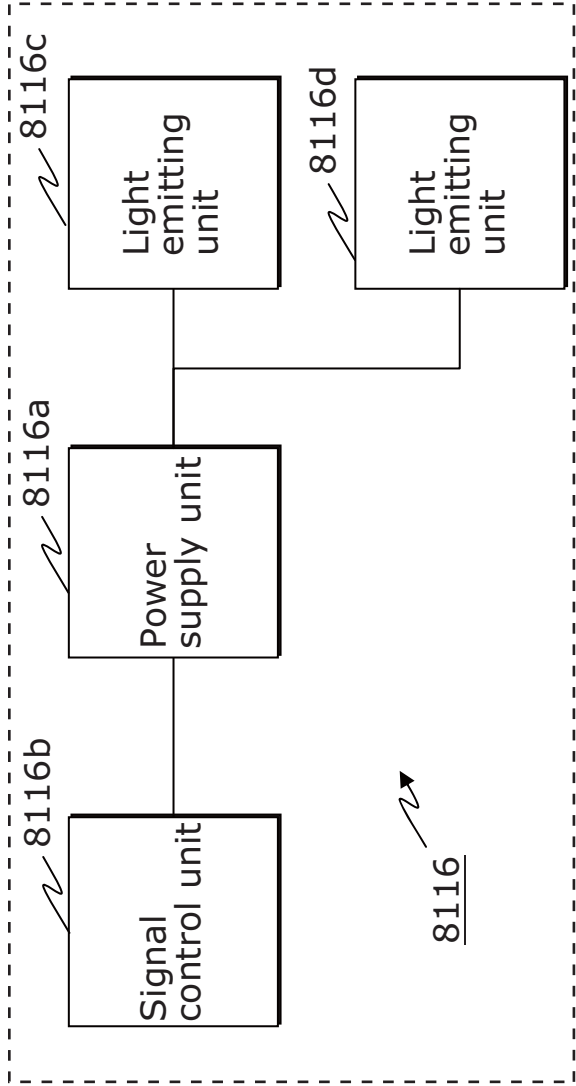


FIG. 57

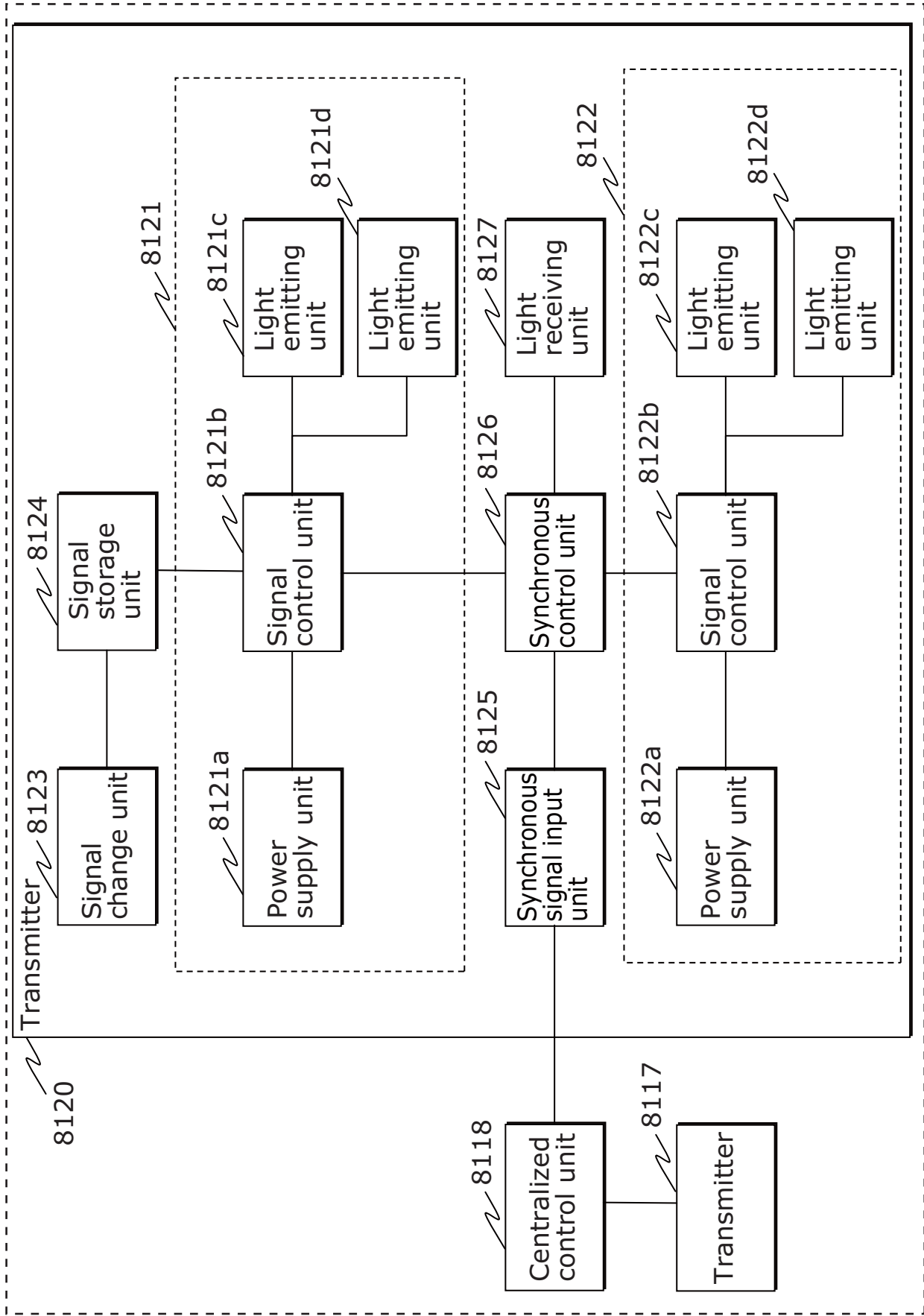


FIG. 58

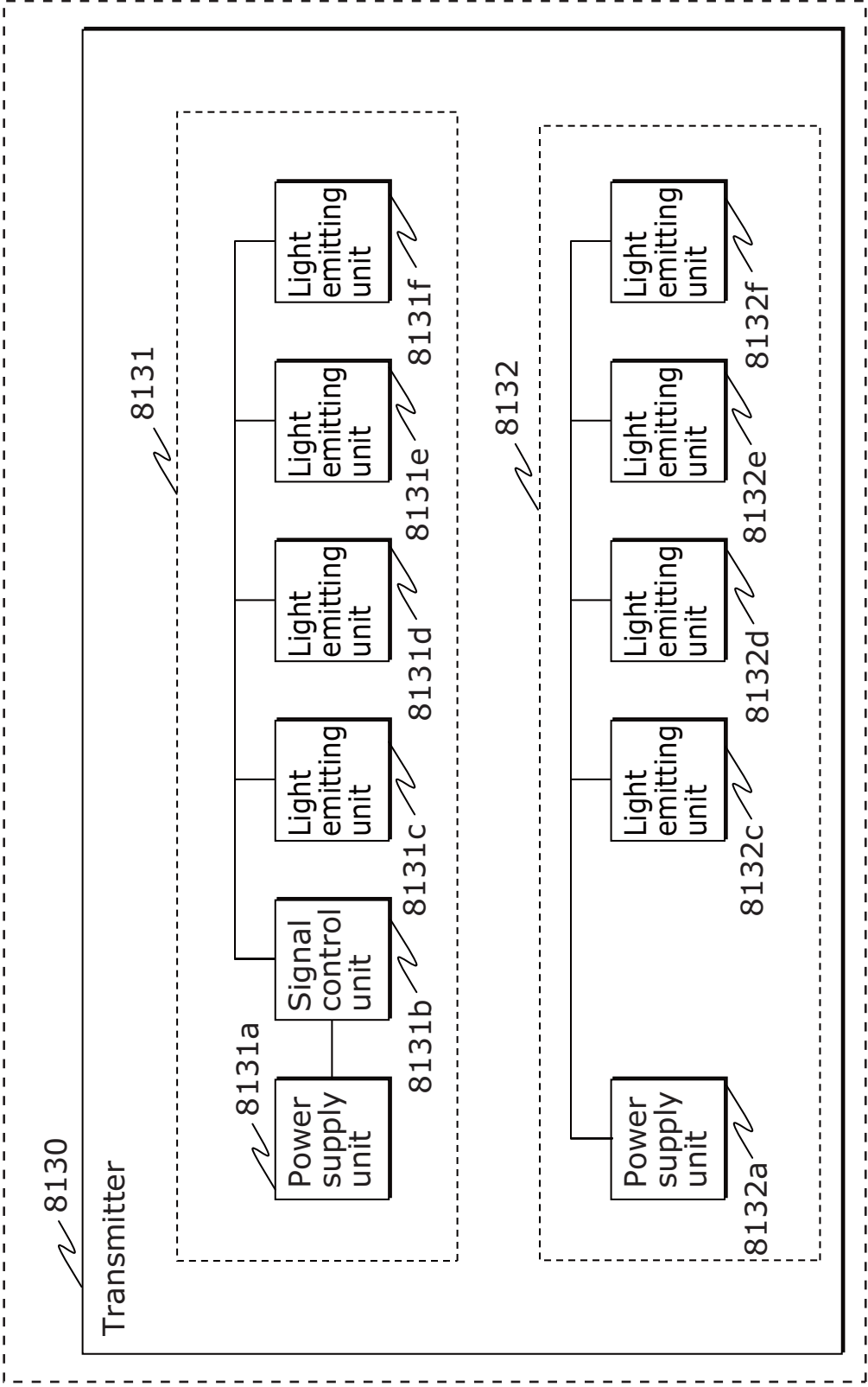


FIG. 59A

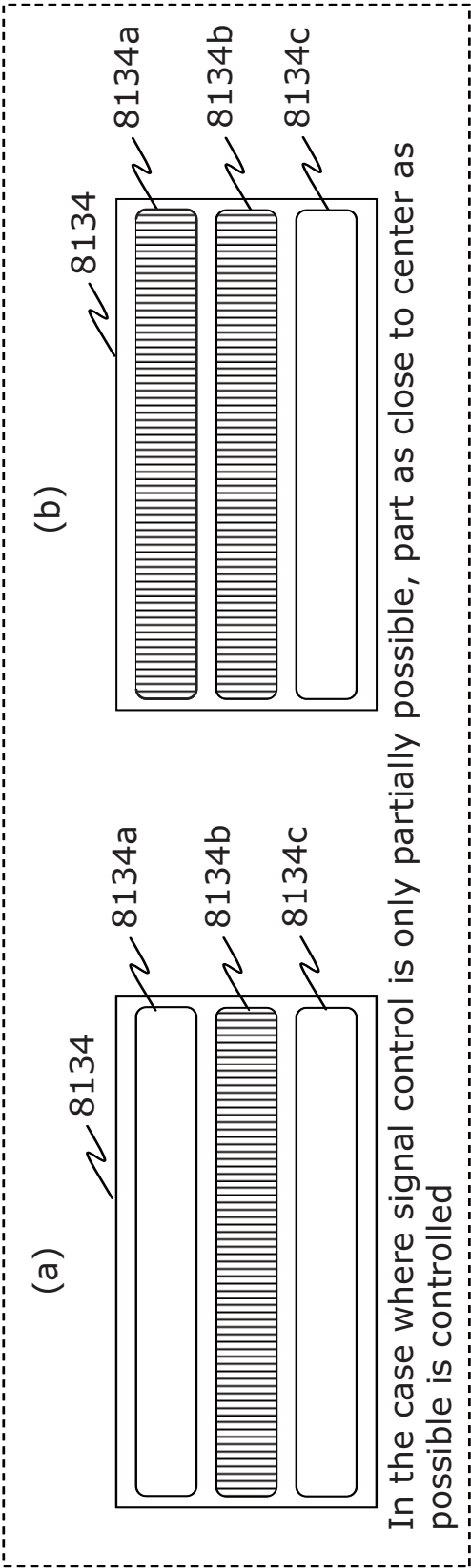


FIG. 59B

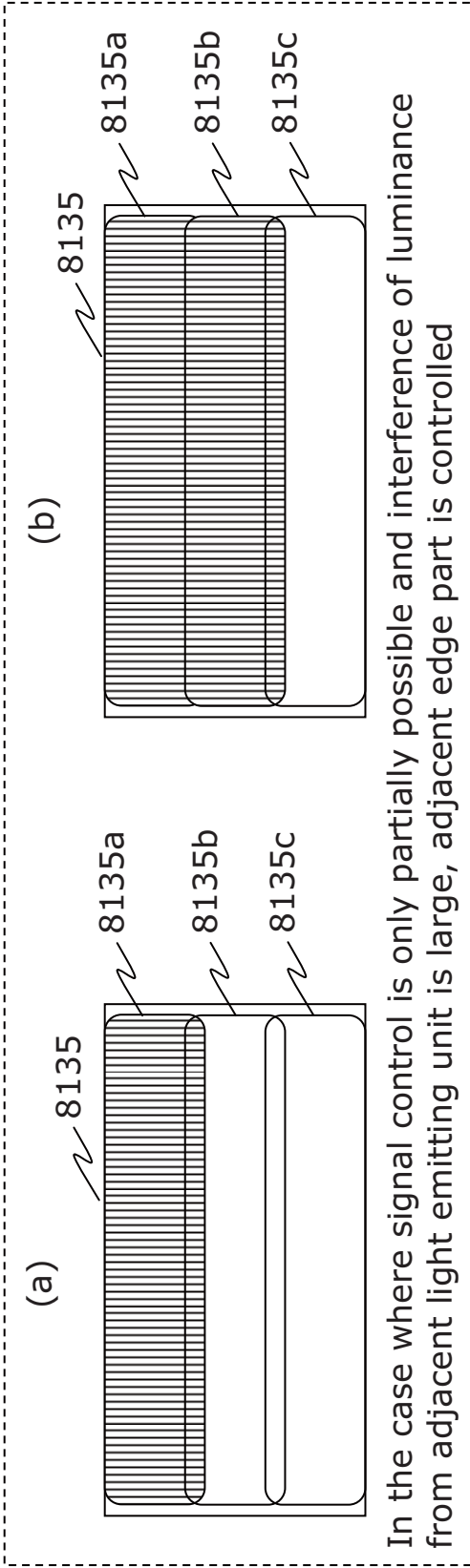


FIG. 59C

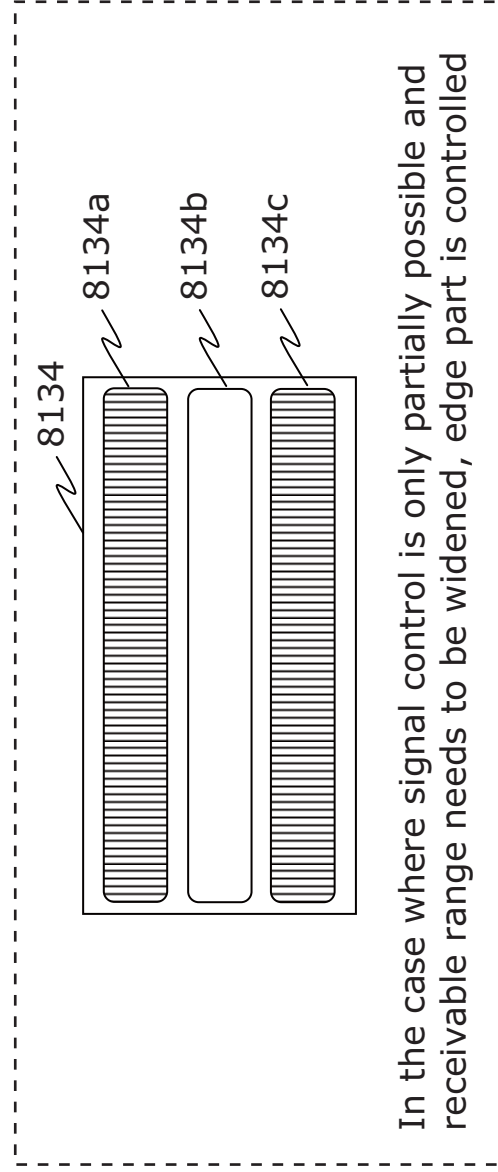


FIG. 60A

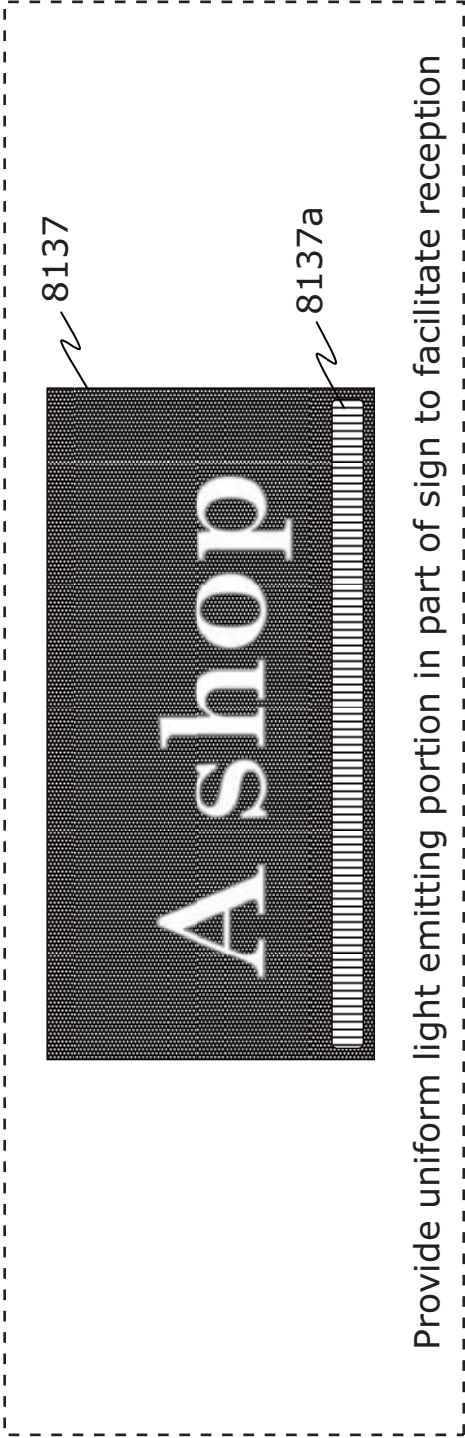


FIG. 60B



FIG. 61

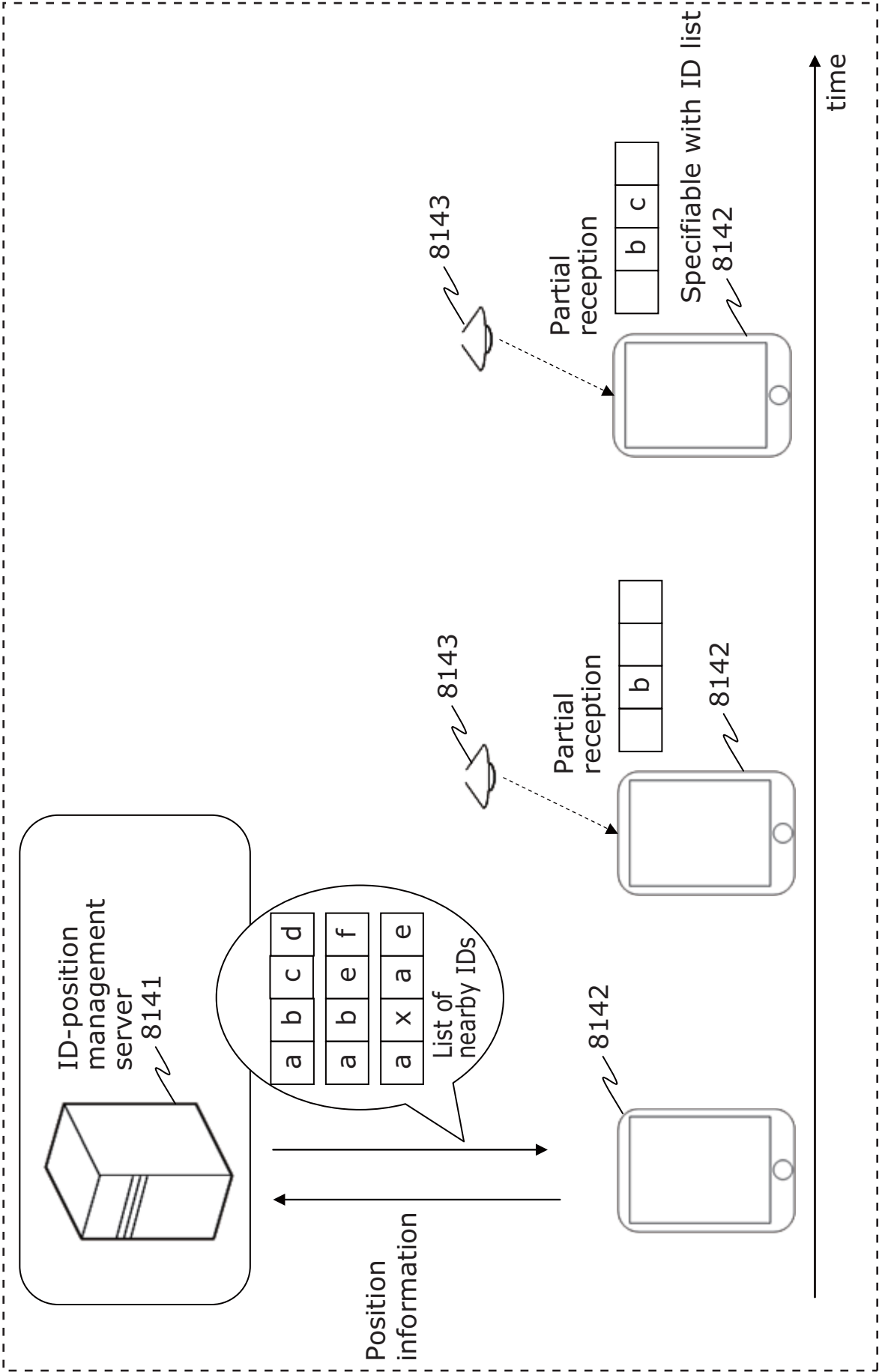


FIG. 62

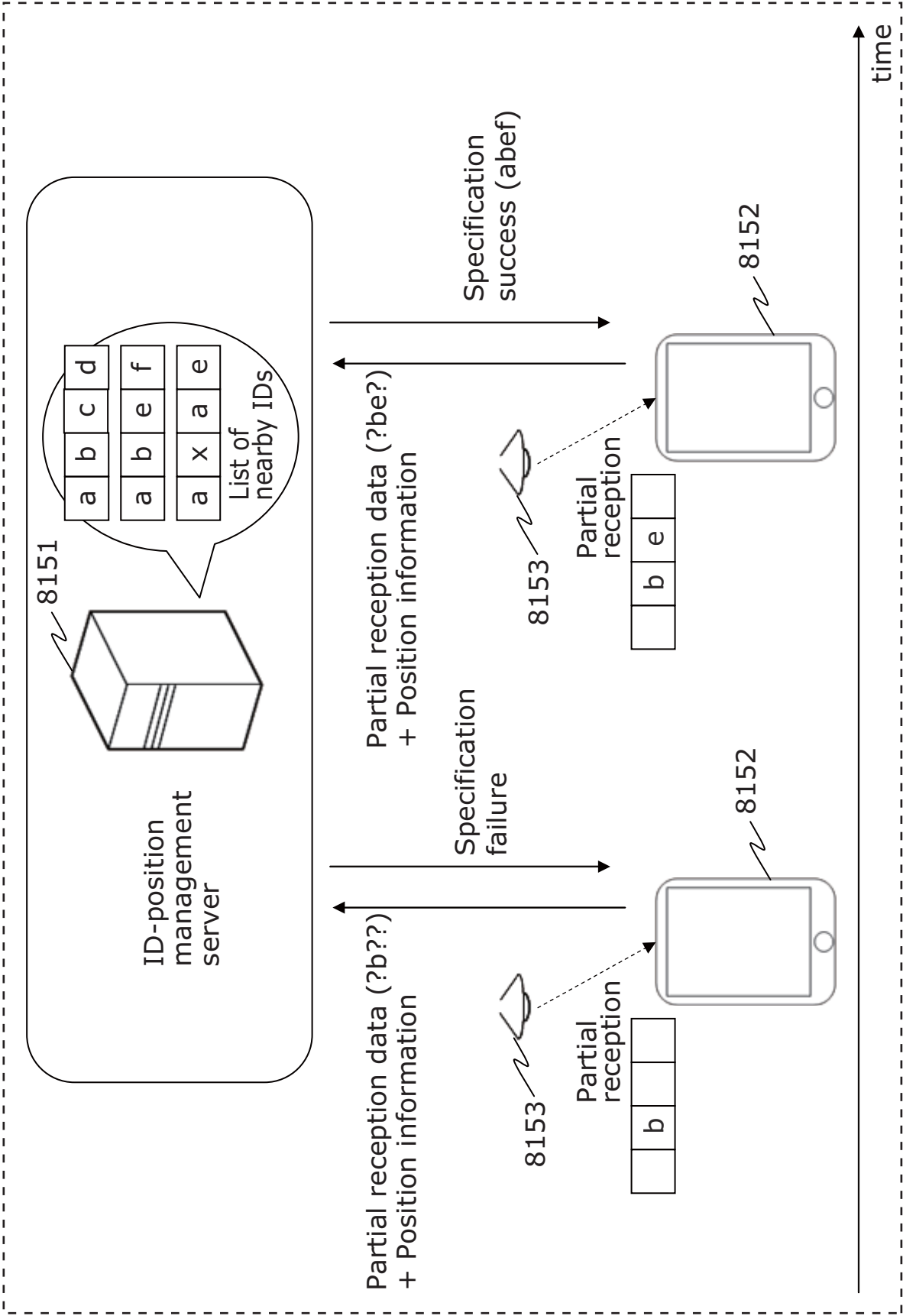


FIG. 63

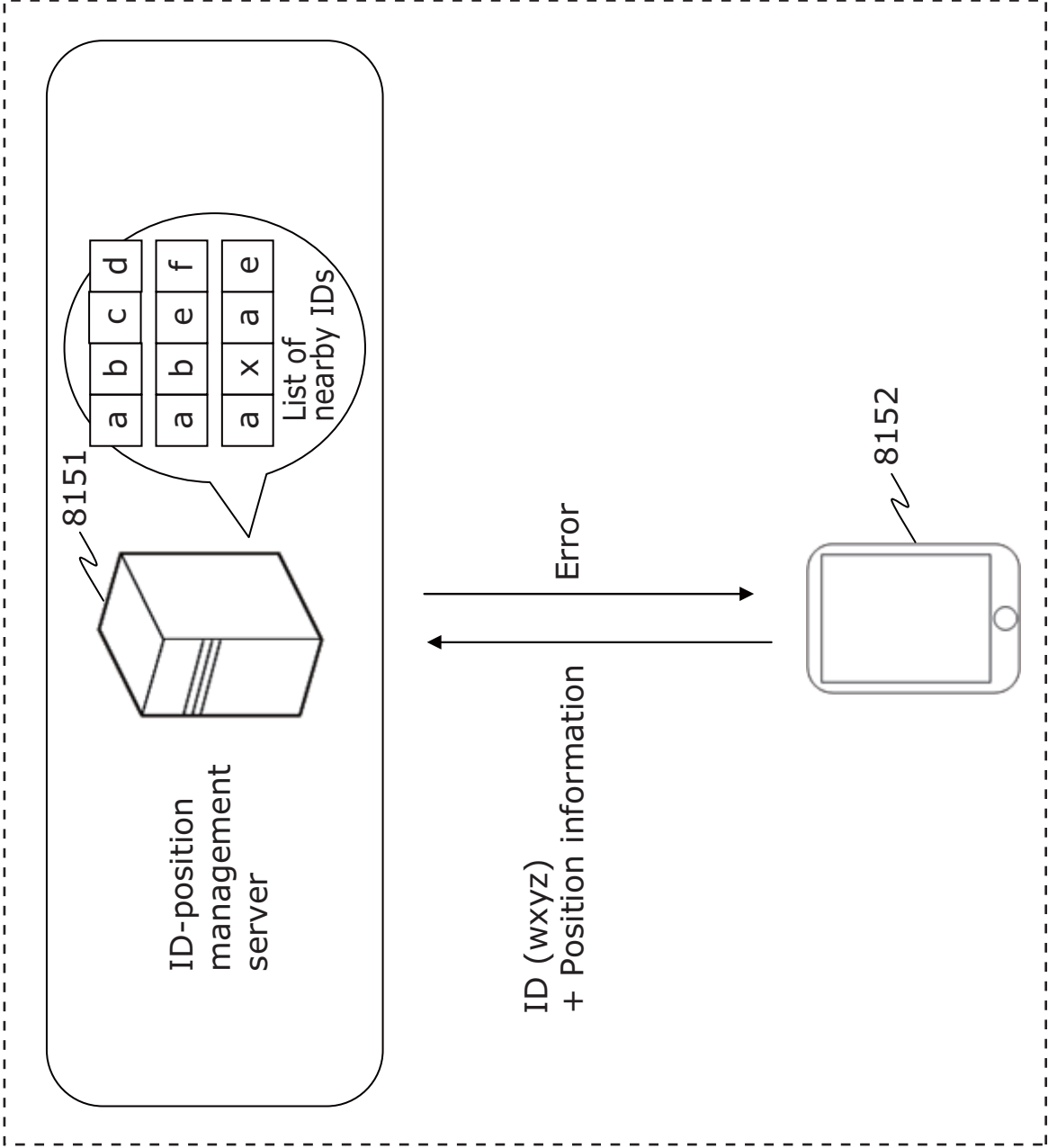


FIG. 64A

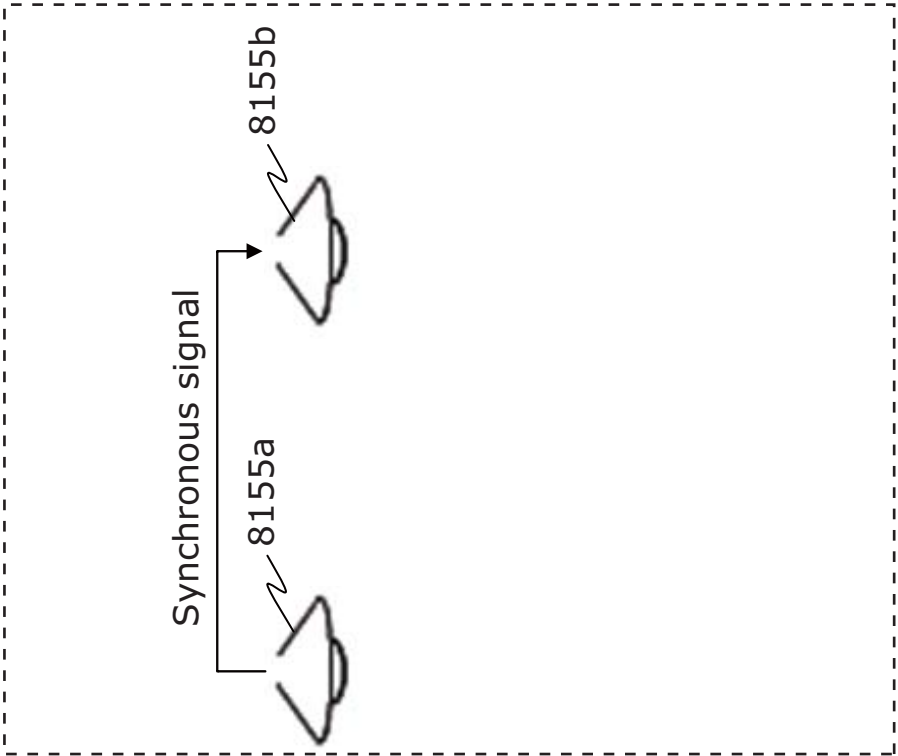


FIG. 64B

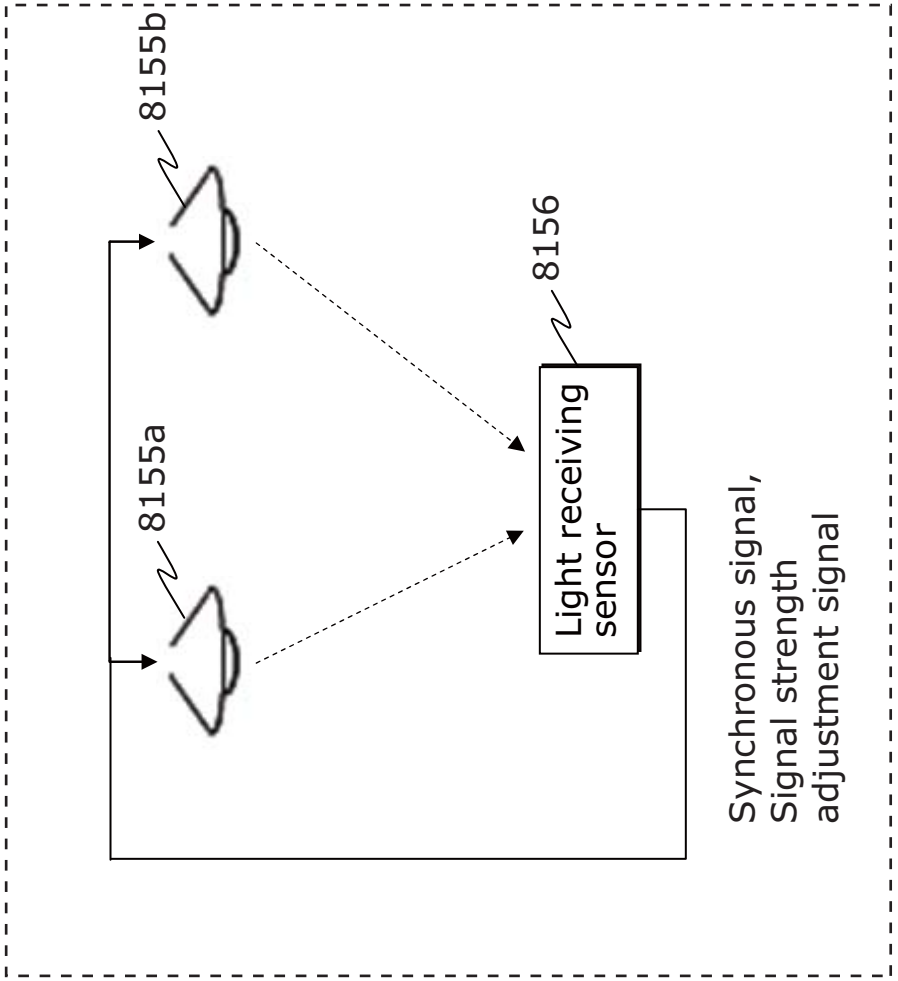


FIG. 65

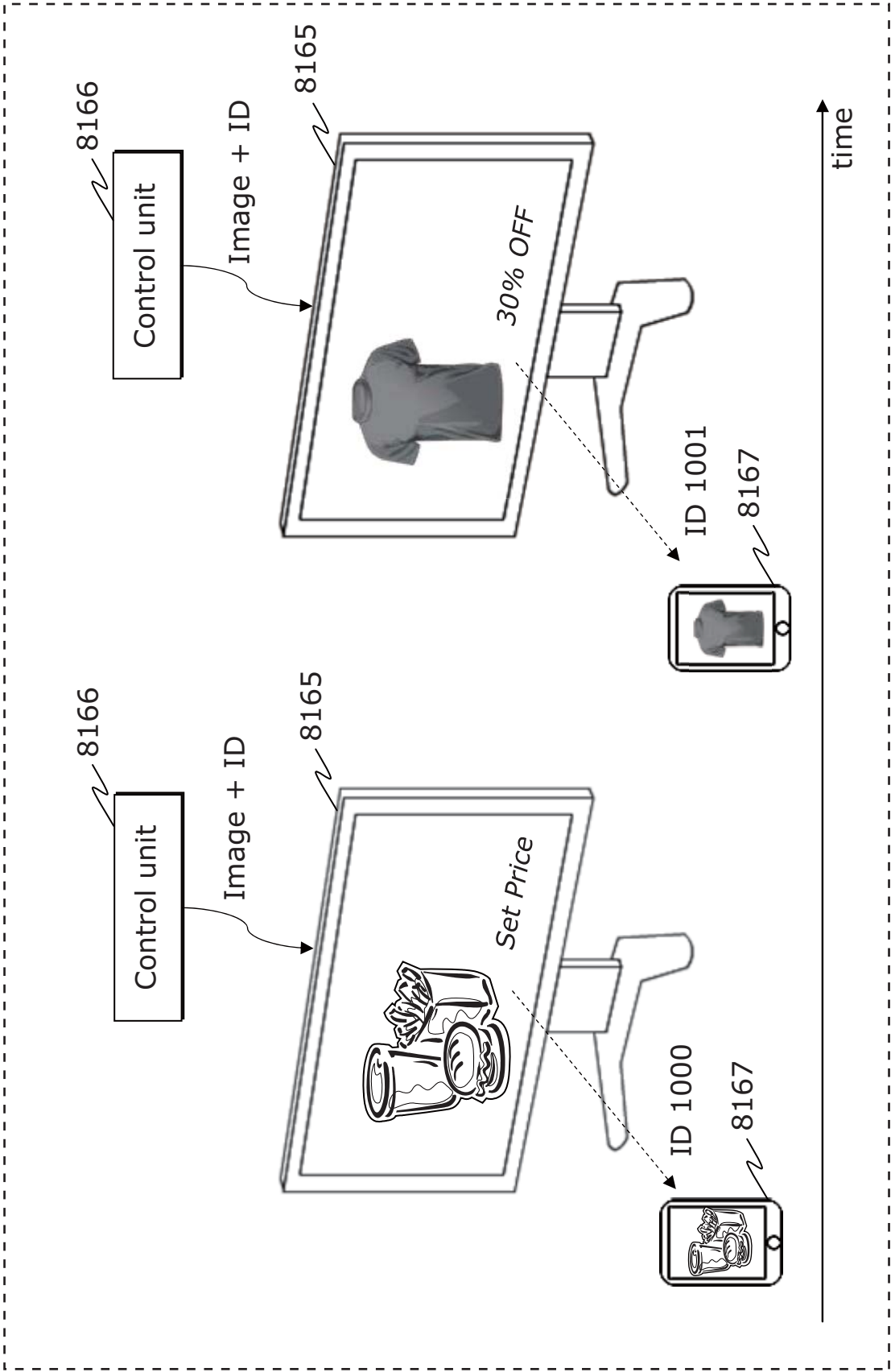


FIG. 66

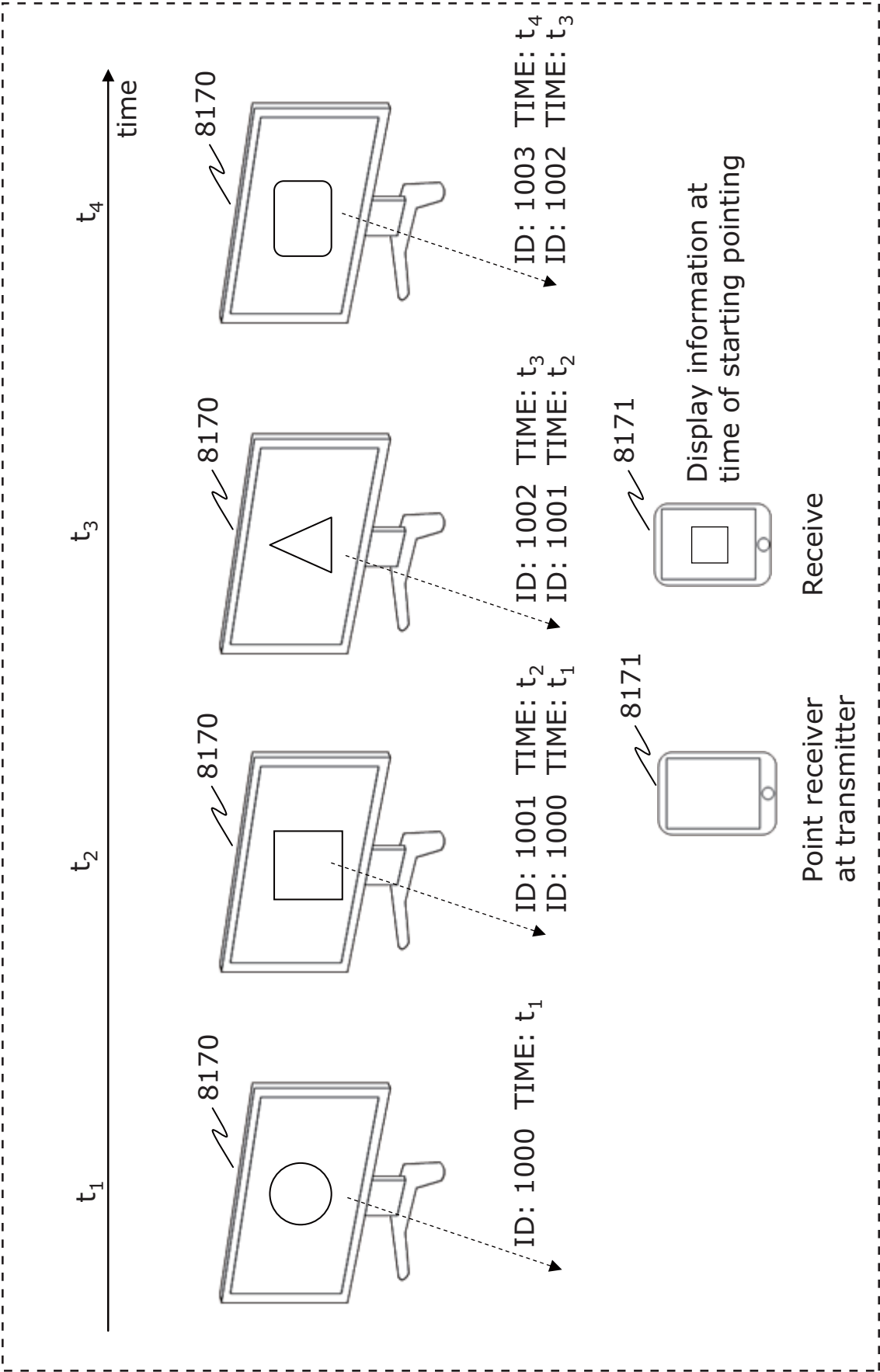


FIG. 67

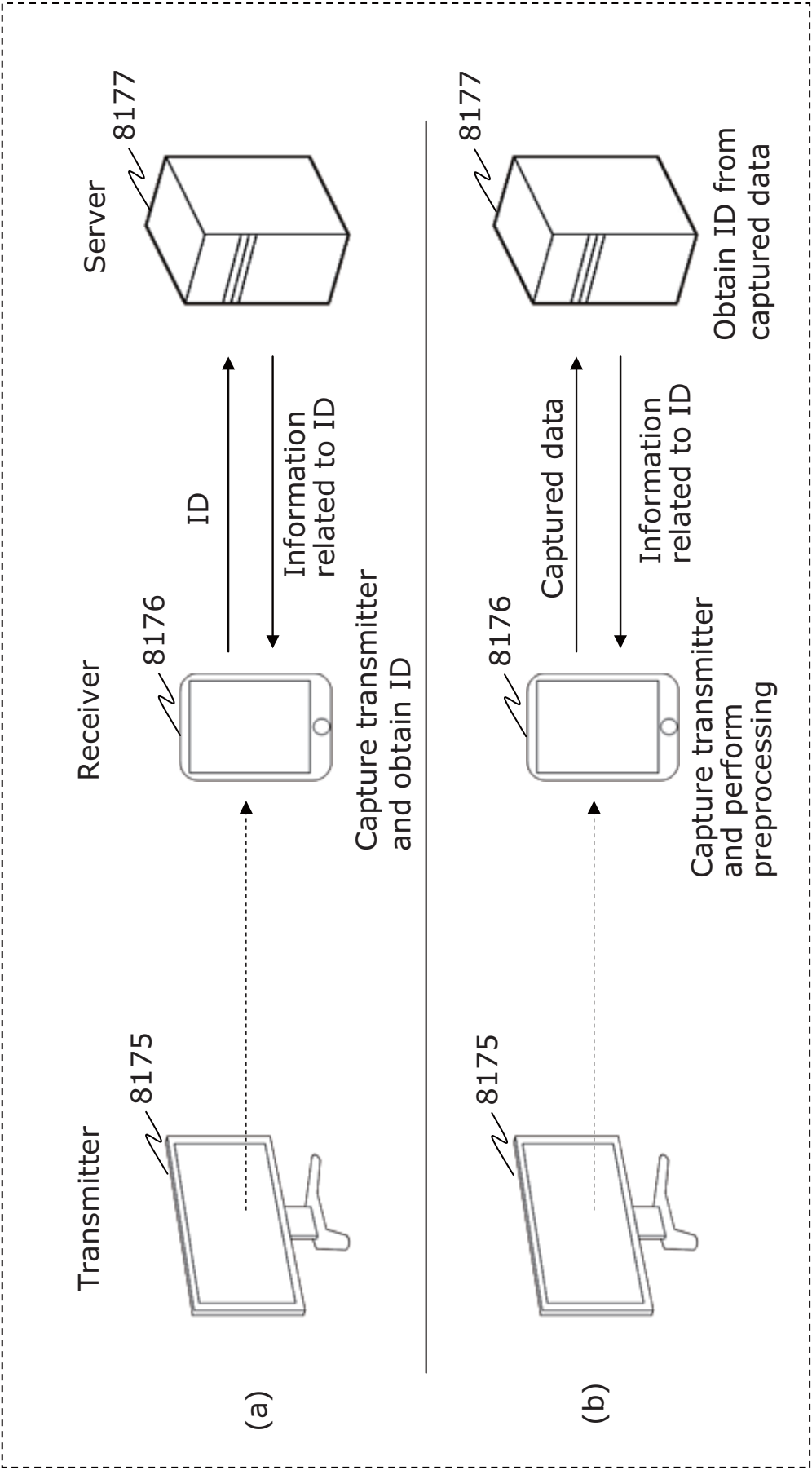


FIG. 68

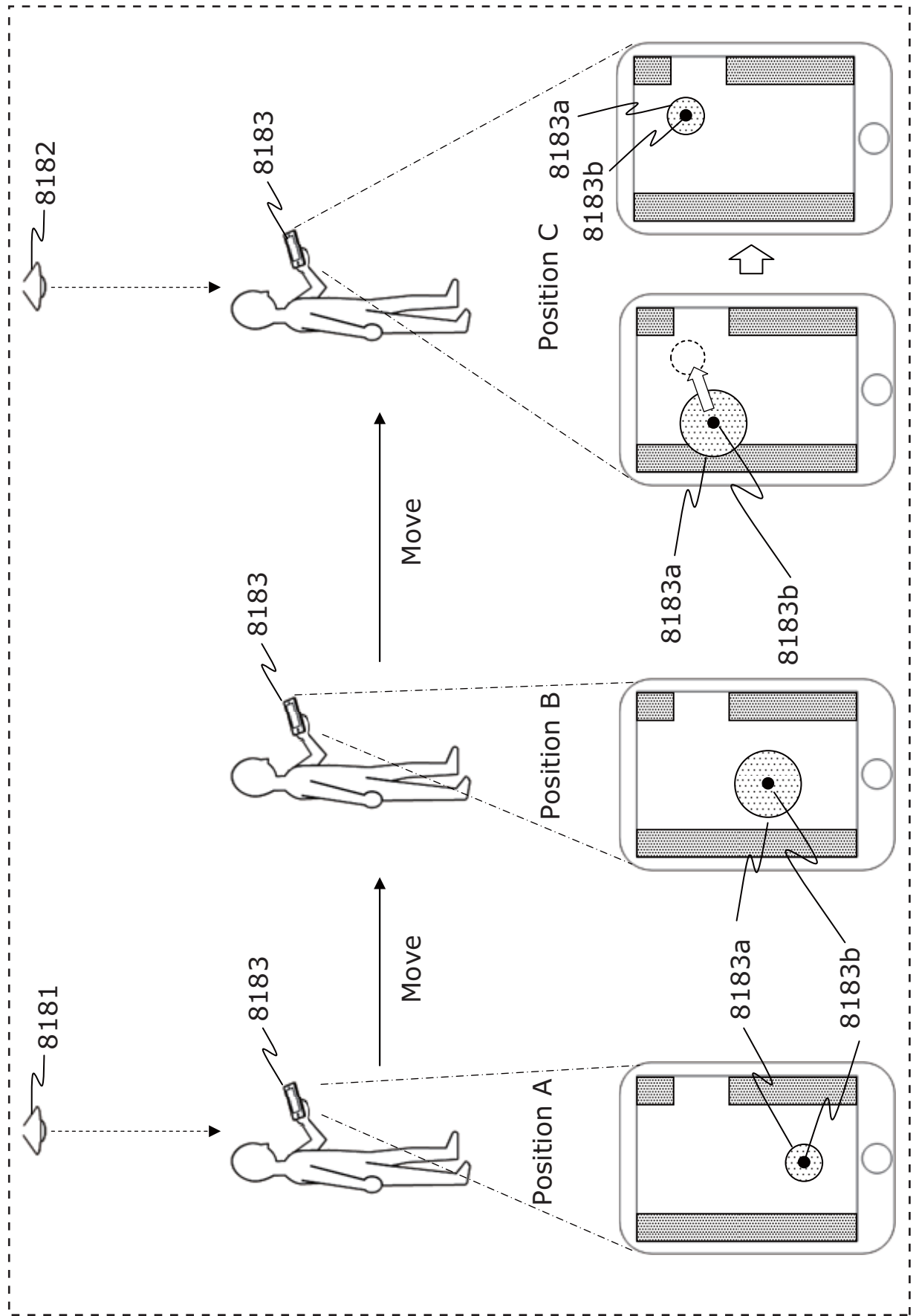


FIG. 69

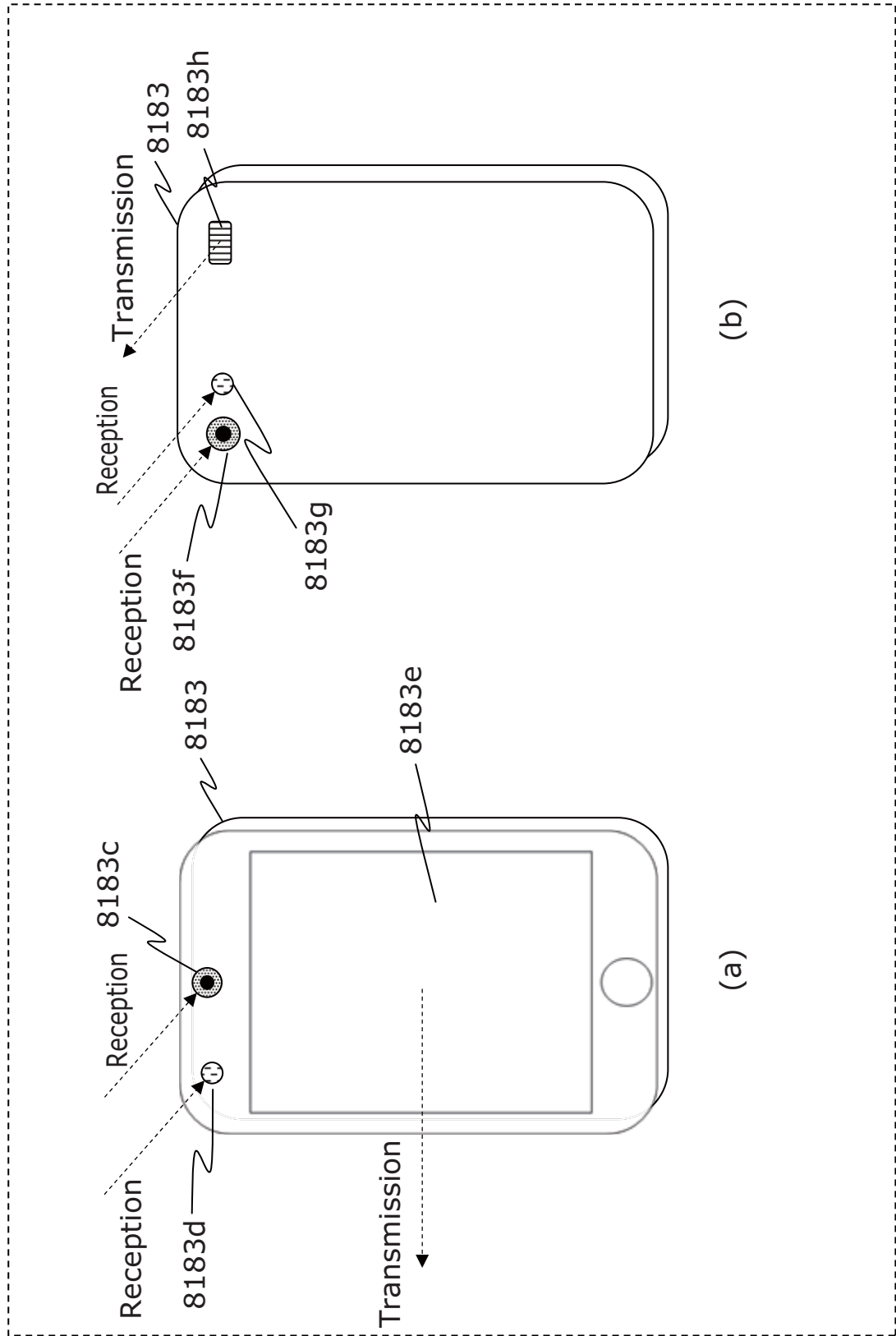


FIG. 70

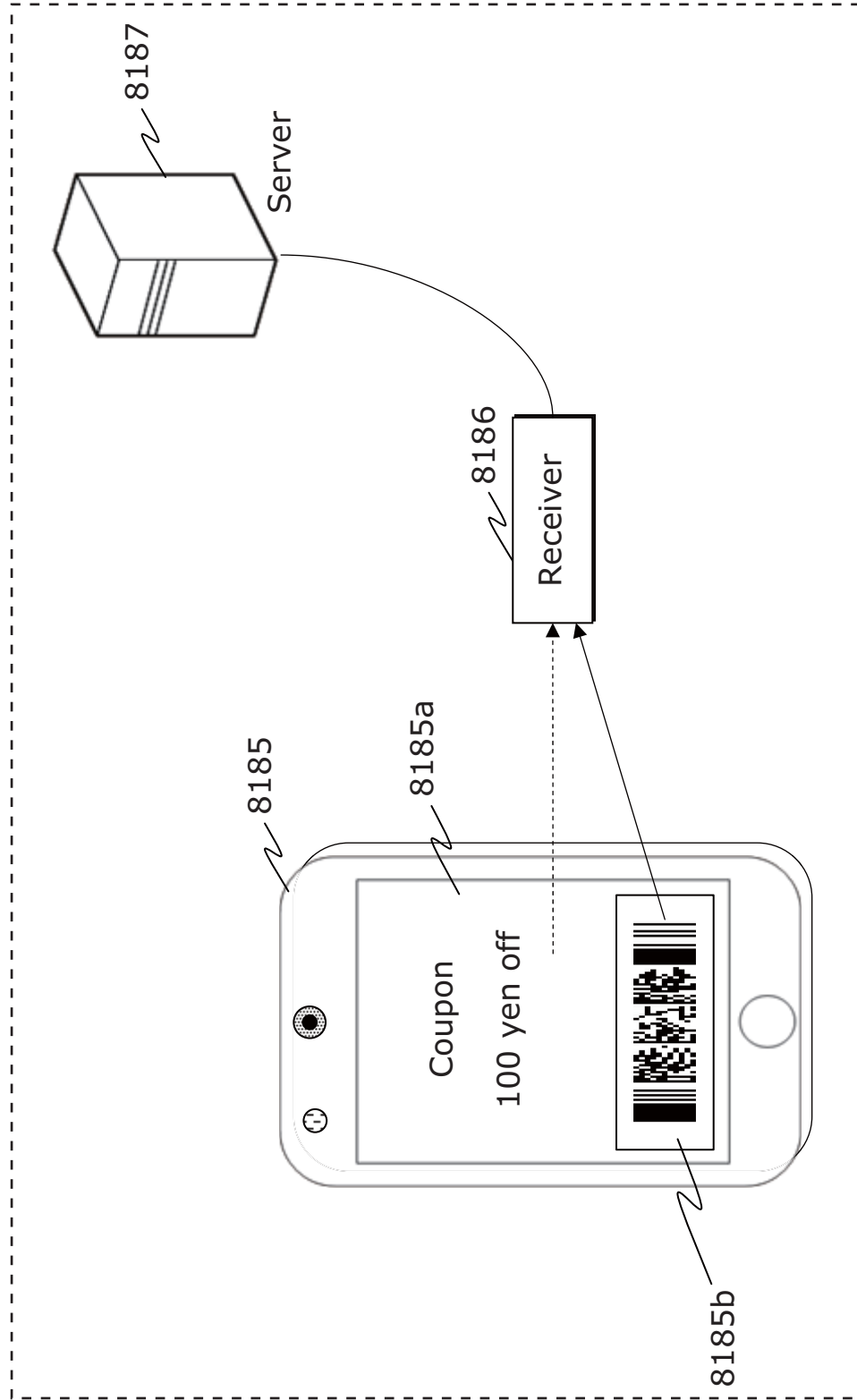


FIG. 71

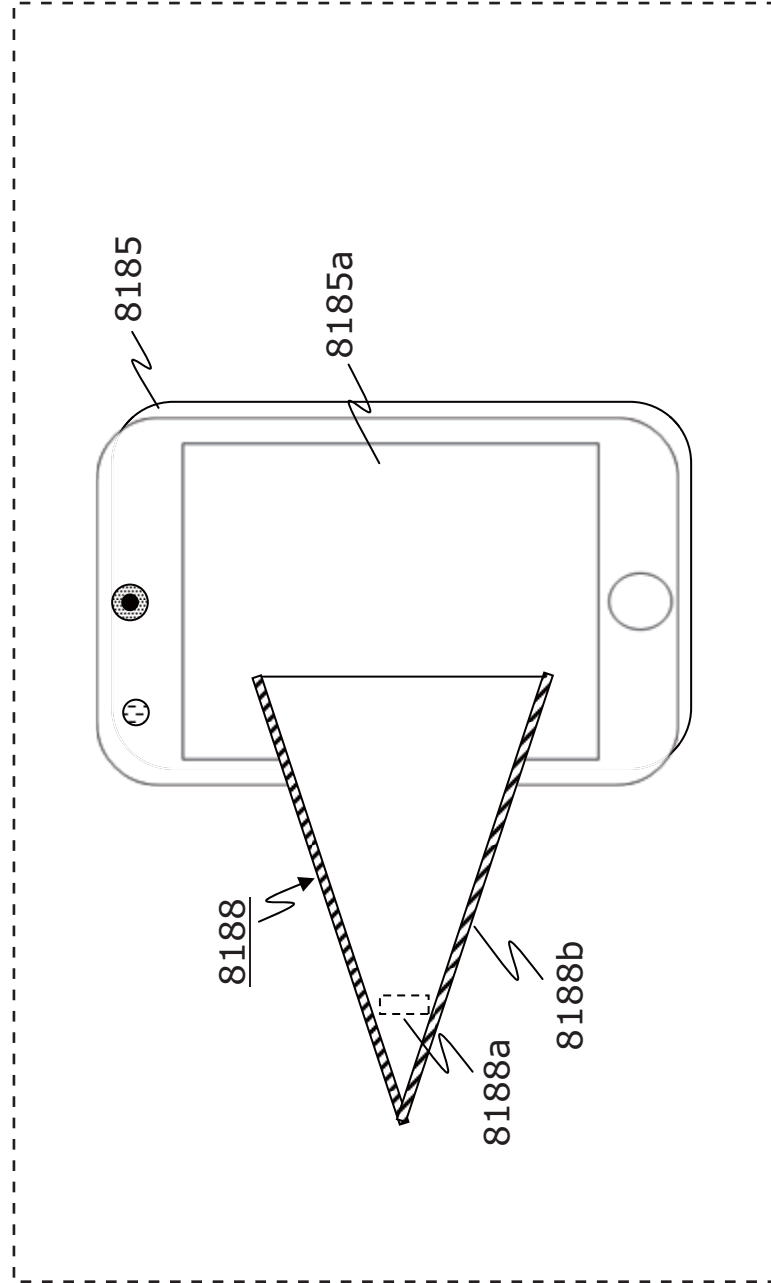


FIG. 72

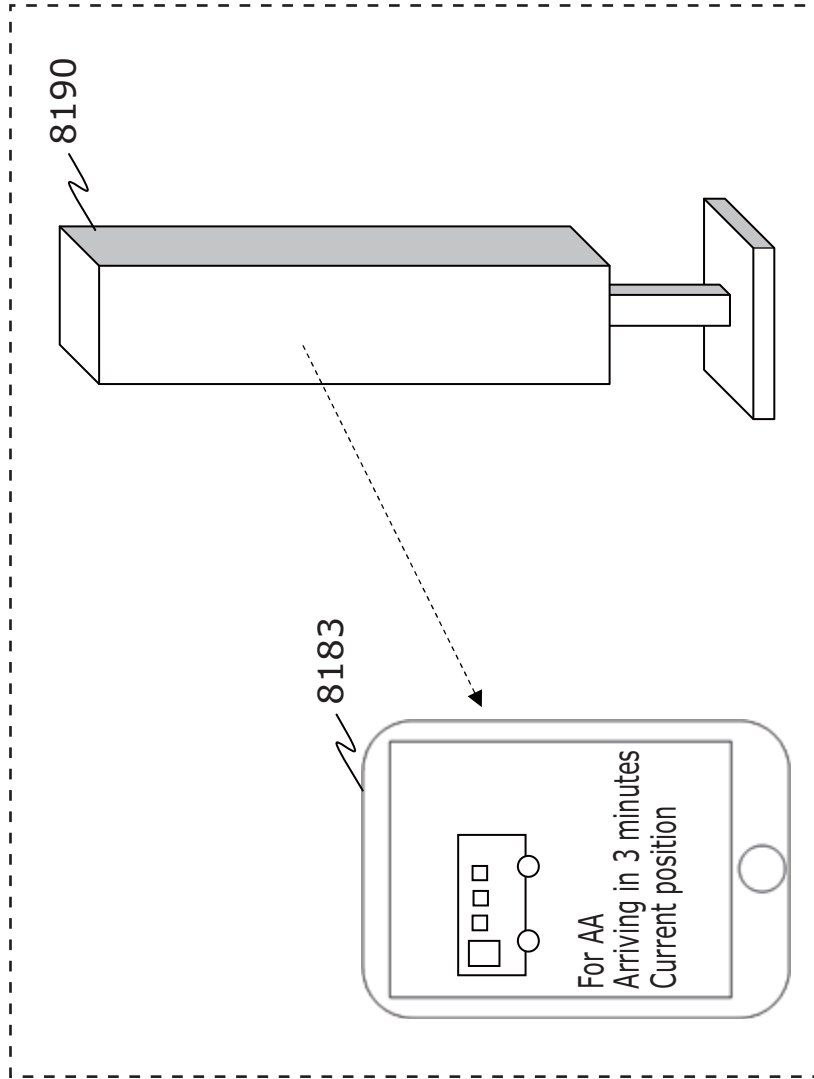


FIG. 73

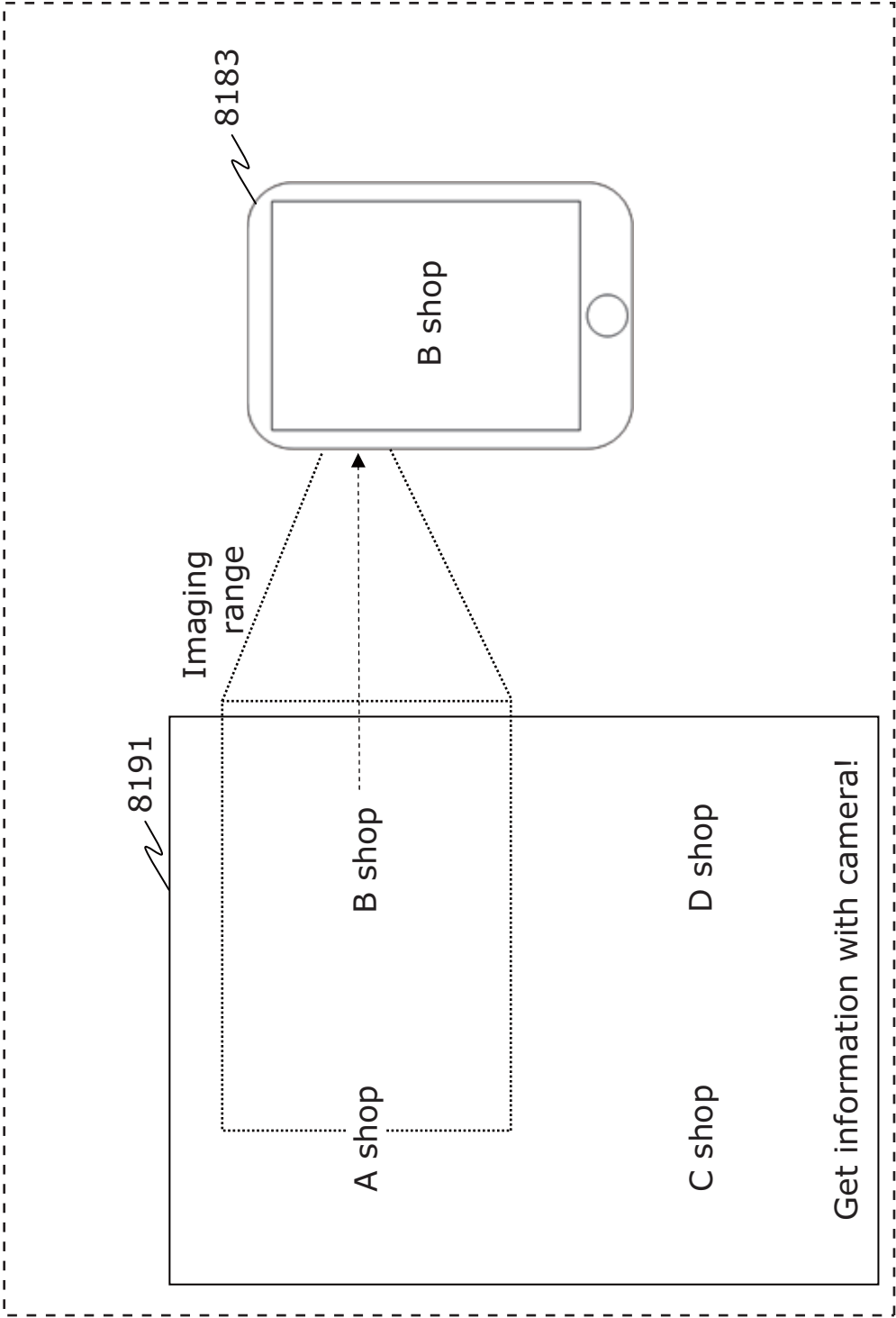


FIG. 74

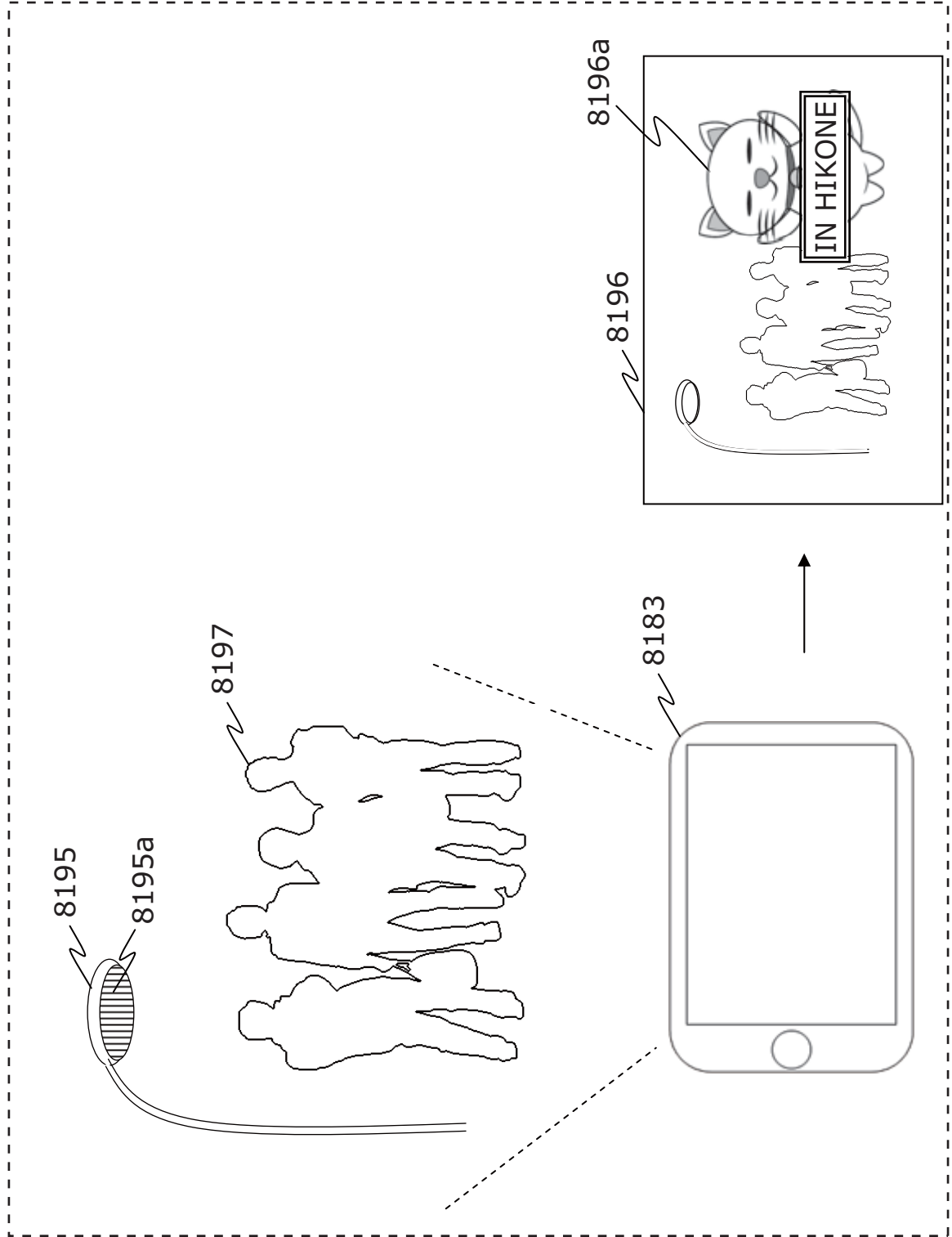


FIG. 75A

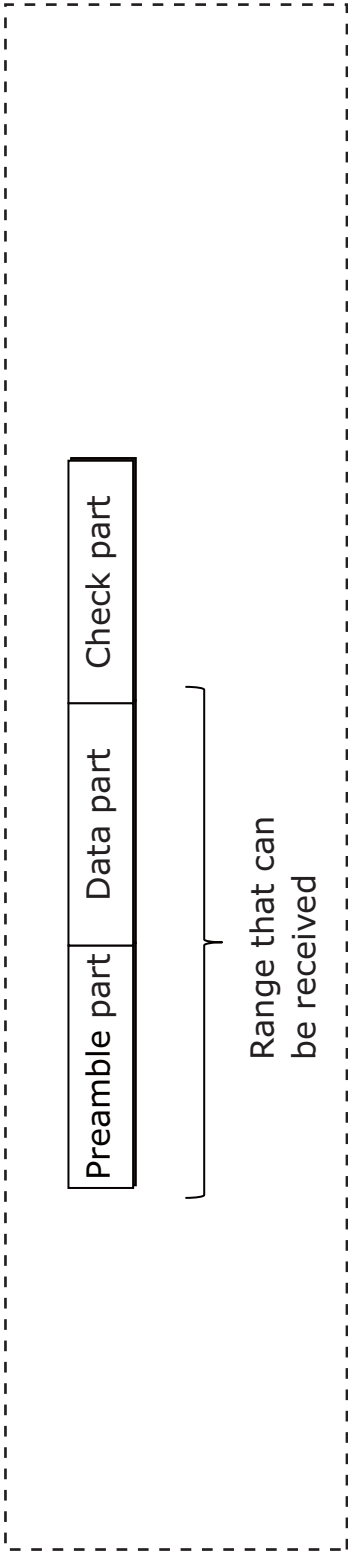


FIG. 75B

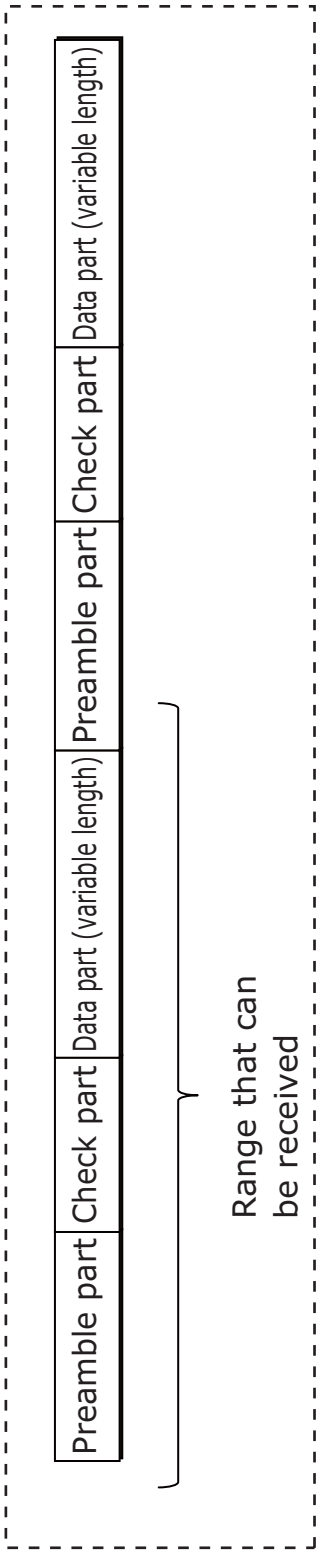


FIG. 76

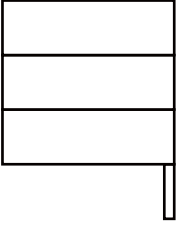
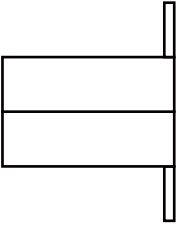
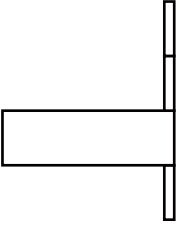
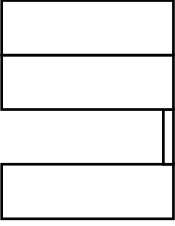
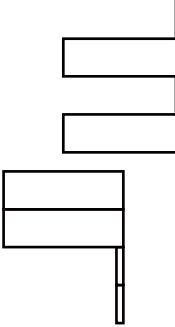
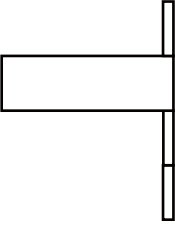
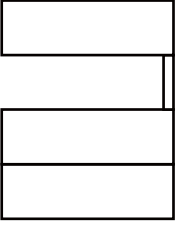
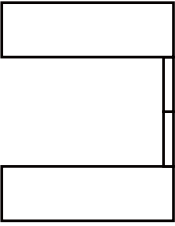
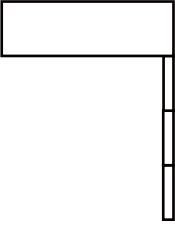
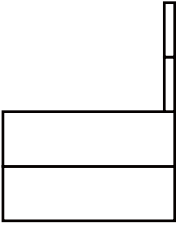
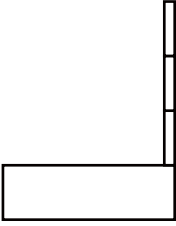
Signal	Rise position	Brightness		
		75 %	50 %	25 %
00	Slots 1 → 2			
01	Slots 2 → 3			
10	Slots 3 → 4			
11	None			

FIG. 77

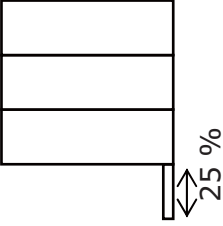
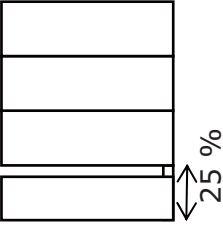
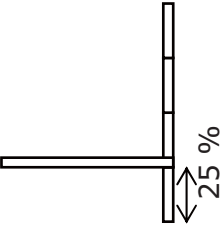
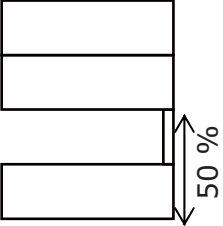
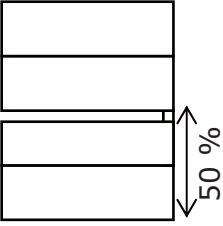
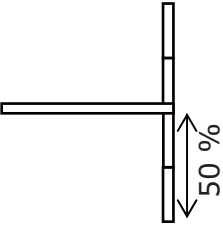
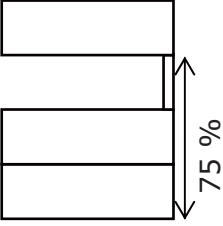
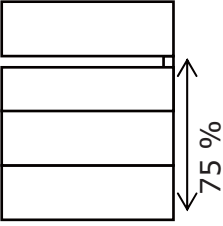
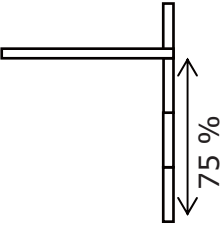
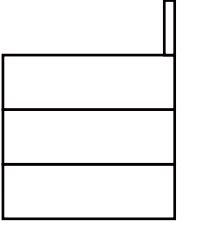
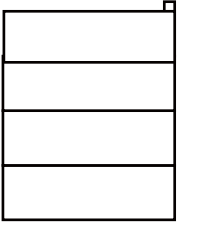
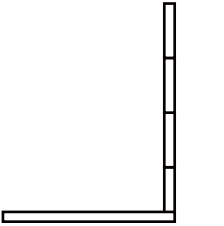
Signal	Rise position	Brightness		
		75 % (Basic)	99 %	1 %
00	Position of 25%			
01	Position of 50%			
10	Position of 75%			
11	None			

FIG. 78

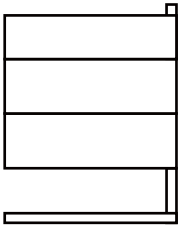
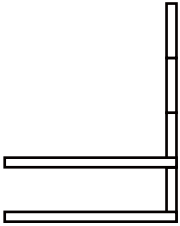
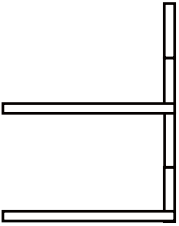
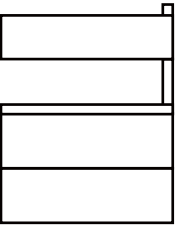
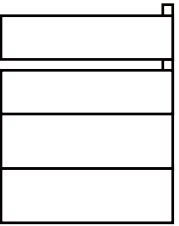
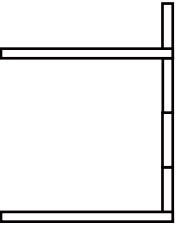
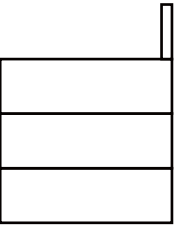
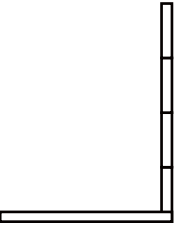
Signal	Rise position	Brightness		
		75 %	99 %	1 %
00	Position of 25%			
01	Position of 50%			
10	Position of 75%			
11	None			

FIG. 79A

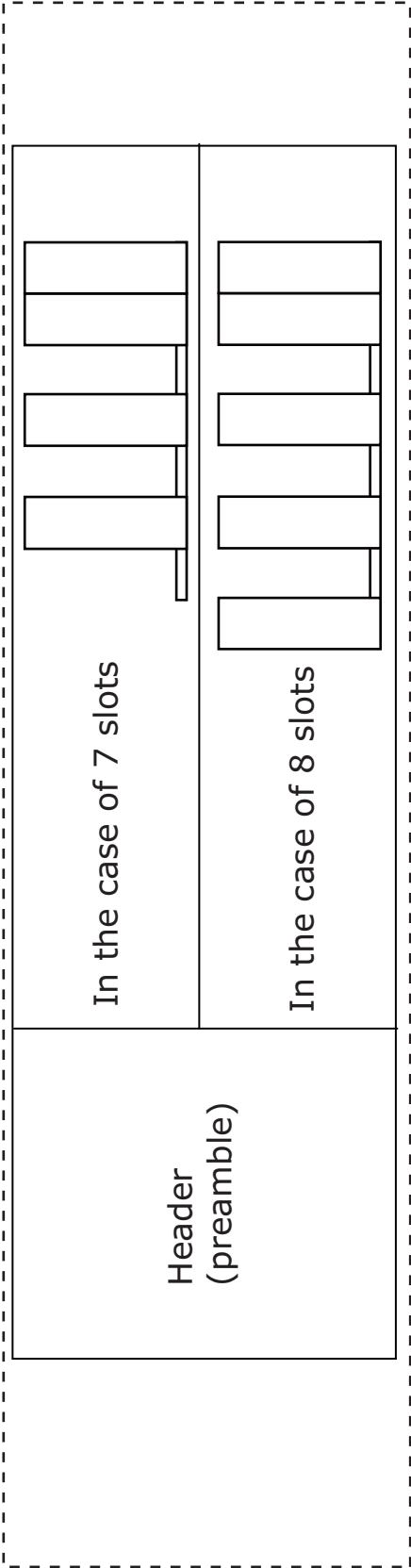


FIG. 79B

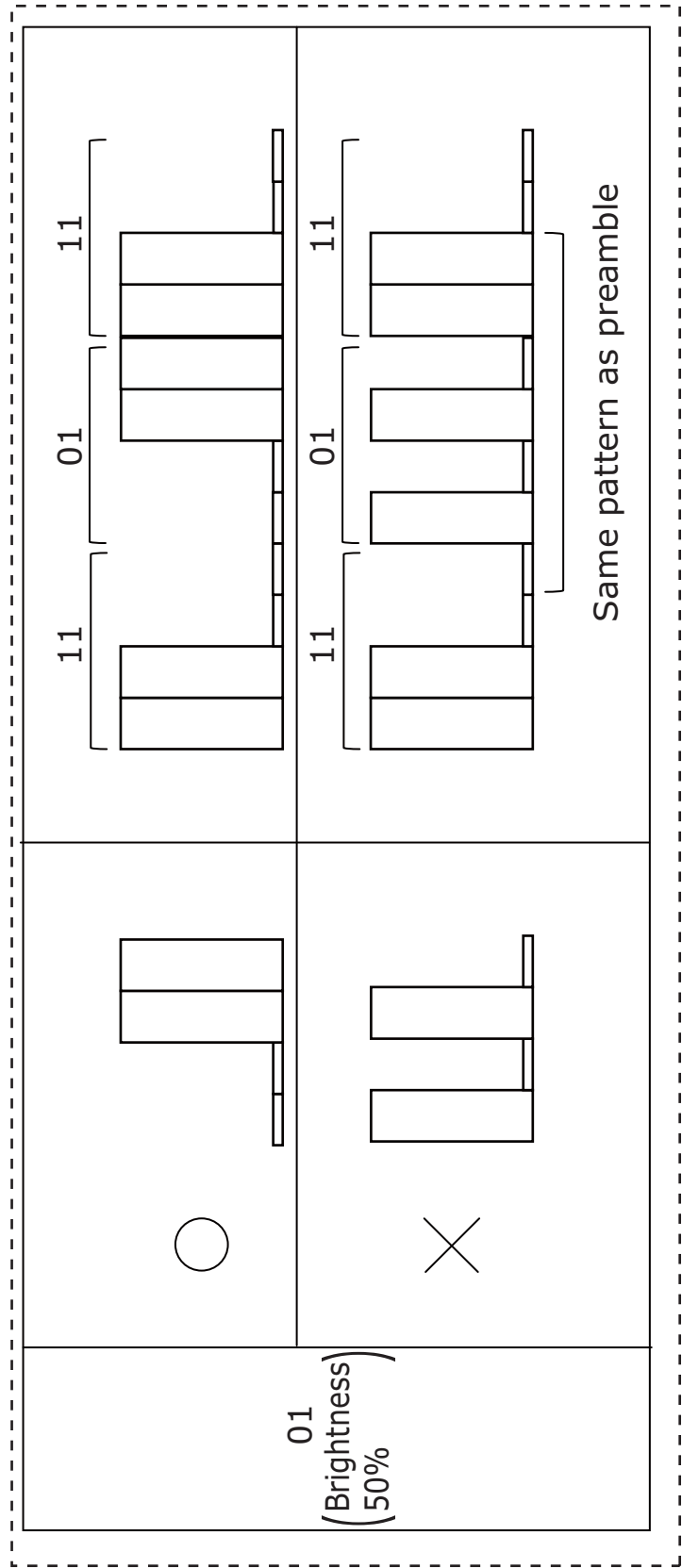


FIG. 80A

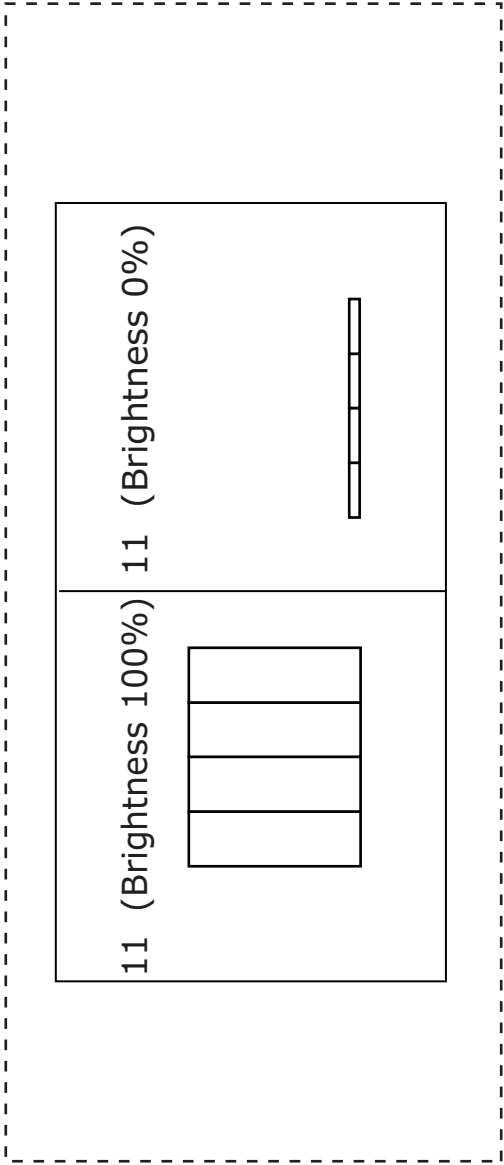


FIG. 80B

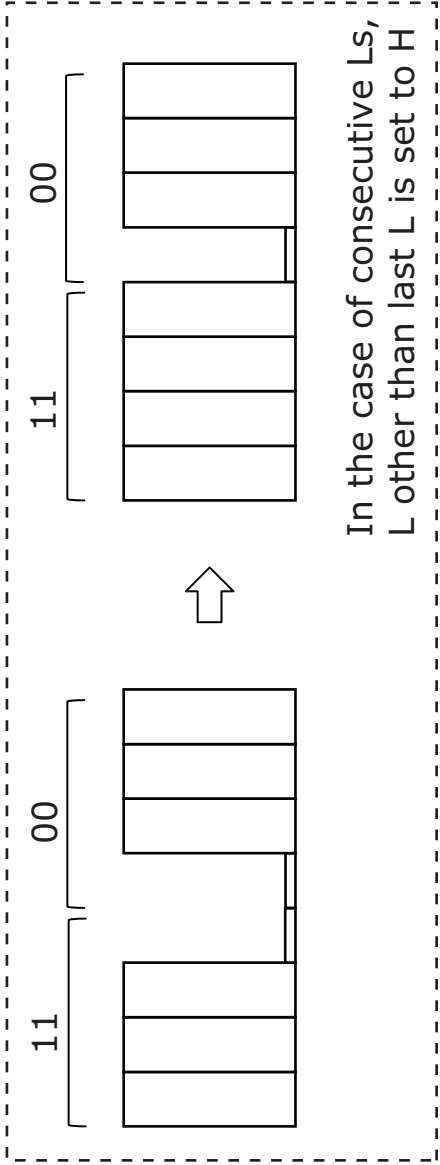


FIG. 81

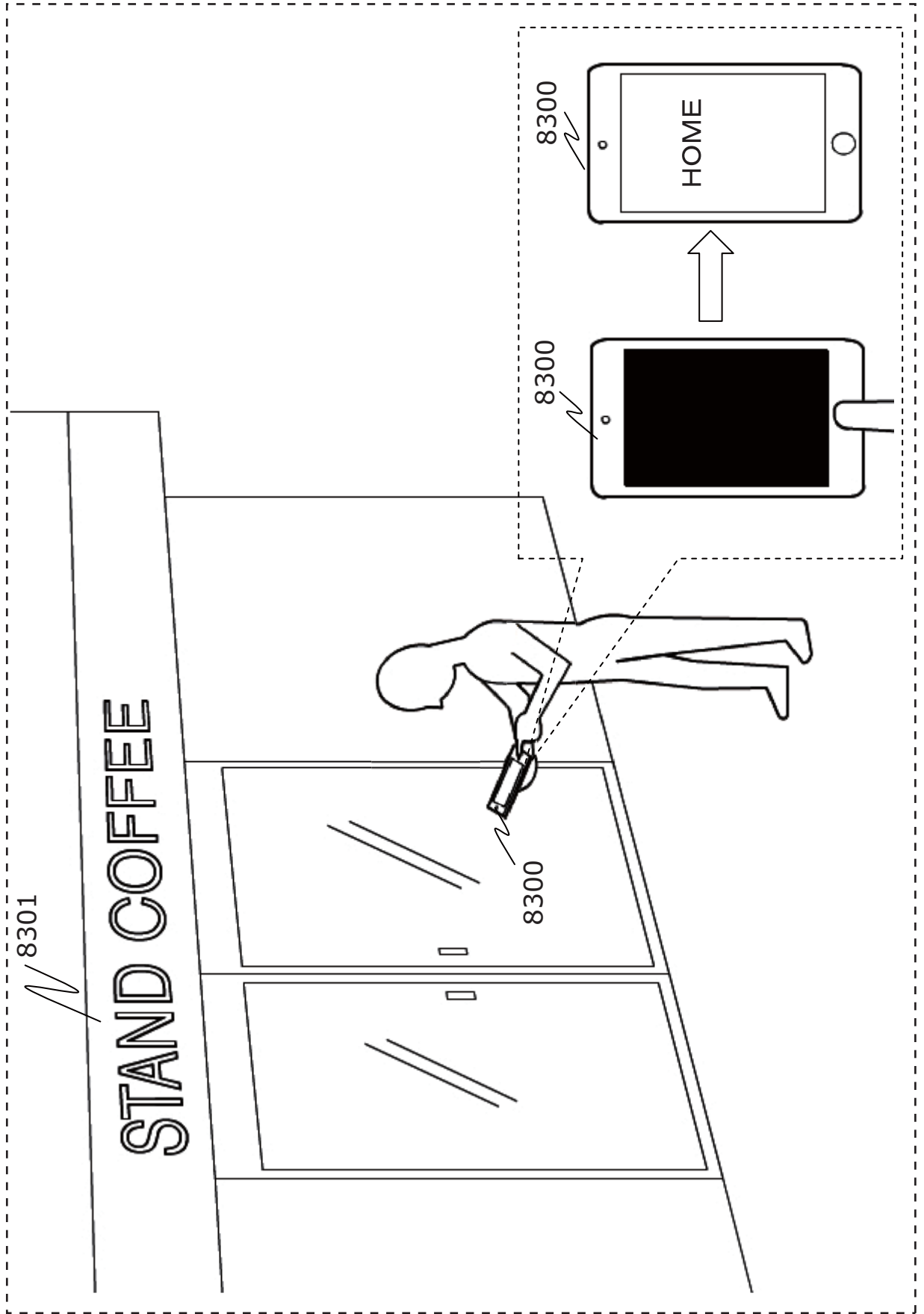


FIG. 82

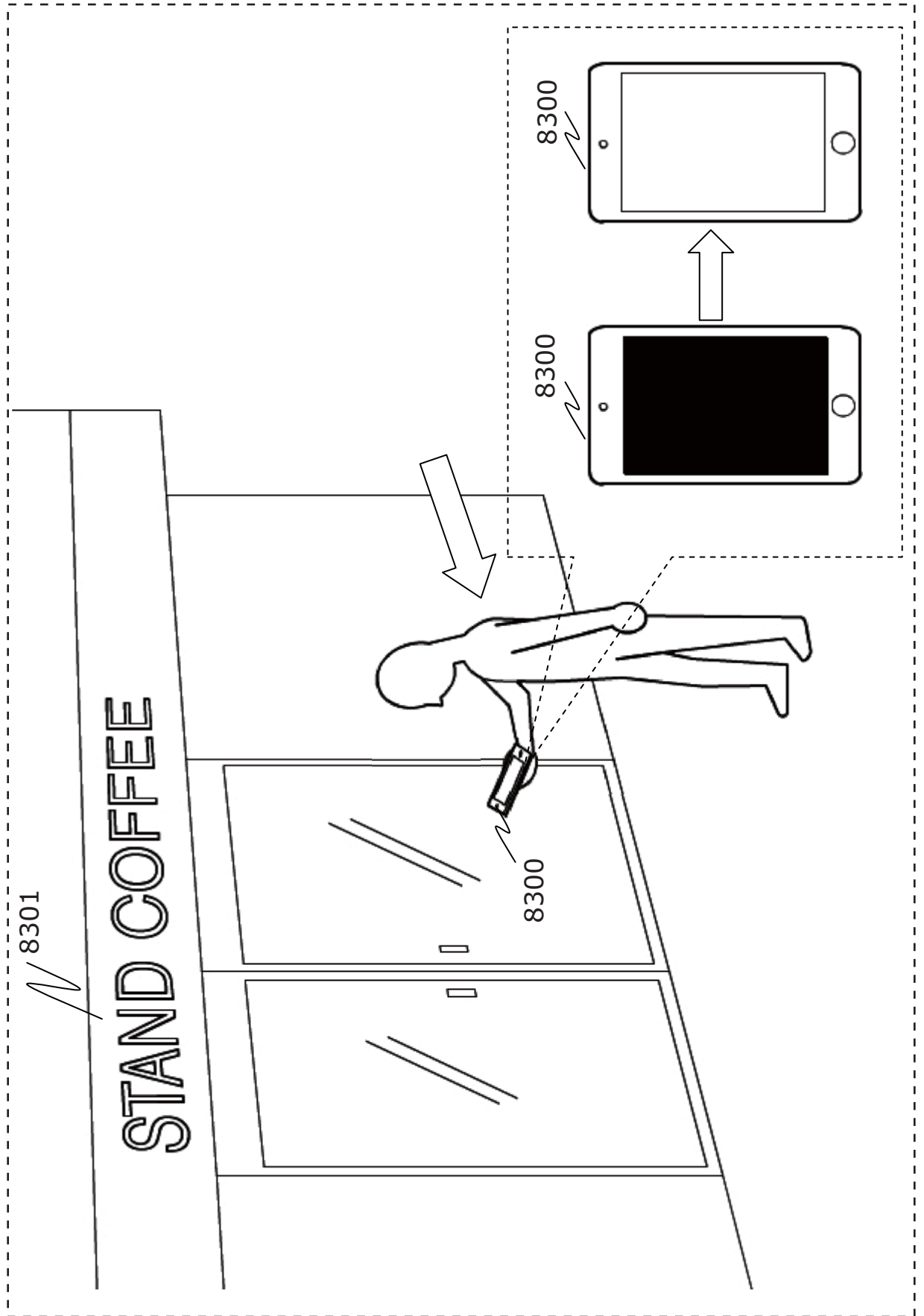


FIG. 83

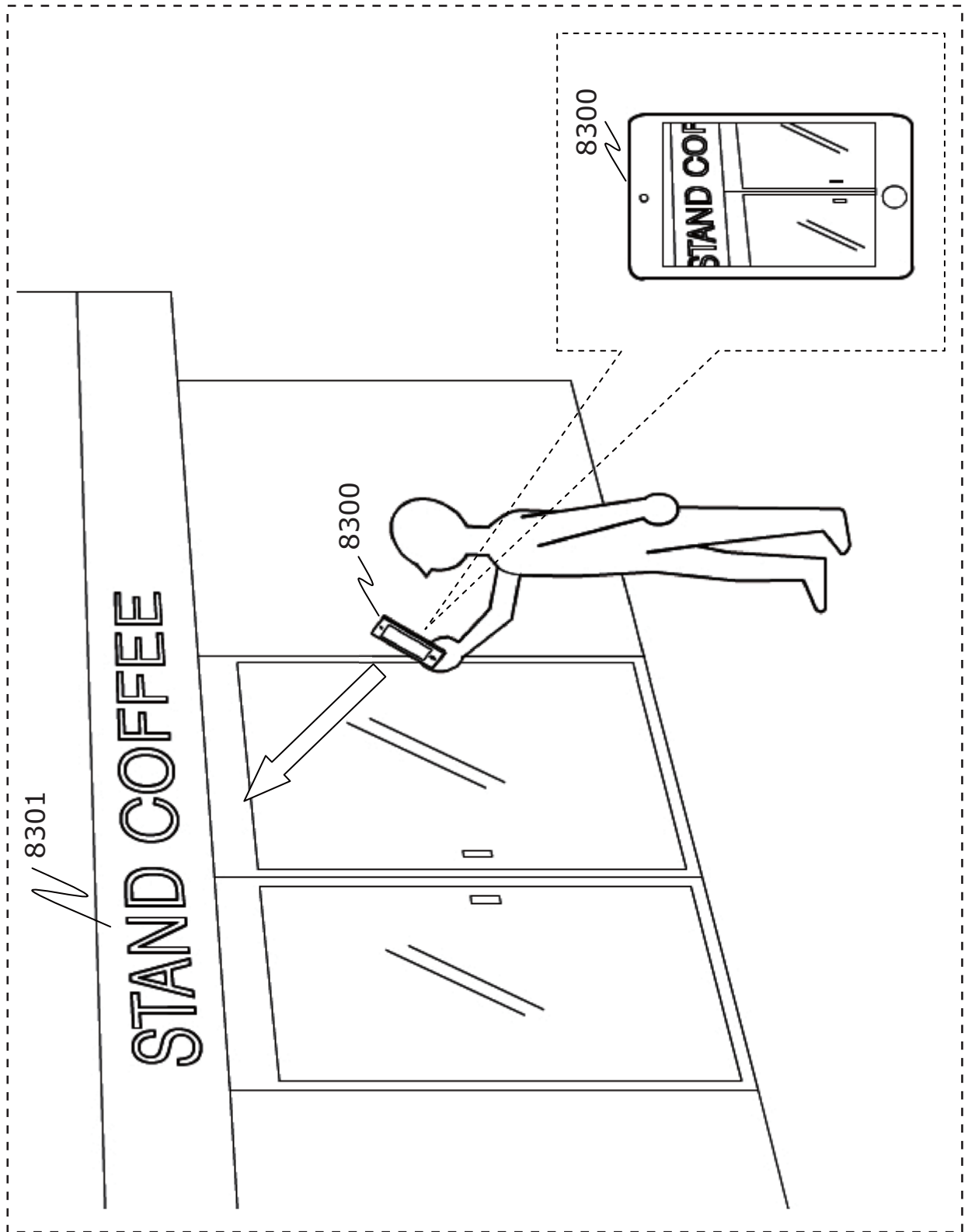


FIG. 84

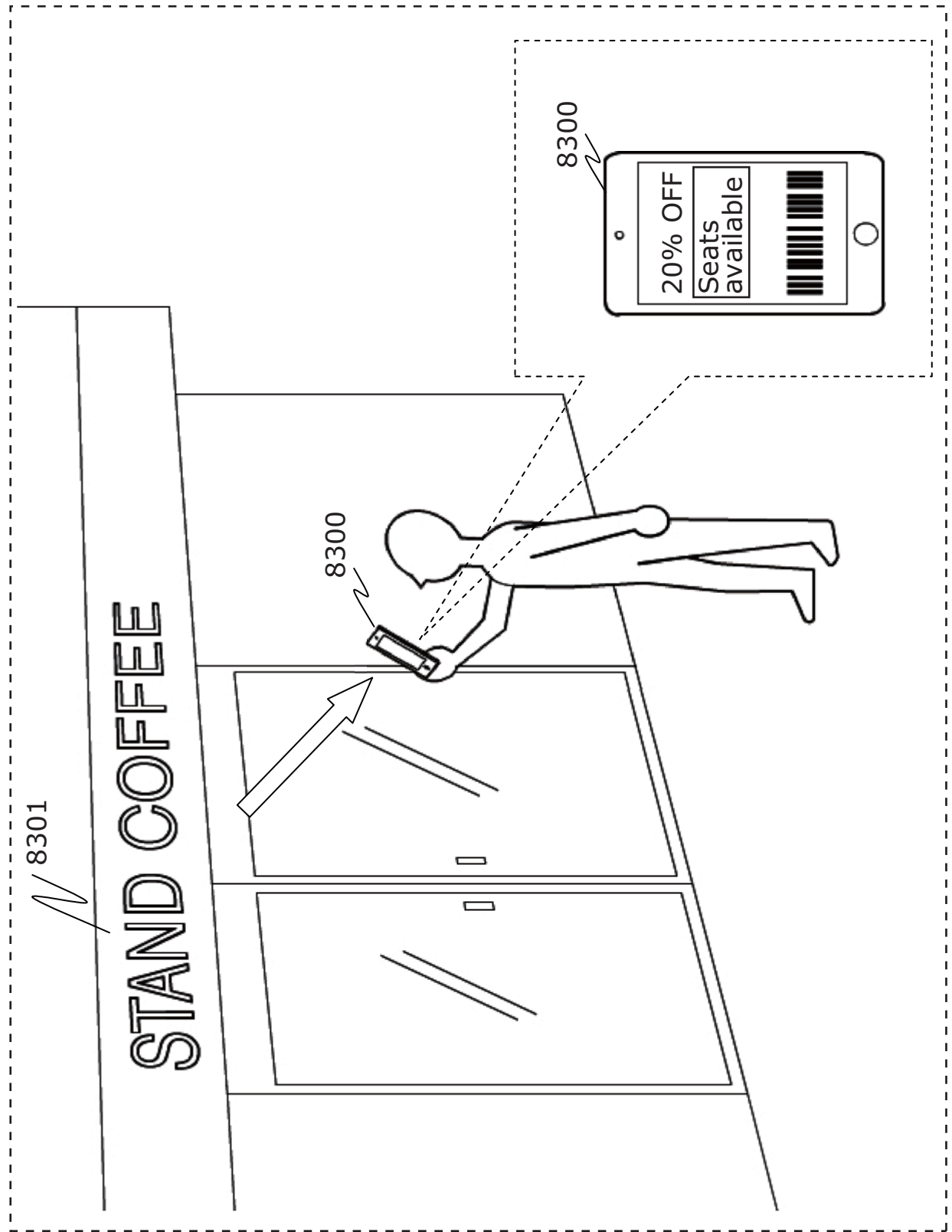


FIG. 85

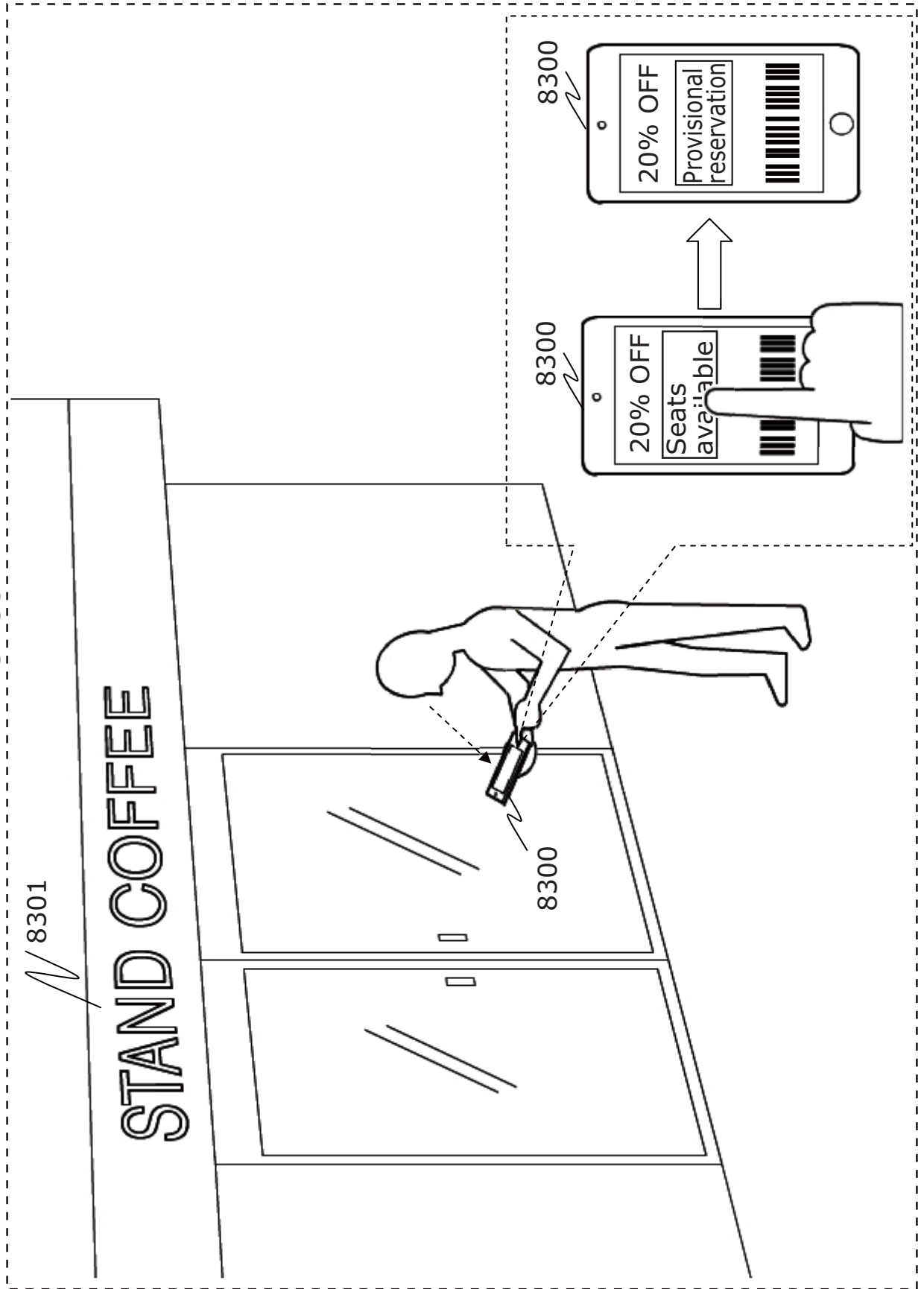


FIG. 86

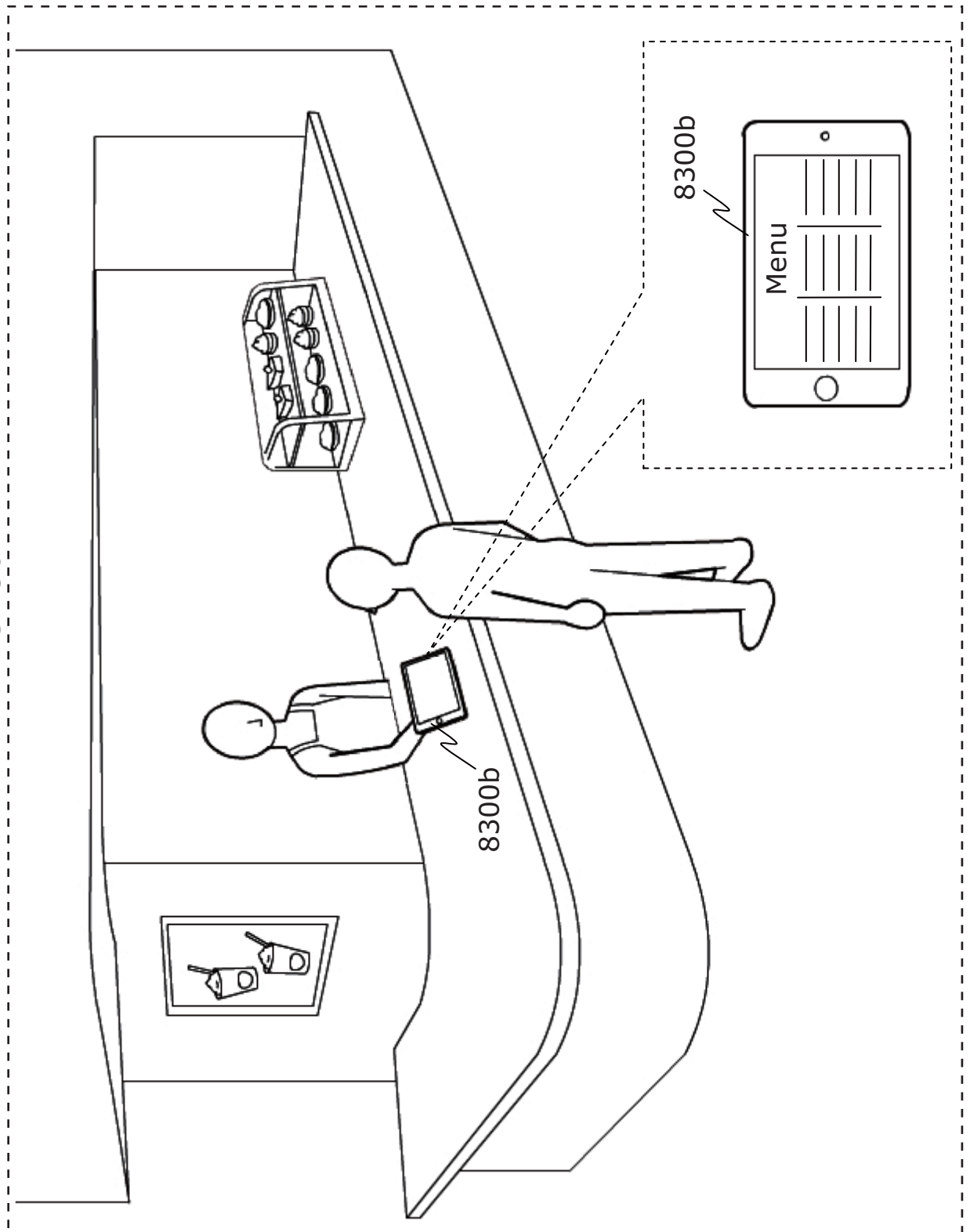


FIG. 87

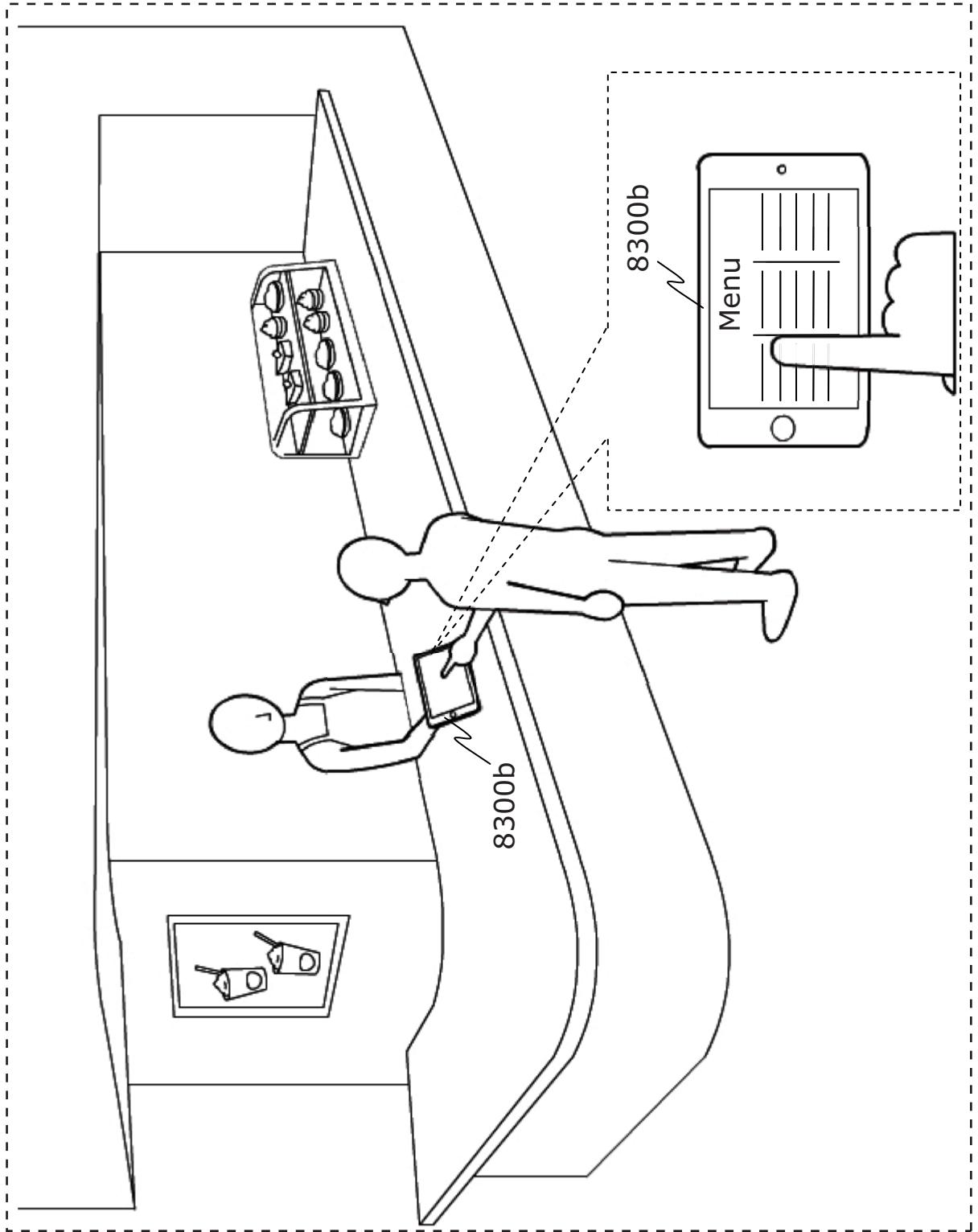


FIG. 88

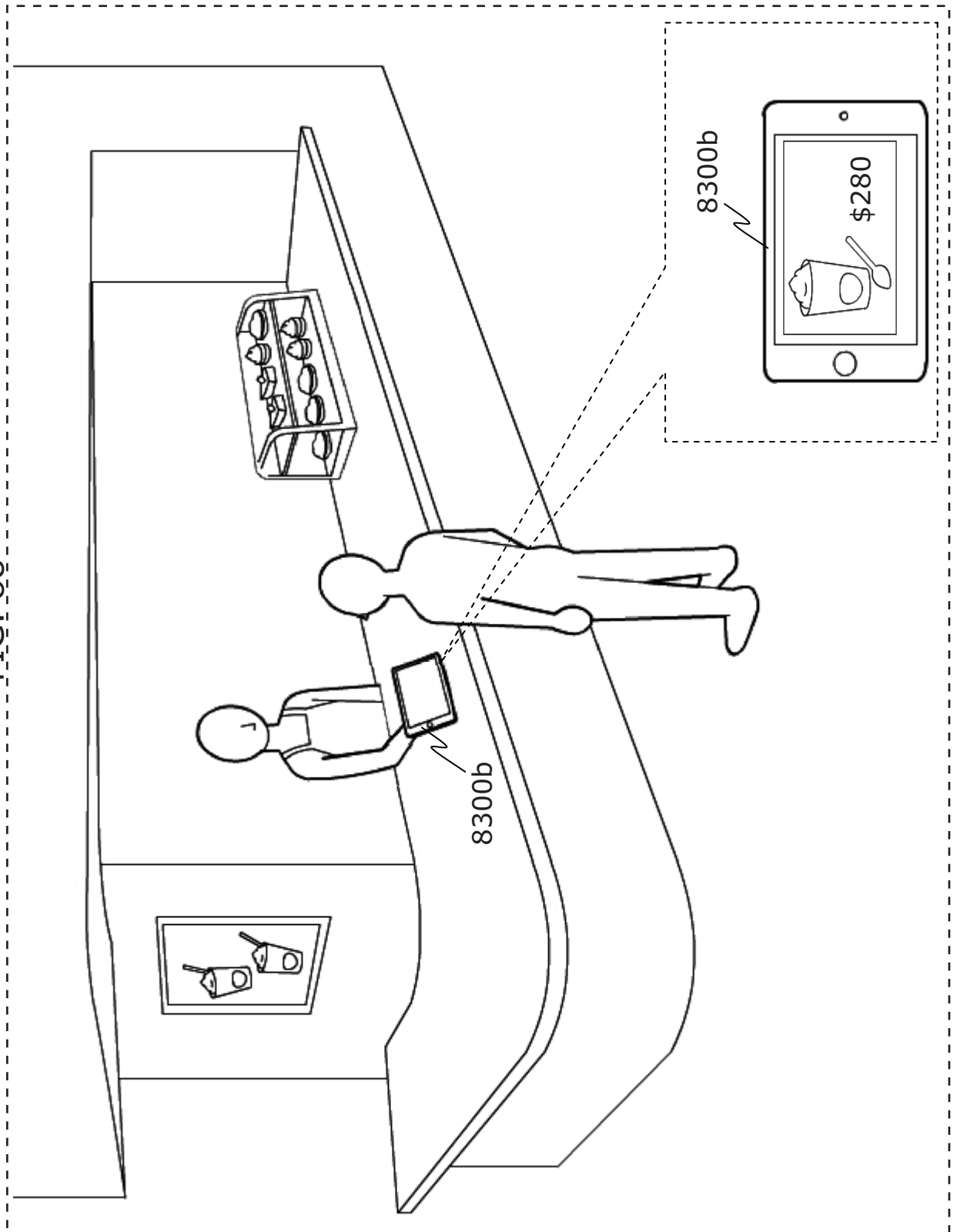


FIG. 89

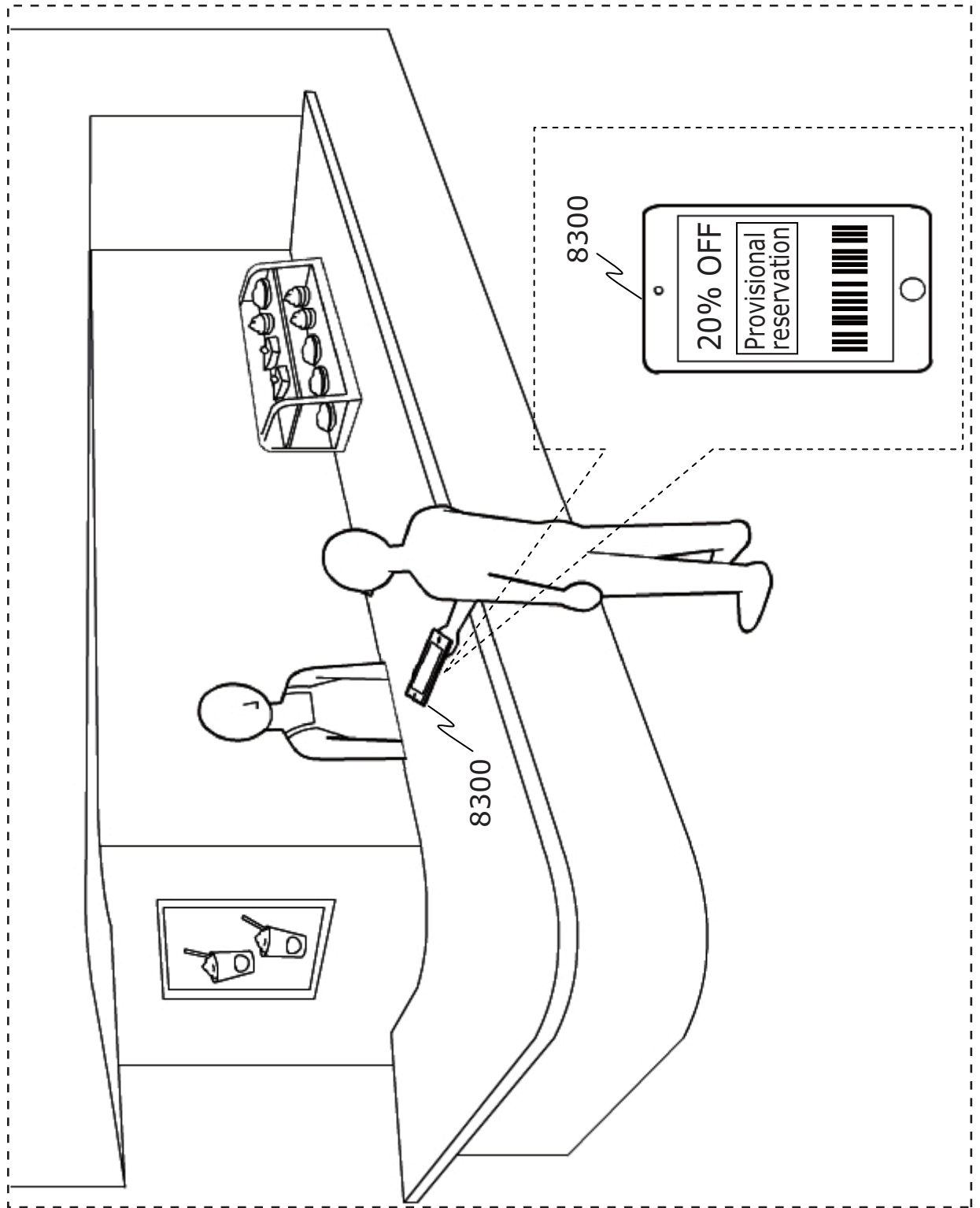


FIG. 90

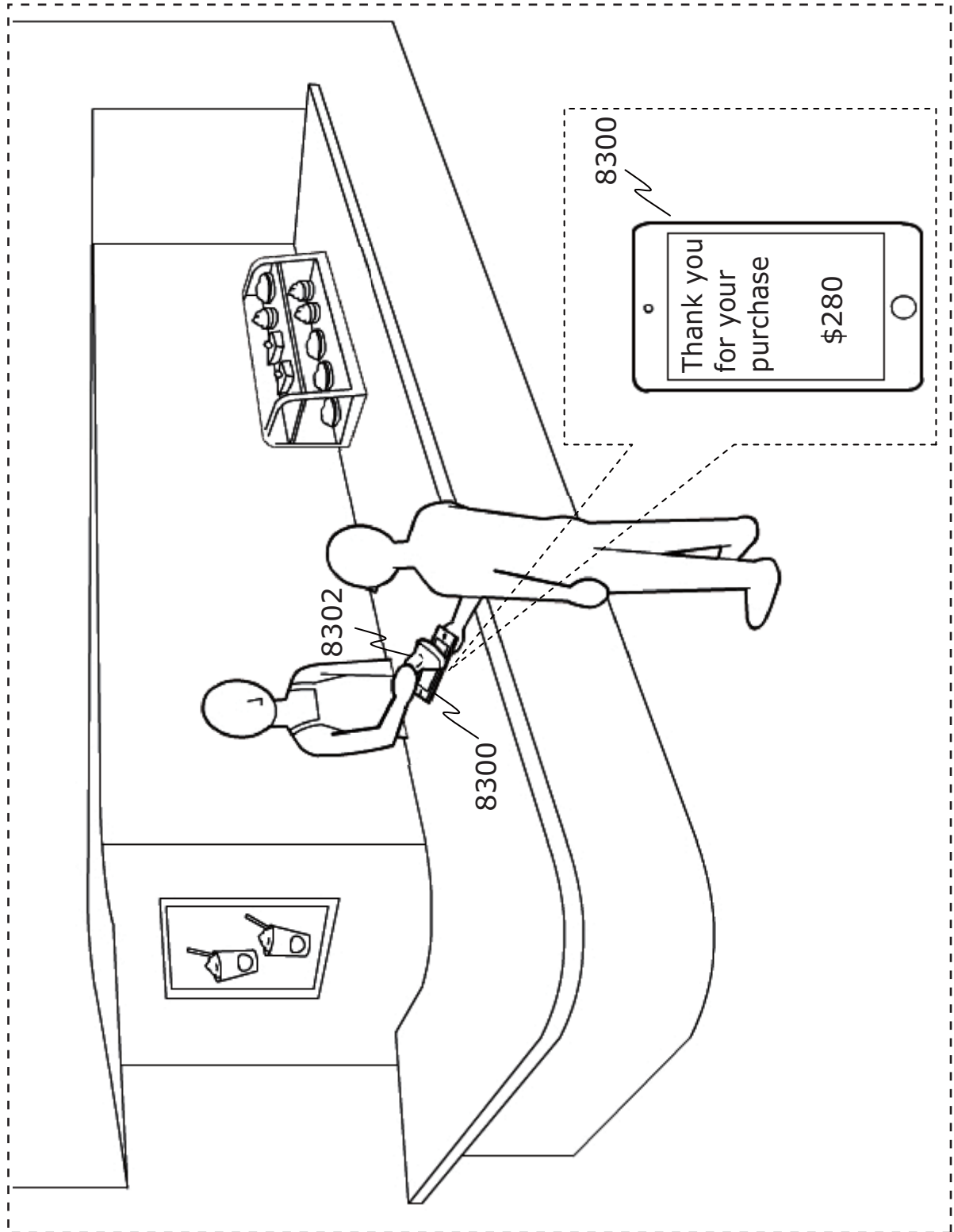


FIG. 91

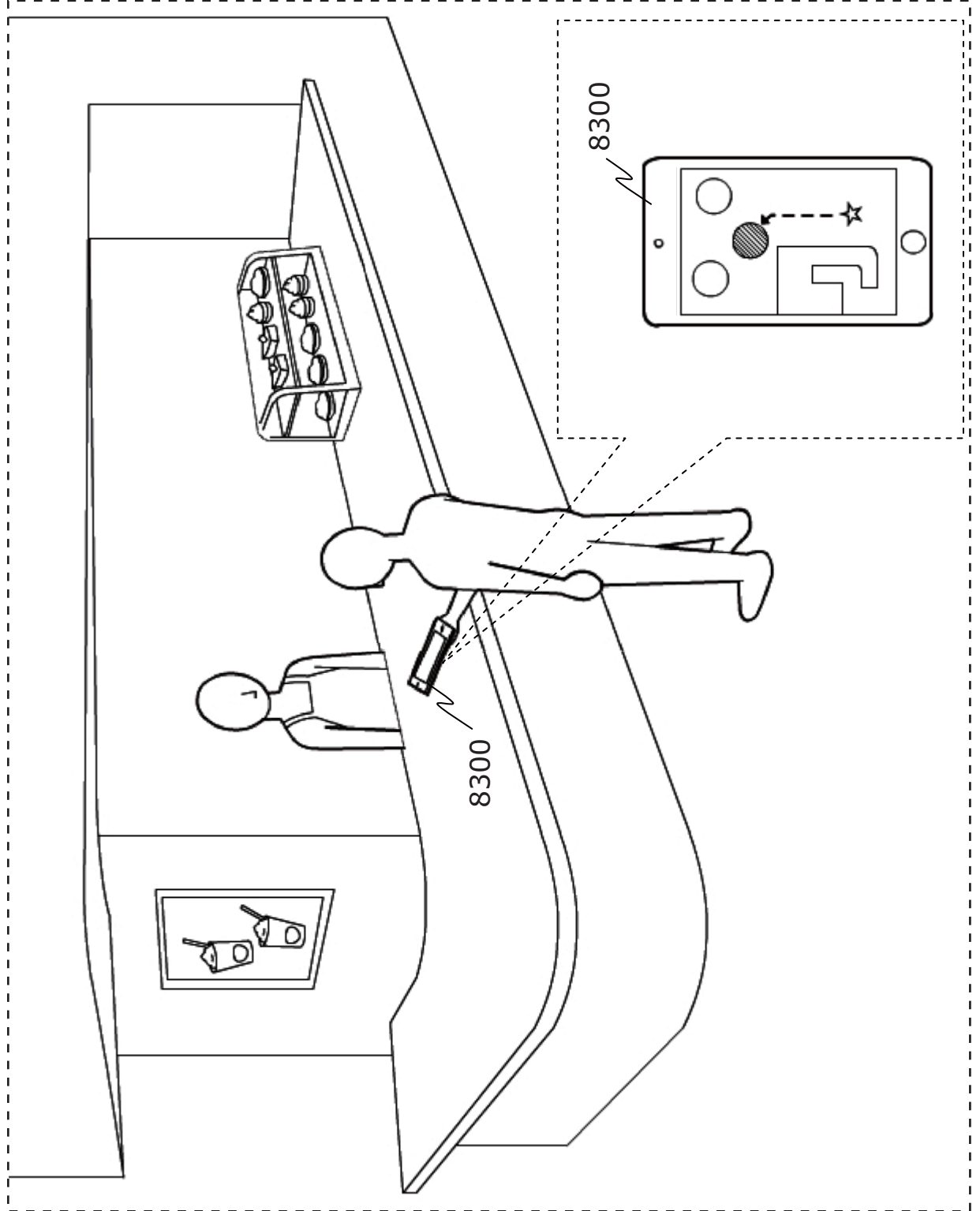


FIG. 92

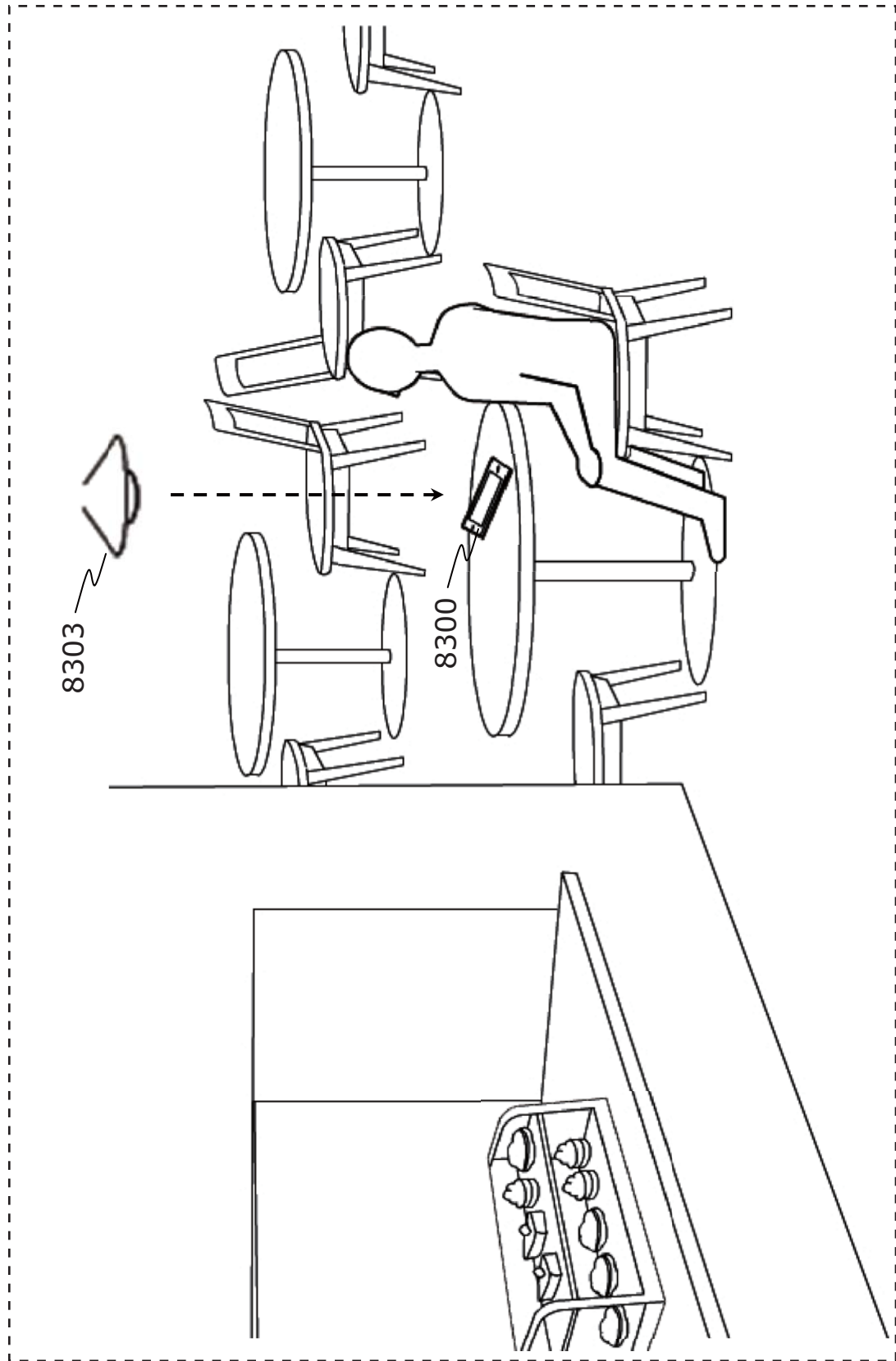


FIG. 93

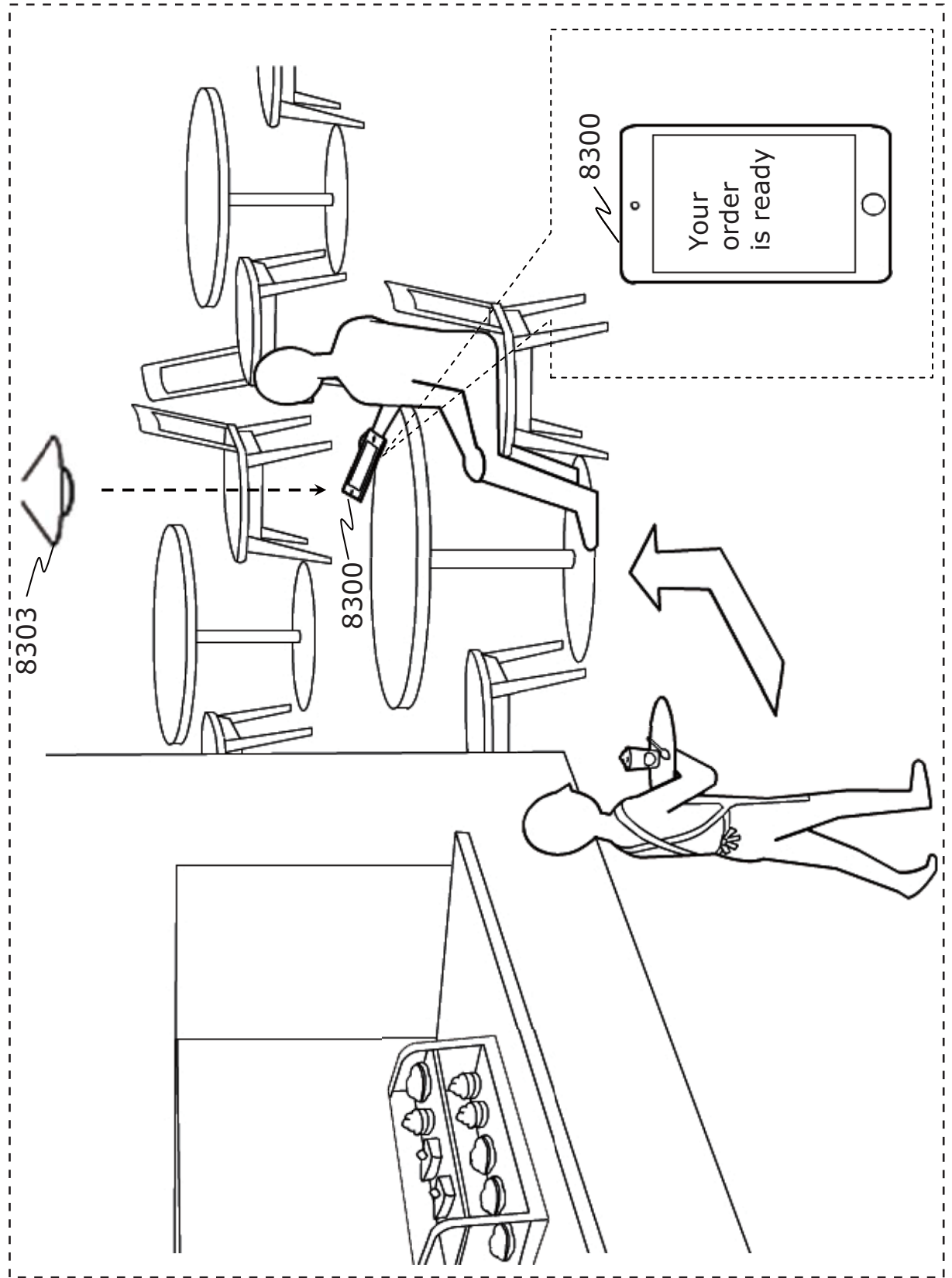


FIG. 94

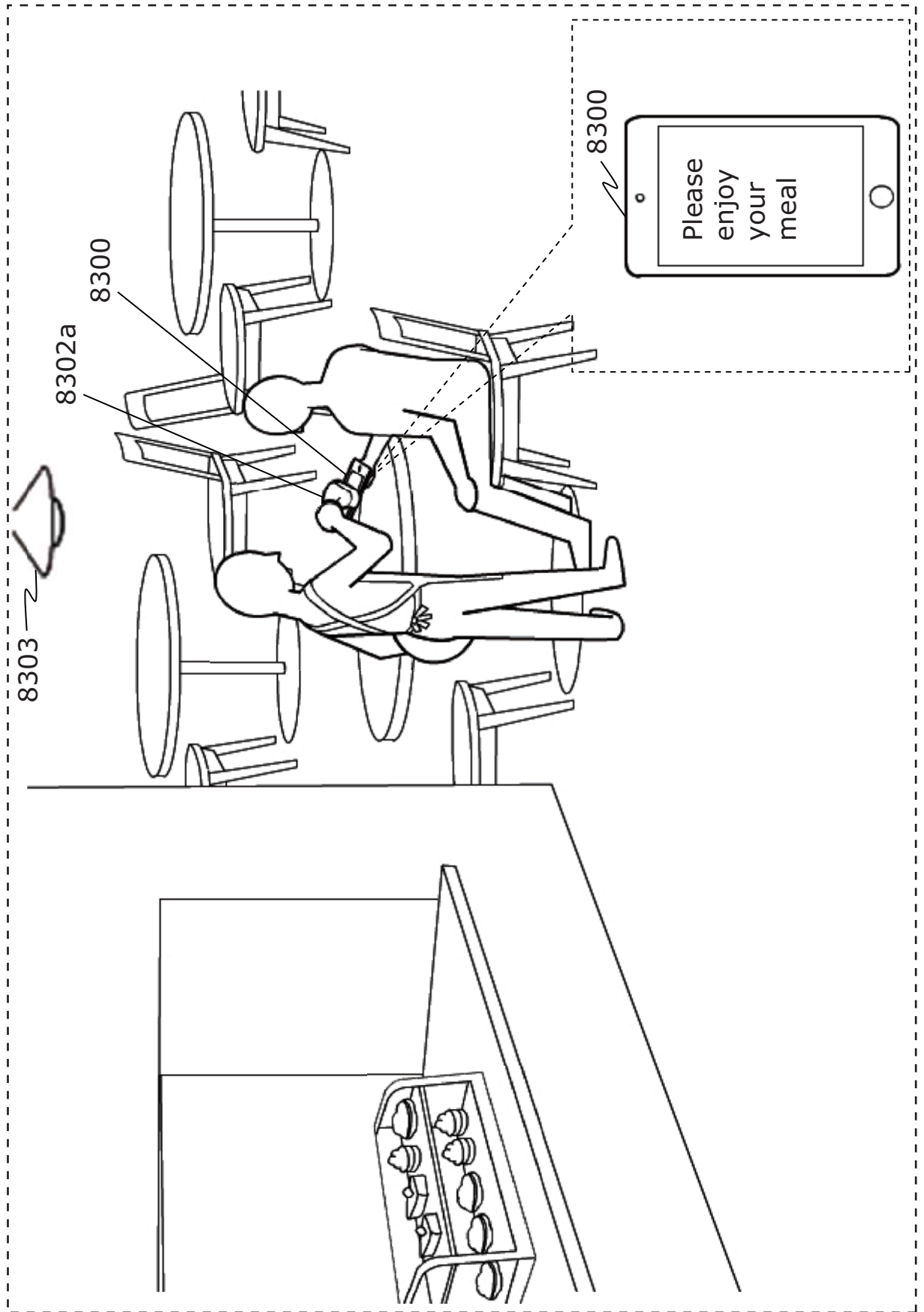


FIG. 95

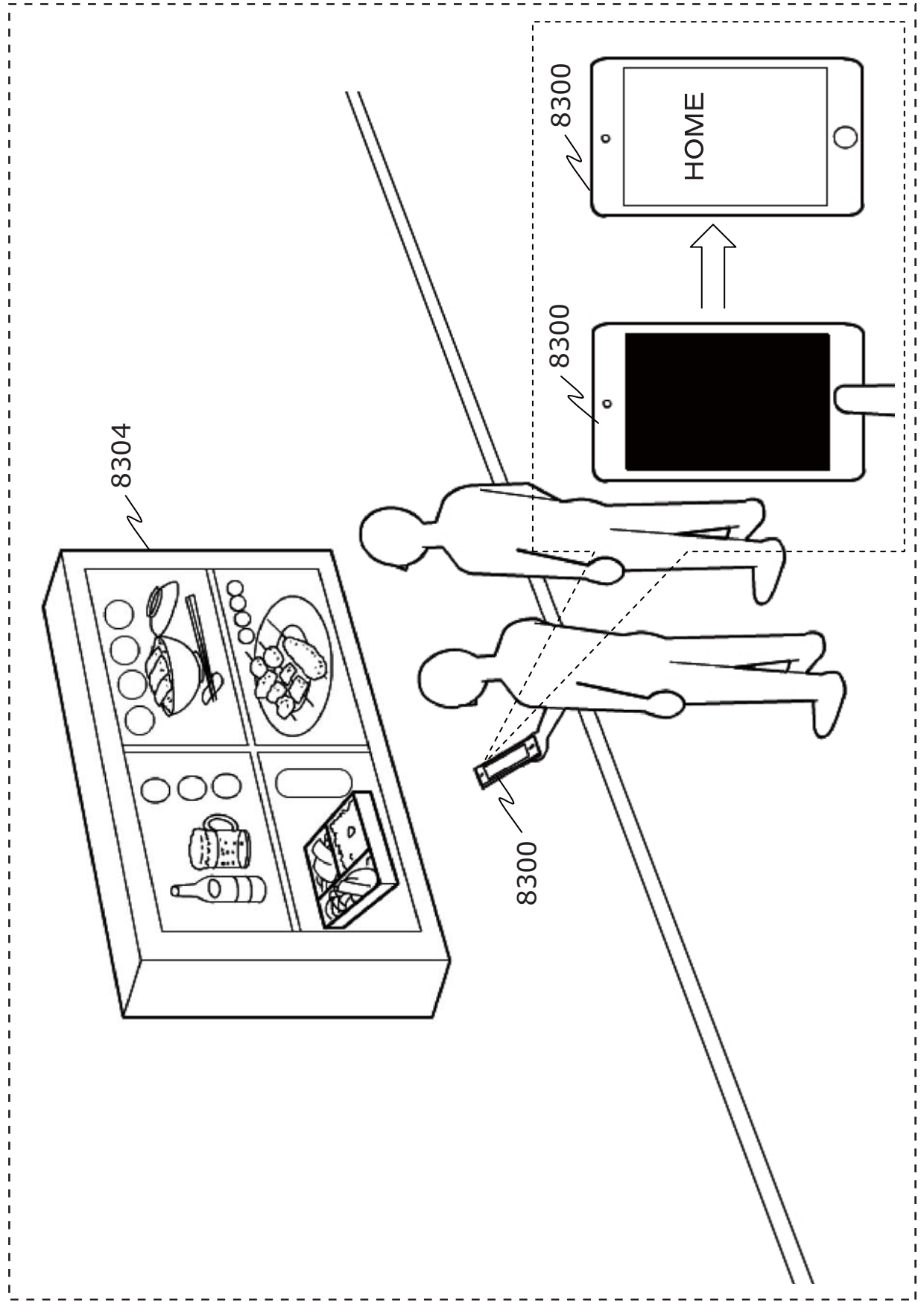


FIG. 96

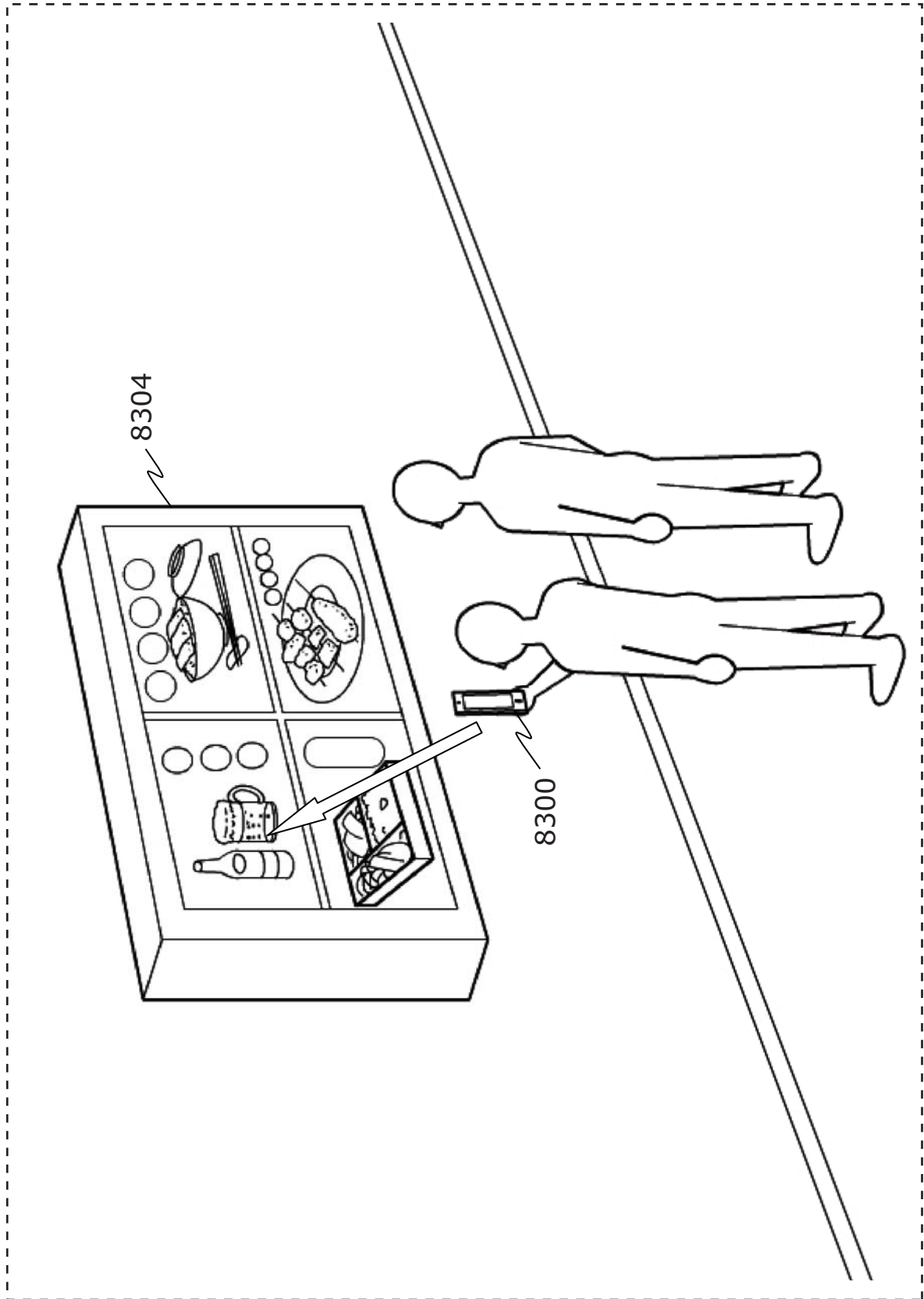


FIG. 97

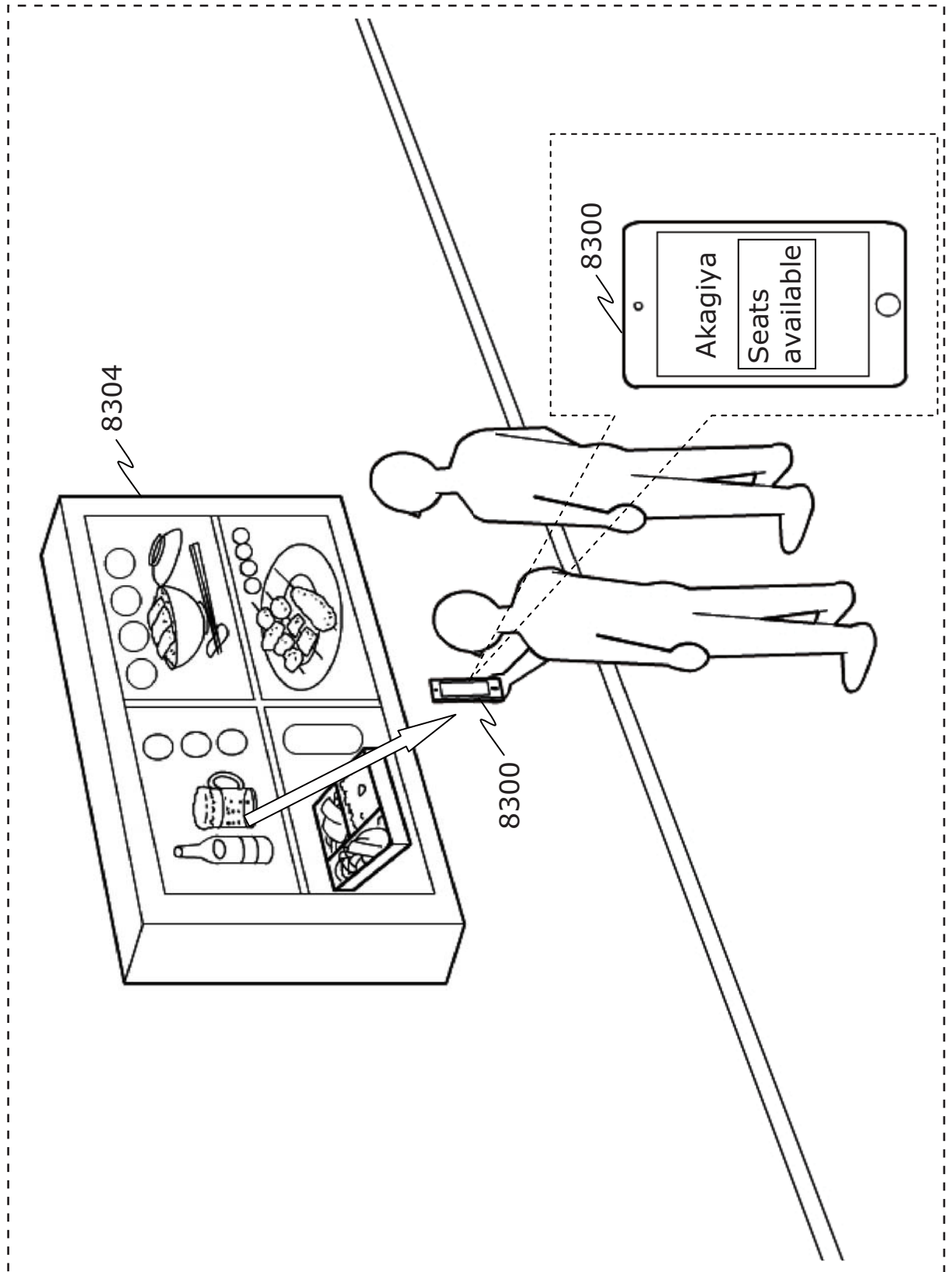


FIG. 98

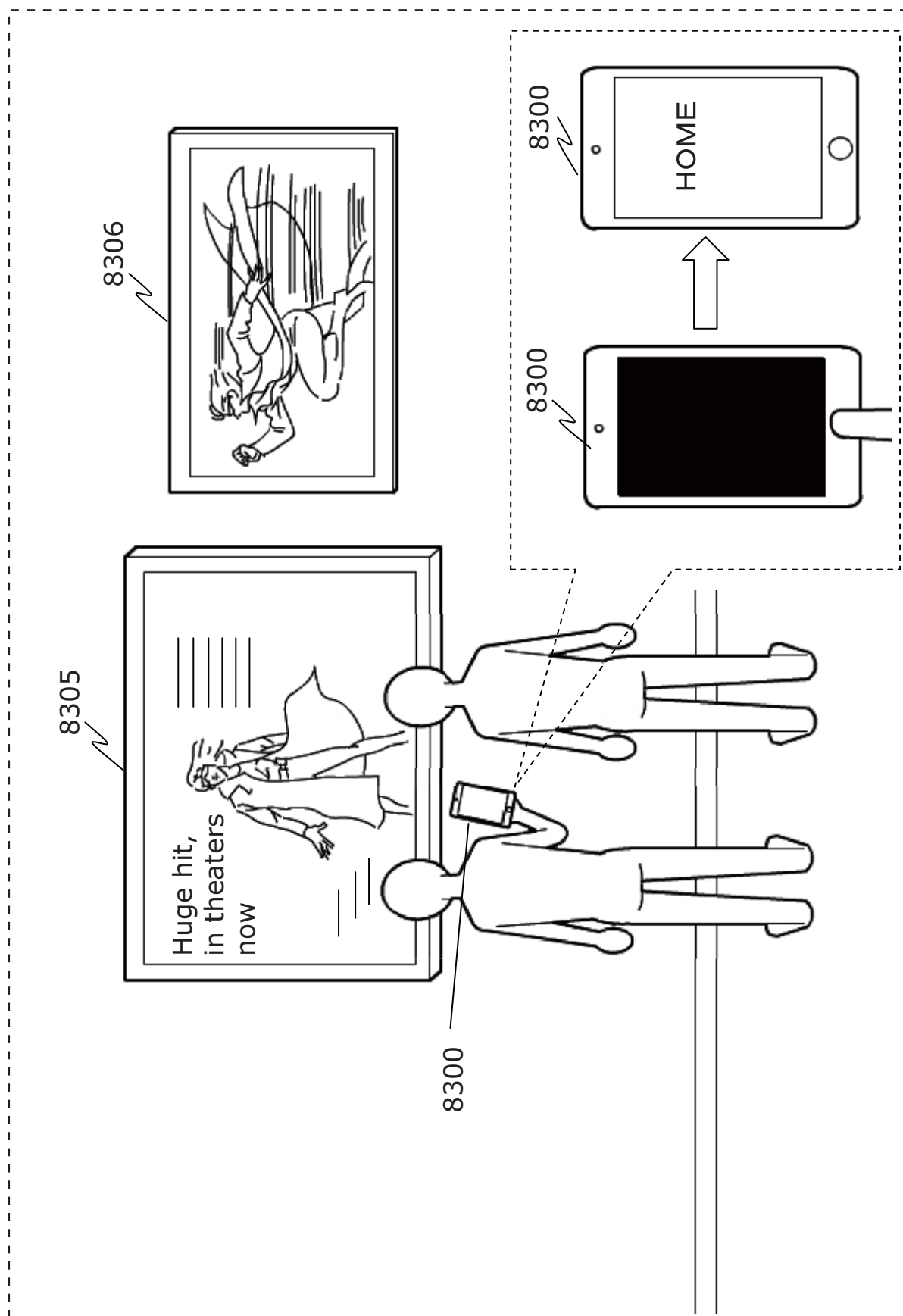


FIG. 99

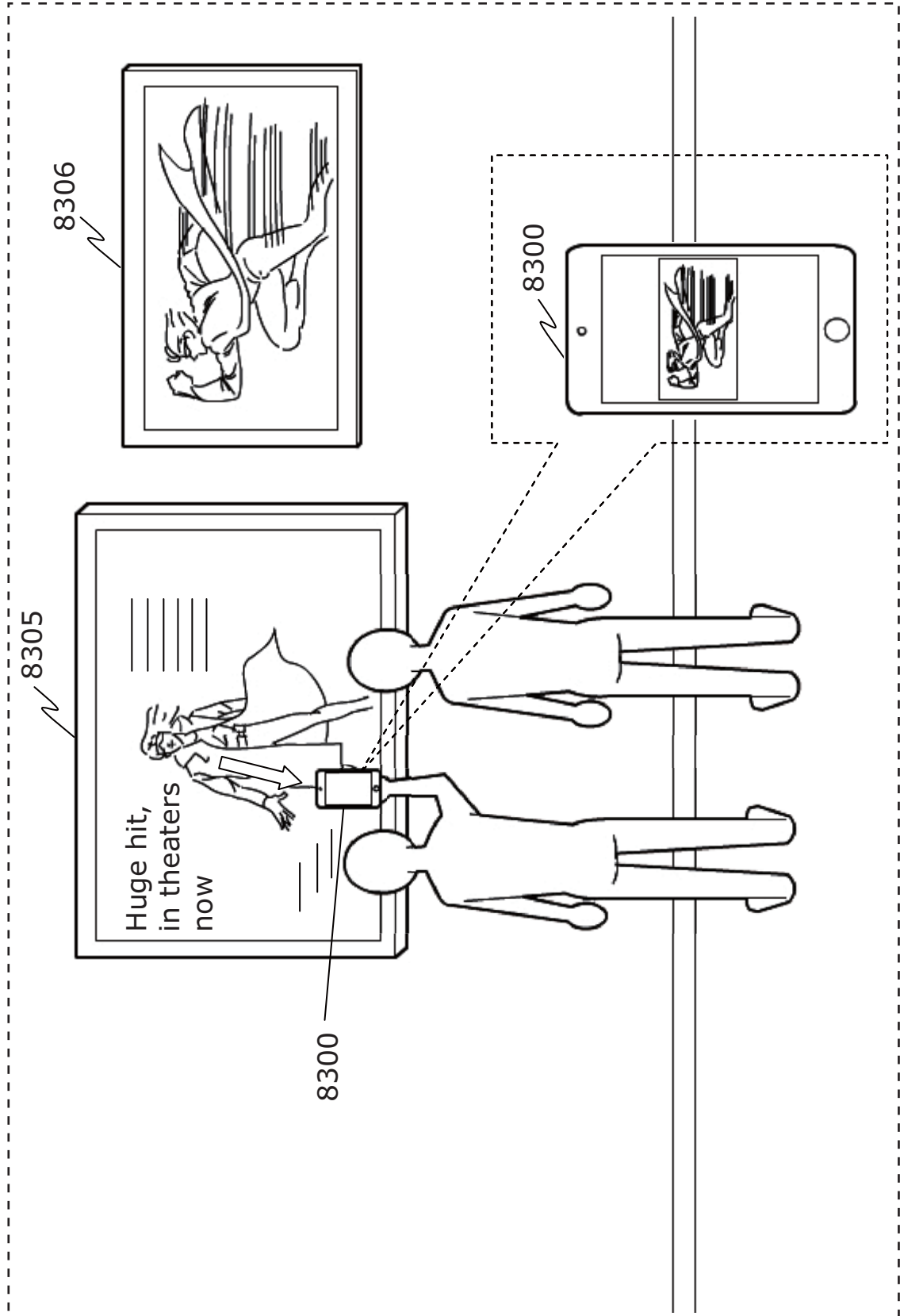


FIG. 100

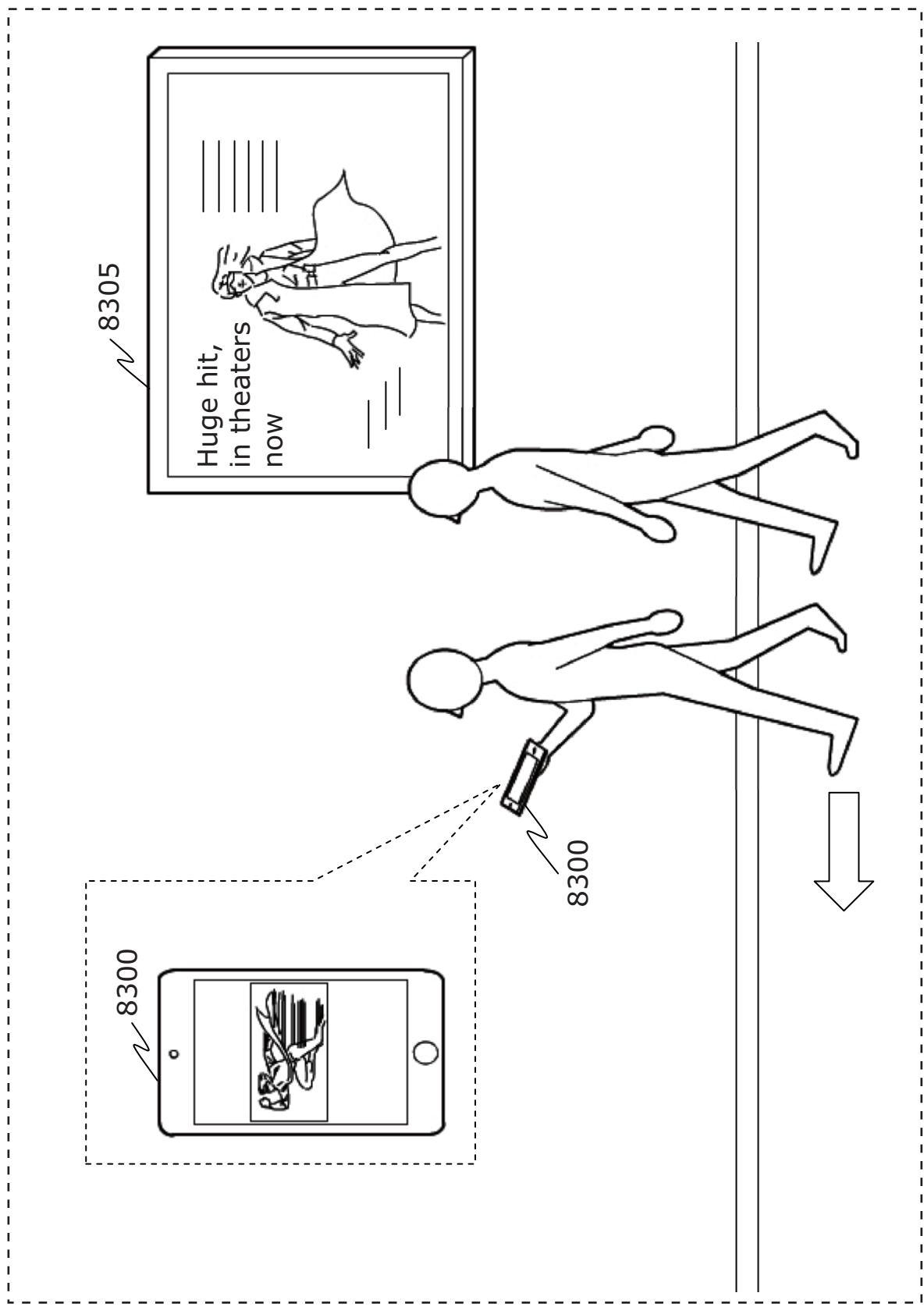


FIG. 101

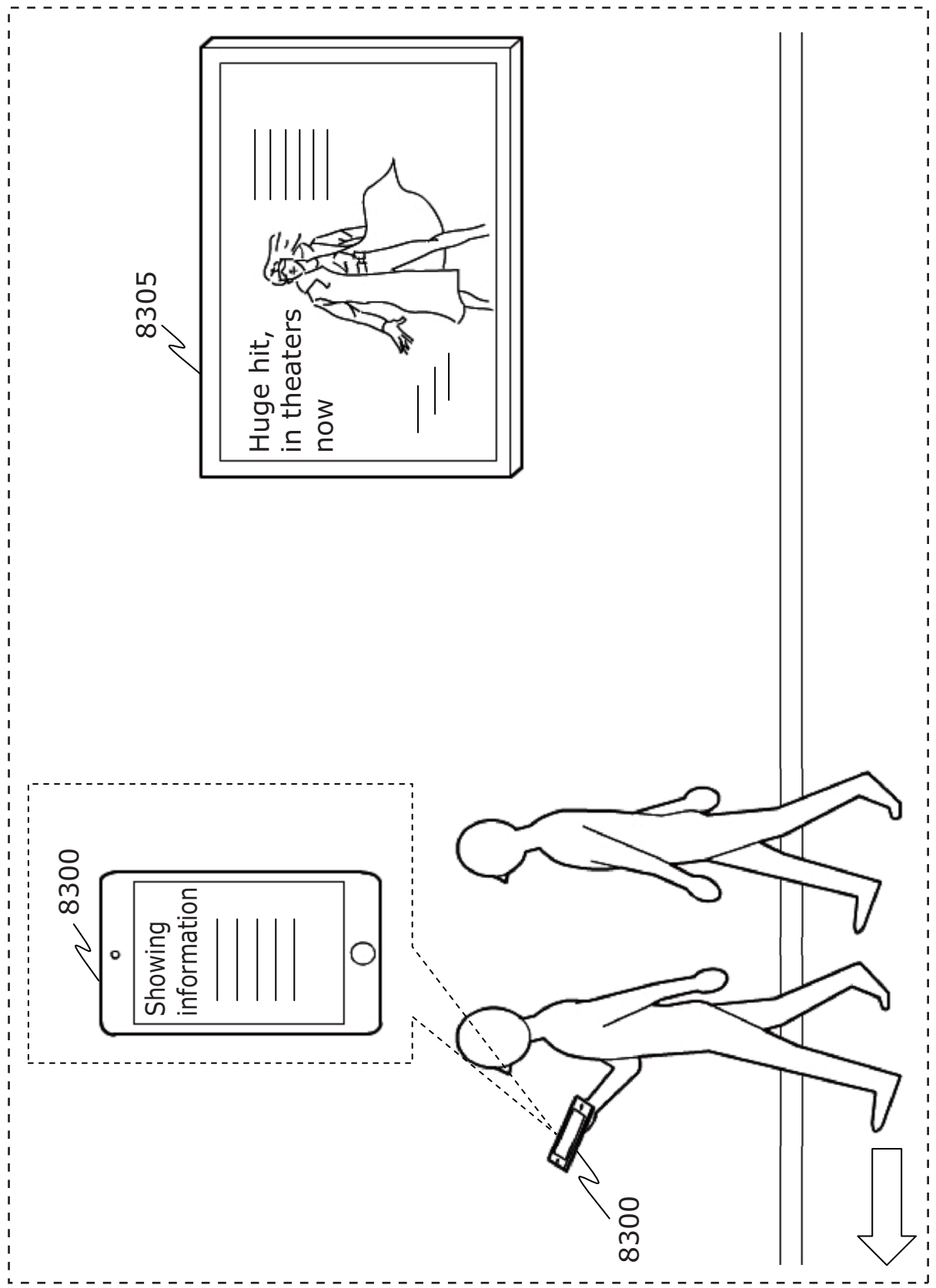


FIG. 102

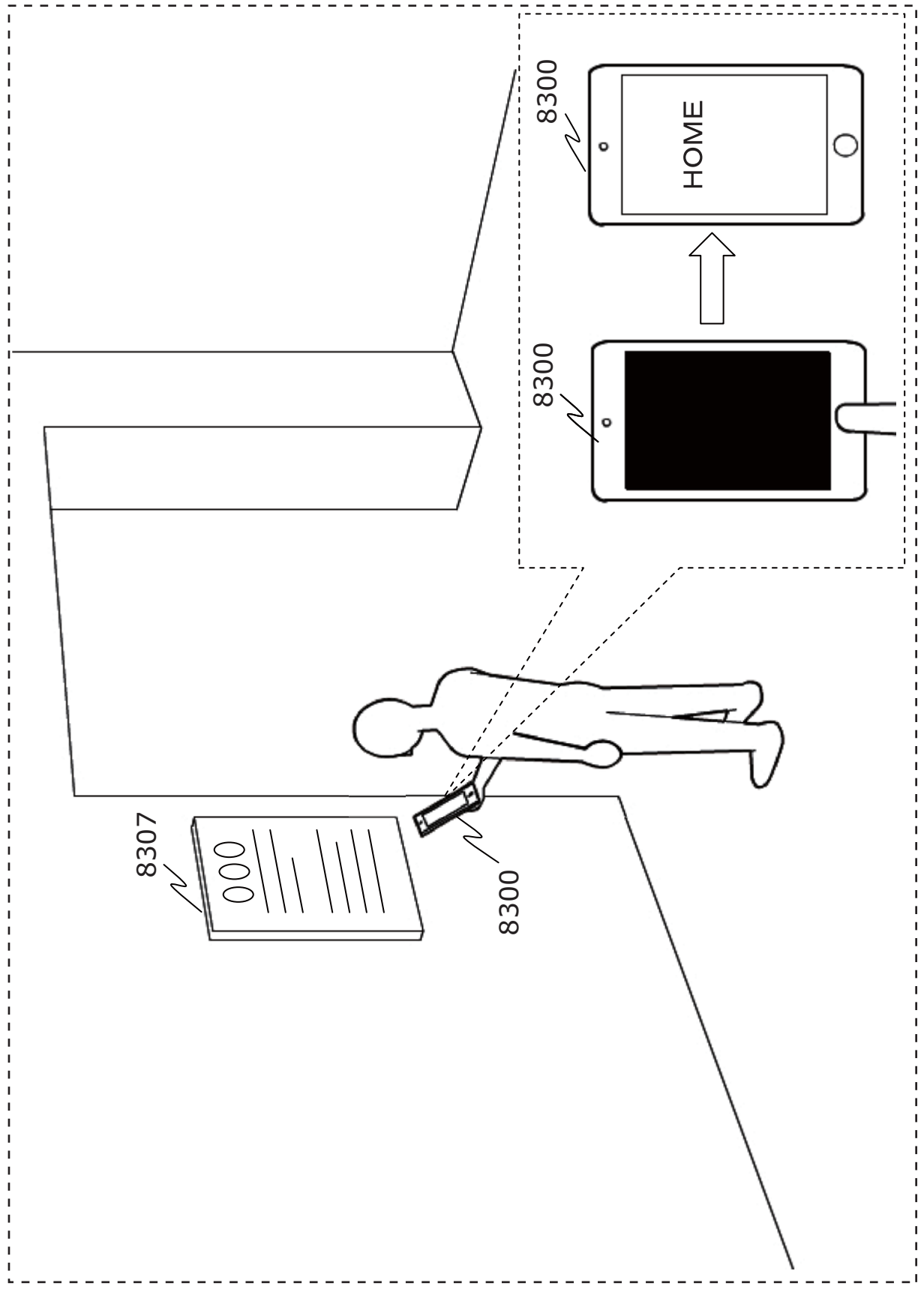


FIG. 103

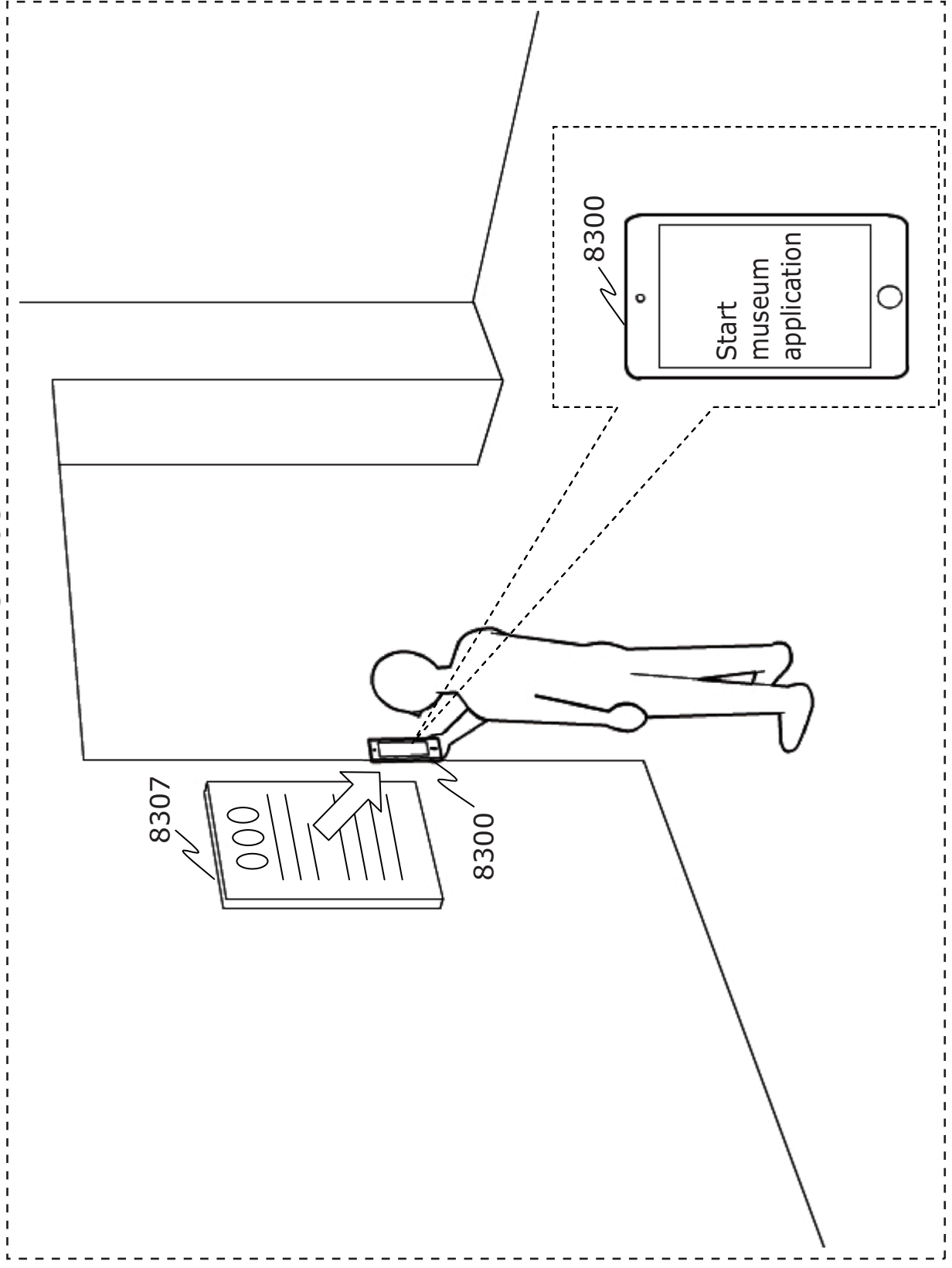


FIG. 104

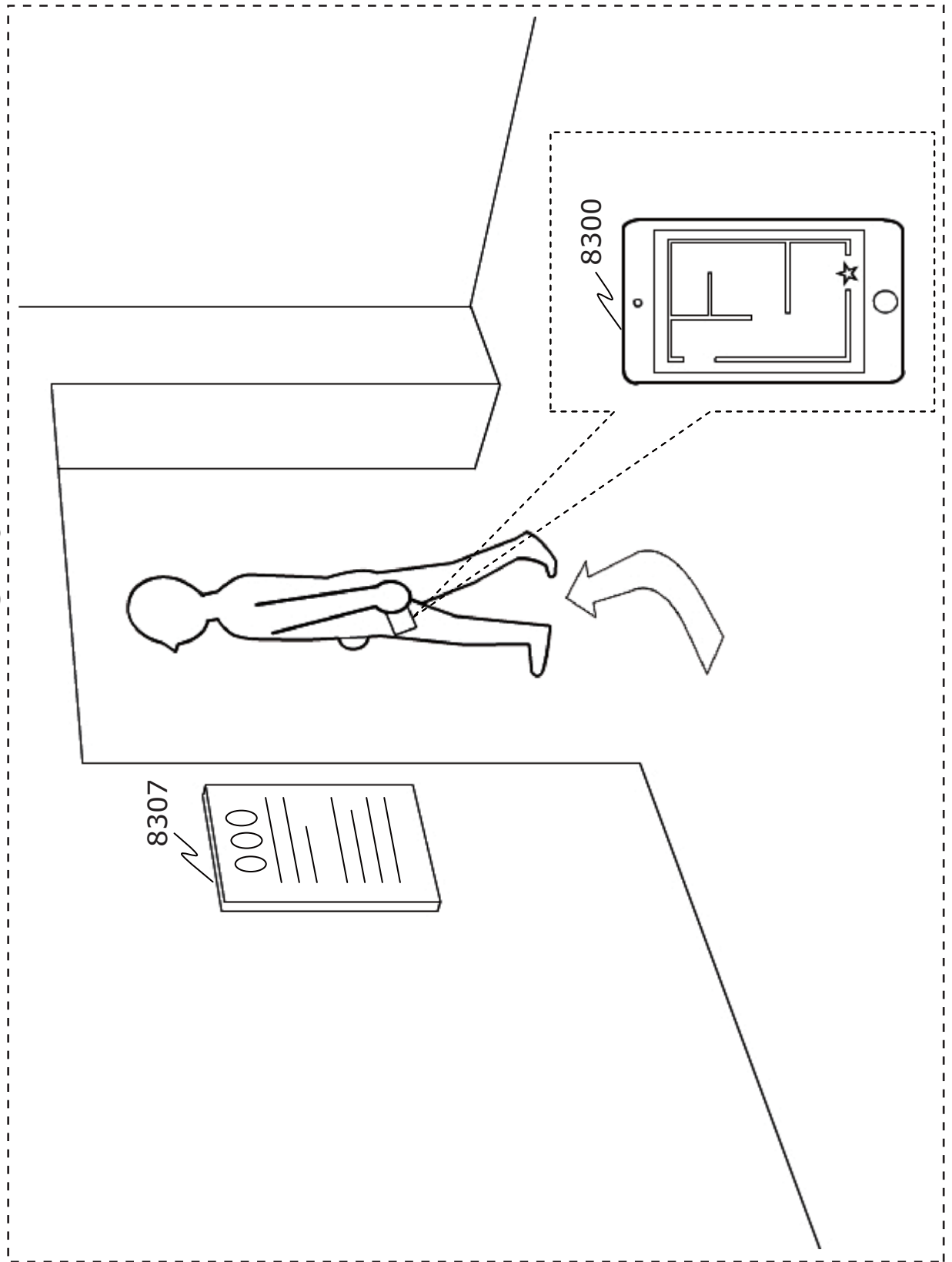


FIG. 105

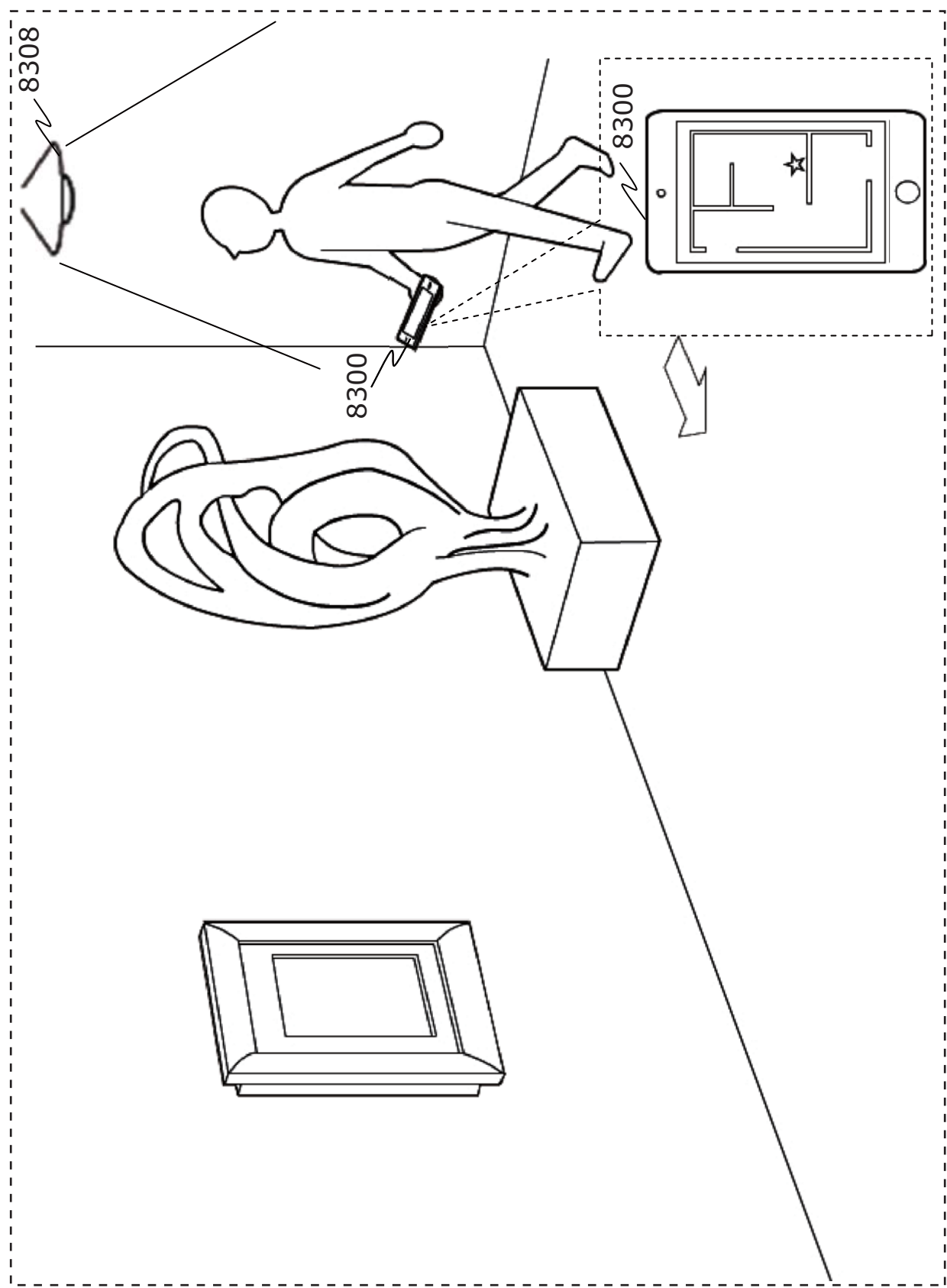


FIG. 106

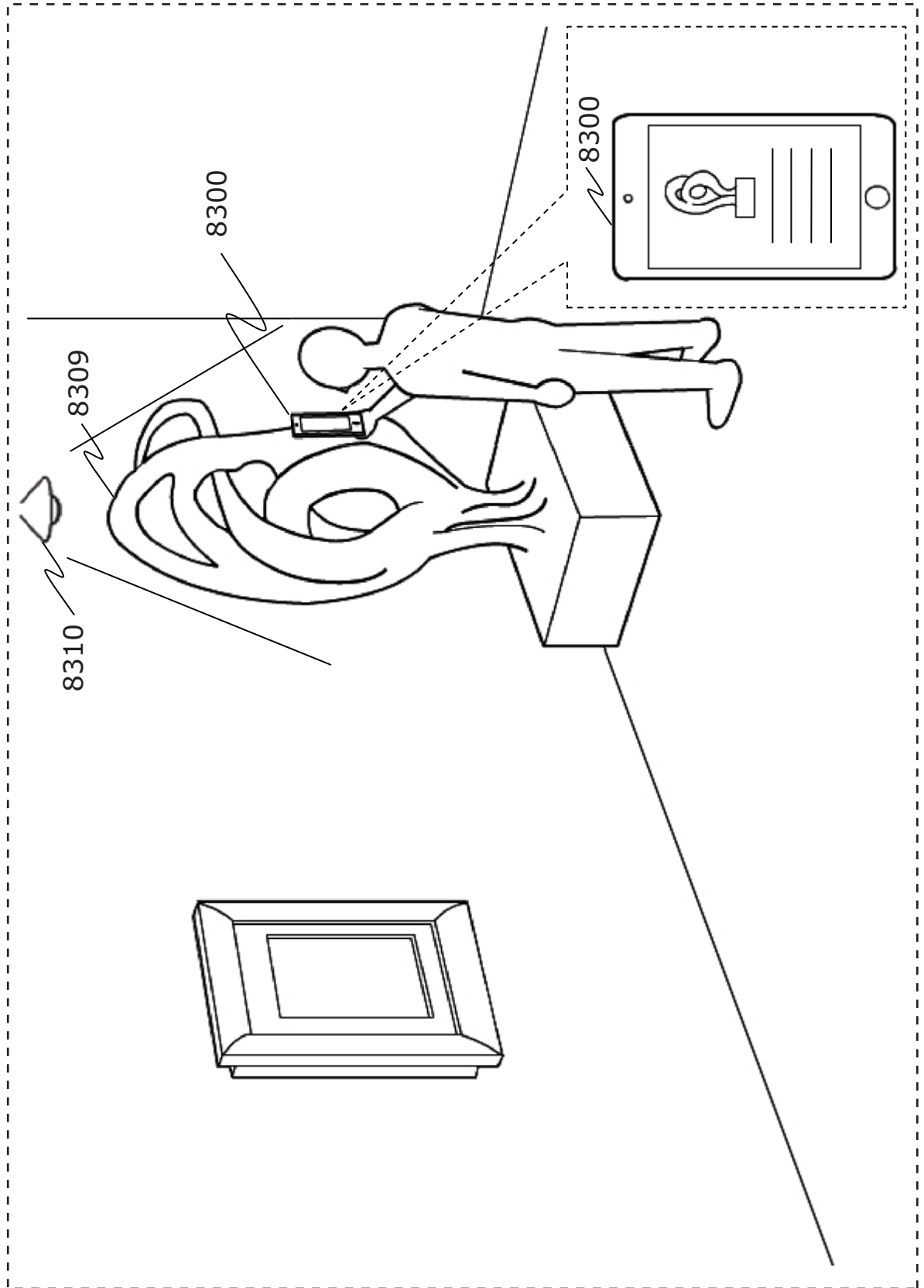


FIG. 107

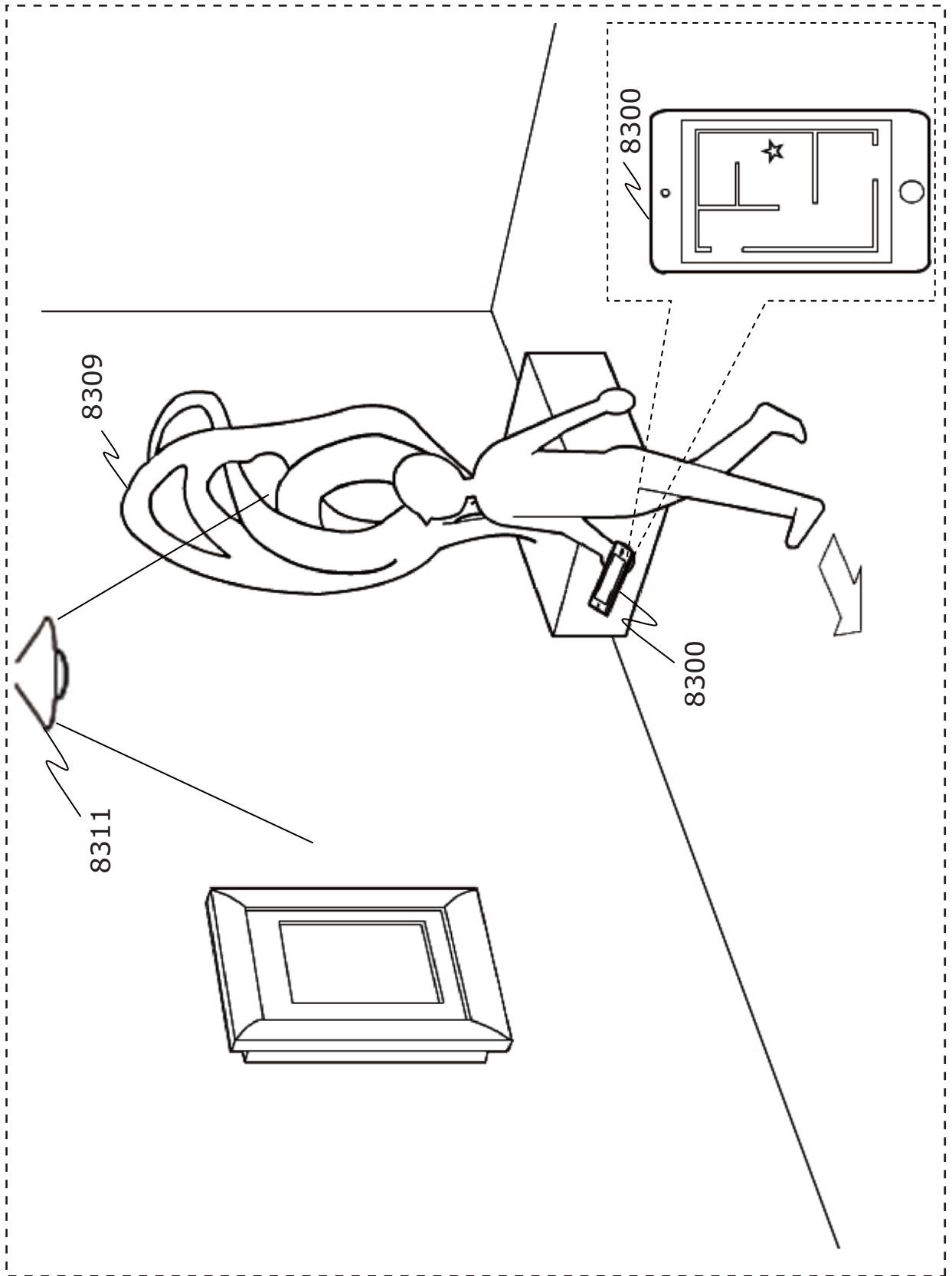


FIG. 108

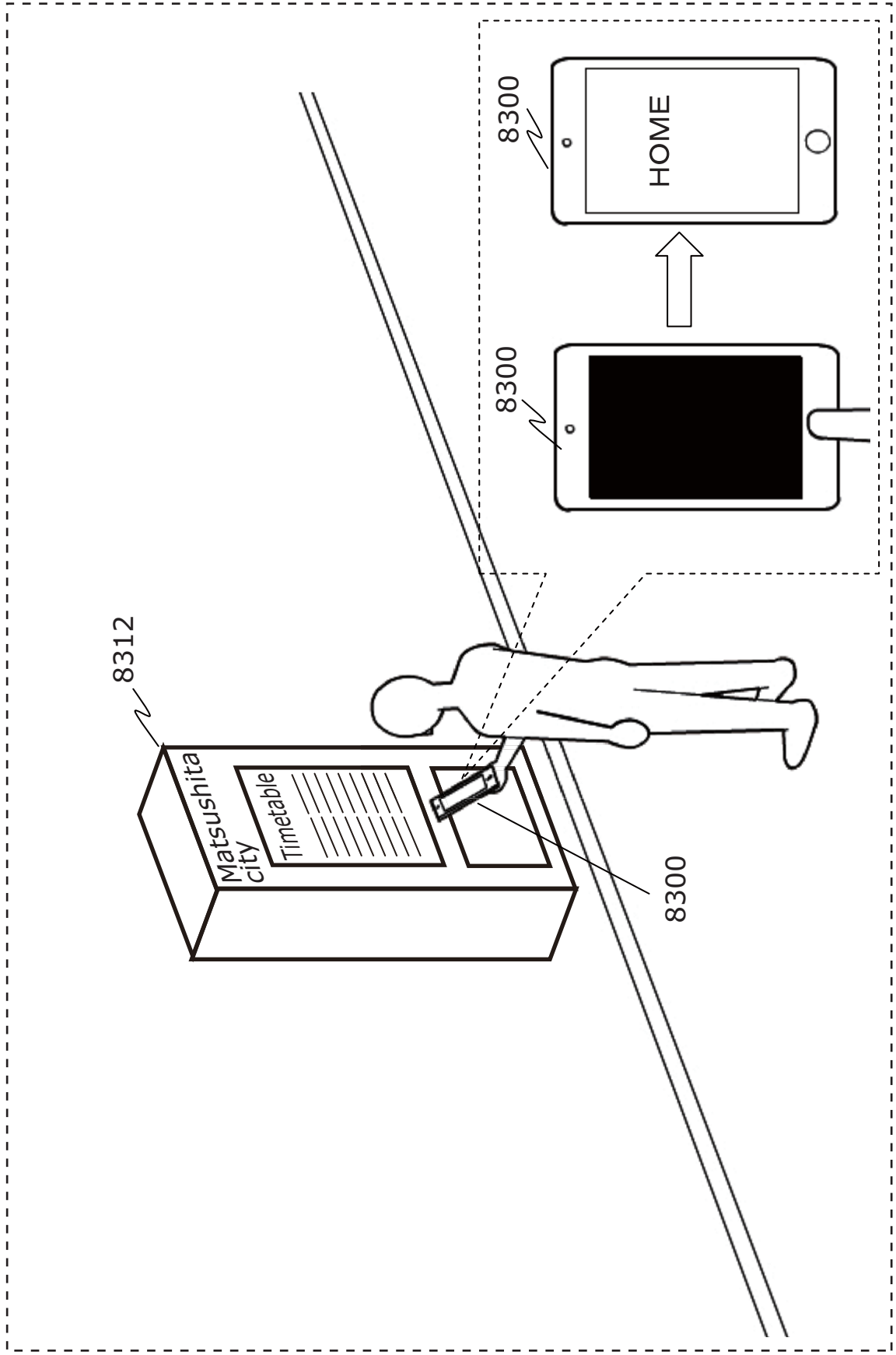


FIG. 109

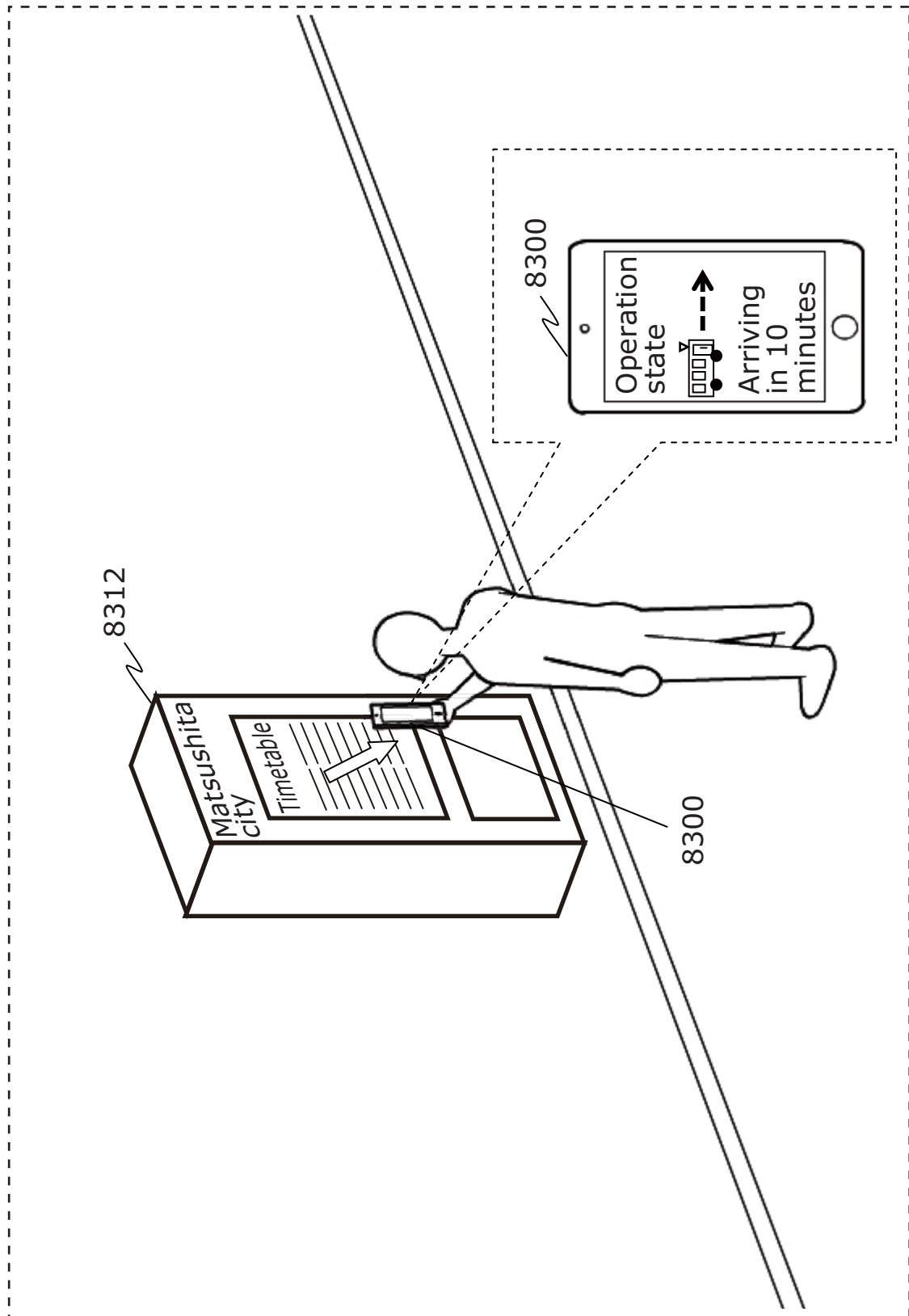


FIG. 110

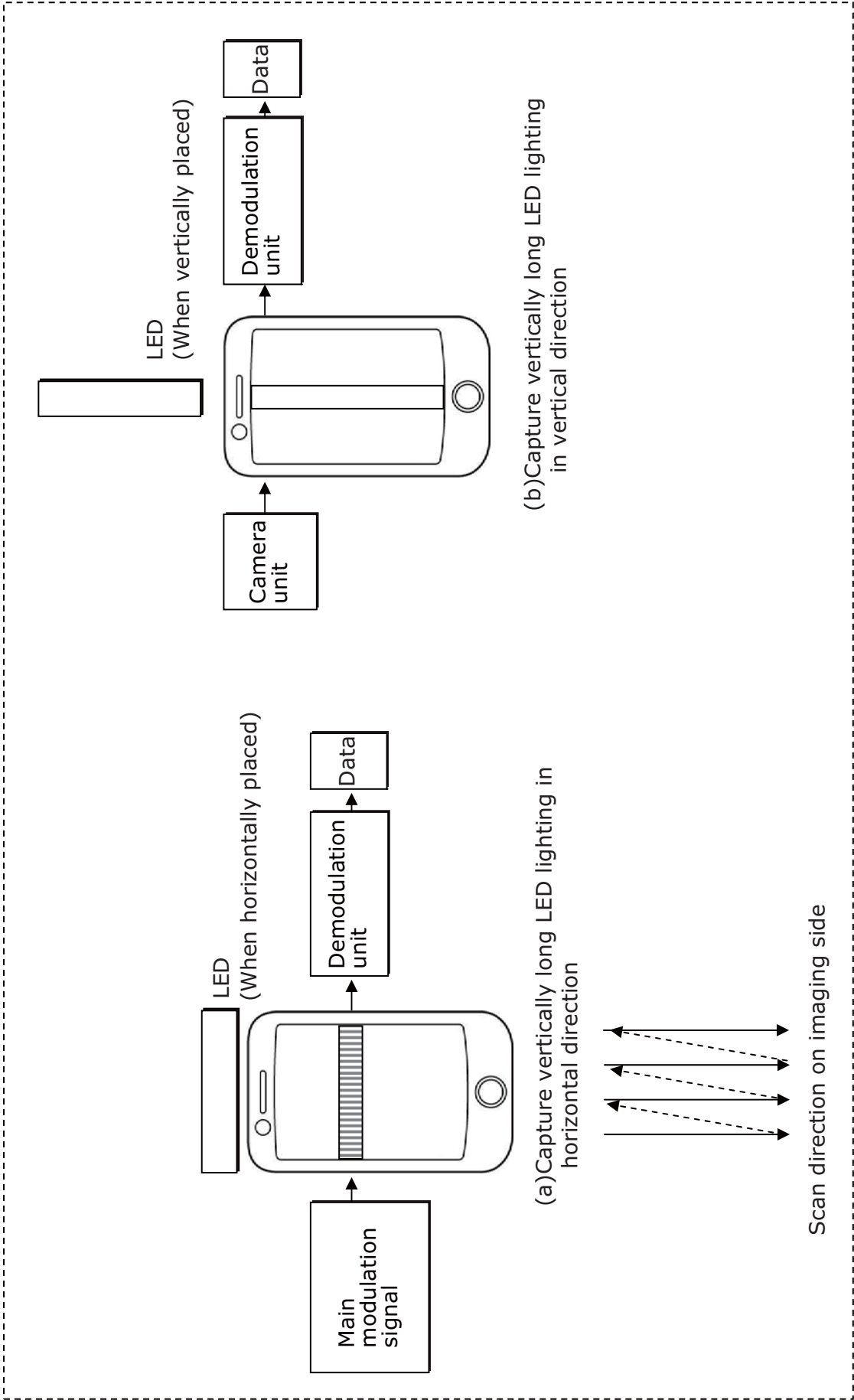


FIG. 111

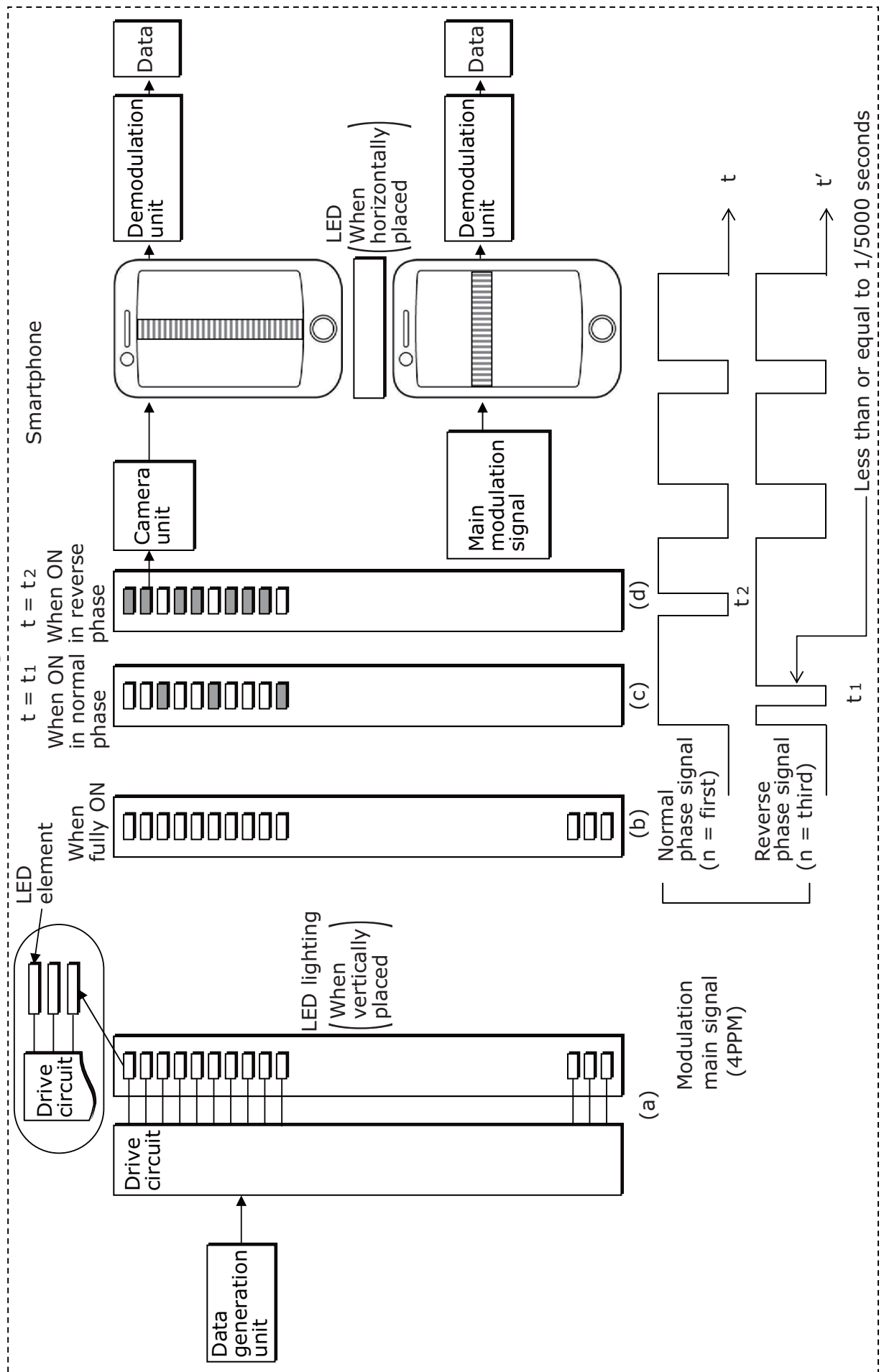


FIG. 112

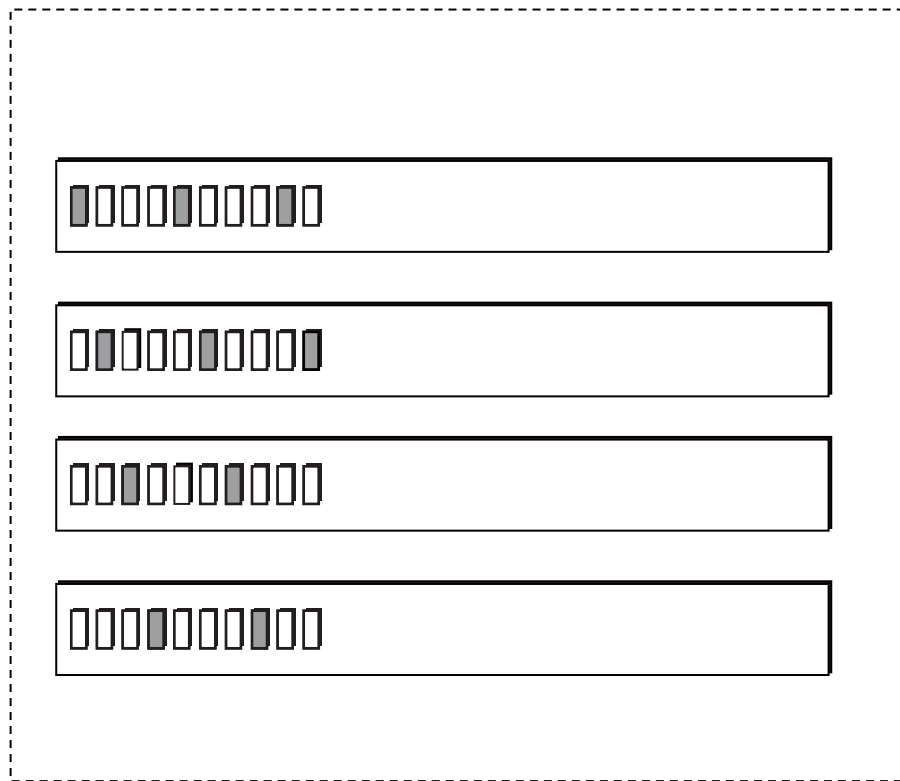


FIG. 113

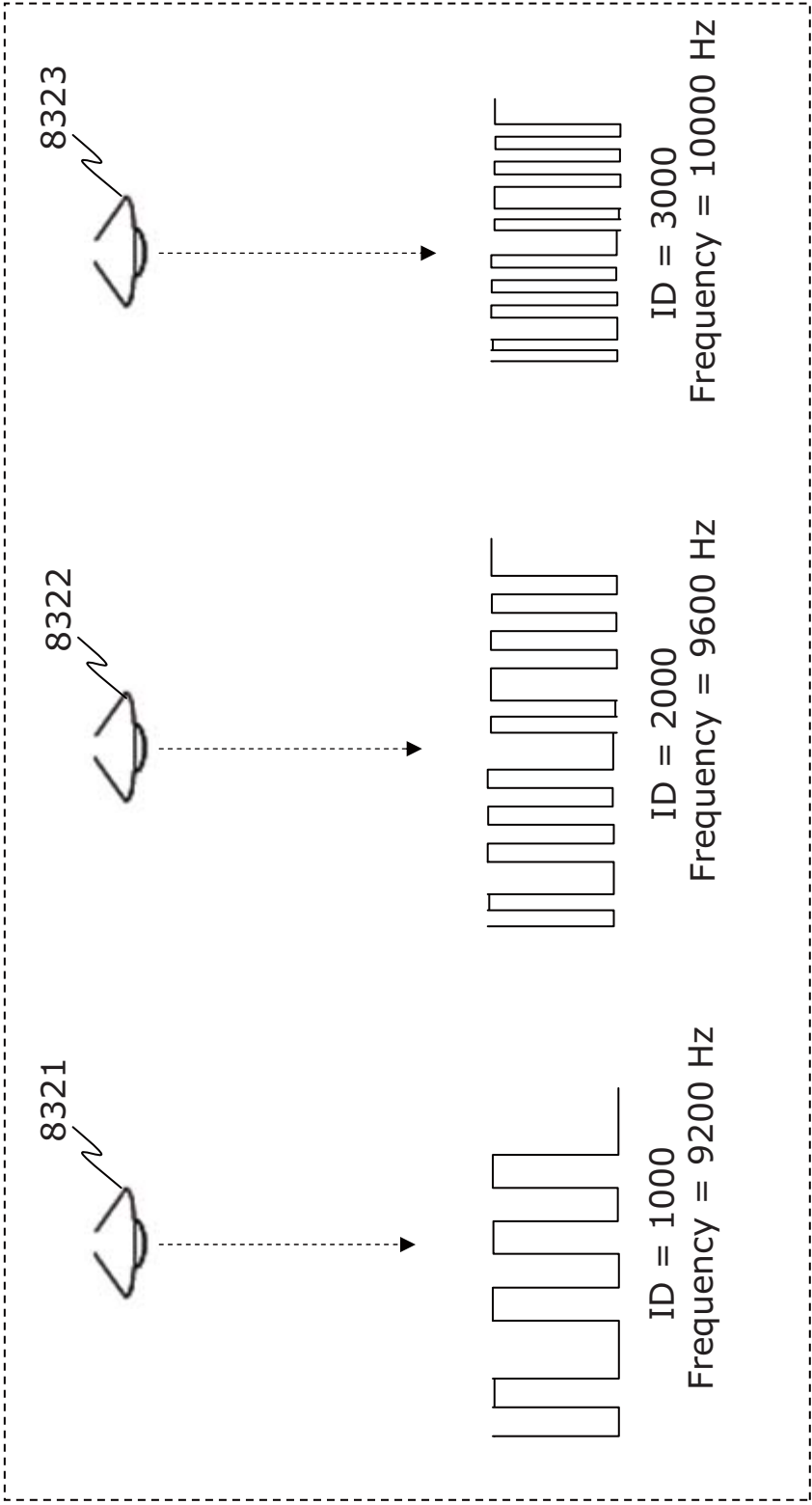


FIG. 114

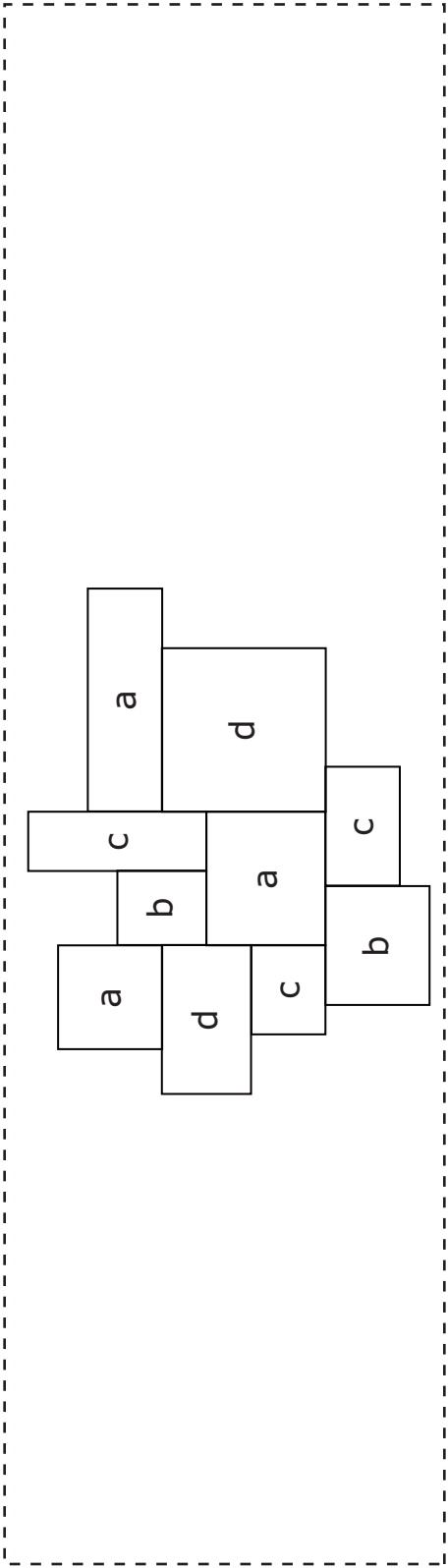


FIG. 115

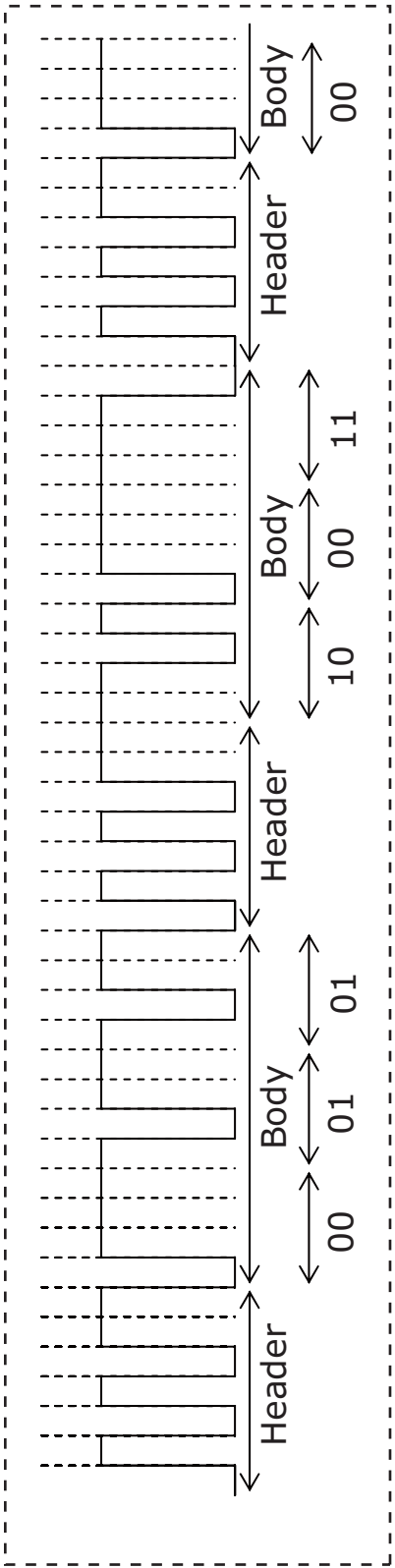


FIG. 116

ID	100	110
Frequency	2 kHz	1 kHz
Signal	1002̄	1101̄

FIG. 117

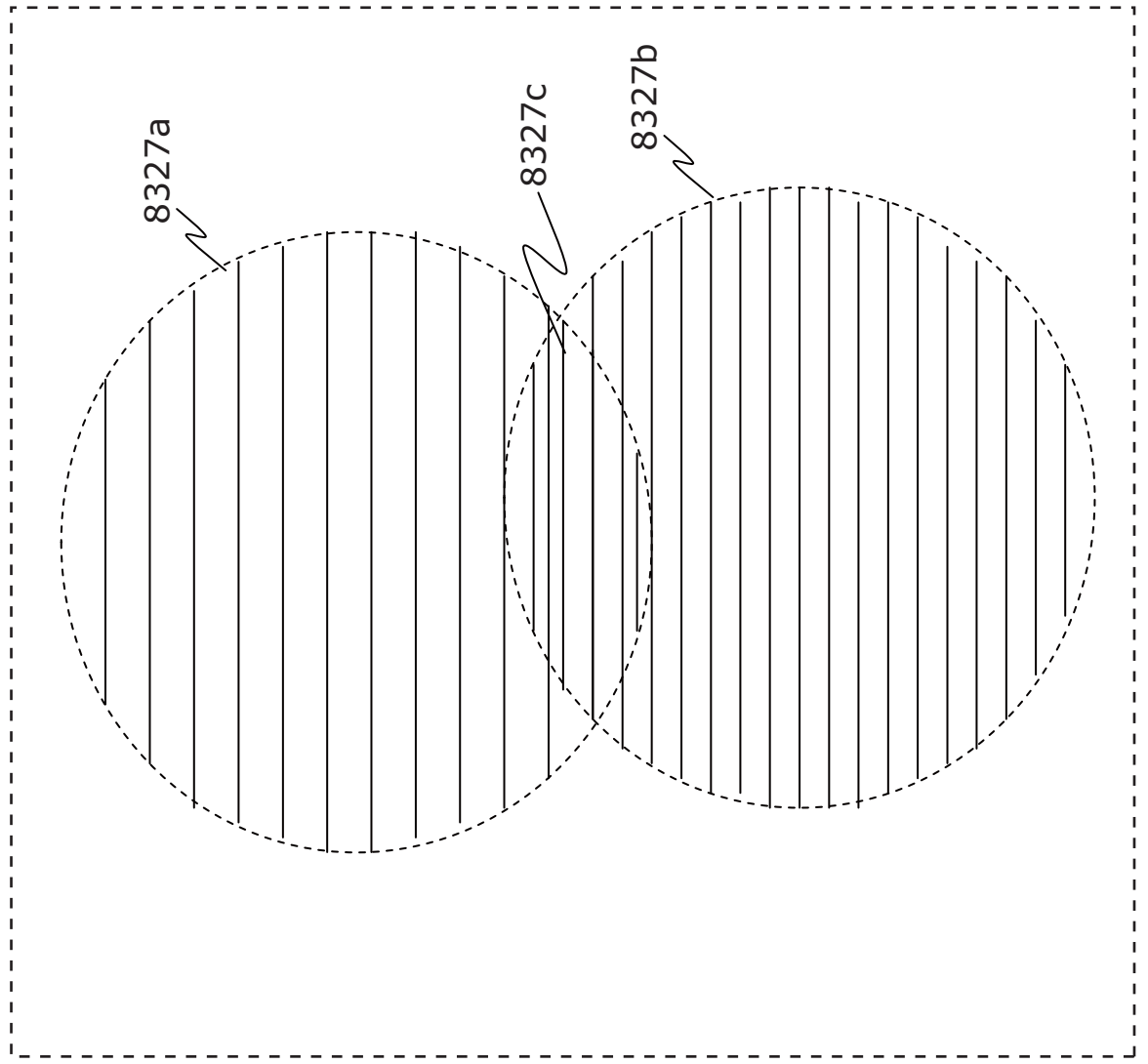


FIG. 118

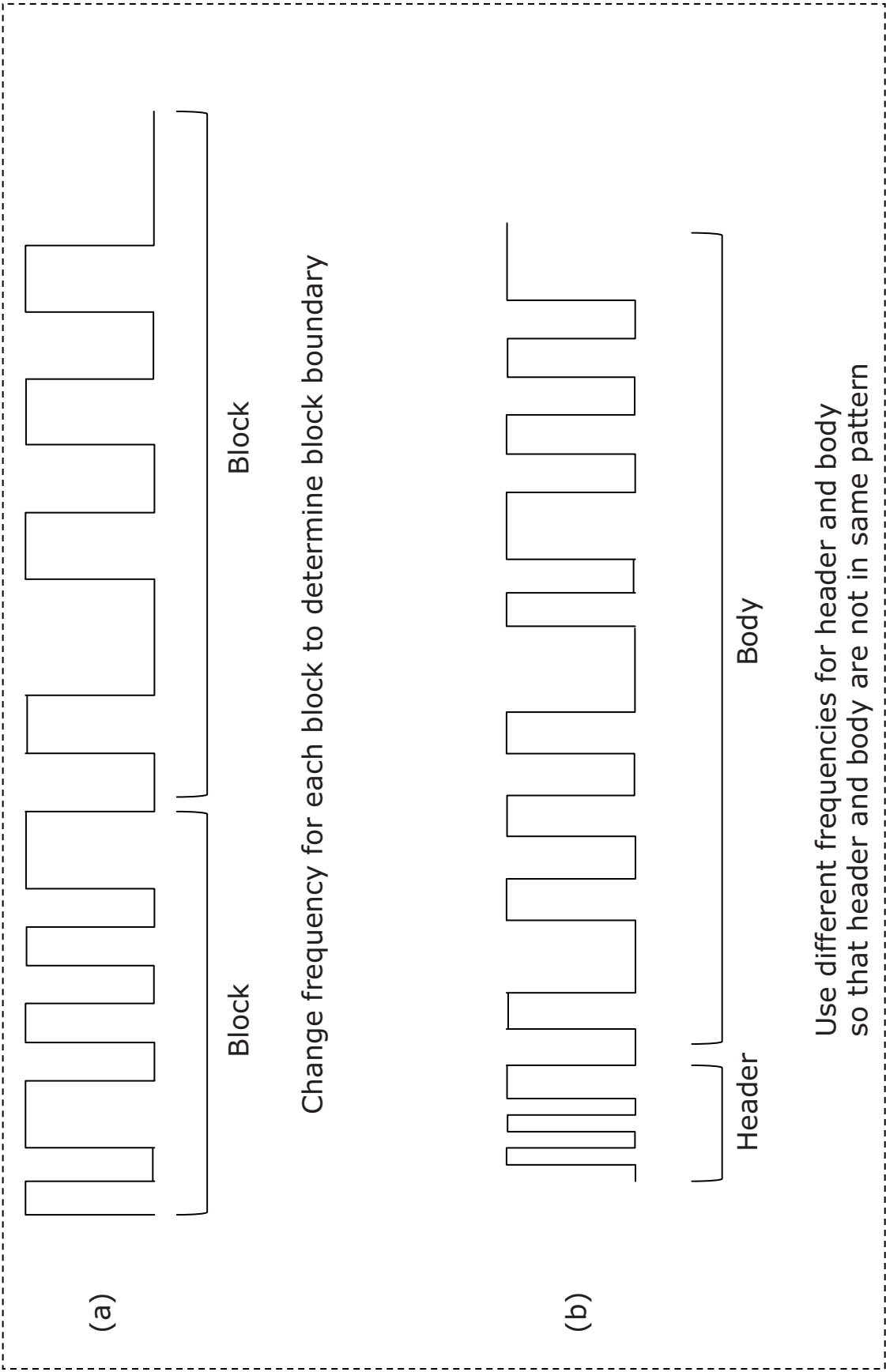


FIG. 119

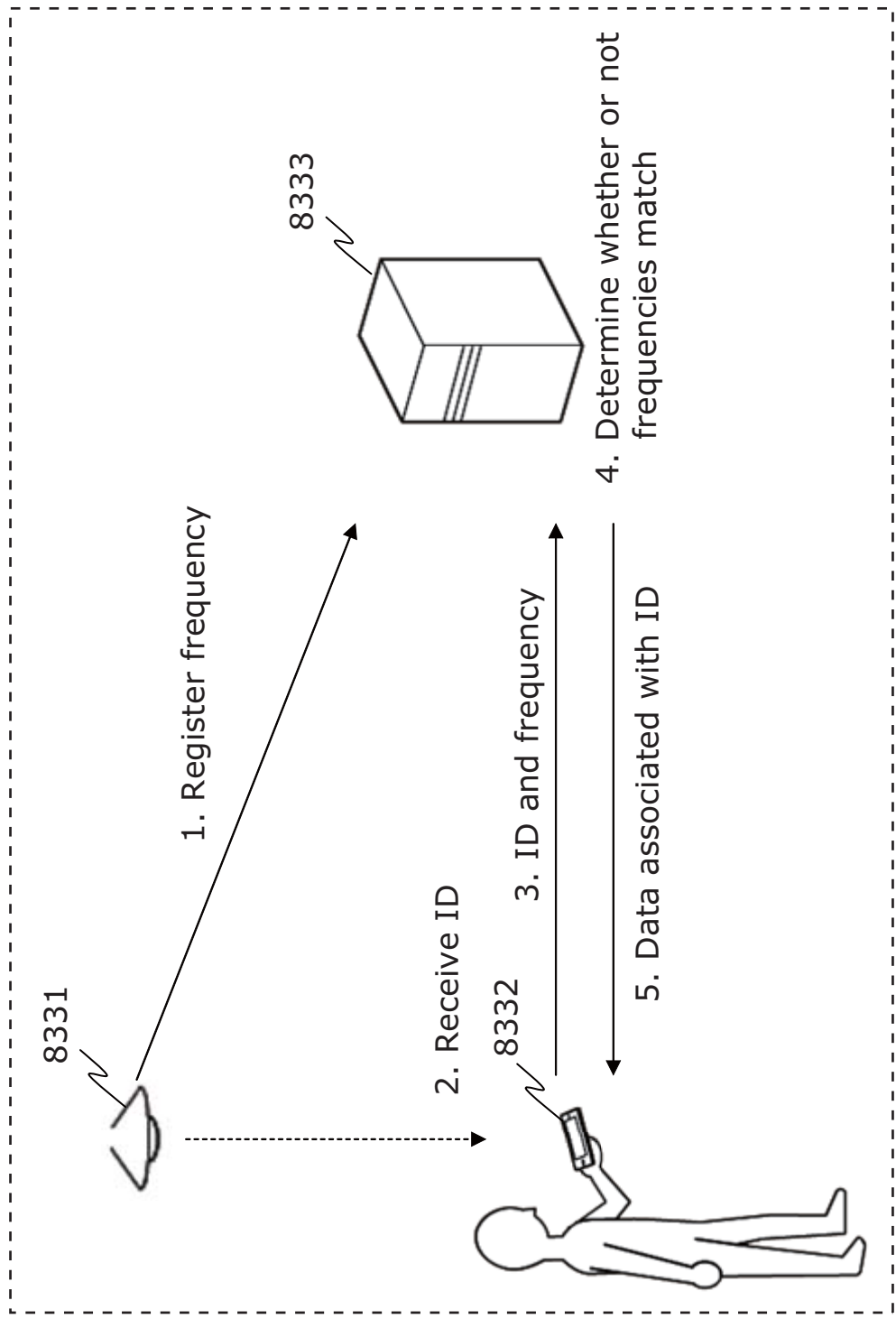


FIG. 120

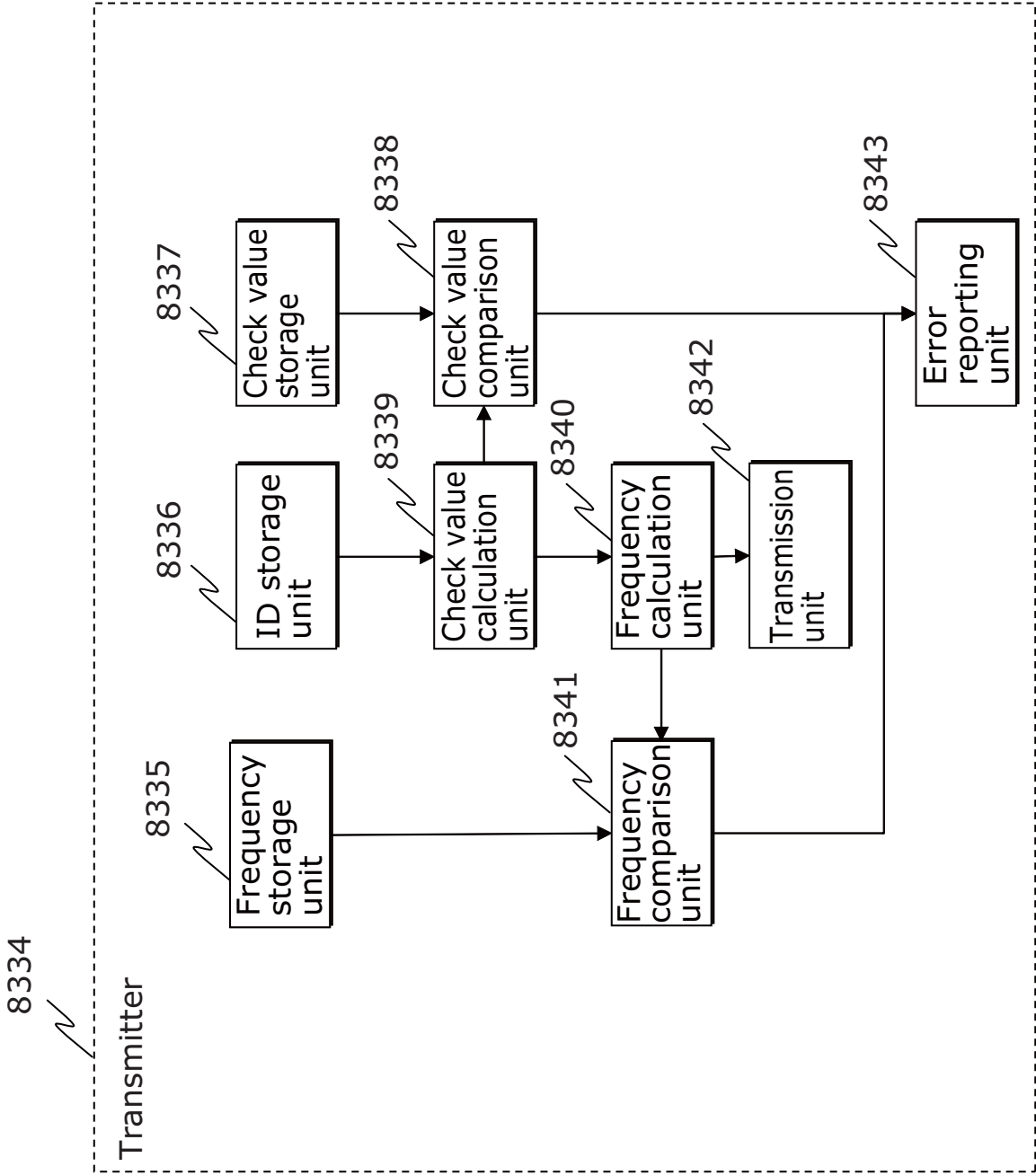


FIG. 121

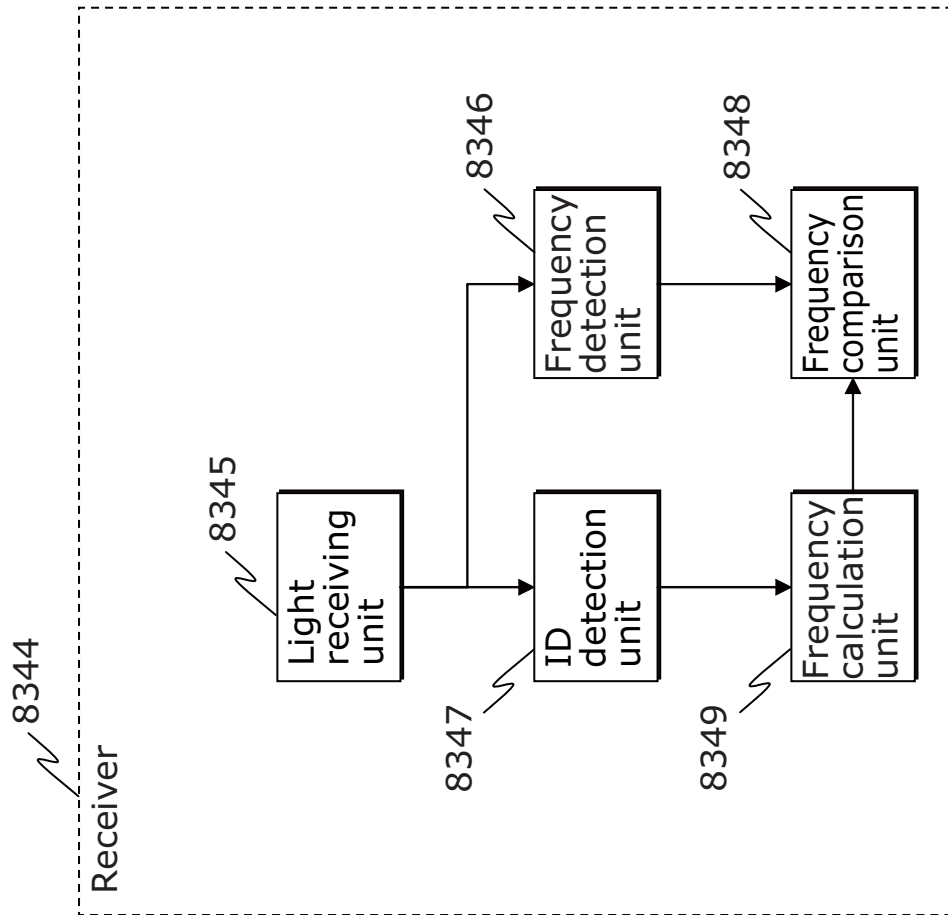


FIG. 122

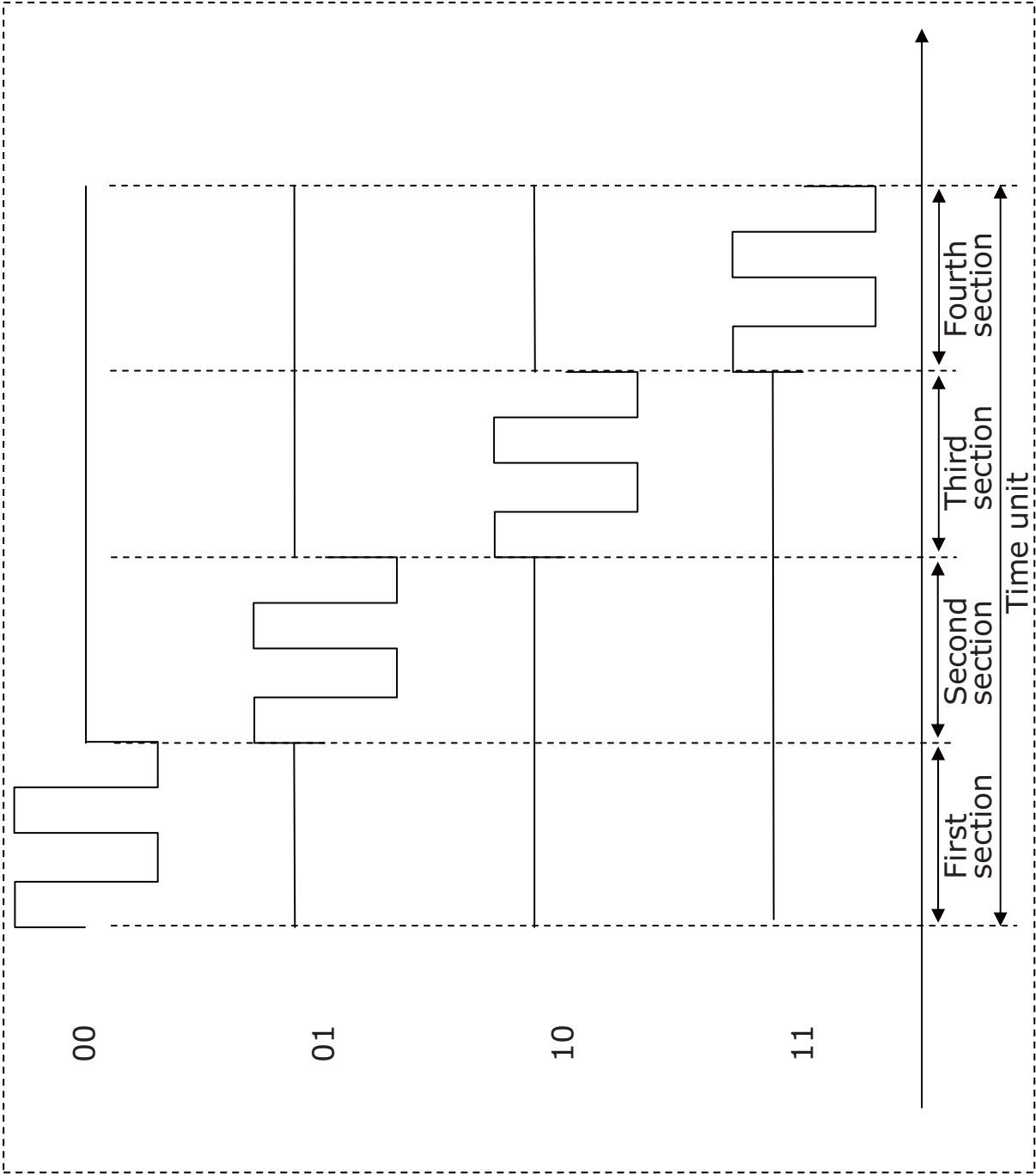


FIG. 123

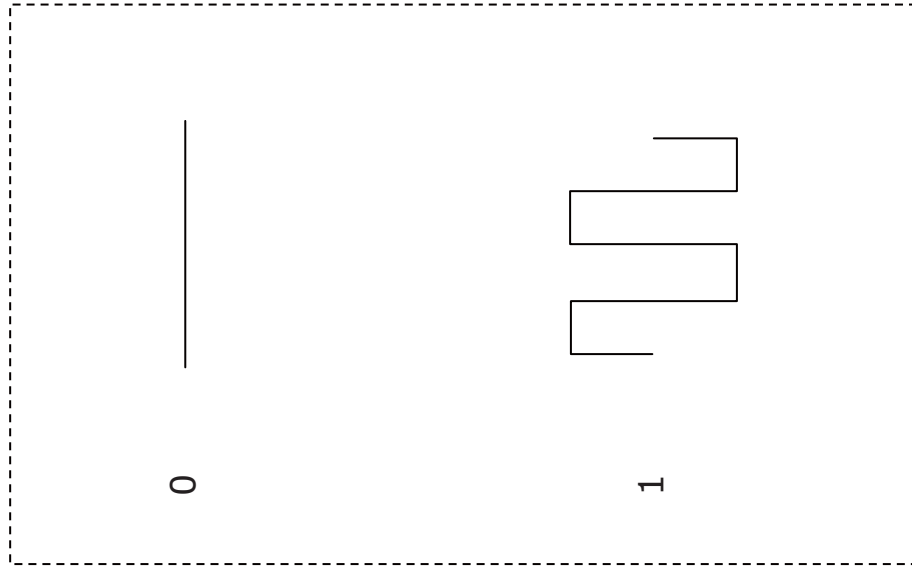


FIG. 124

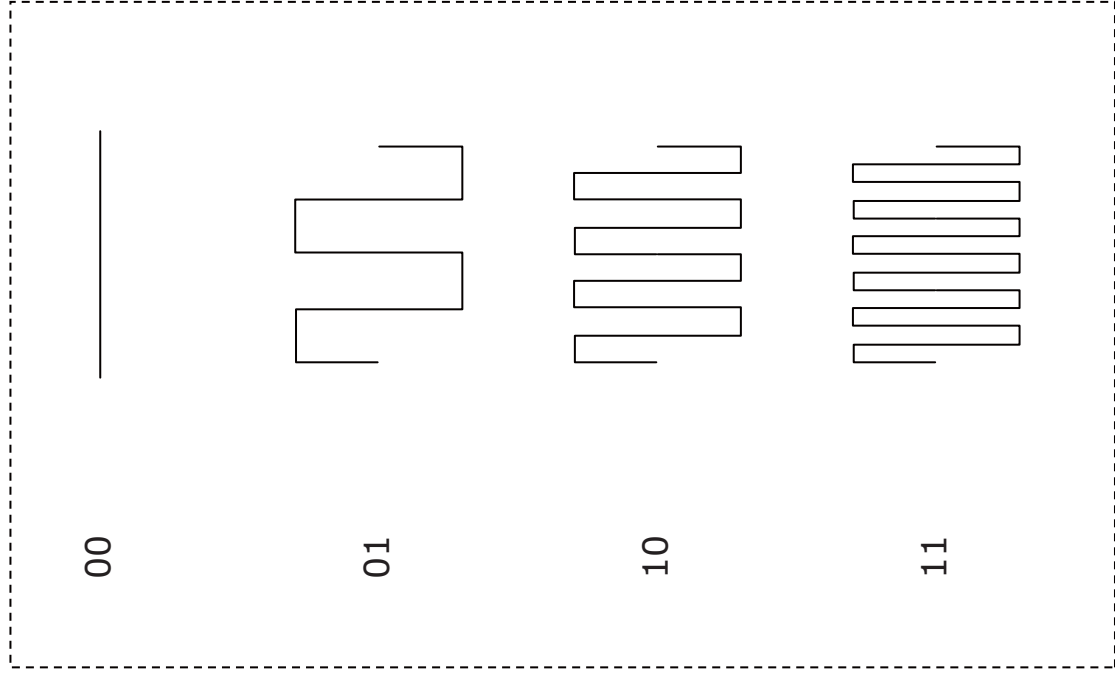


FIG. 125

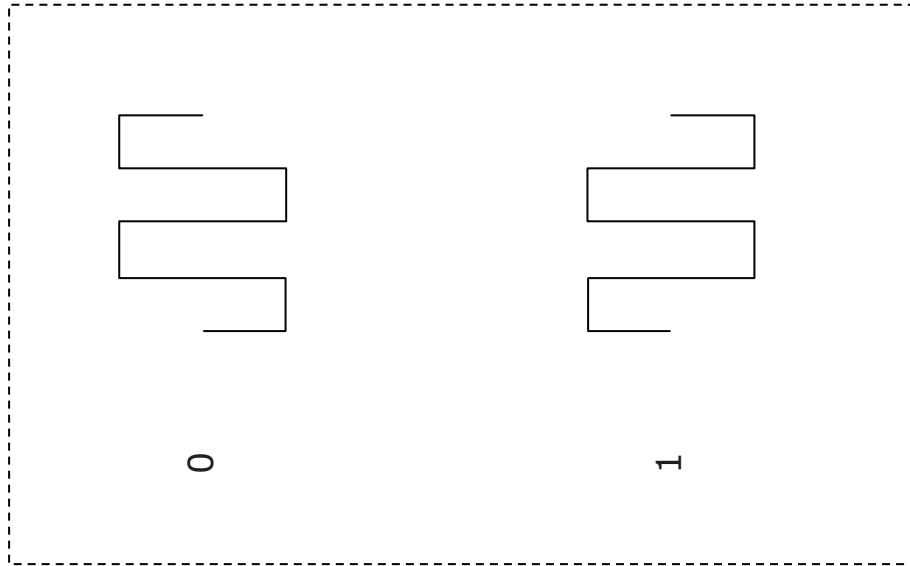


FIG. 126

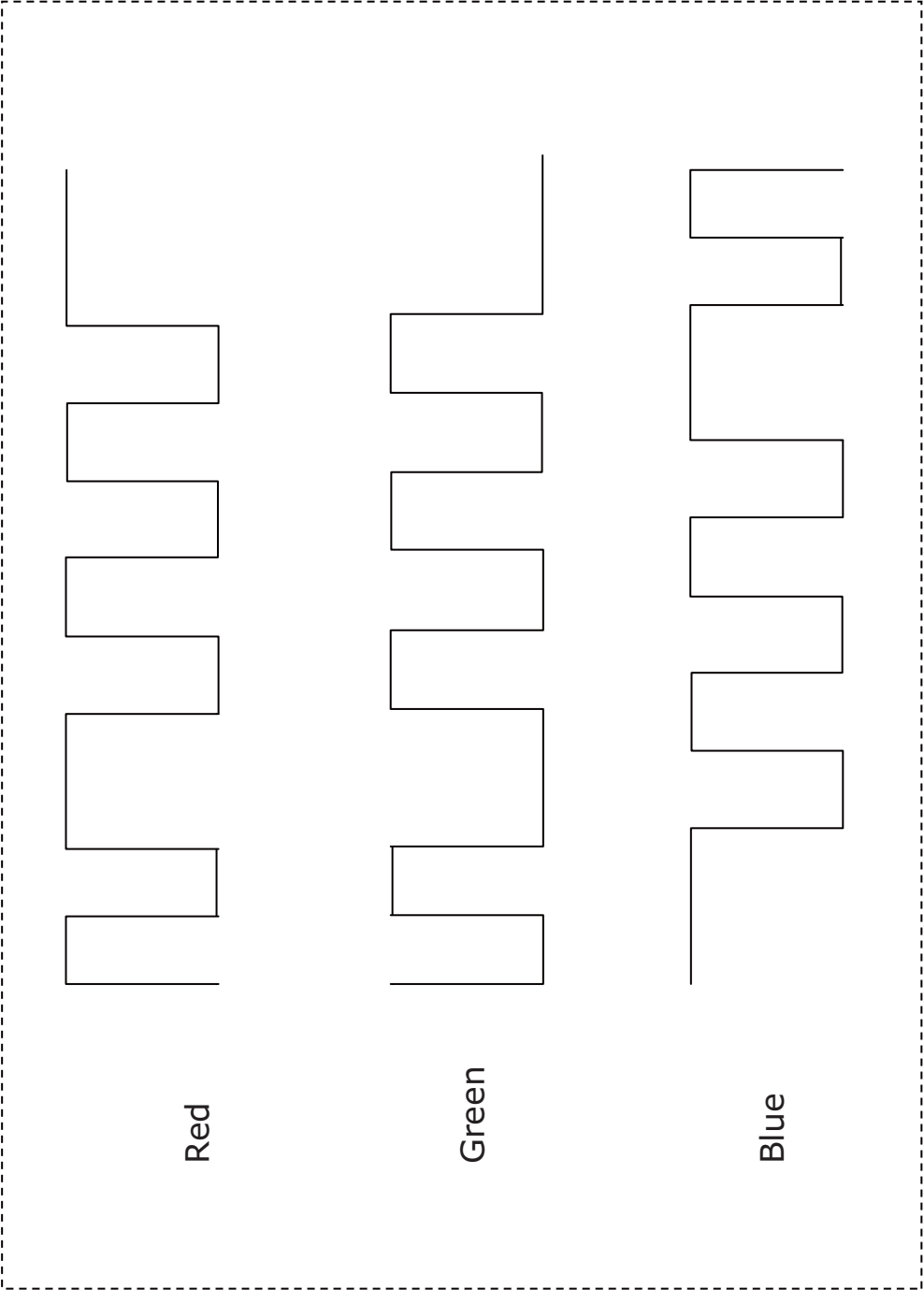


FIG. 127

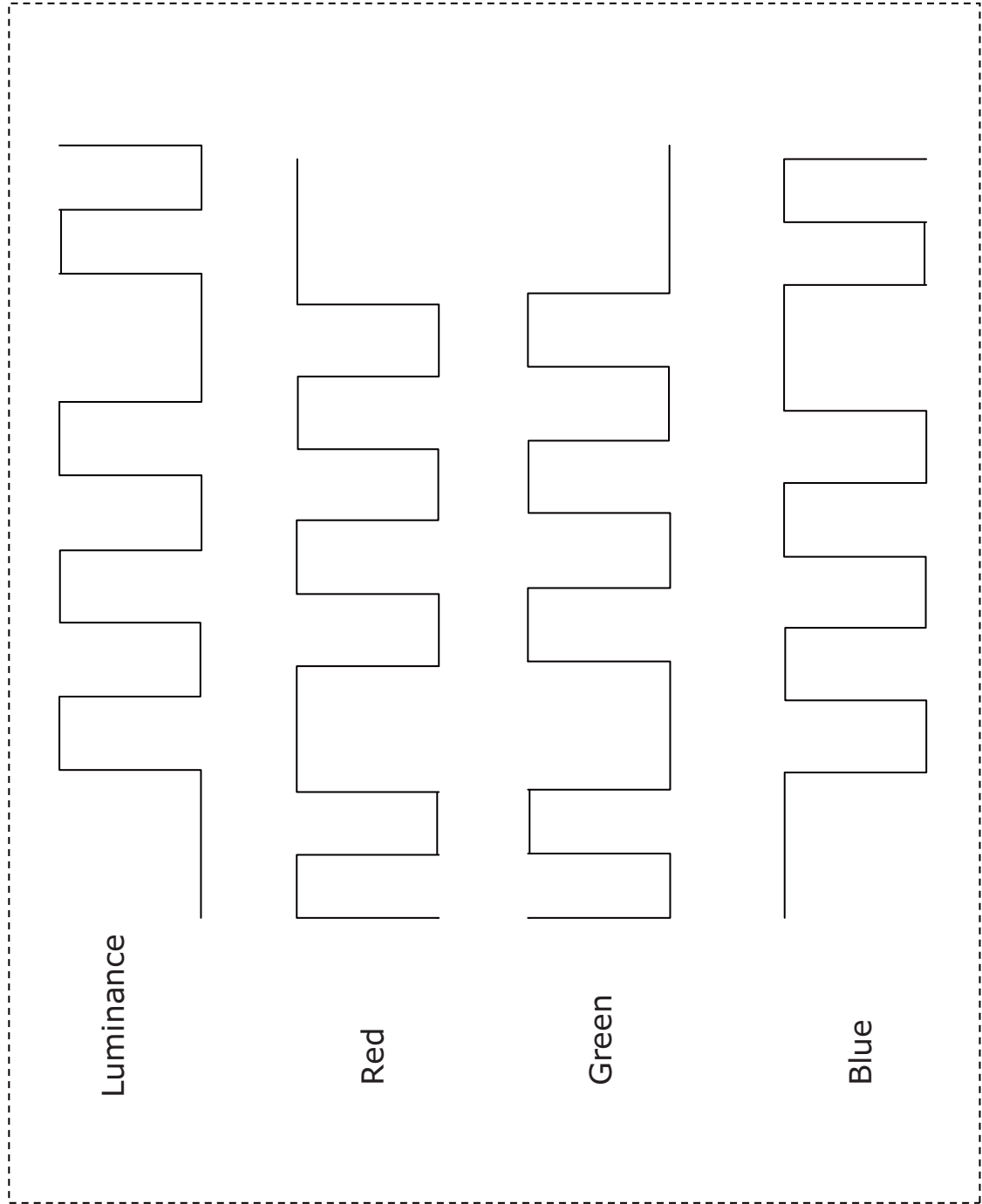


FIG. 128

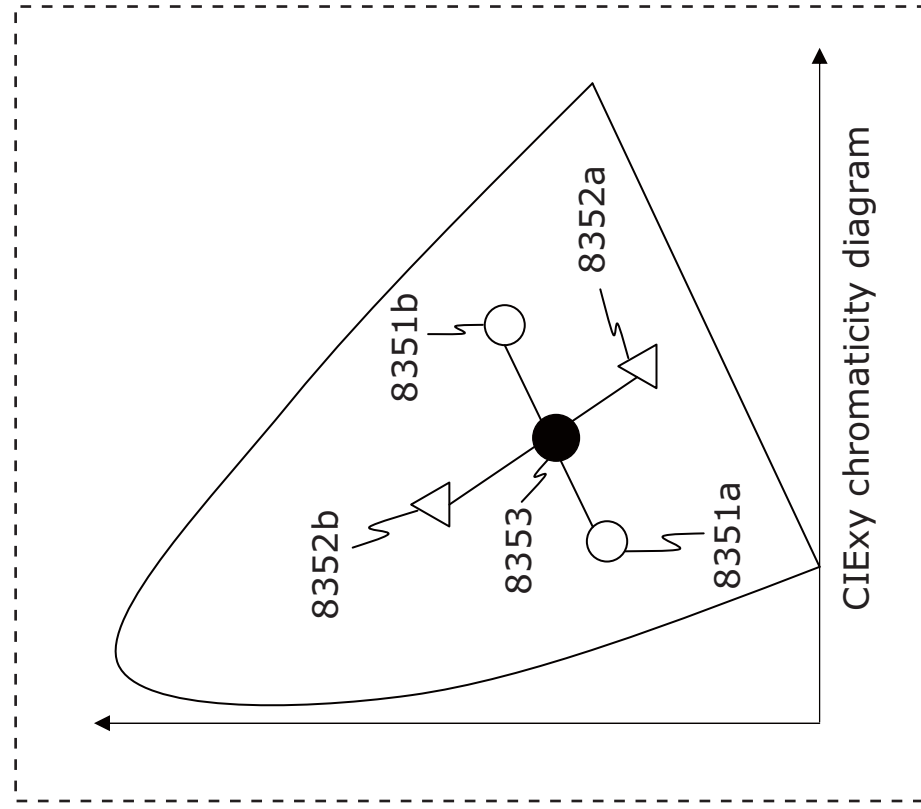


FIG. 129

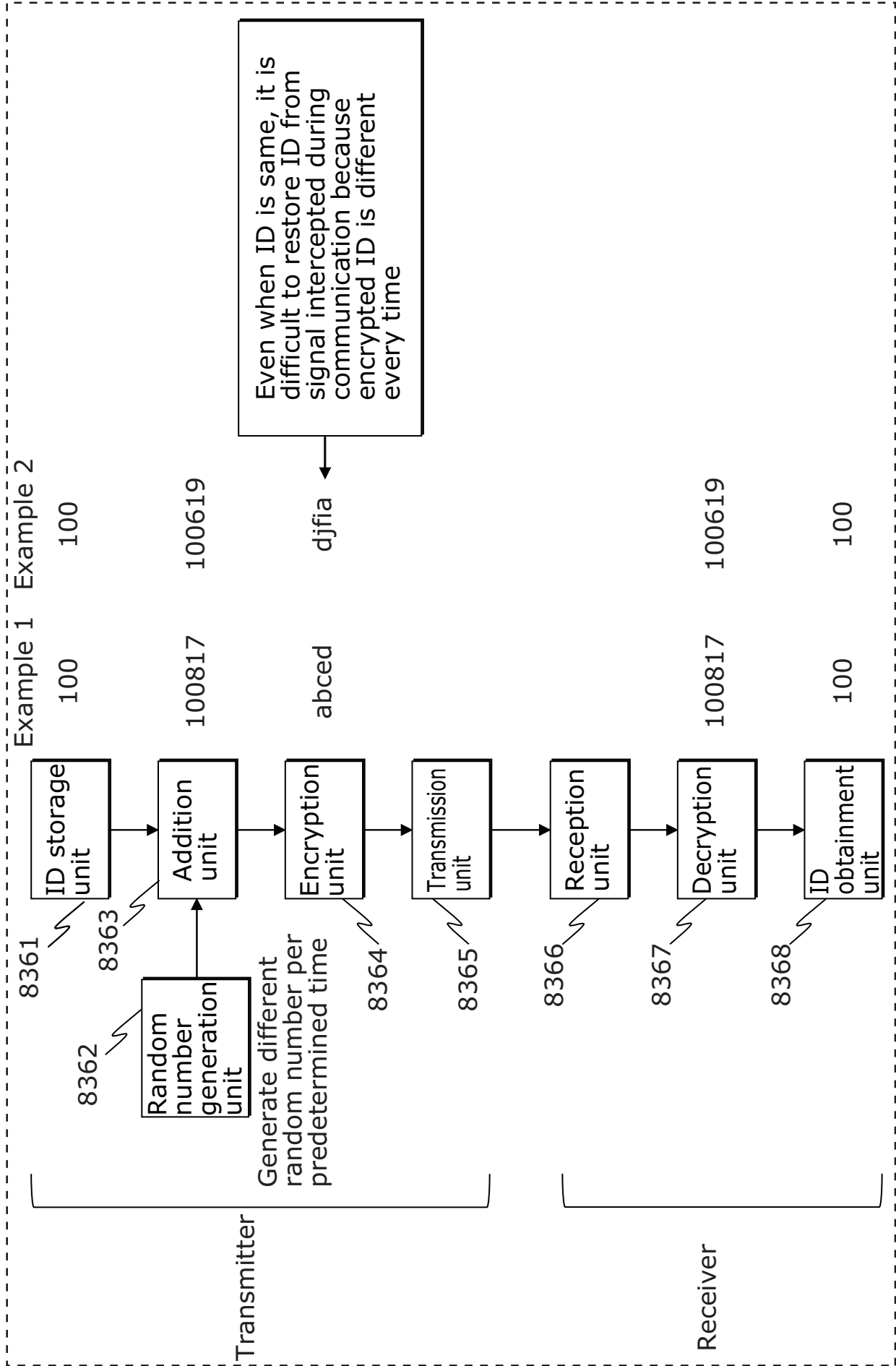


FIG. 130

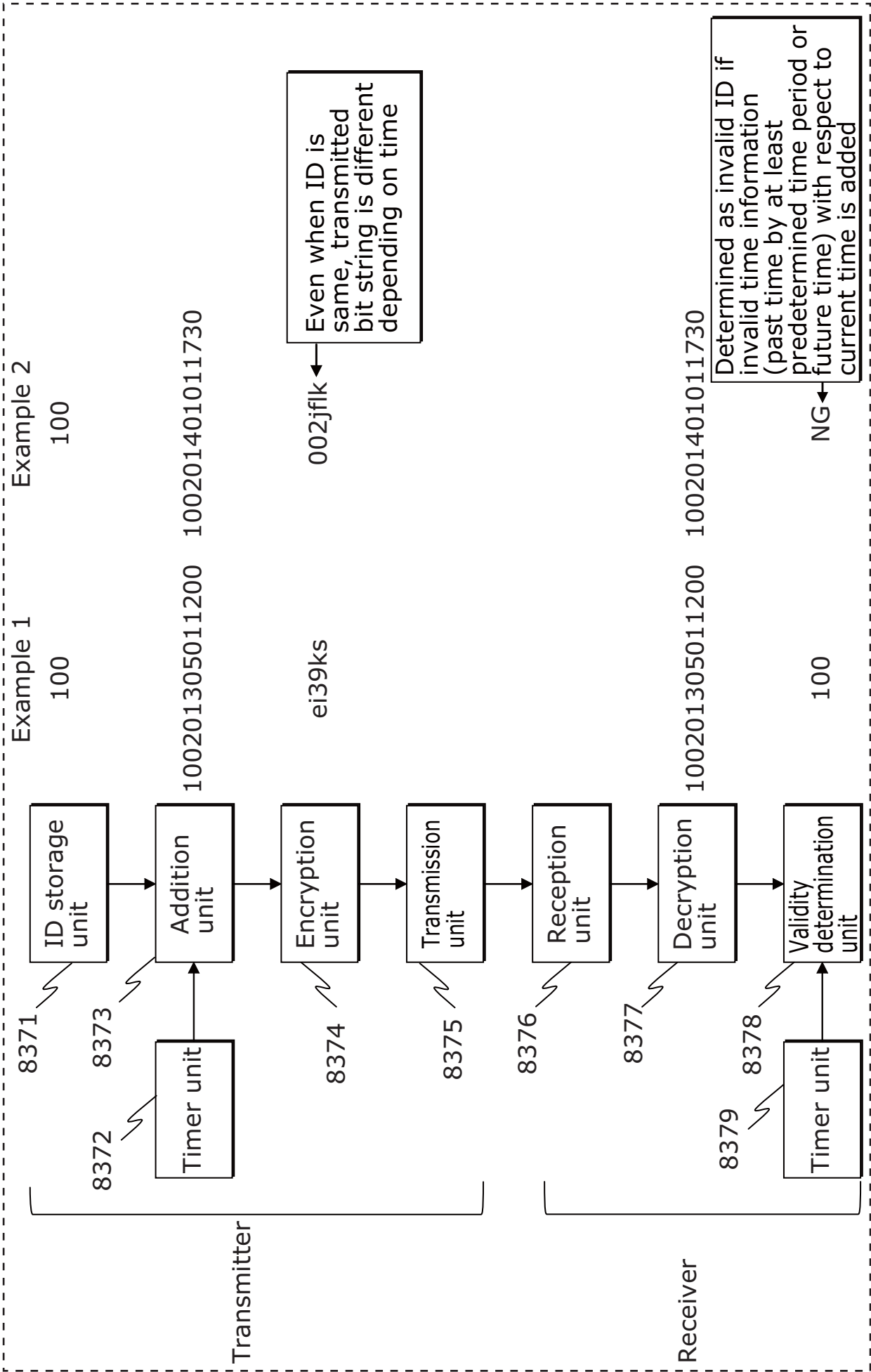


FIG. 131

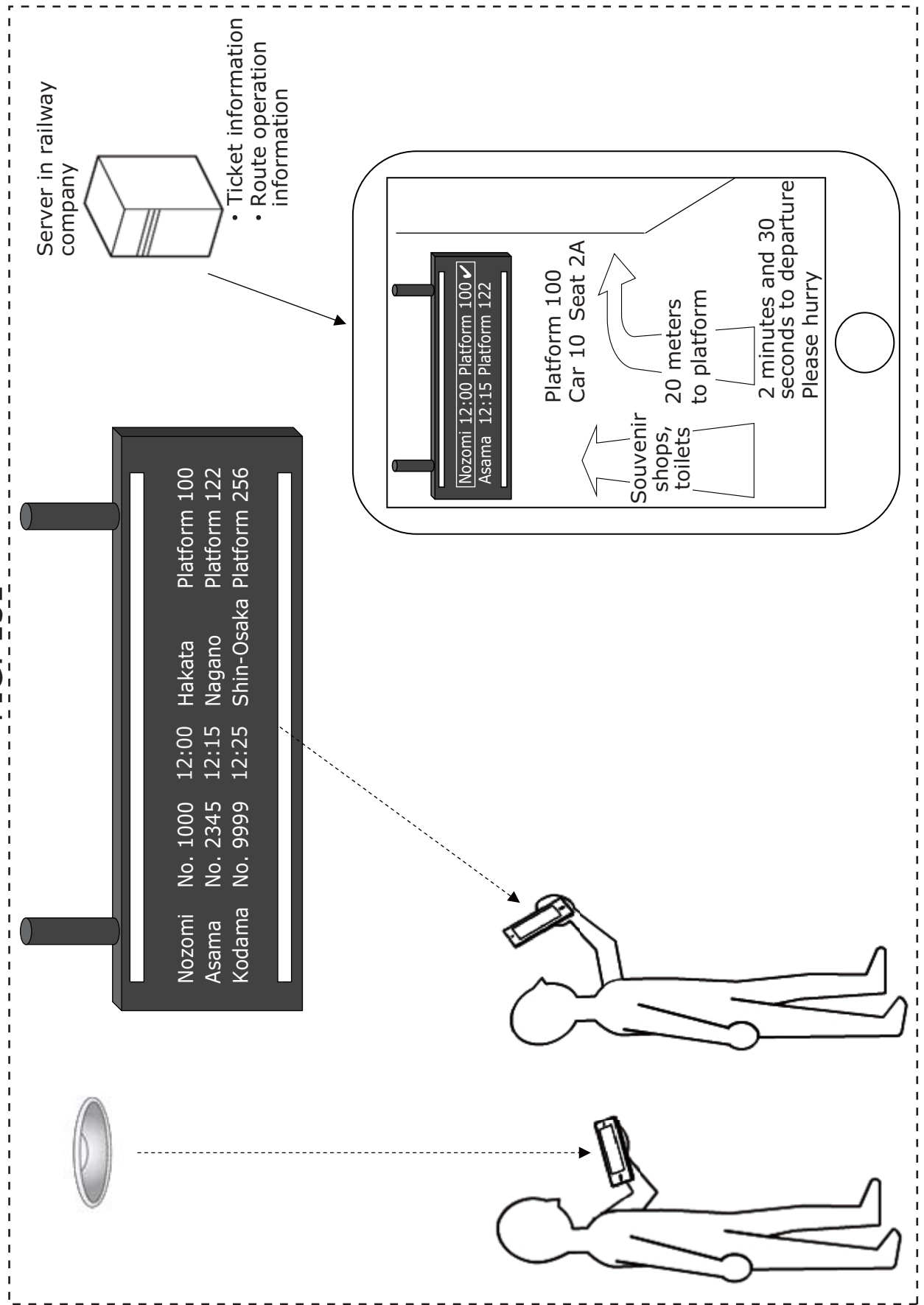


FIG. 132

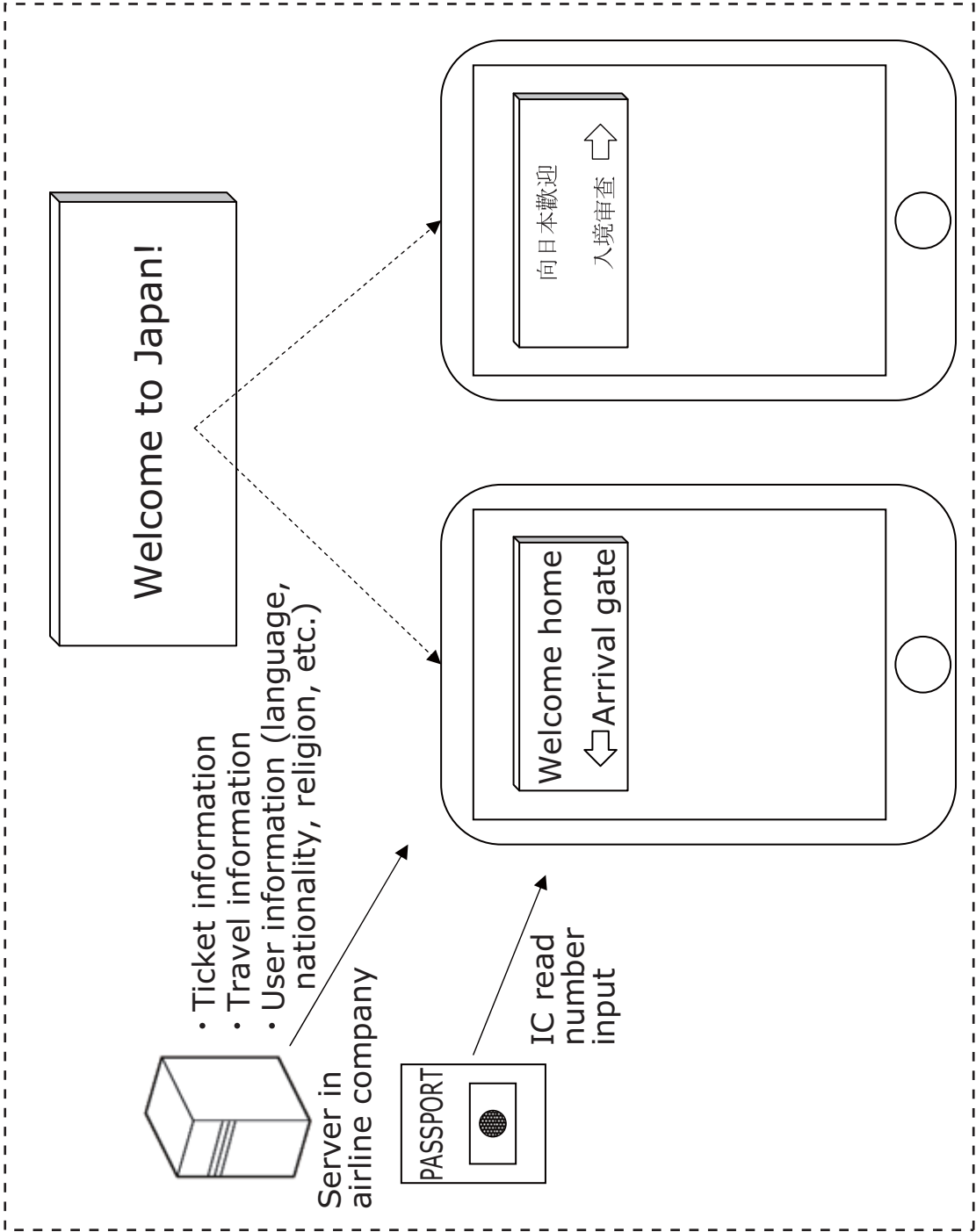


FIG. 133

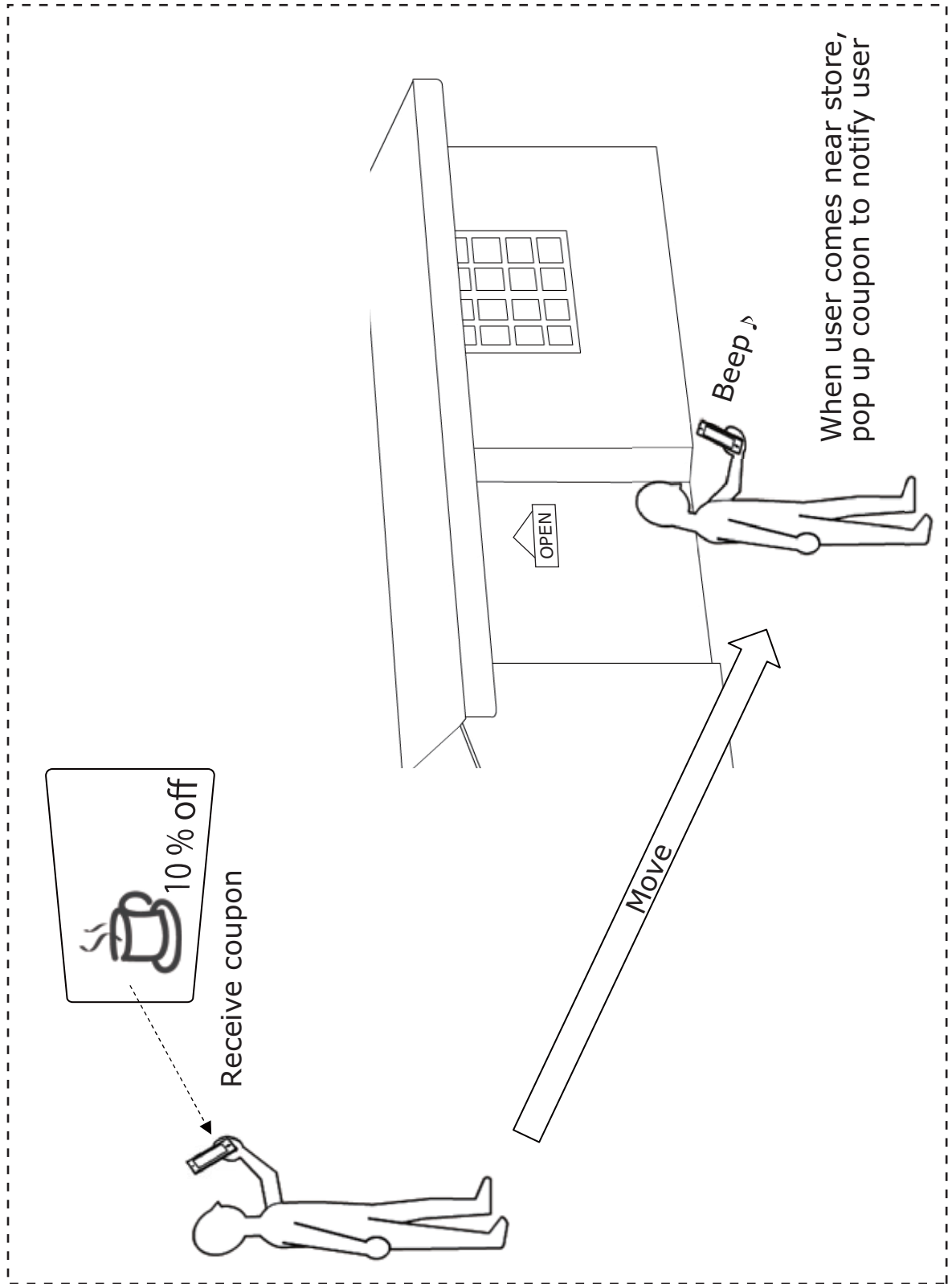


FIG. 134

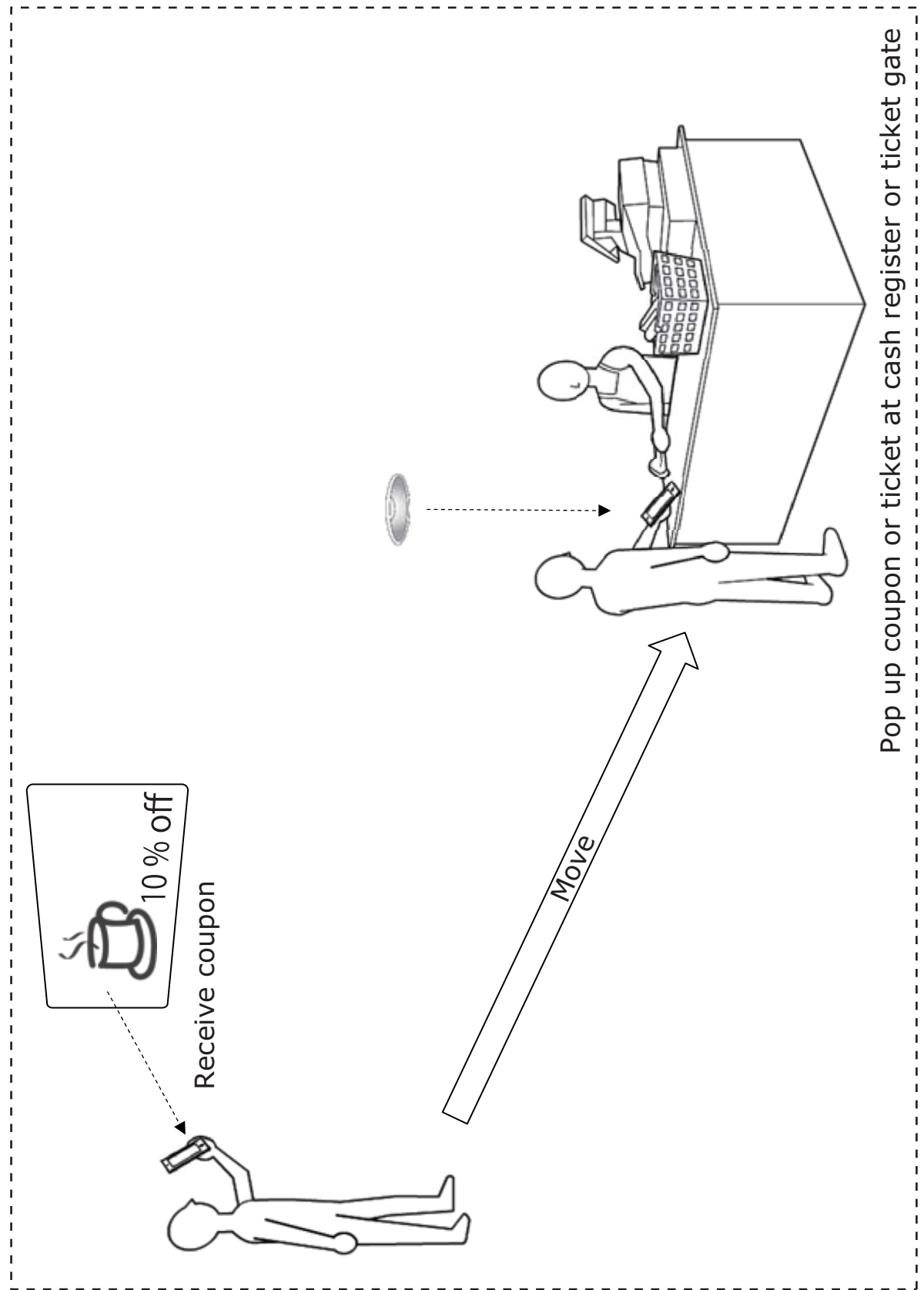


FIG. 135

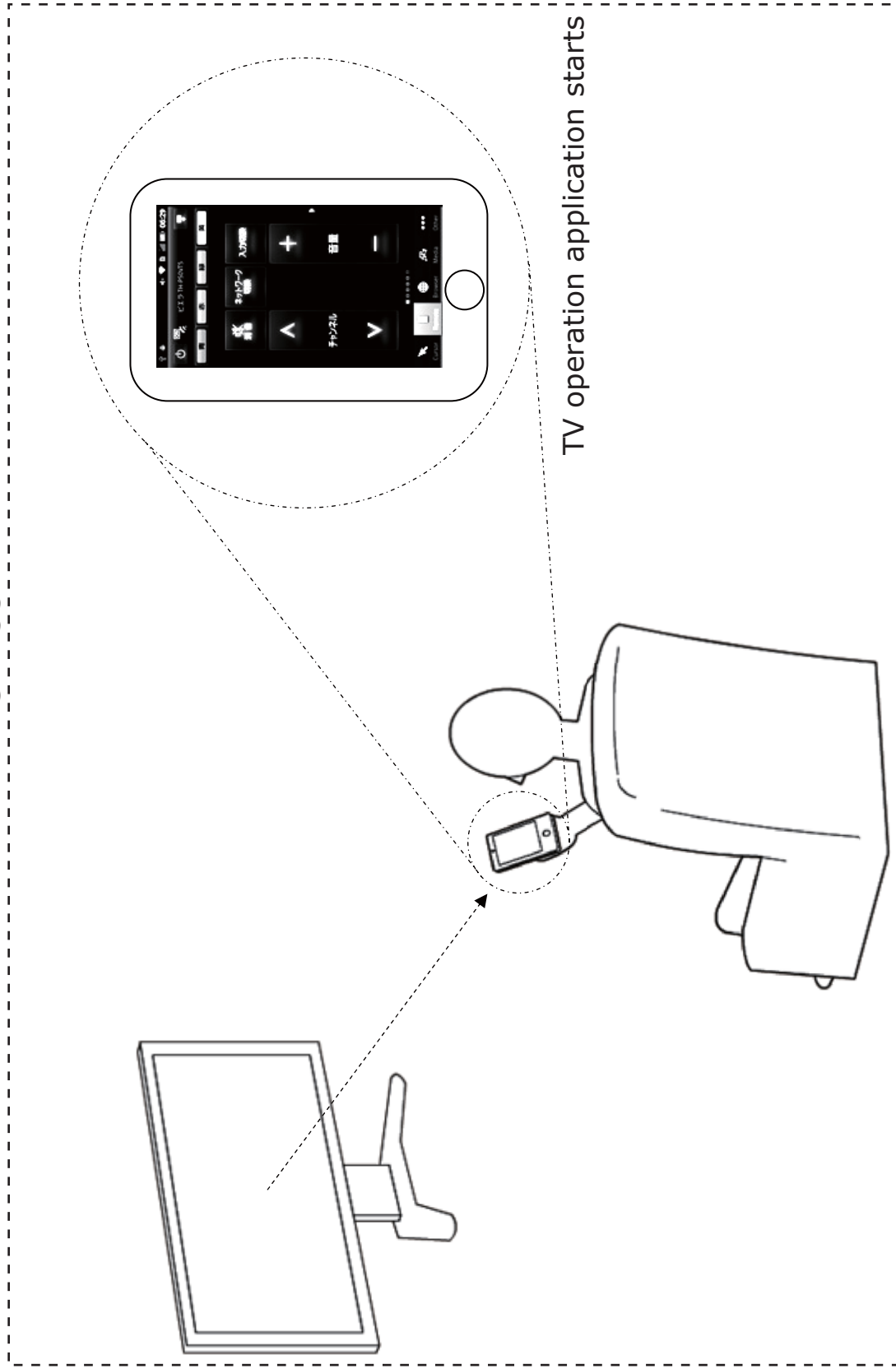


FIG. 136

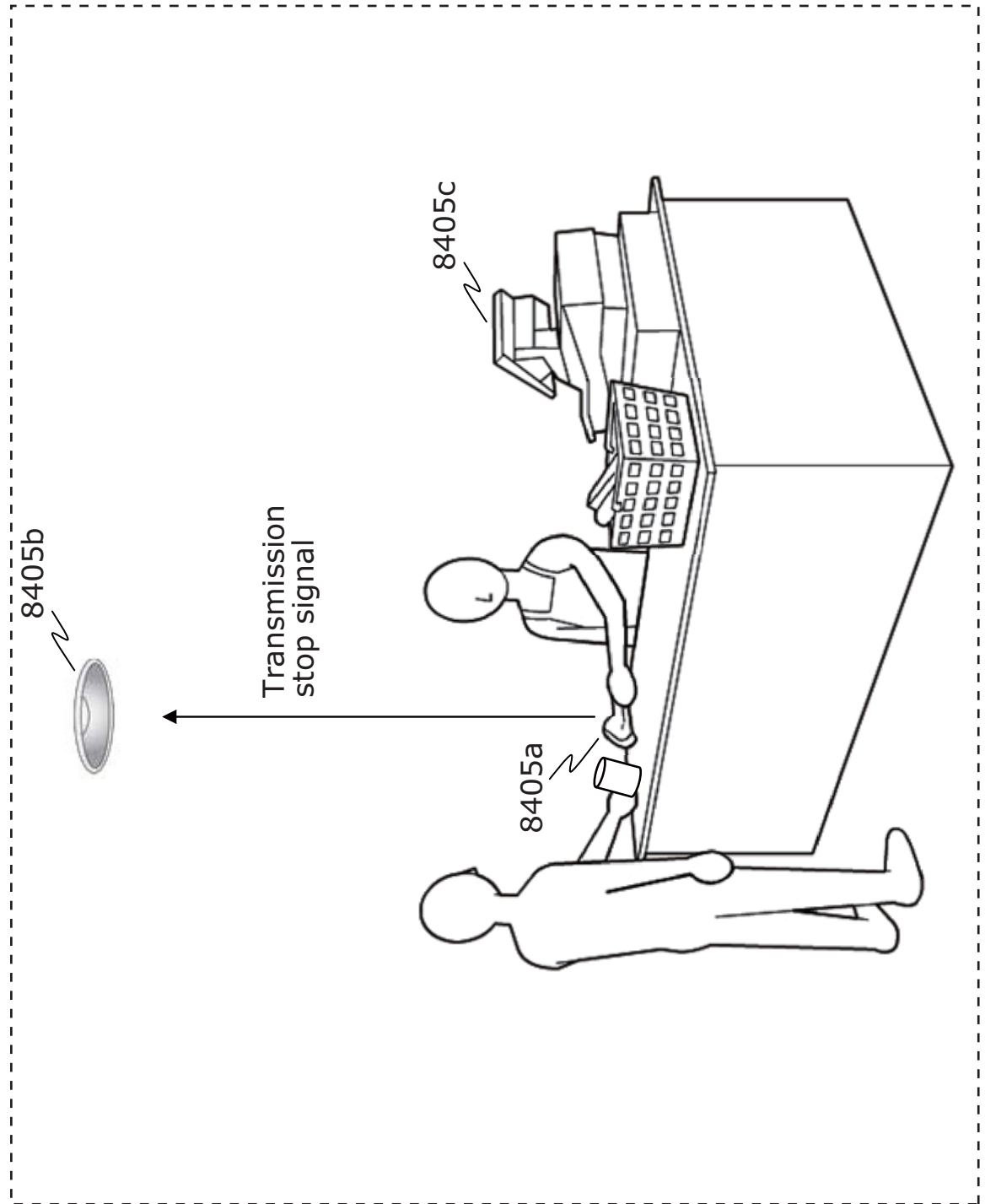


FIG. 137

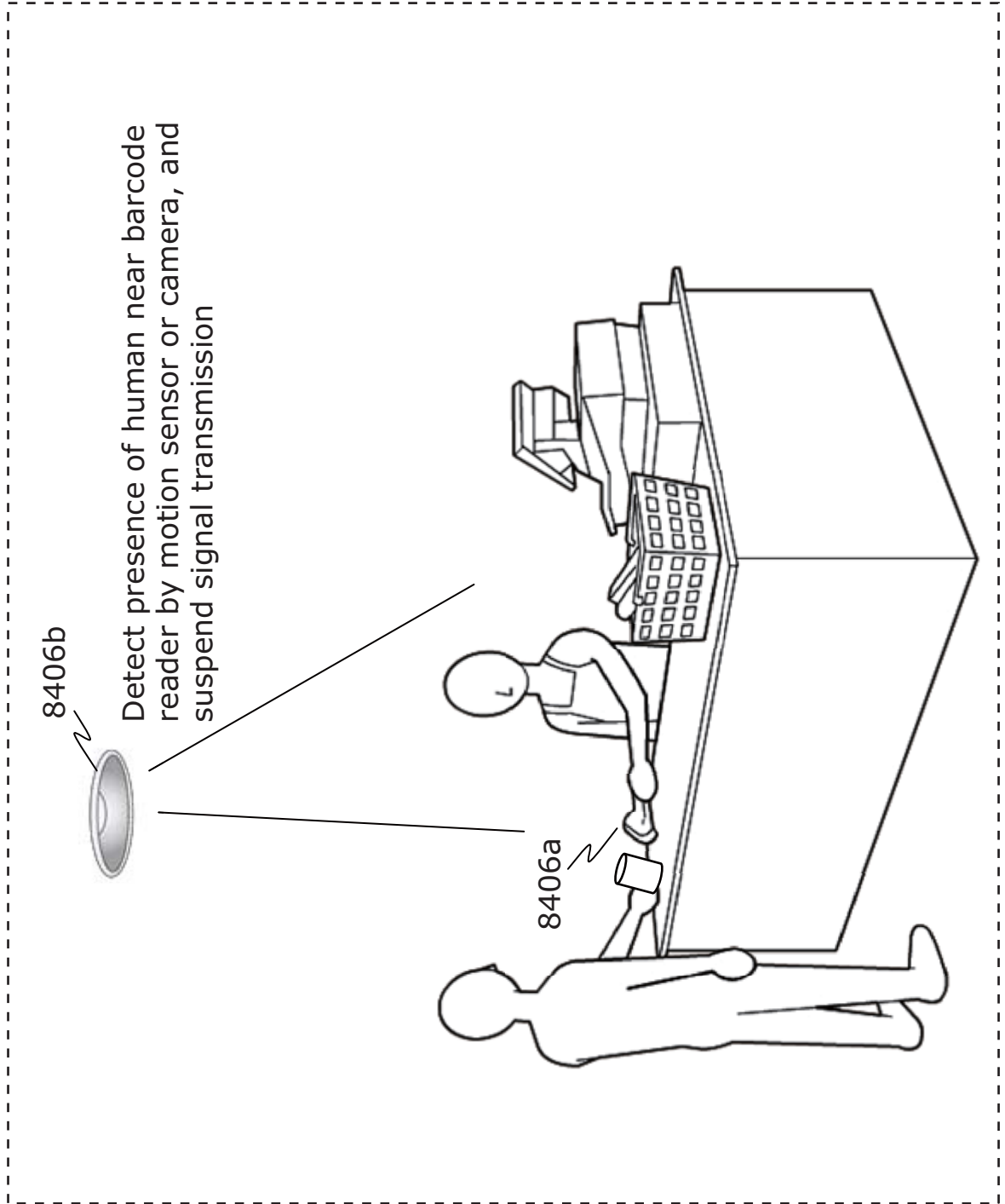


FIG. 138

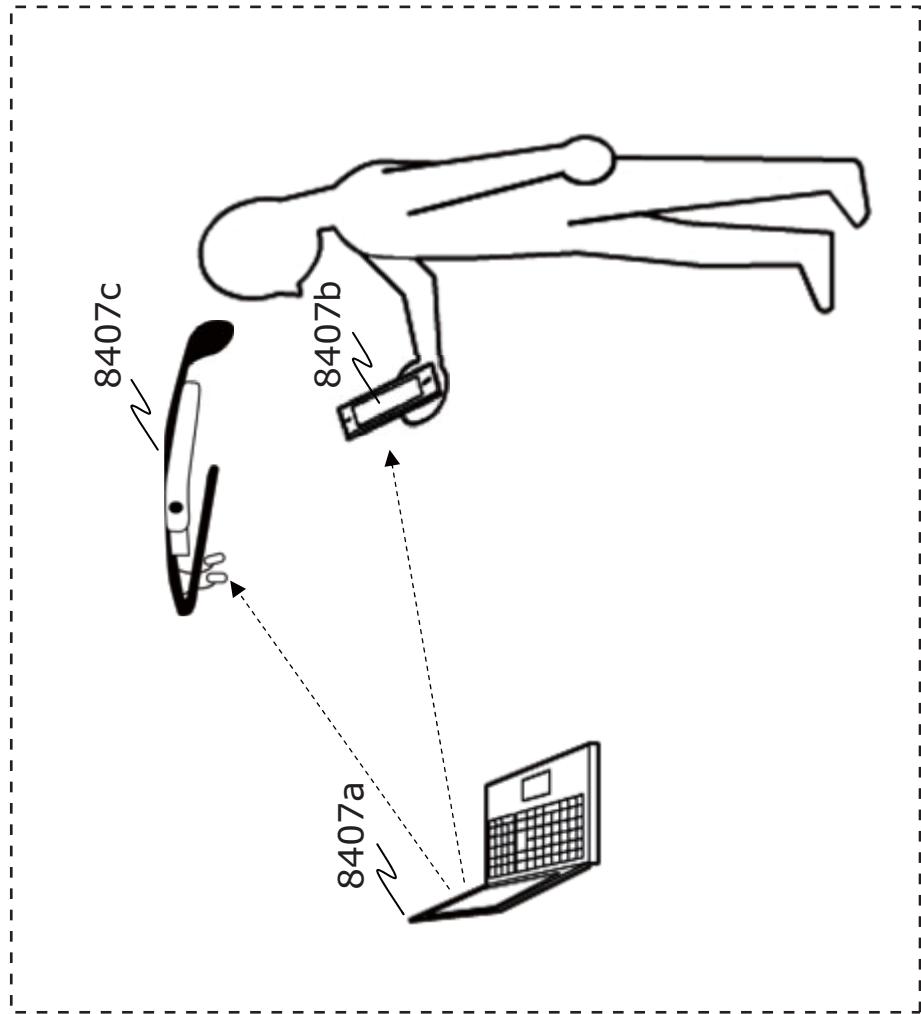


FIG. 139

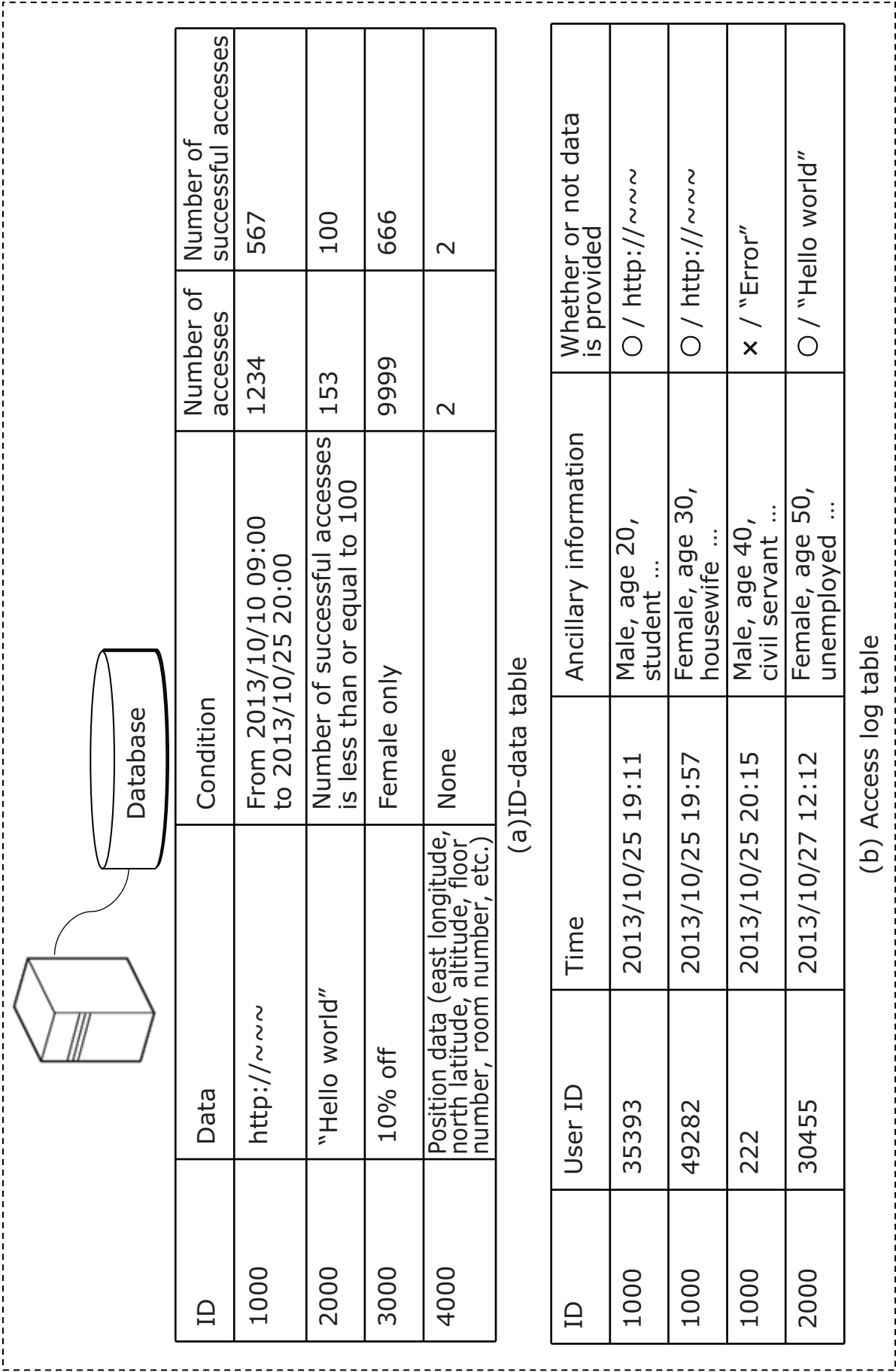


FIG. 140

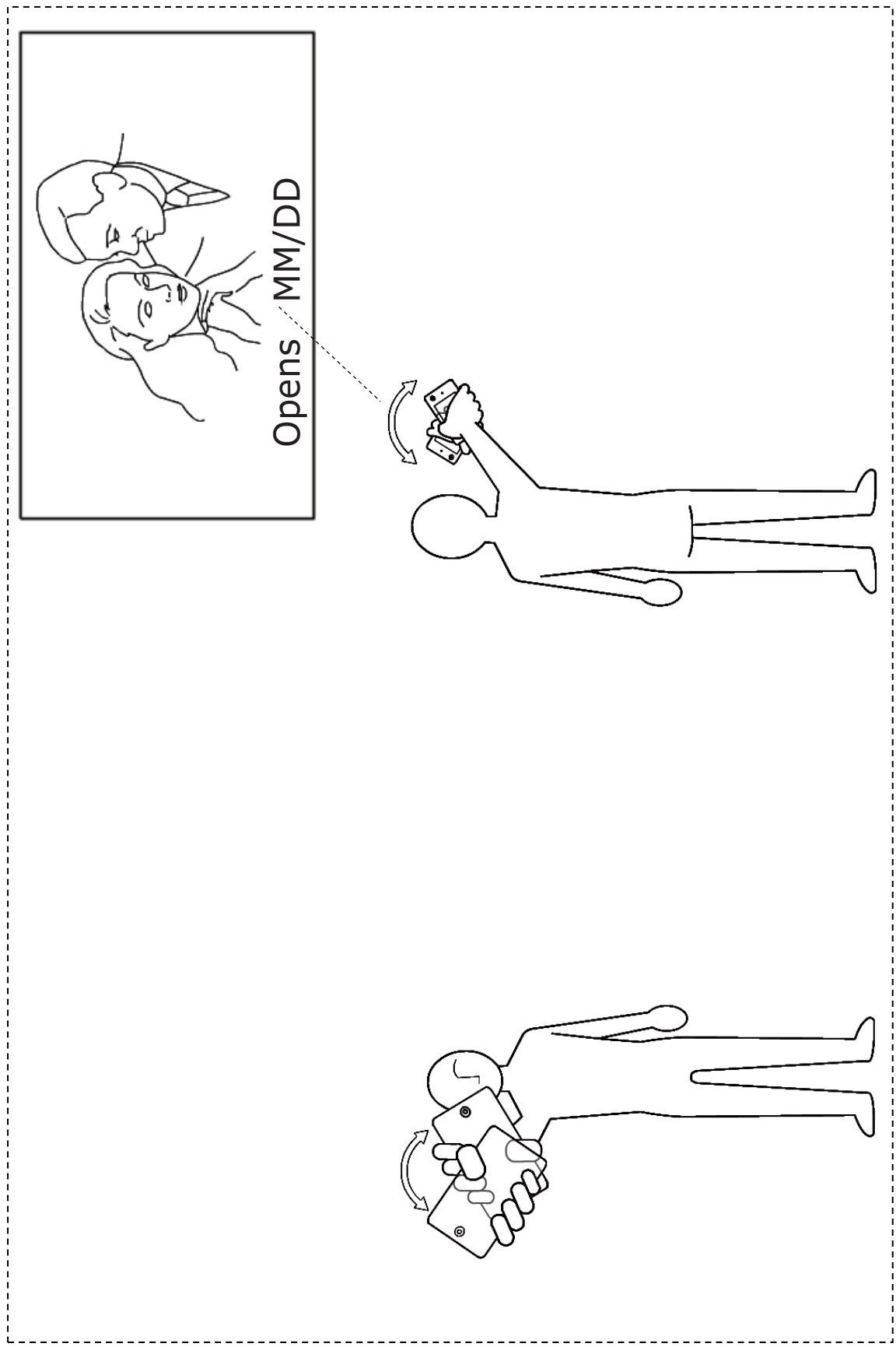


FIG. 141

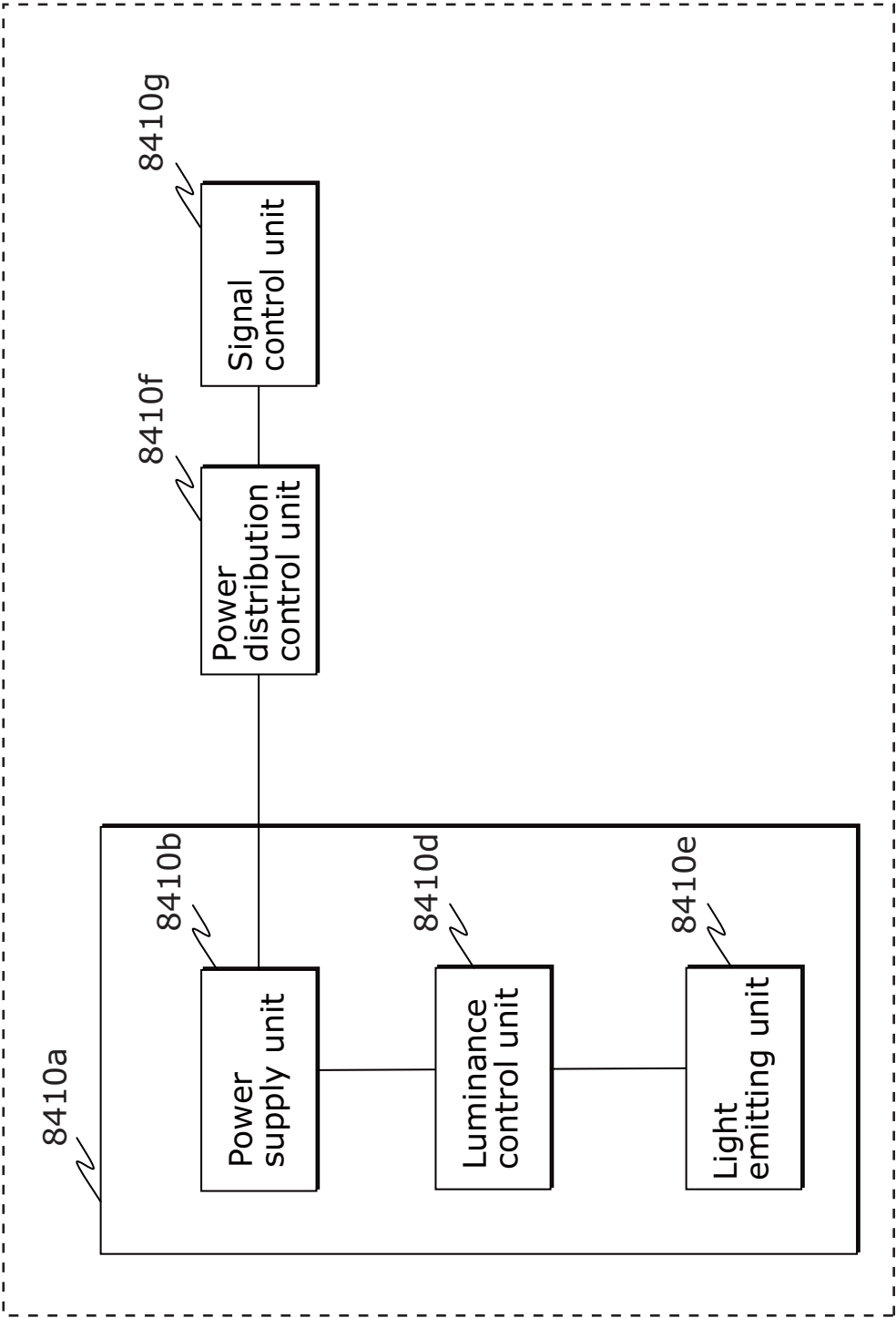


FIG. 142

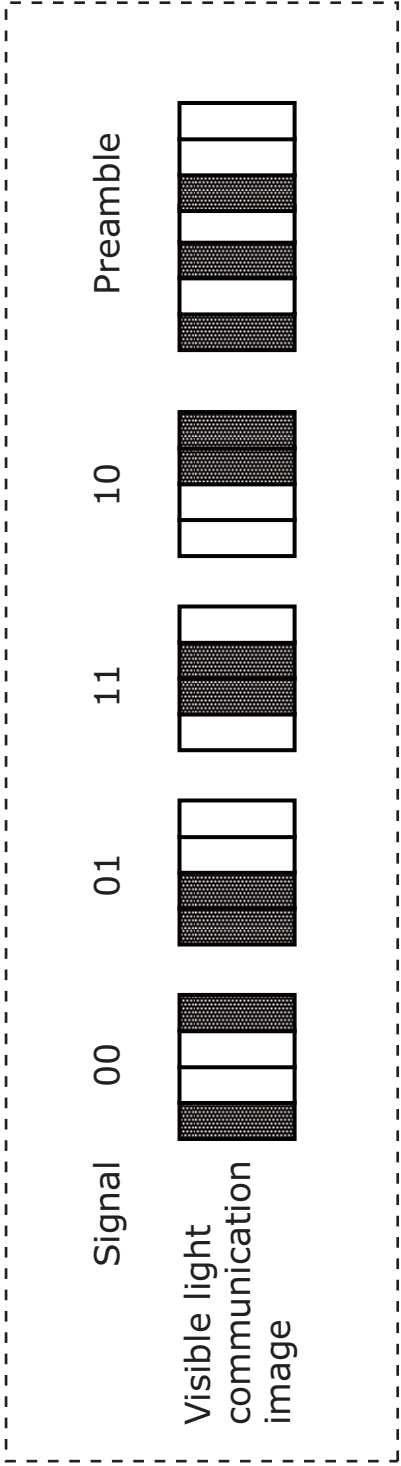


FIG. 143

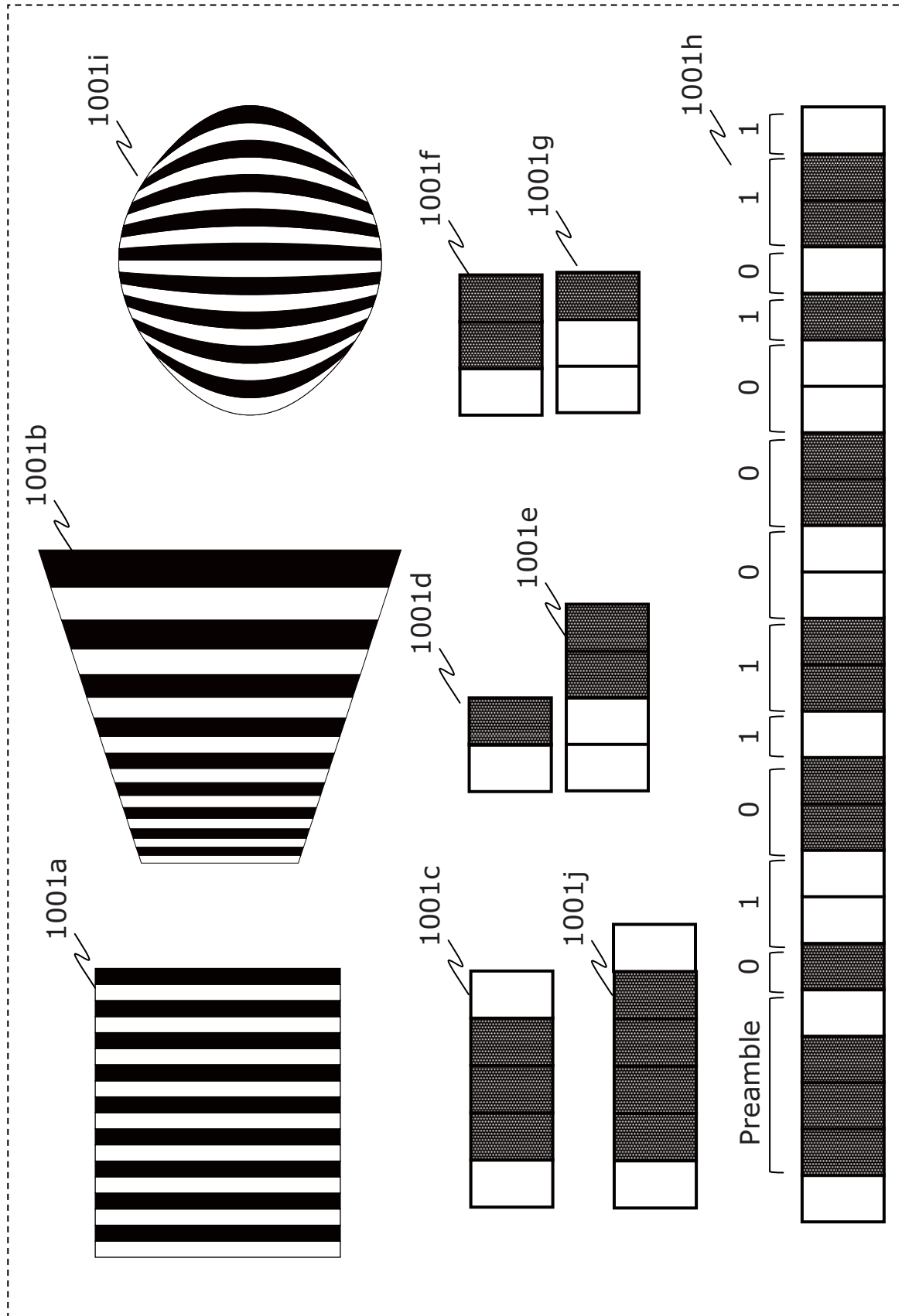


FIG. 144

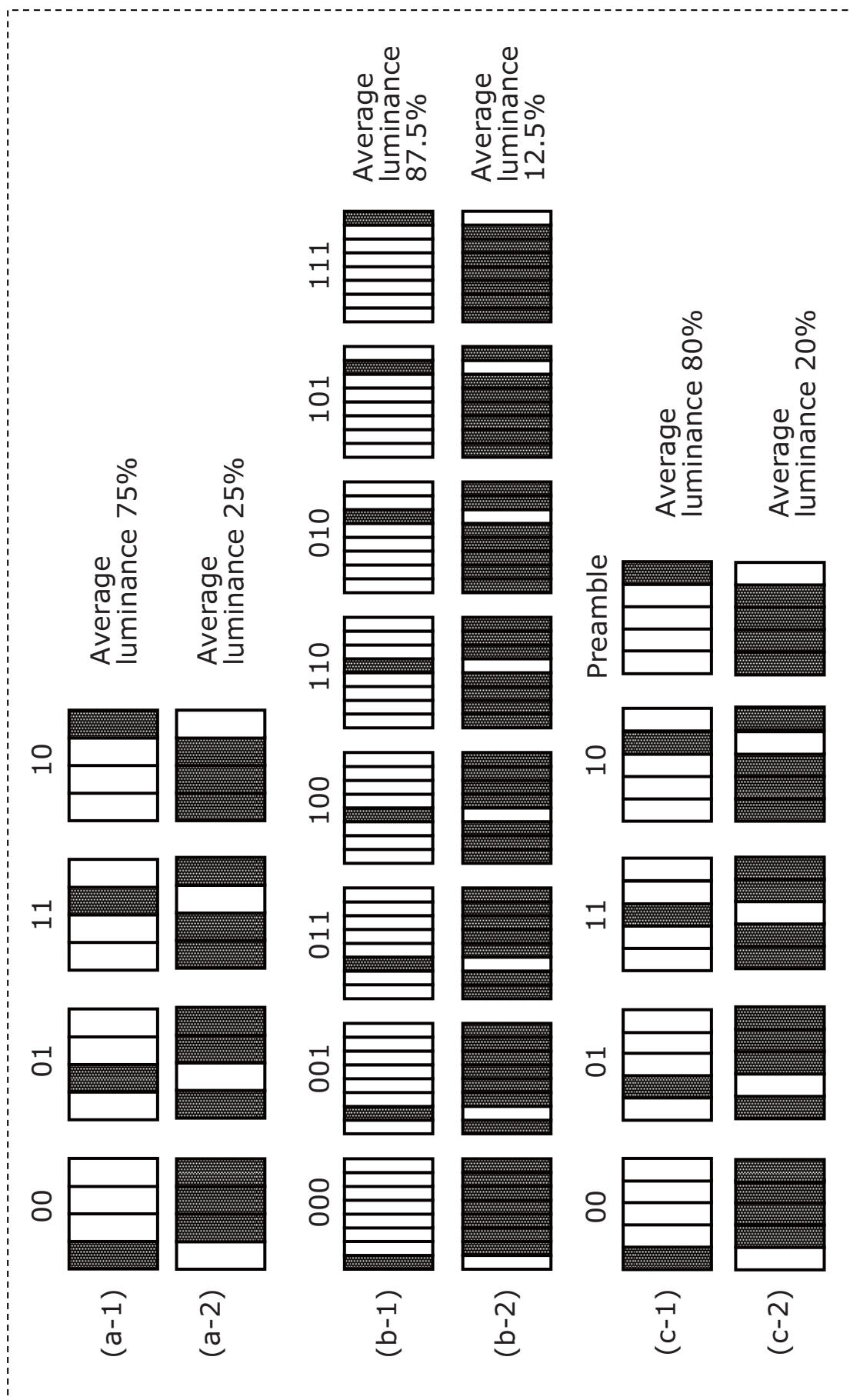


FIG. 145

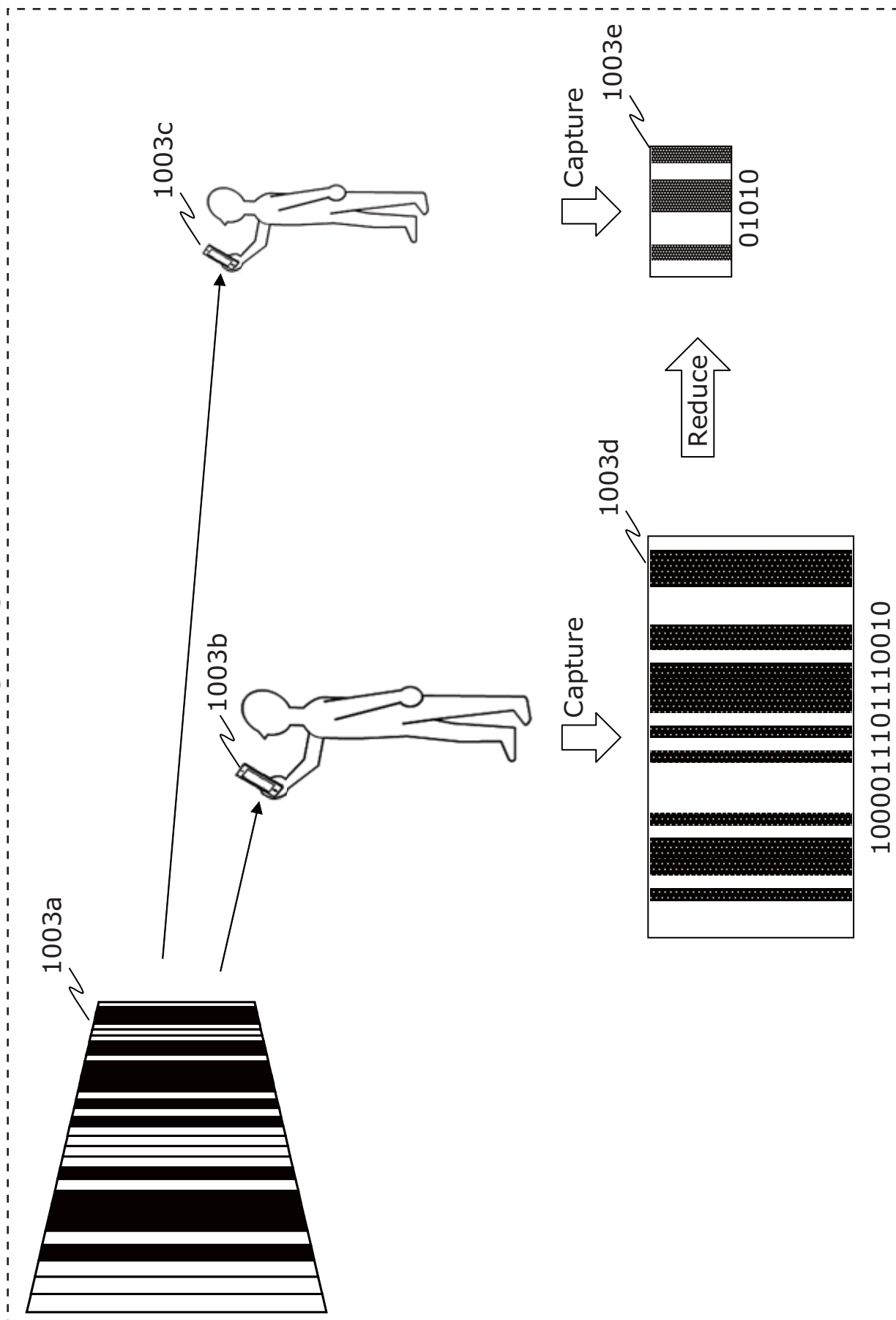


FIG. 146

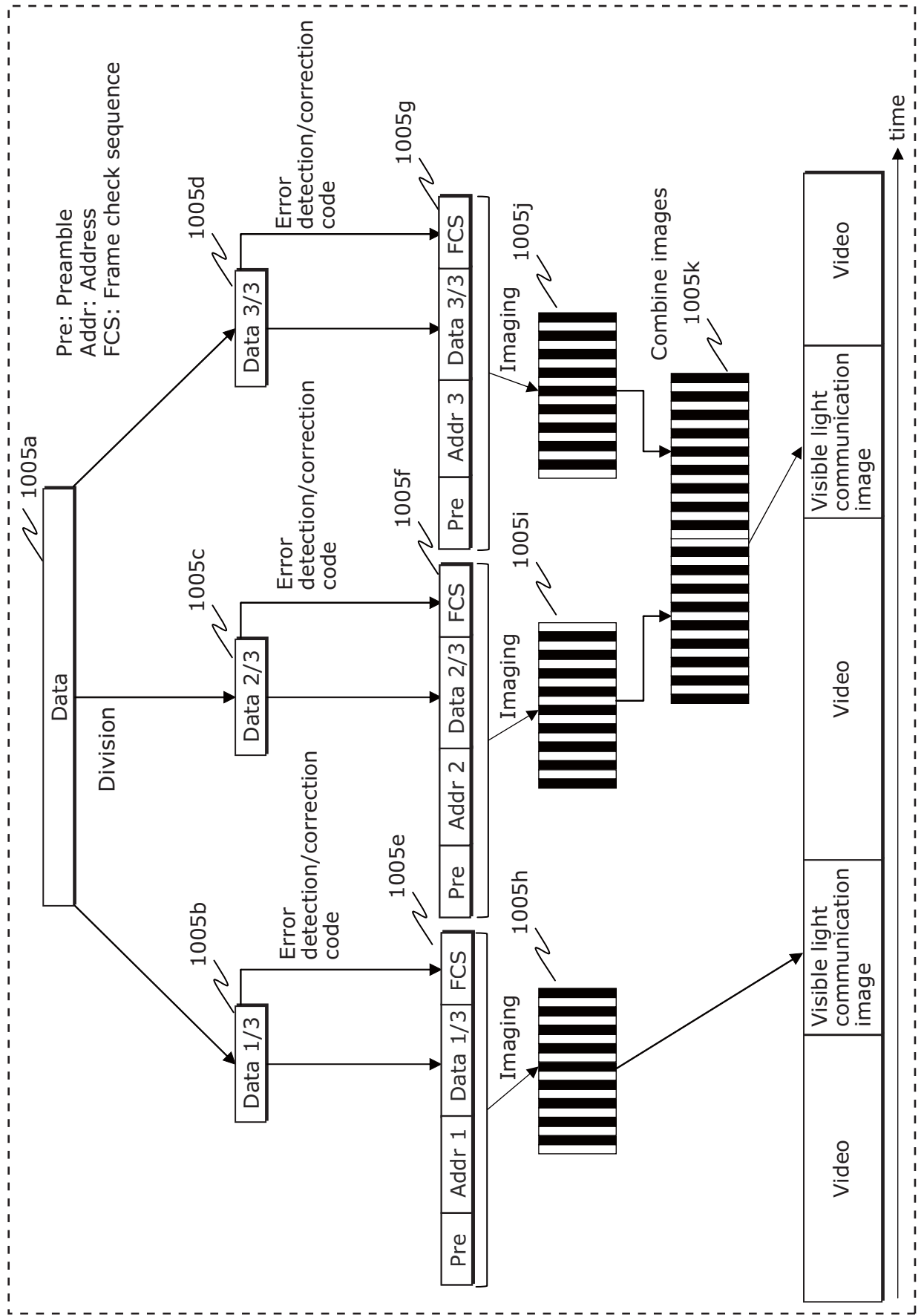


FIG. 147

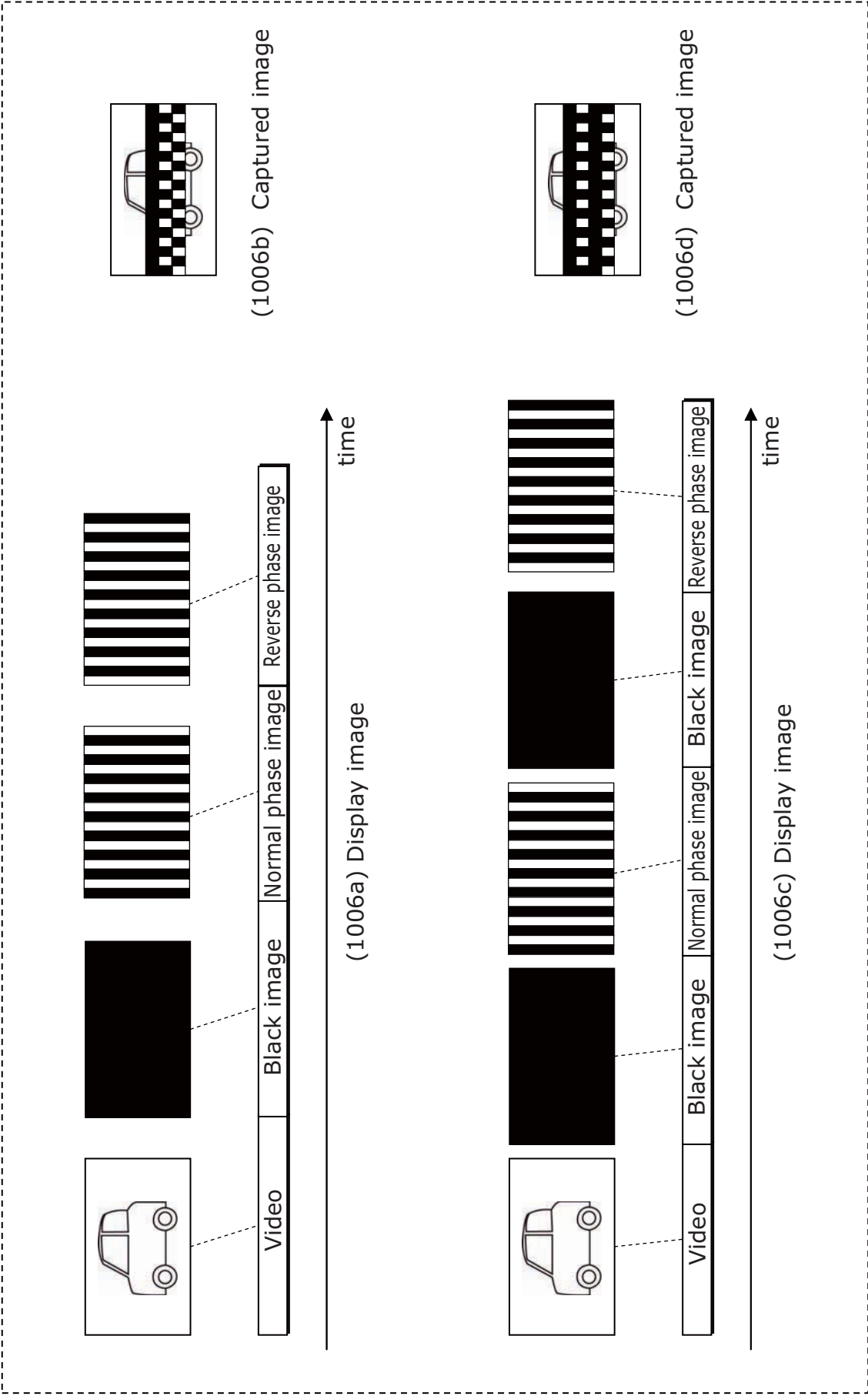


FIG. 148

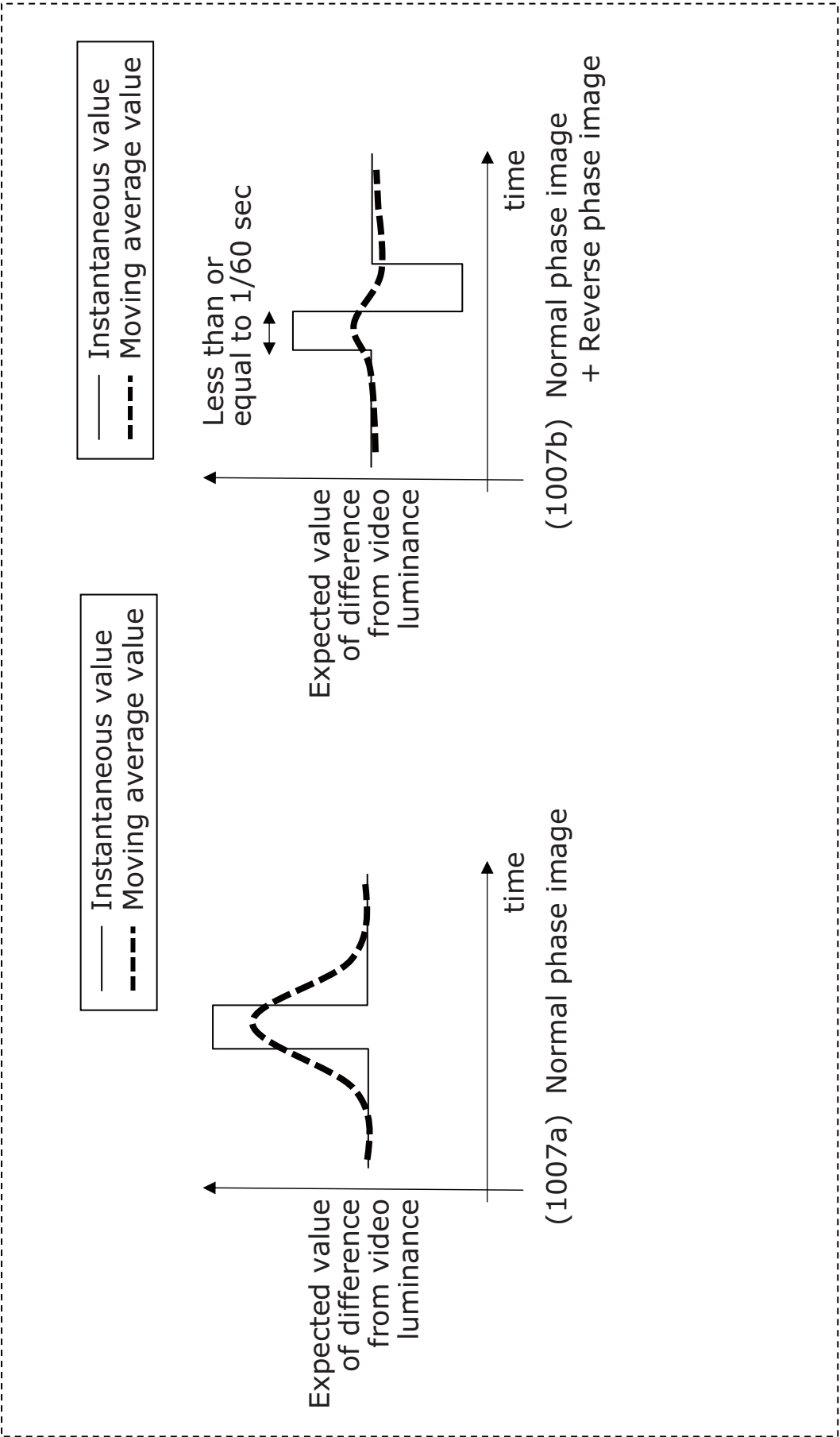


FIG. 149

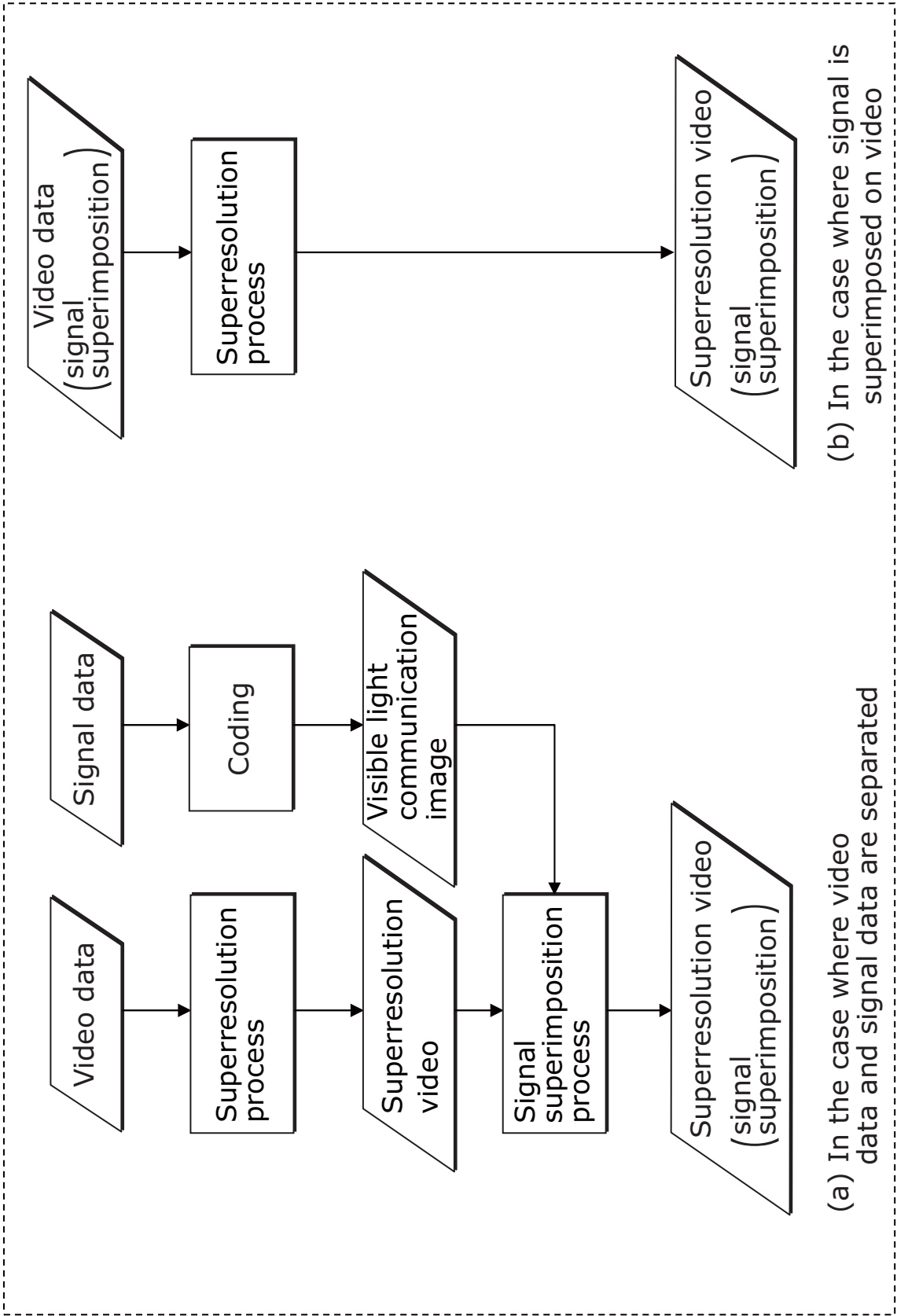


FIG. 150

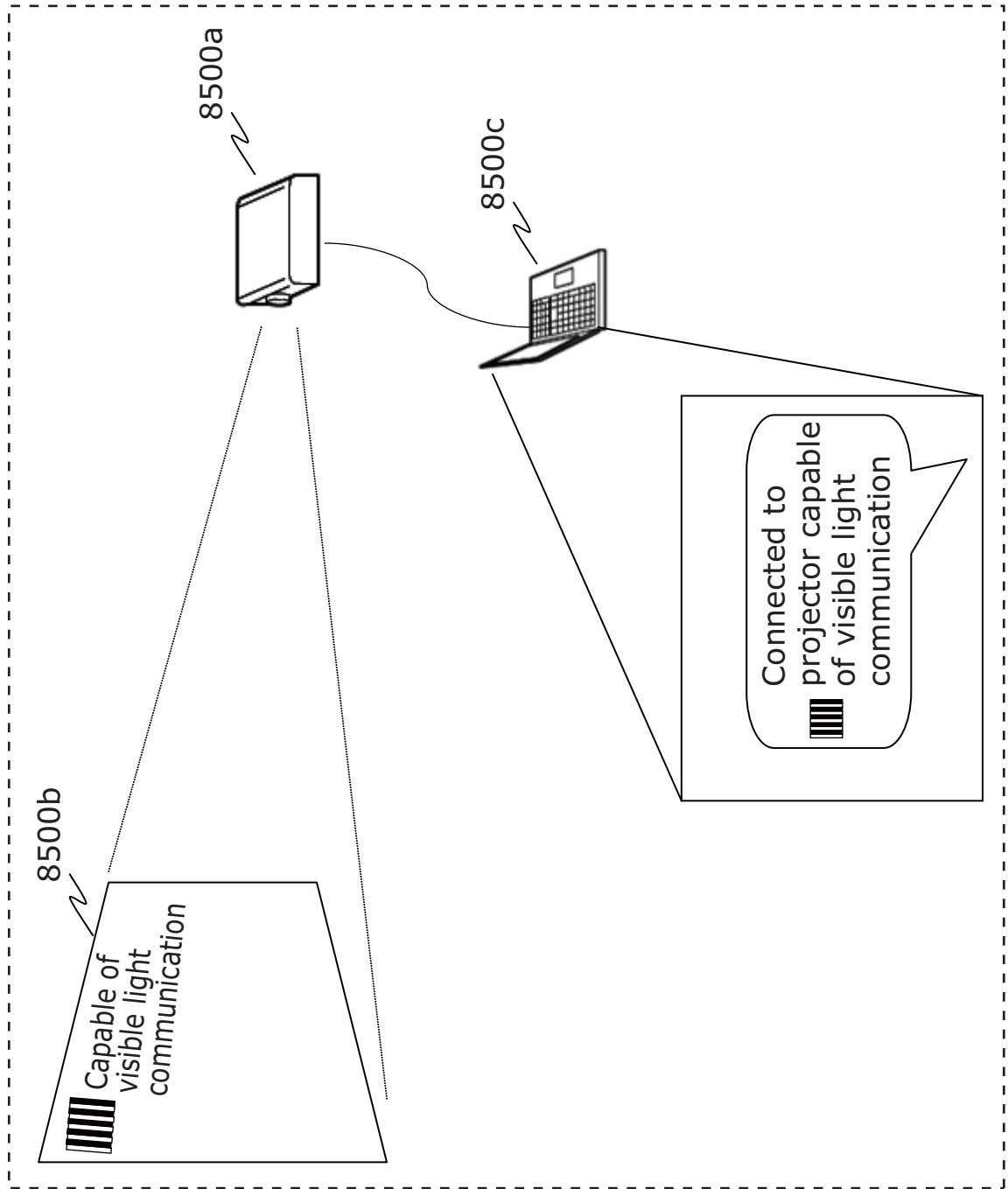


FIG. 151

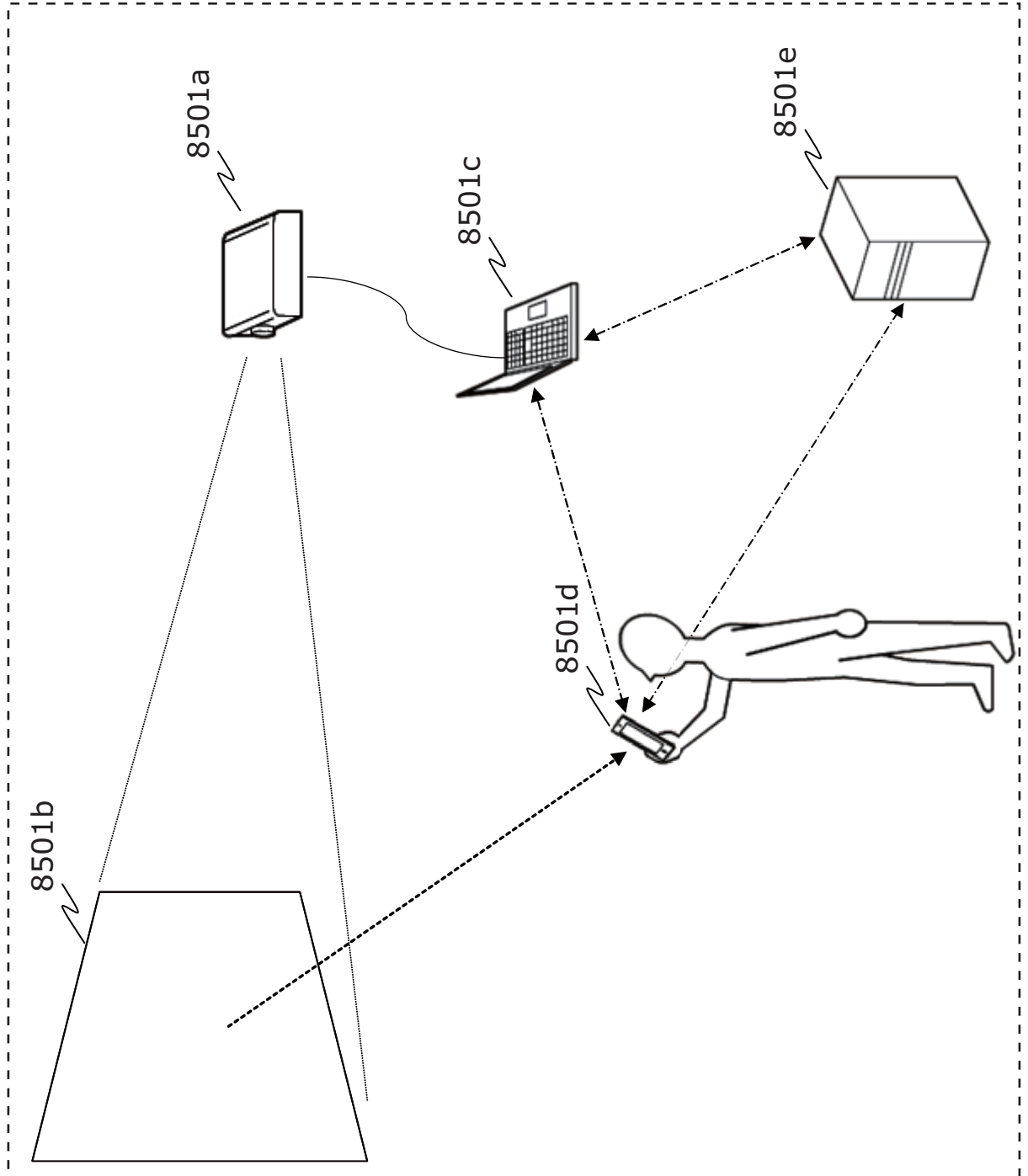


FIG. 152

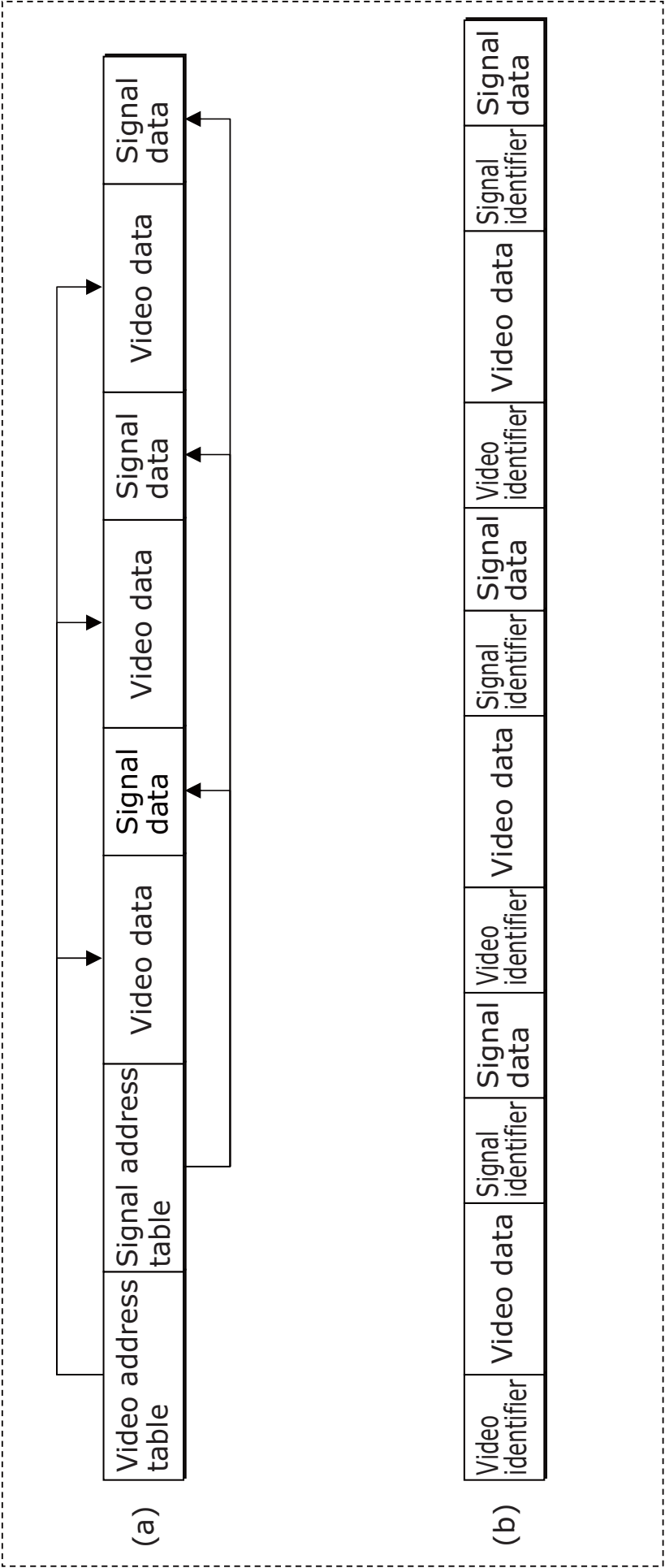


FIG. 153

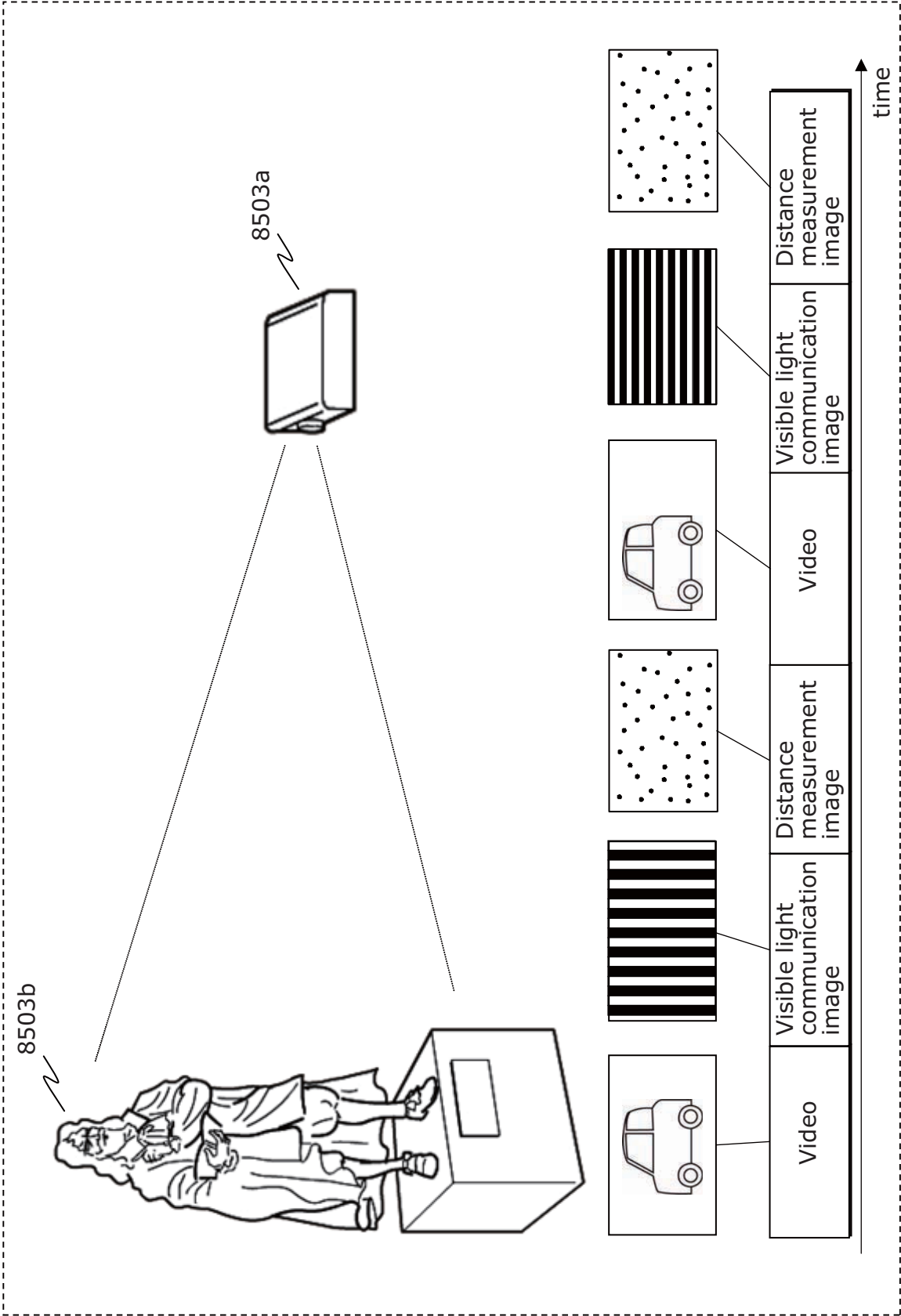


FIG. 154

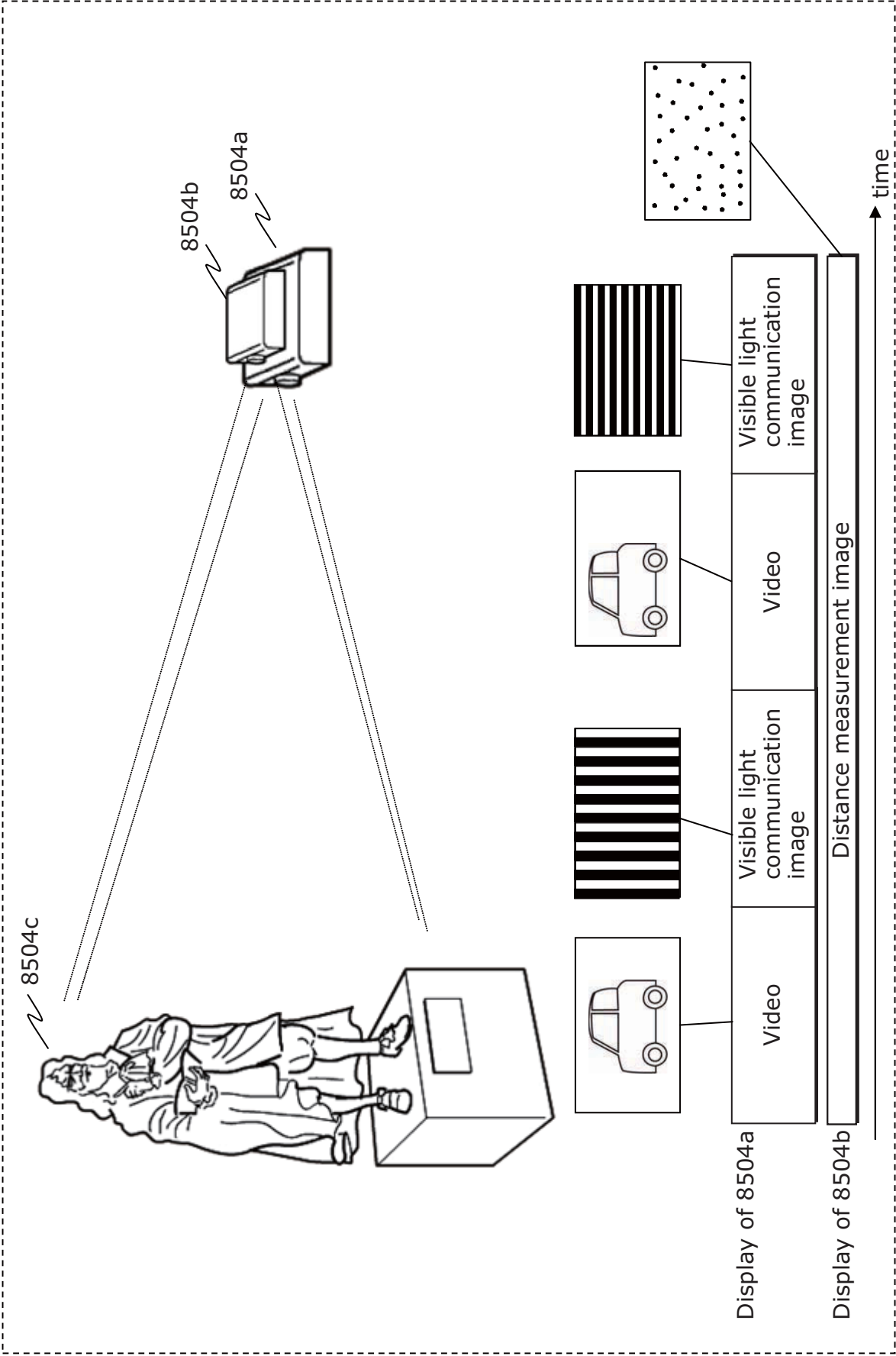


FIG. 155

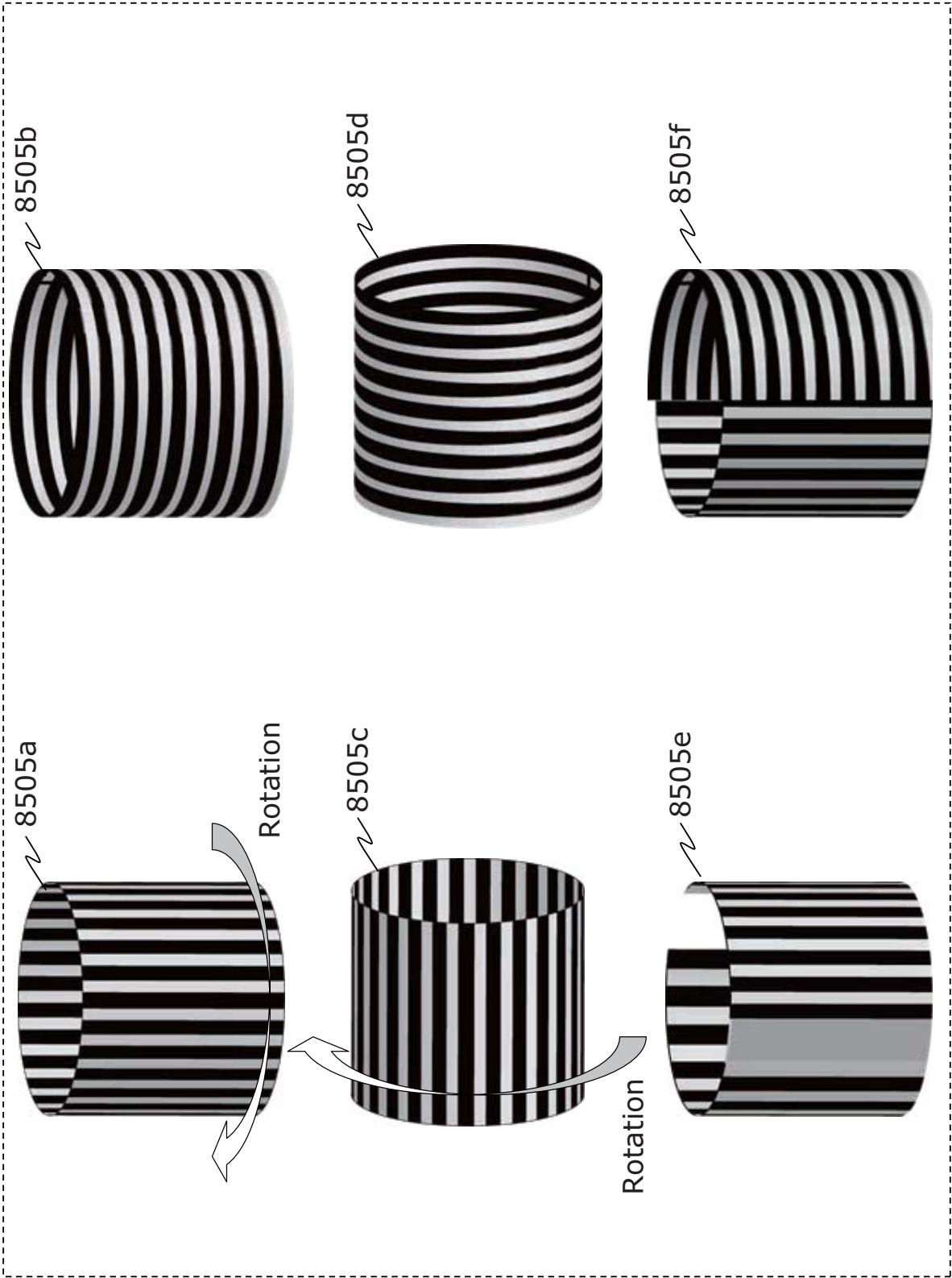


FIG. 156

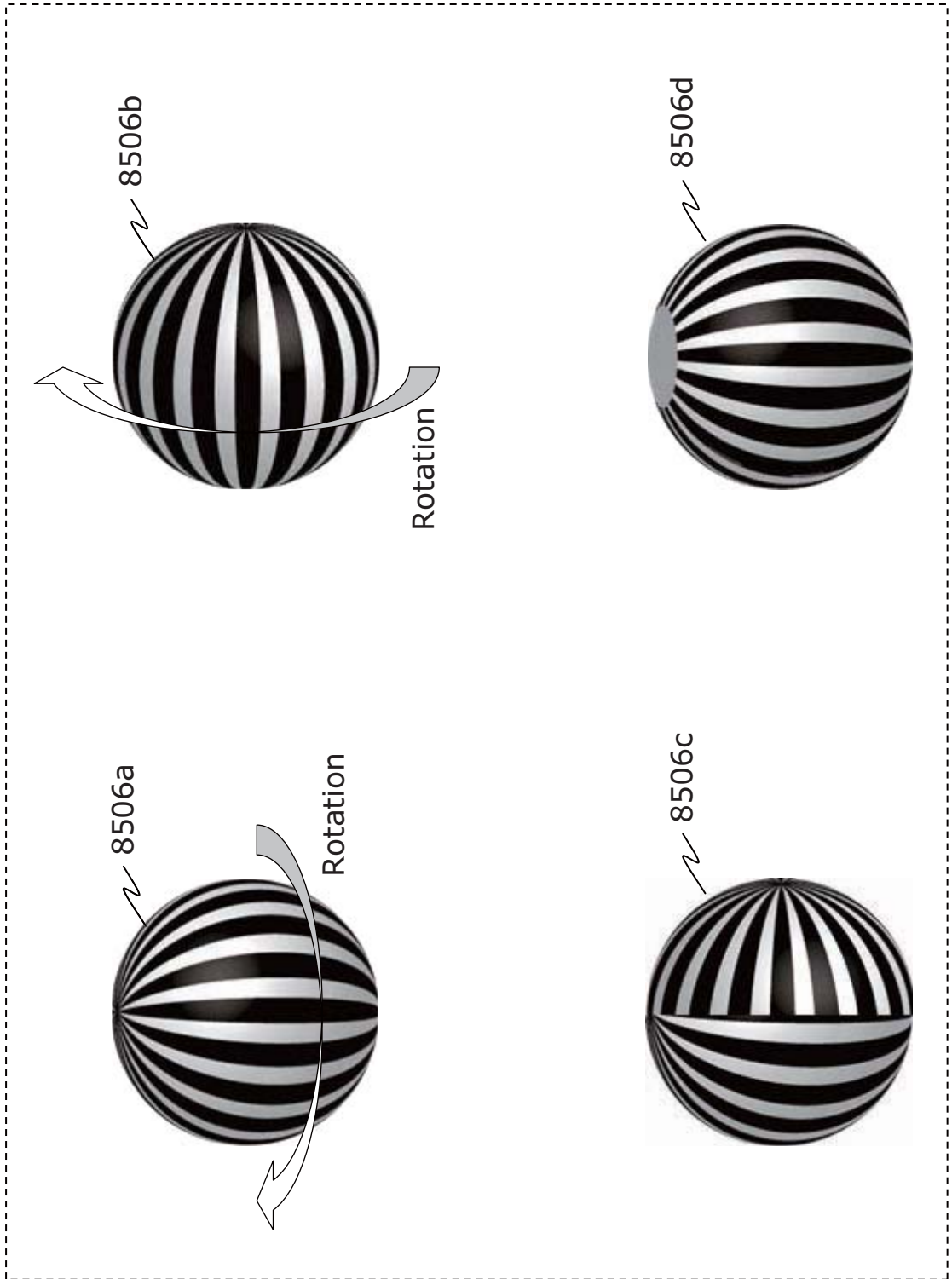


FIG. 157

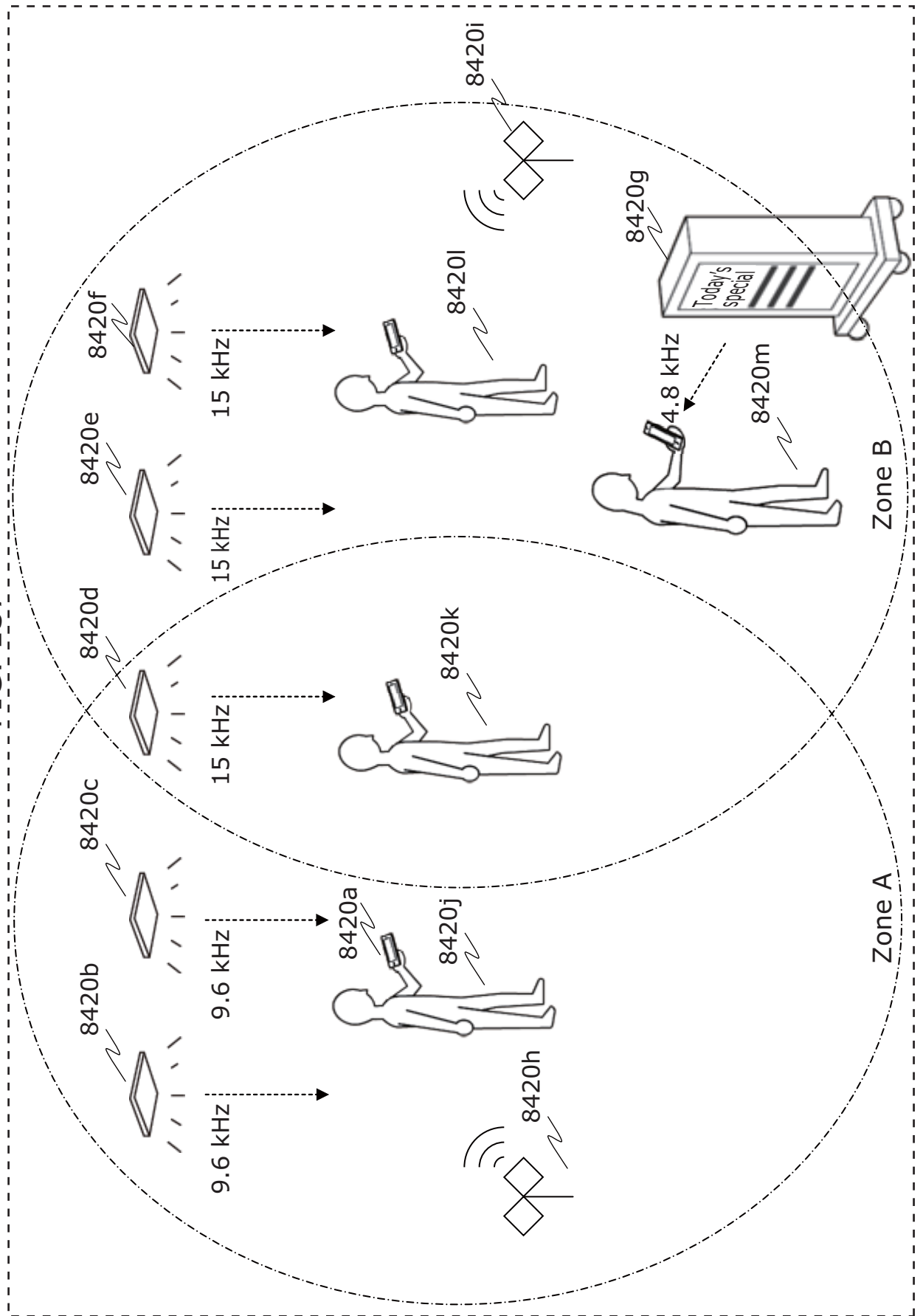


FIG. 158

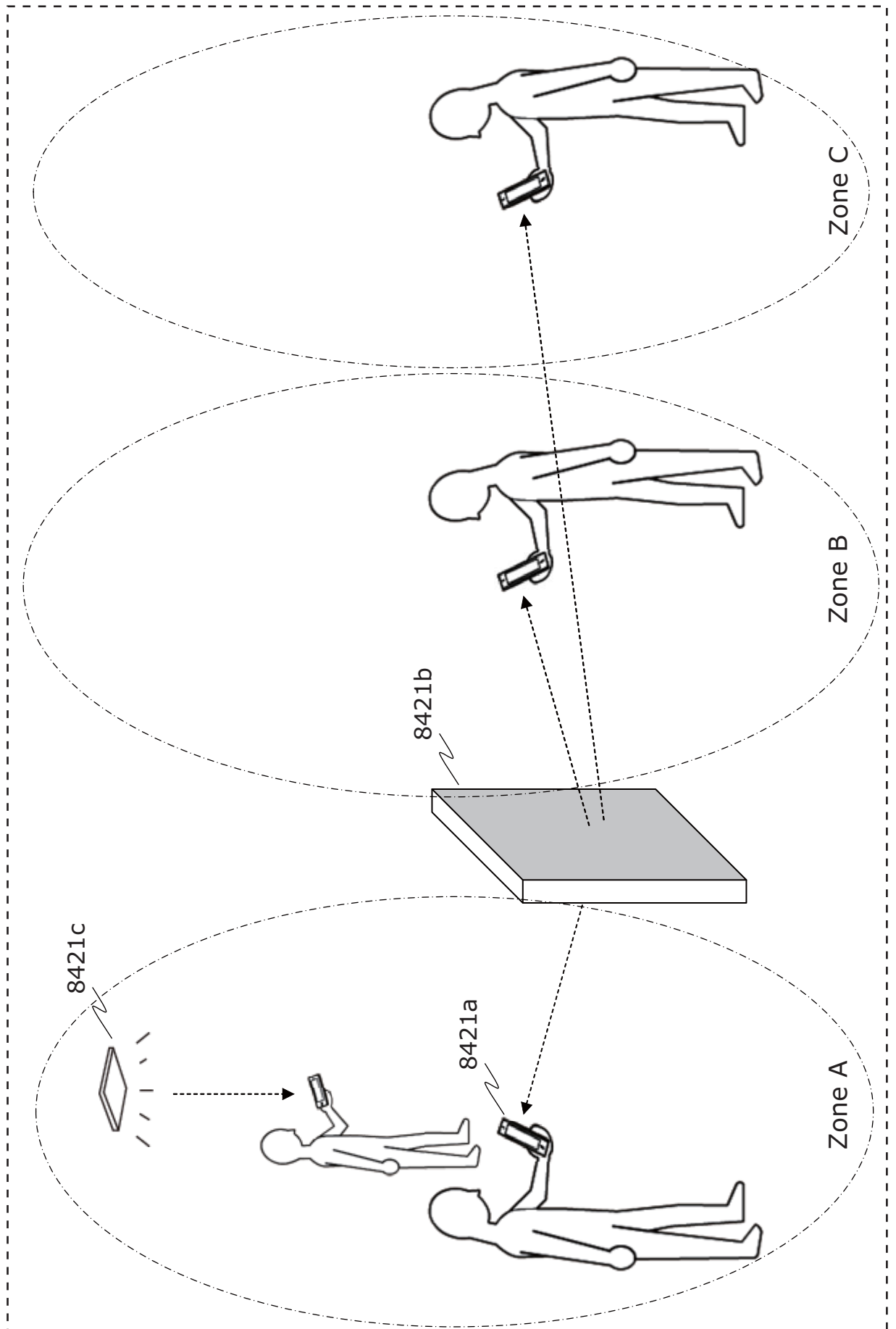


FIG. 159

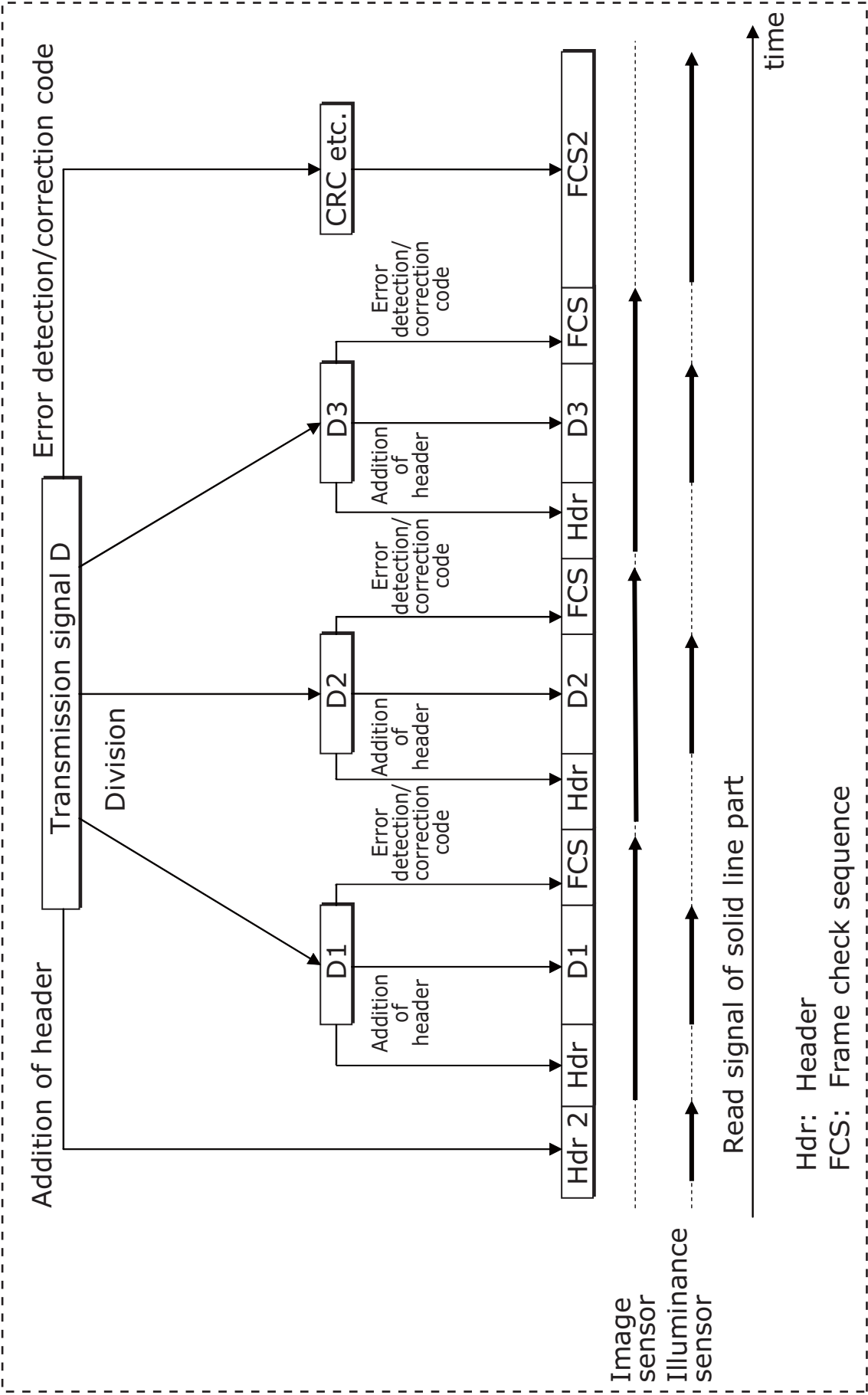


FIG. 160

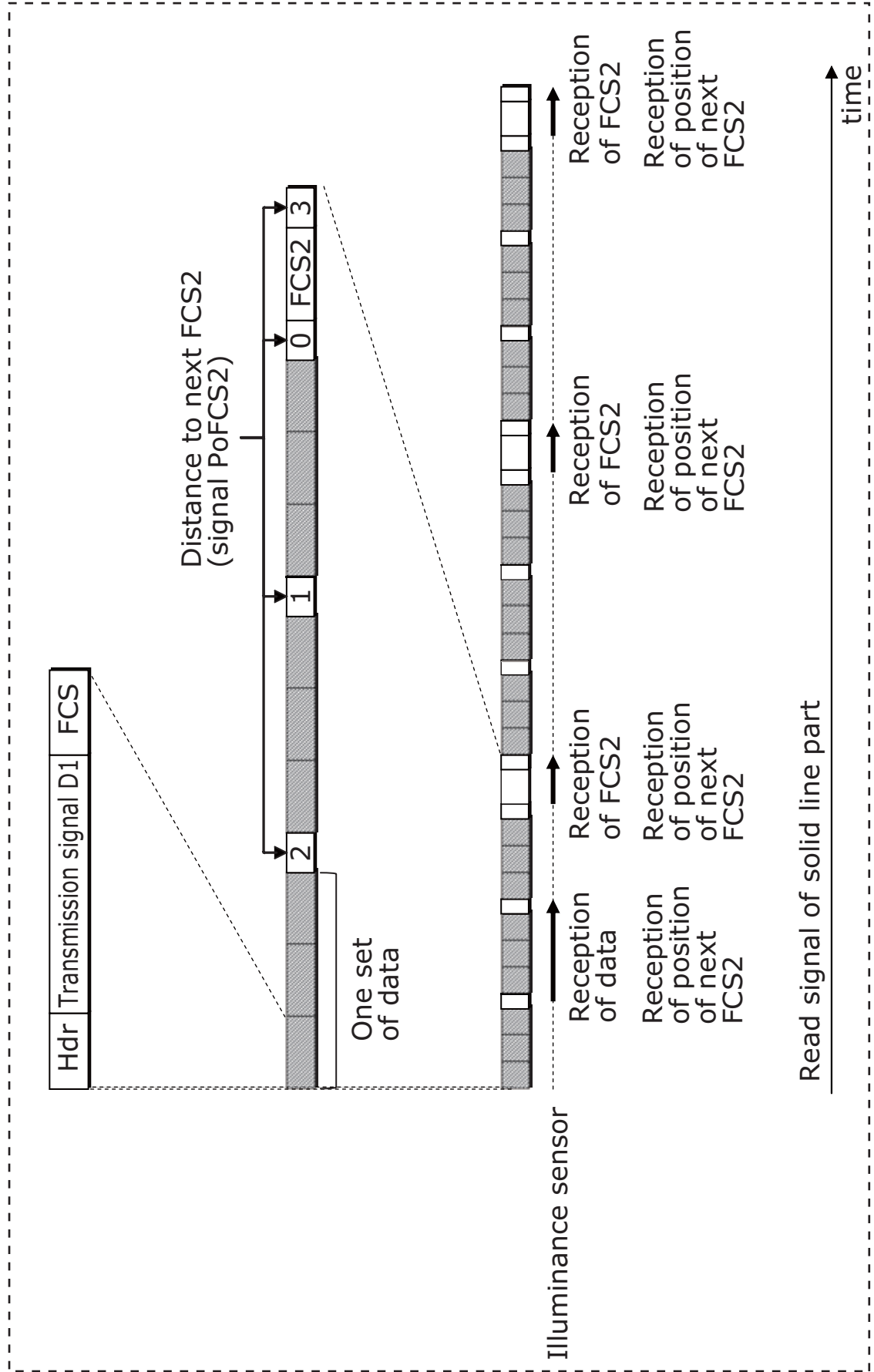


FIG. 161B

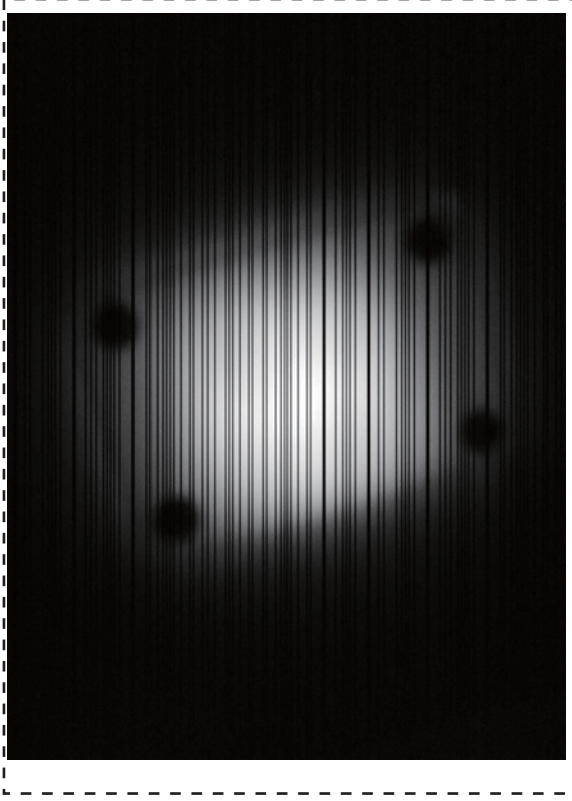


FIG. 161A

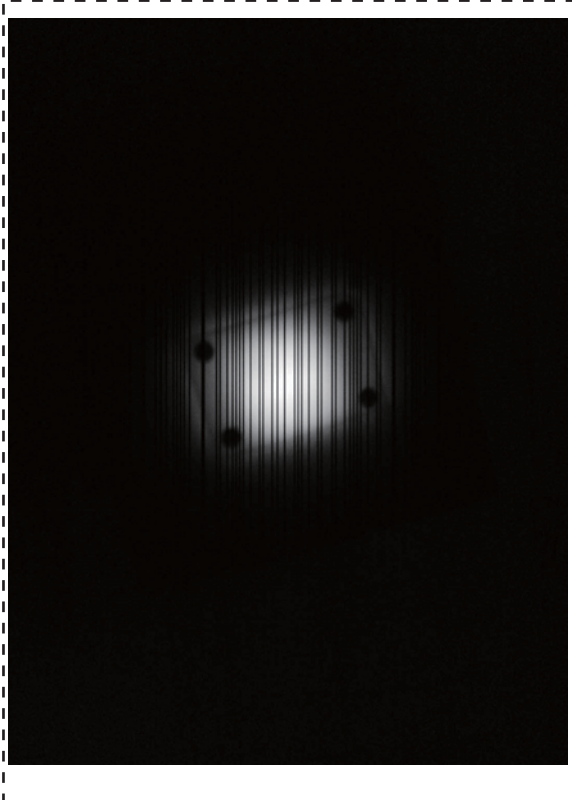


FIG. 161C

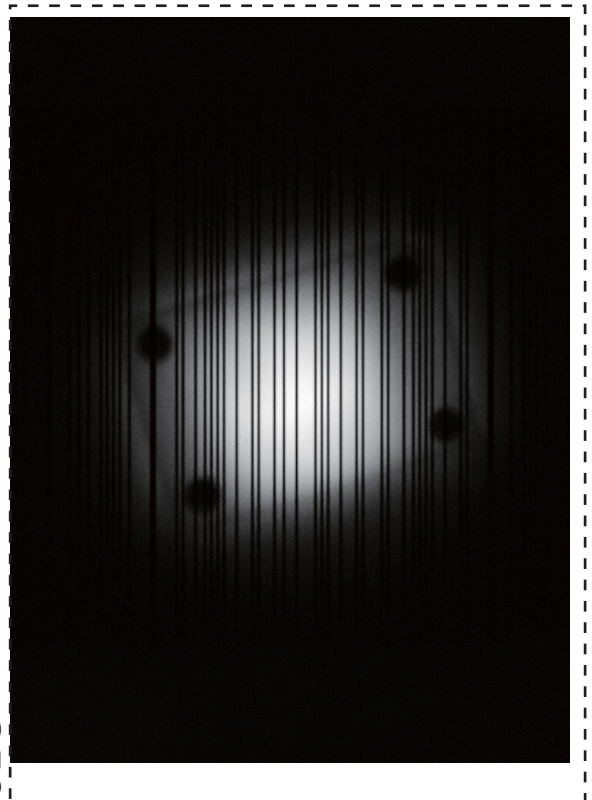


FIG. 162B

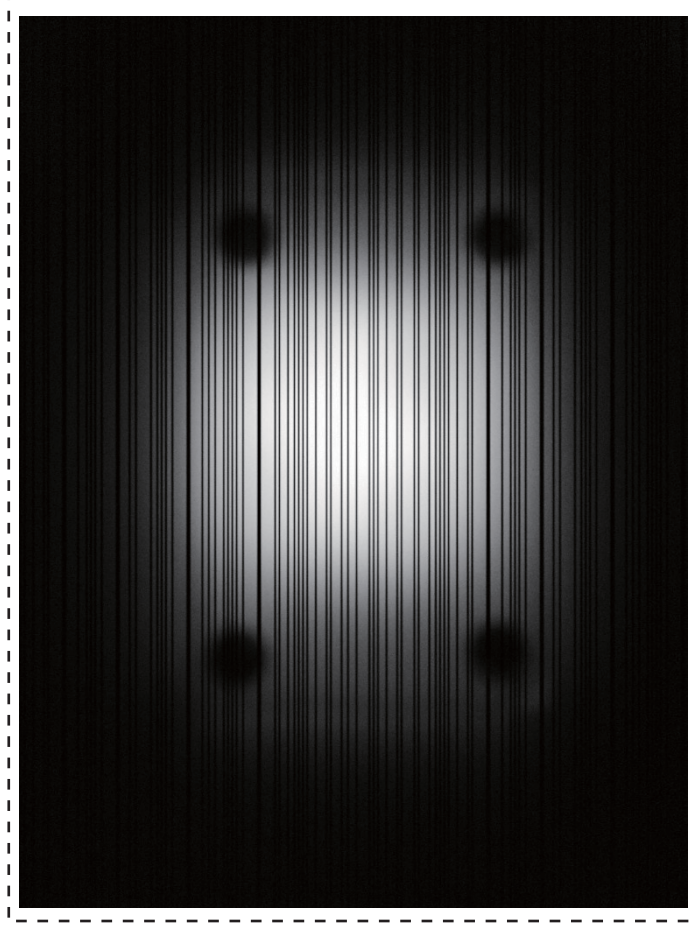


FIG. 162A

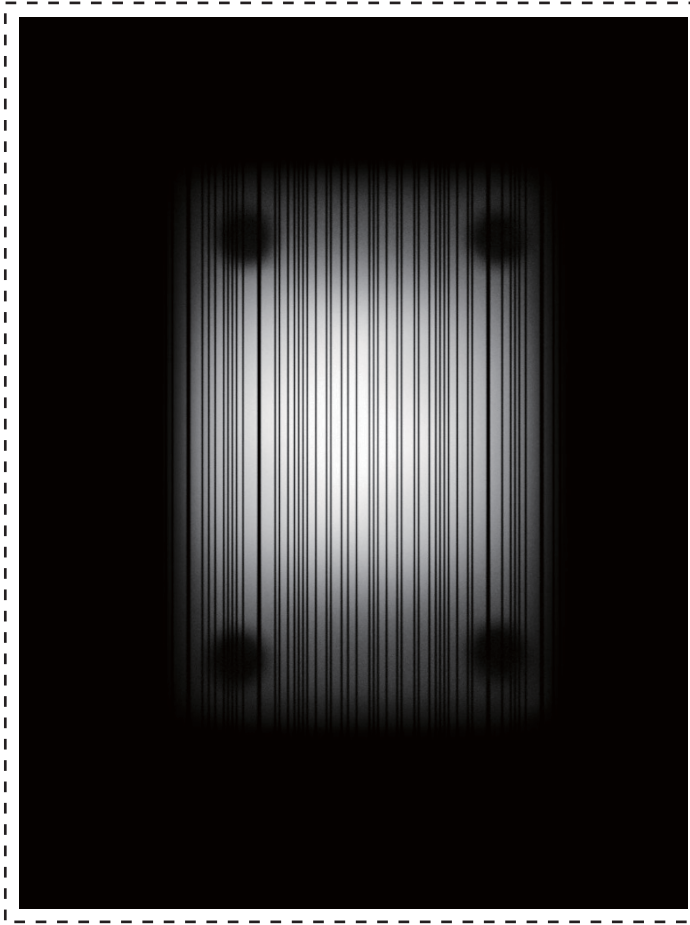


FIG. 163C

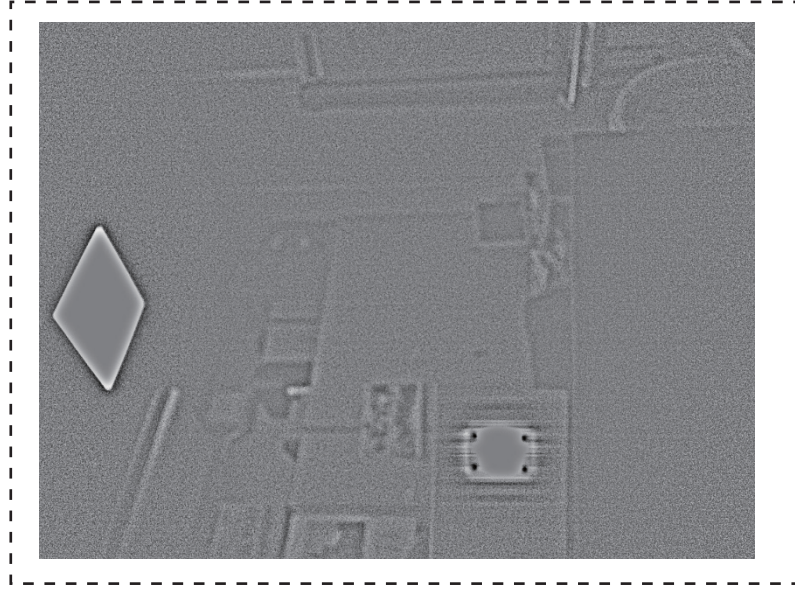


FIG. 163B



FIG. 163A



FIG. 164

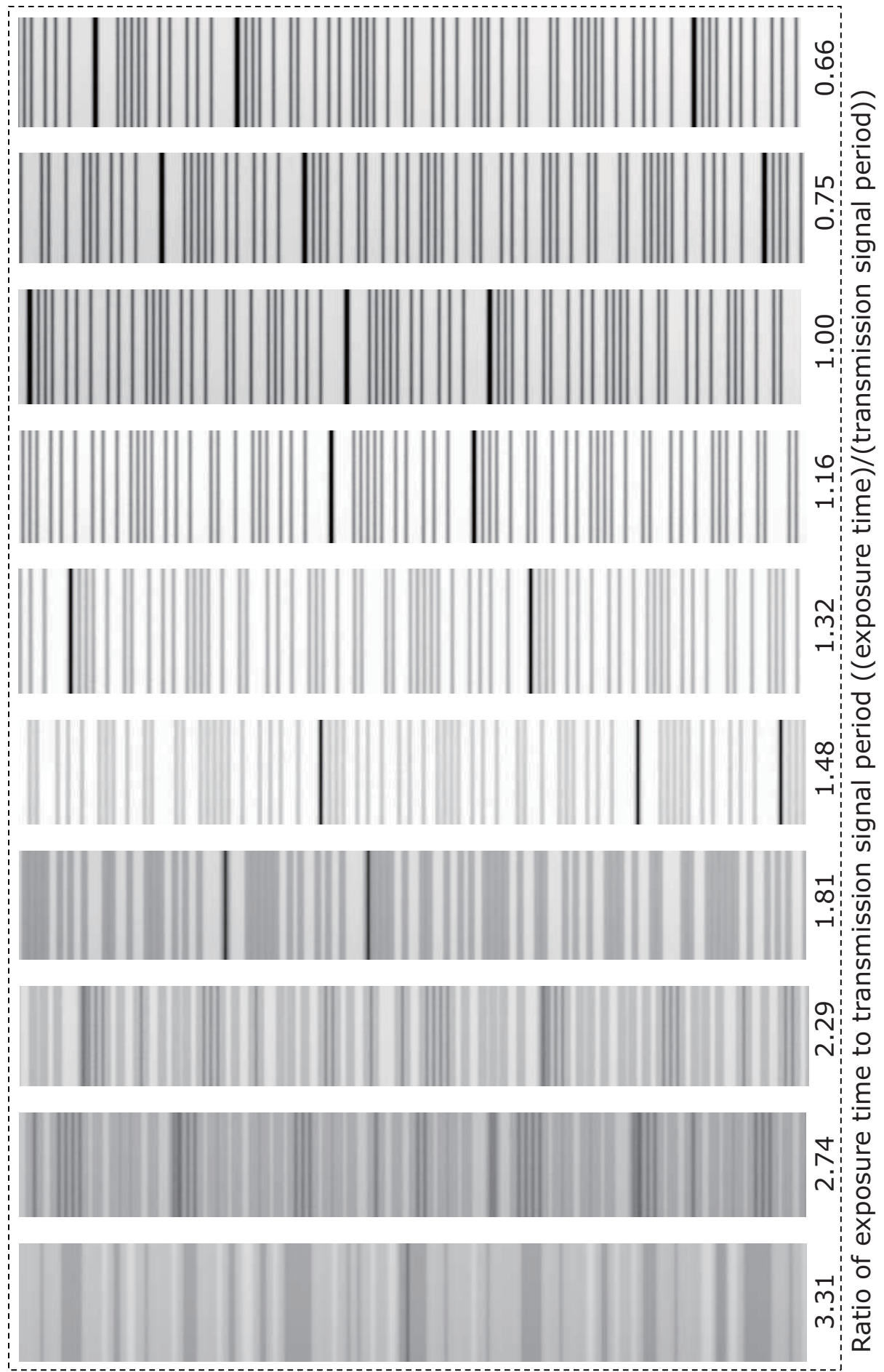


FIG. 165

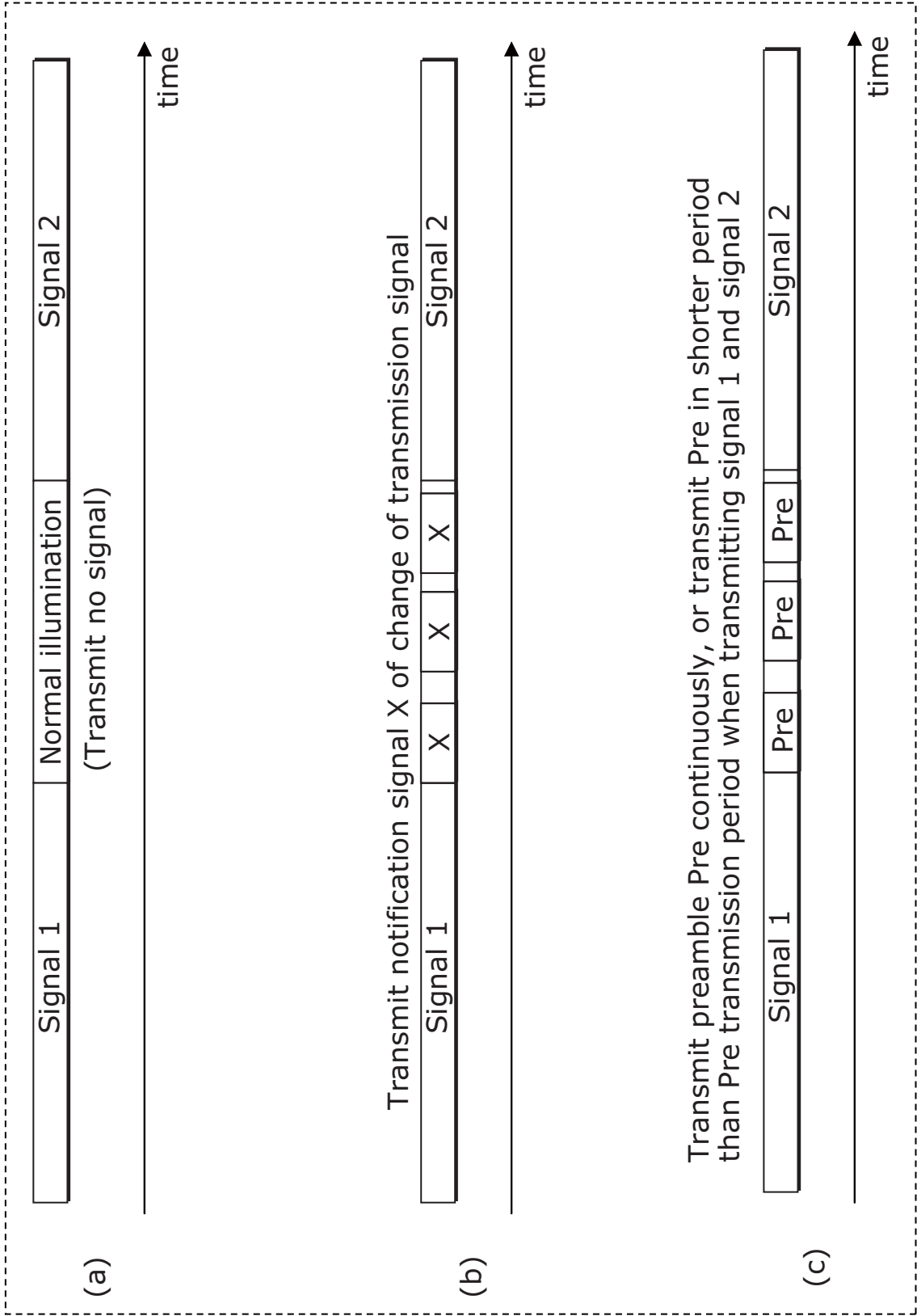


FIG. 166

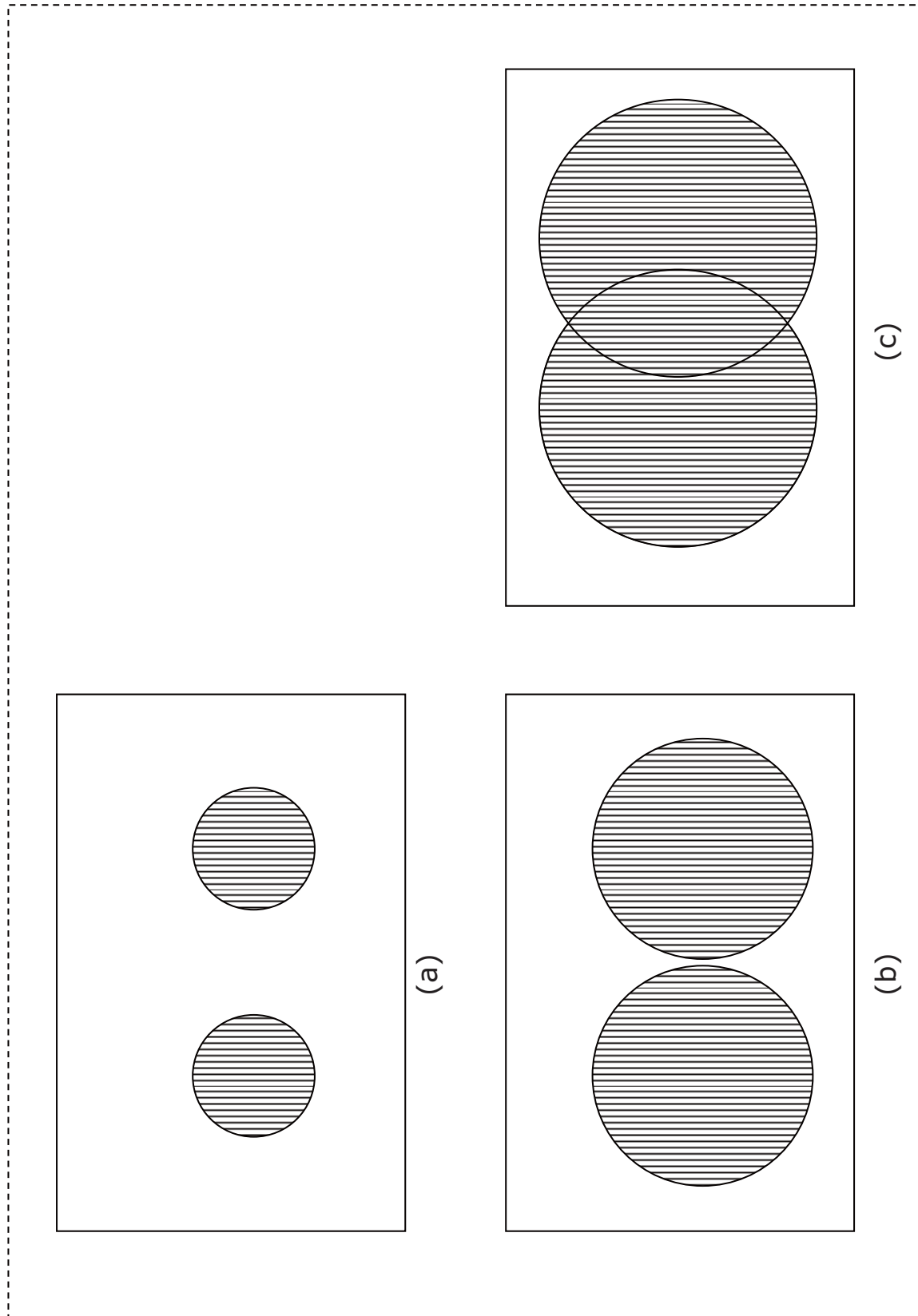


FIG. 167

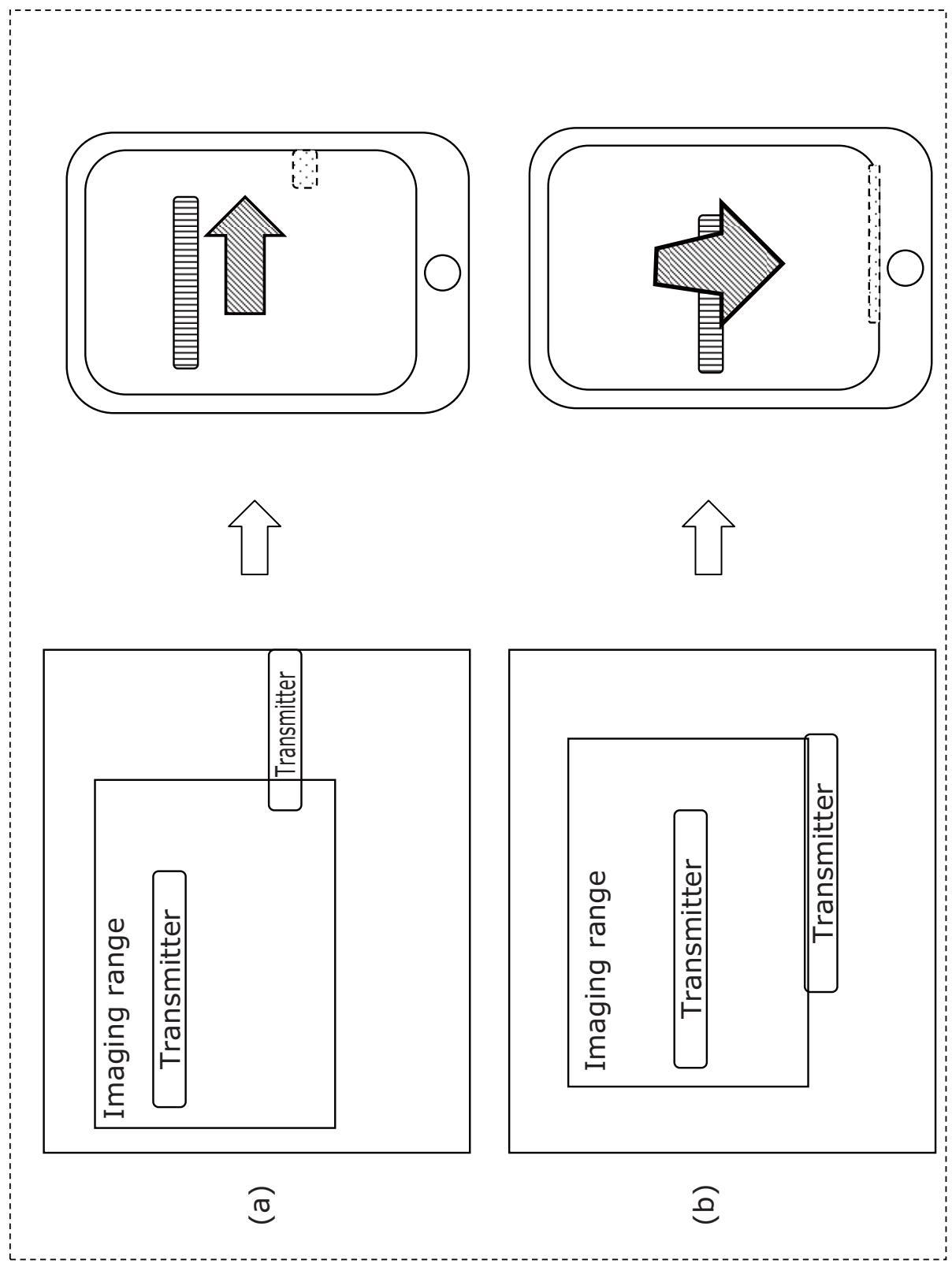


FIG. 168

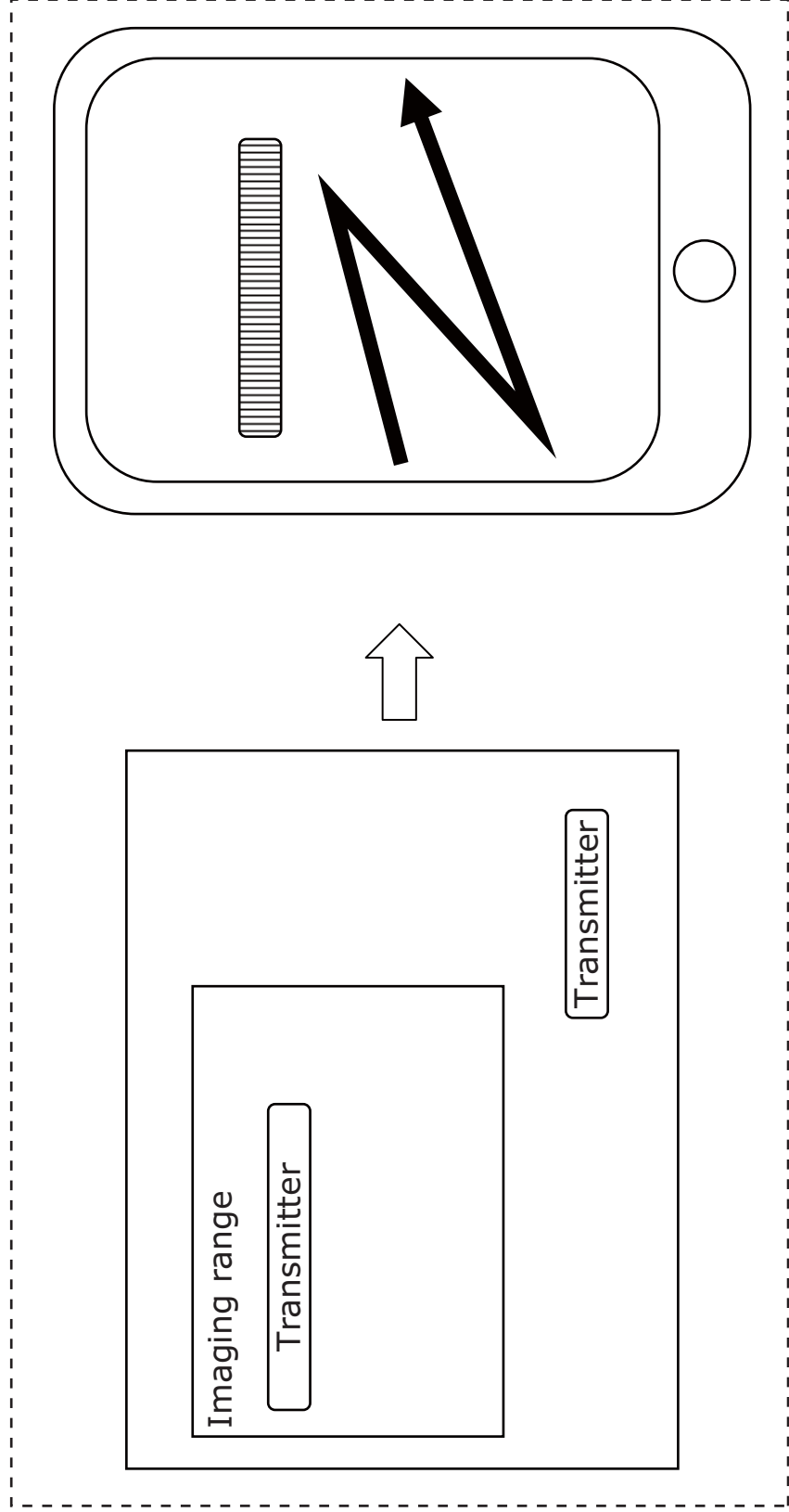
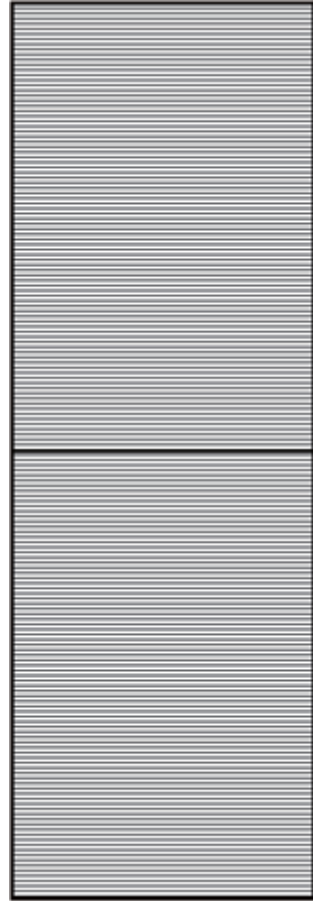
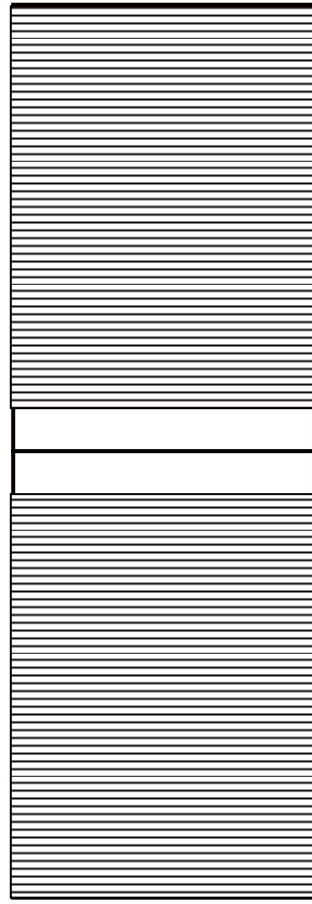


FIG. 169



(a) When transmitting same data



(b) When transmitting different data

FIG. 170

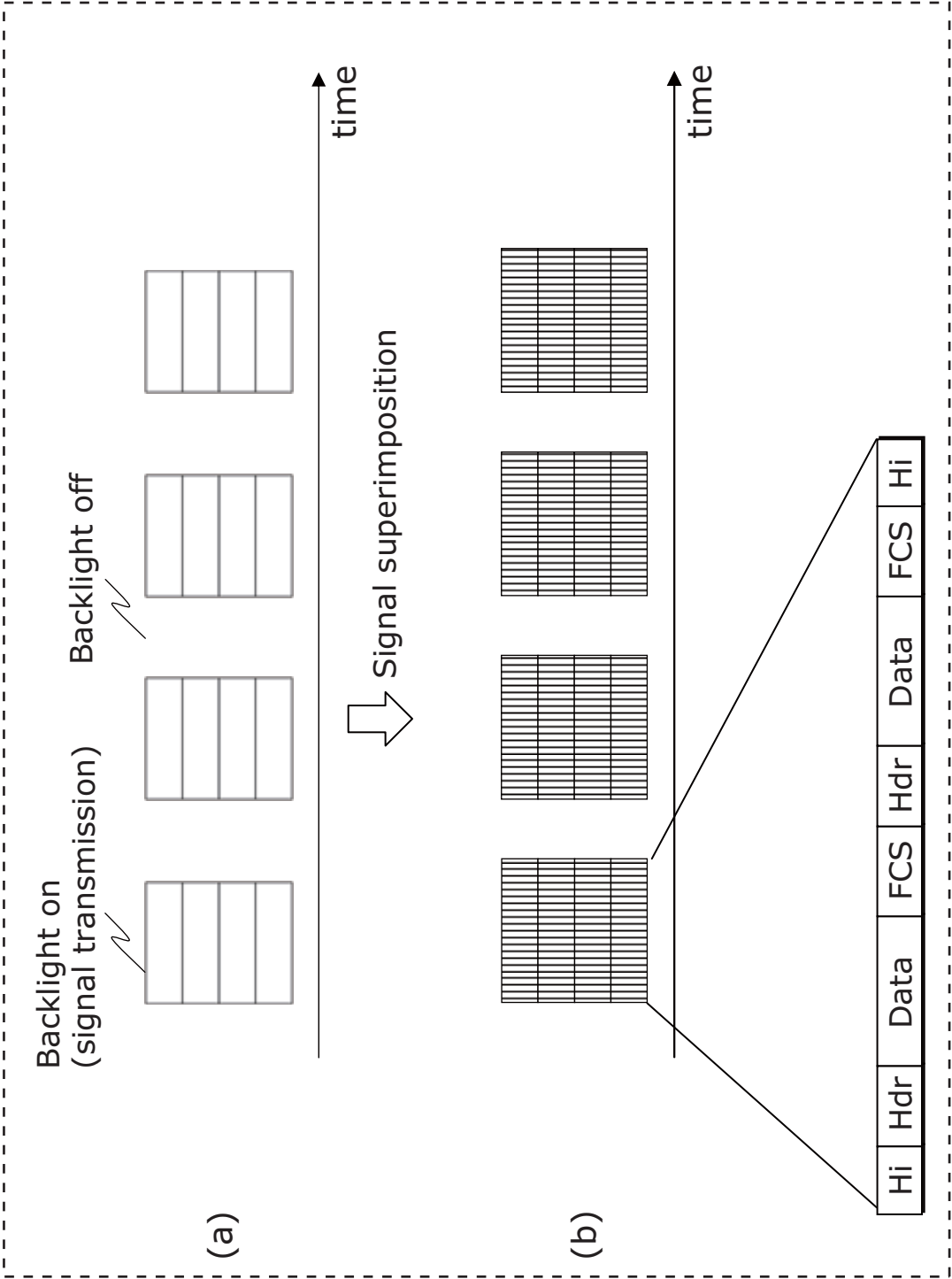


FIG. 171

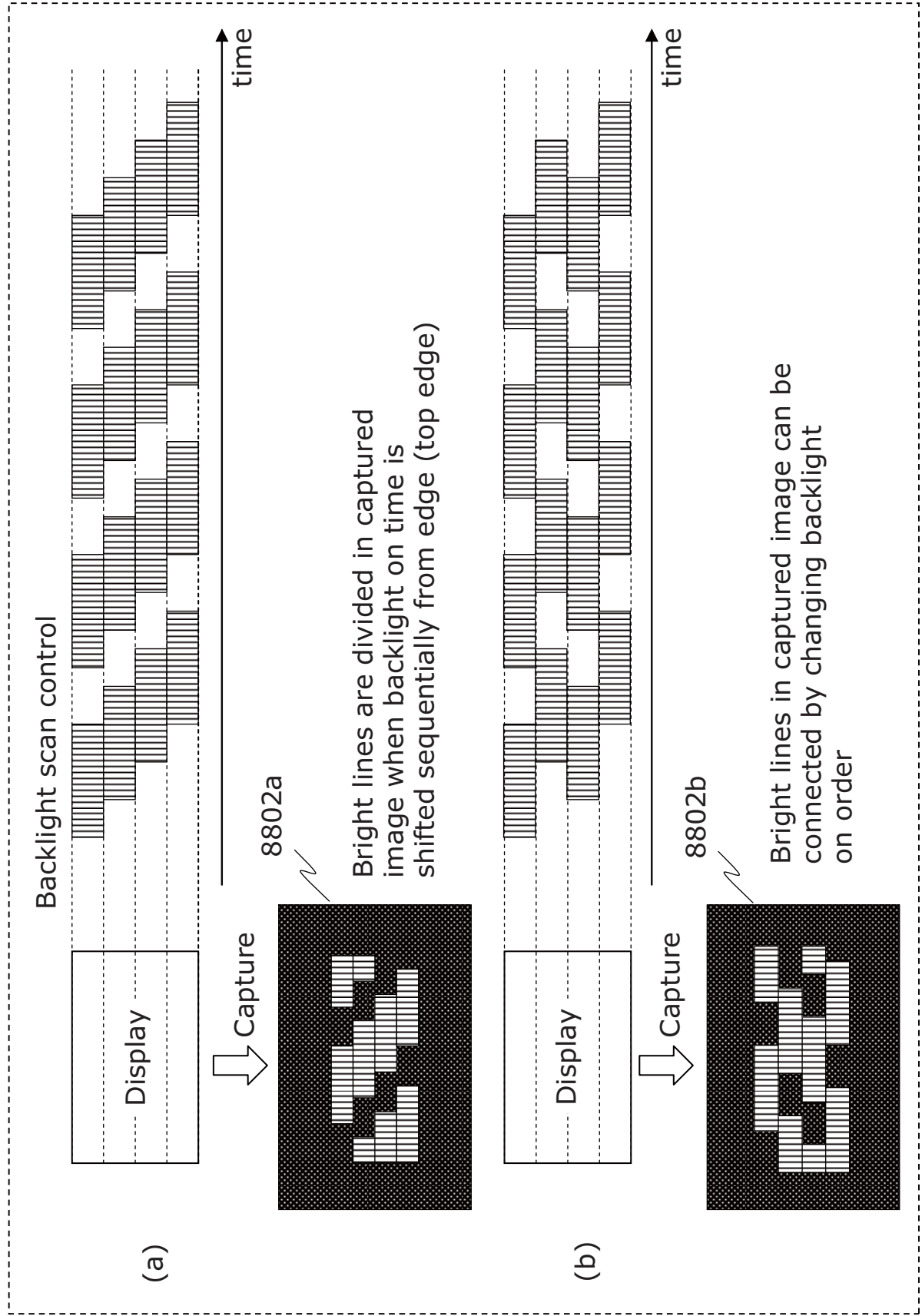


FIG. 172

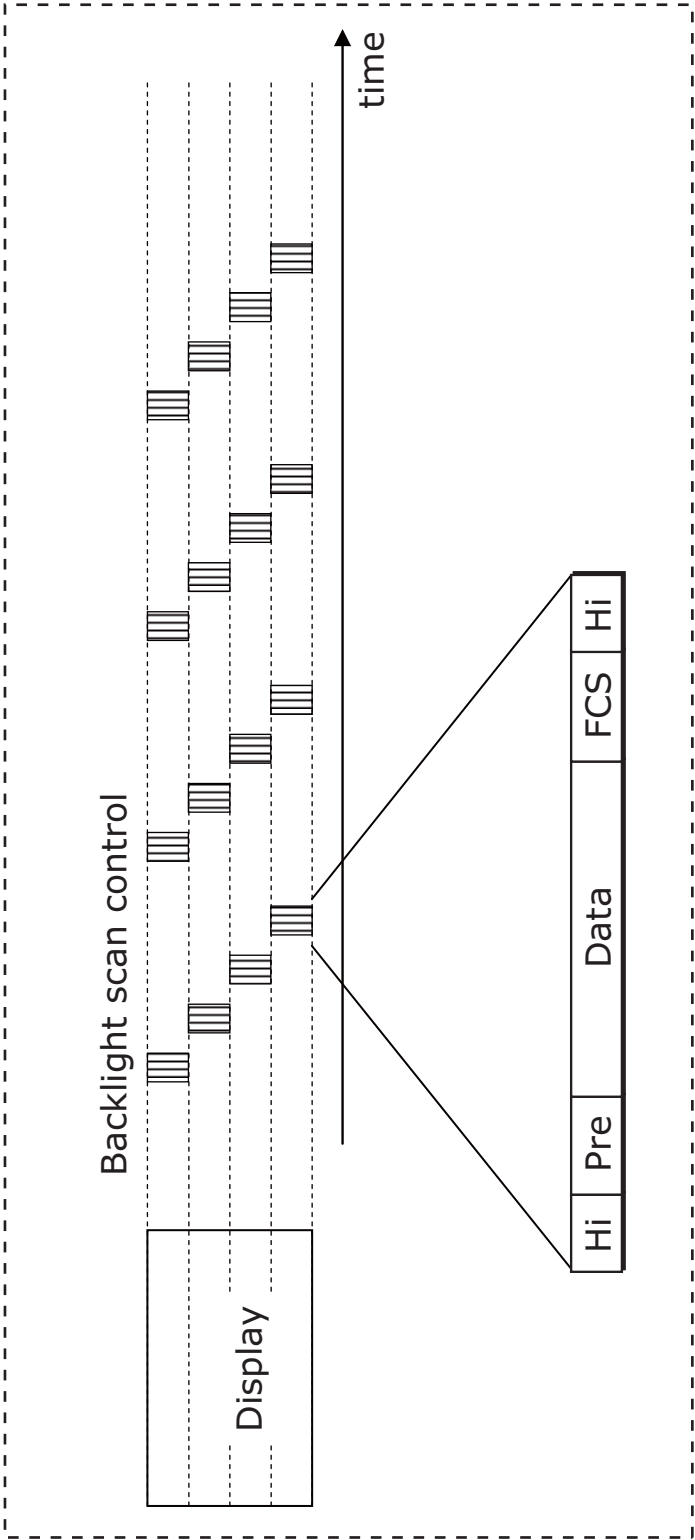


FIG. 173

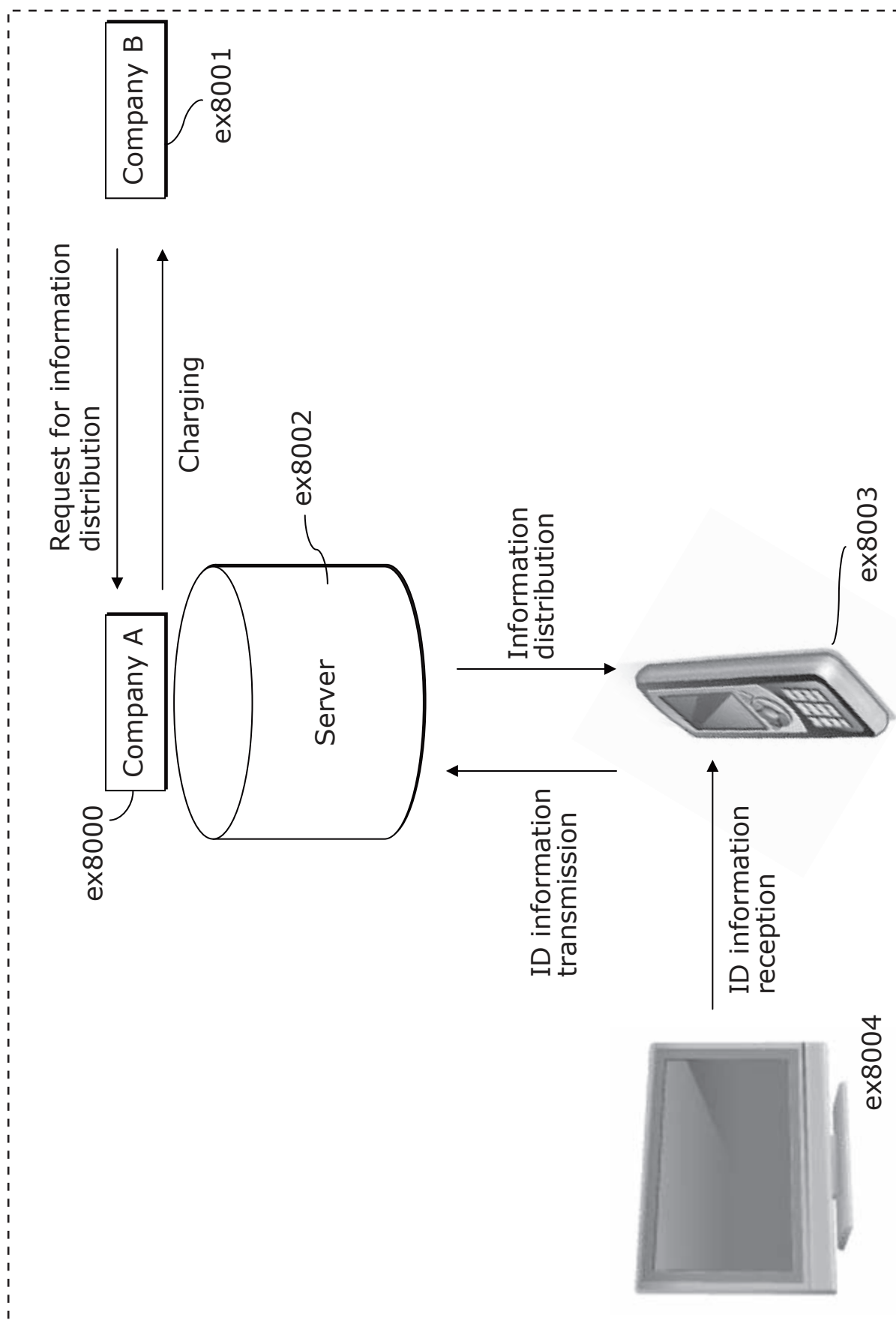


FIG. 174

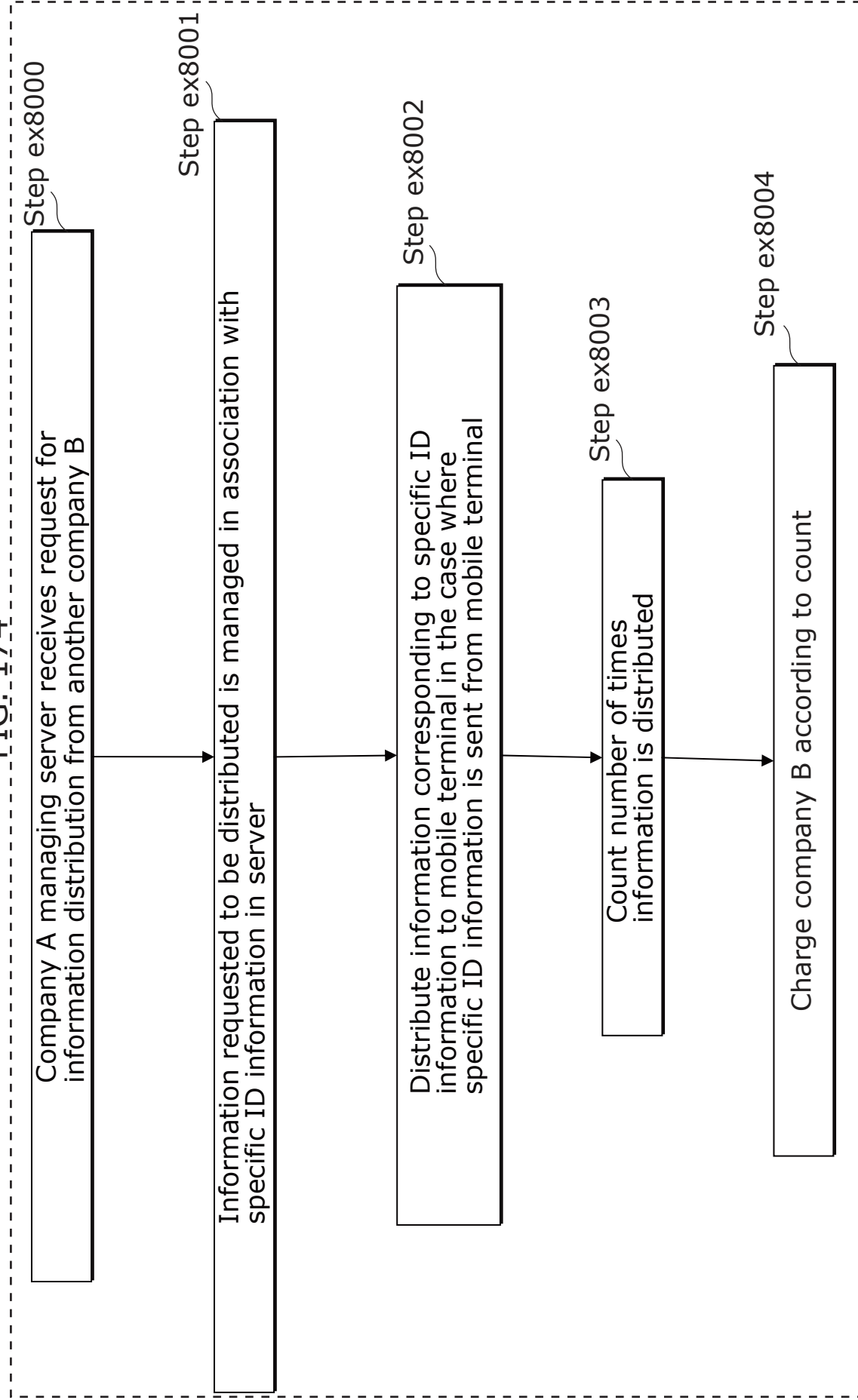


FIG. 175

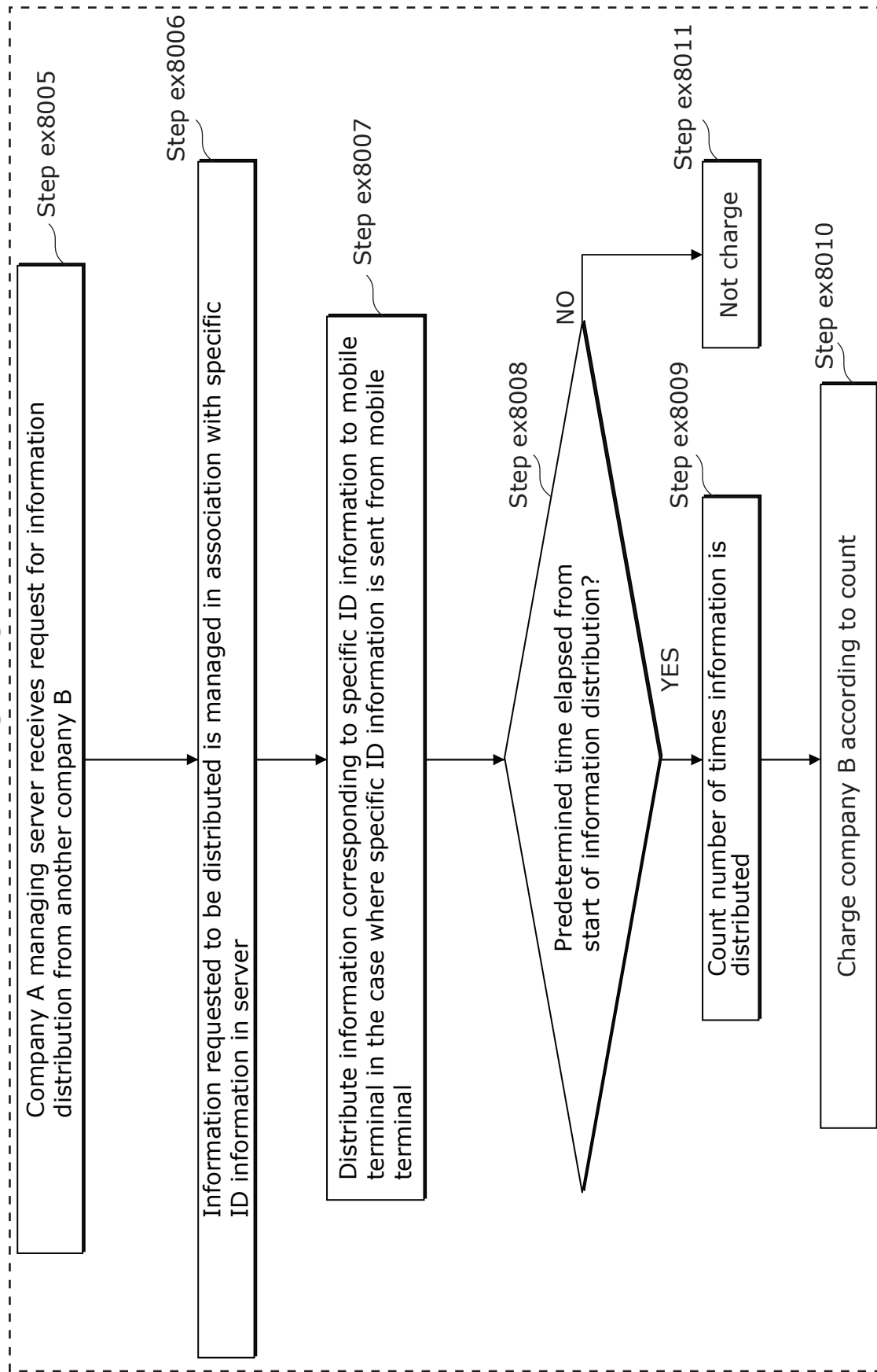


FIG. 176

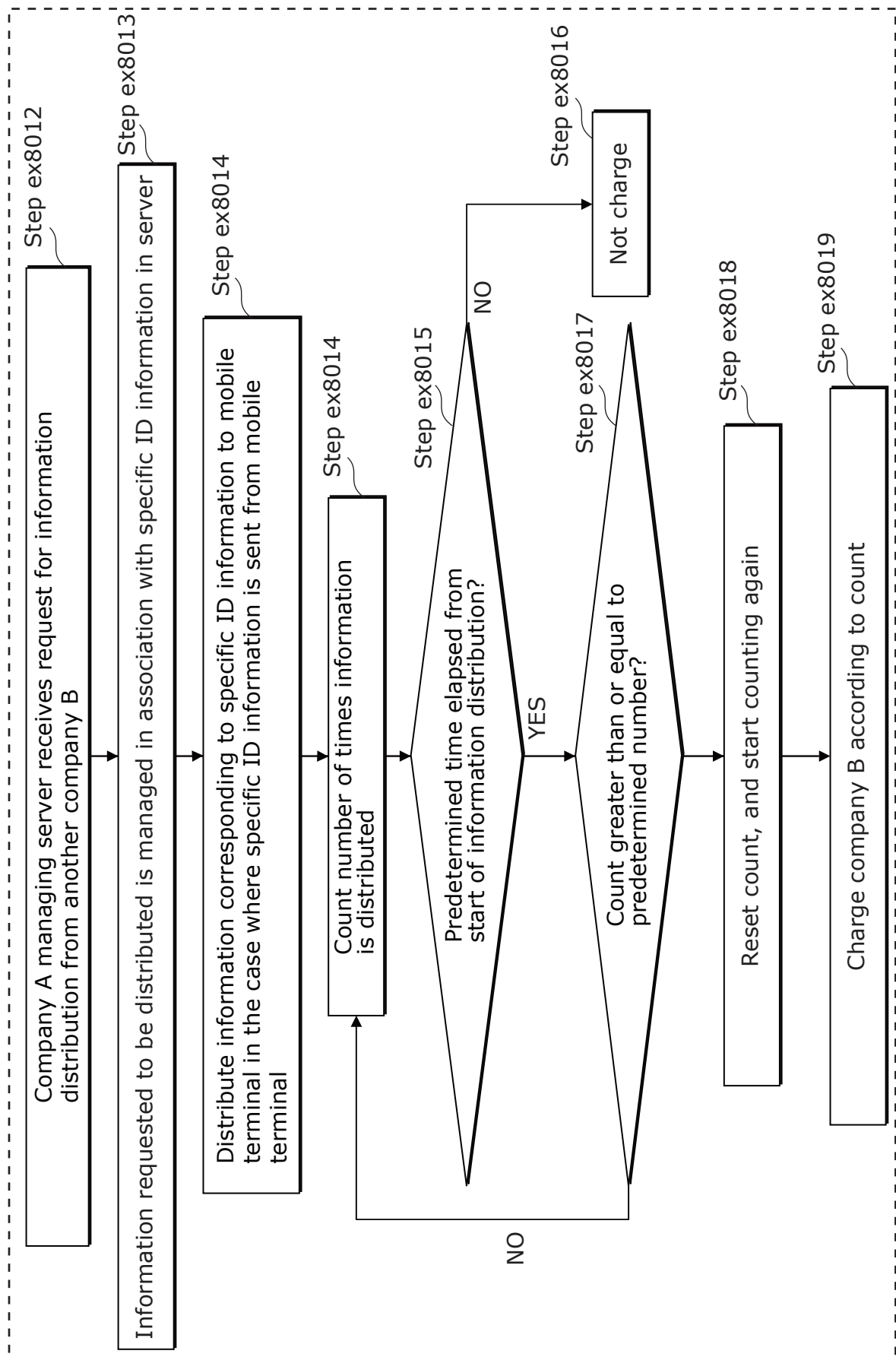


FIG. 177A

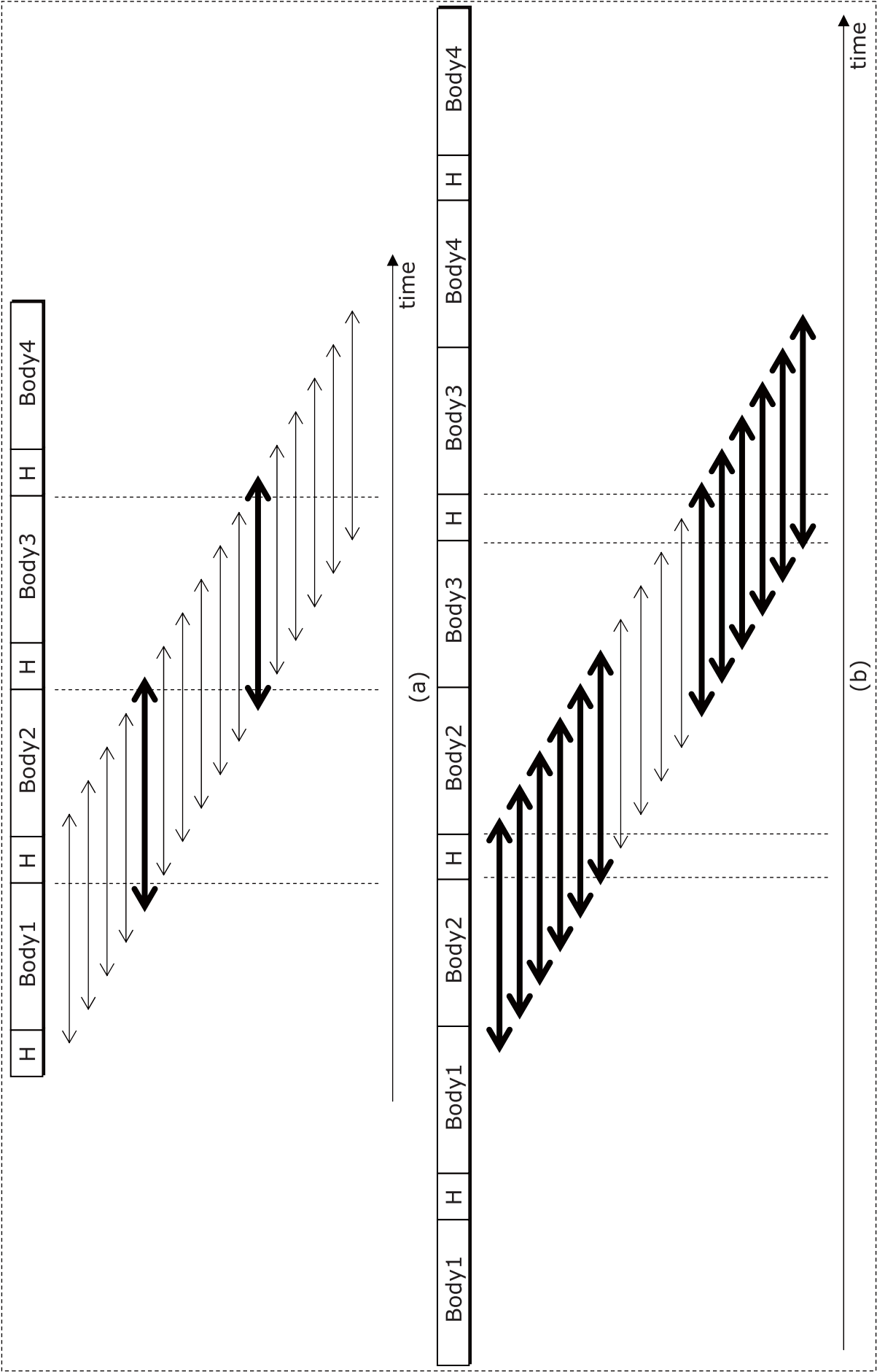


FIG. 177B

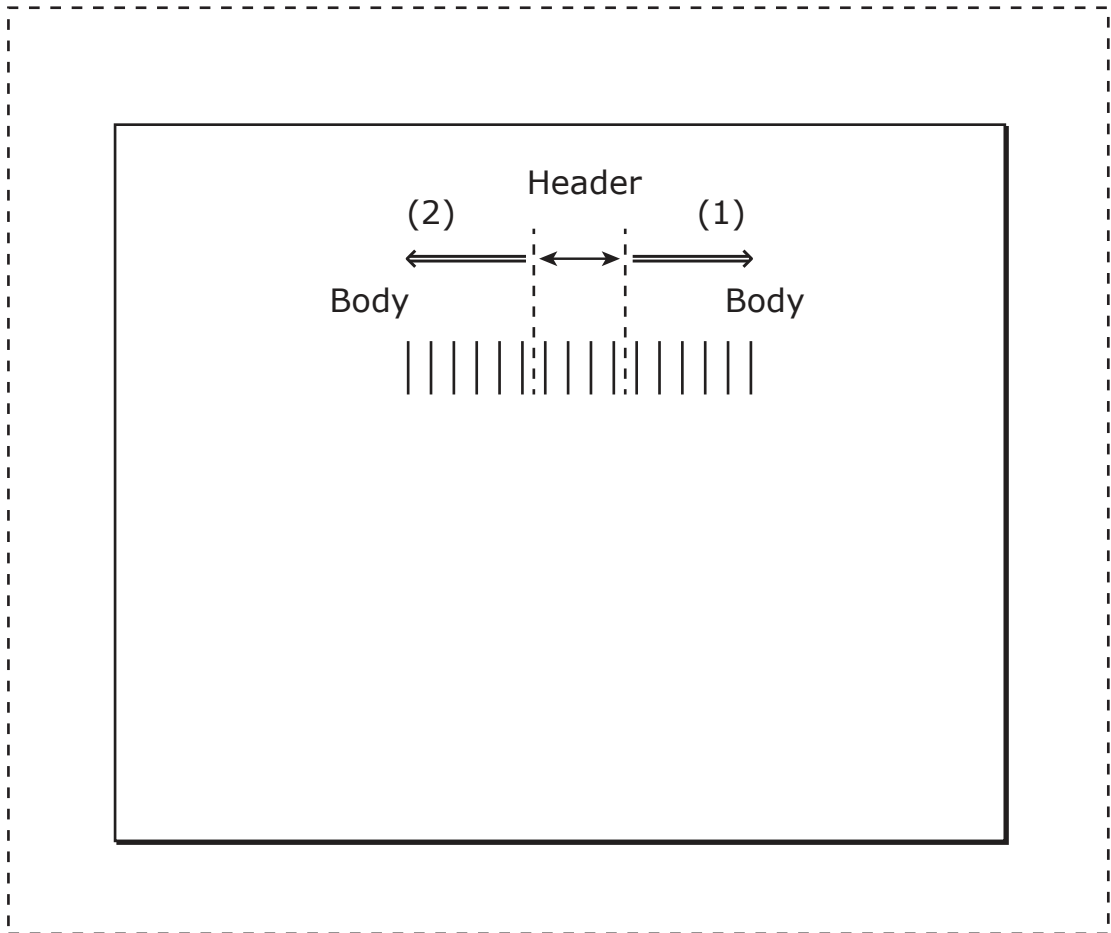


FIG. 178

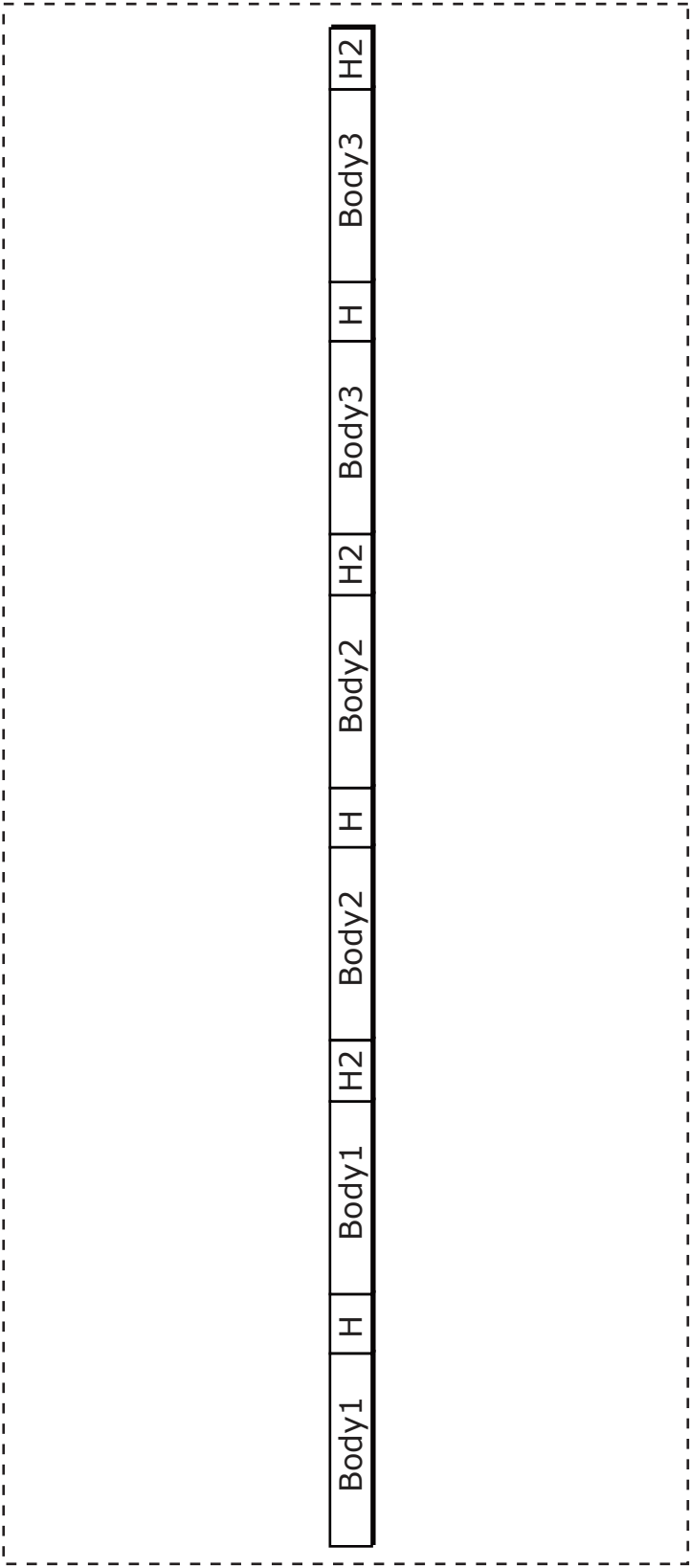


FIG. 179



FIG. 180A

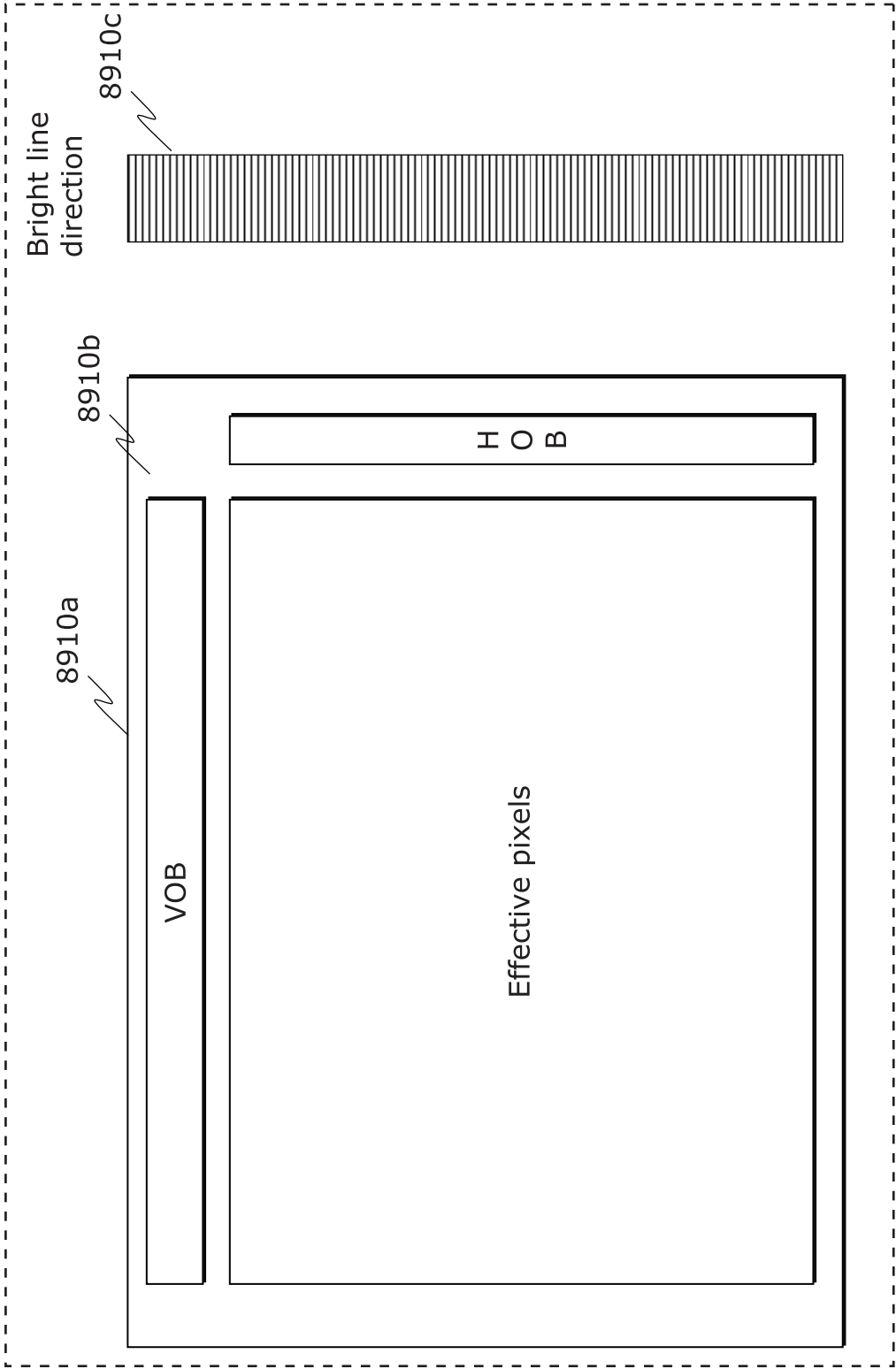


FIG. 180B

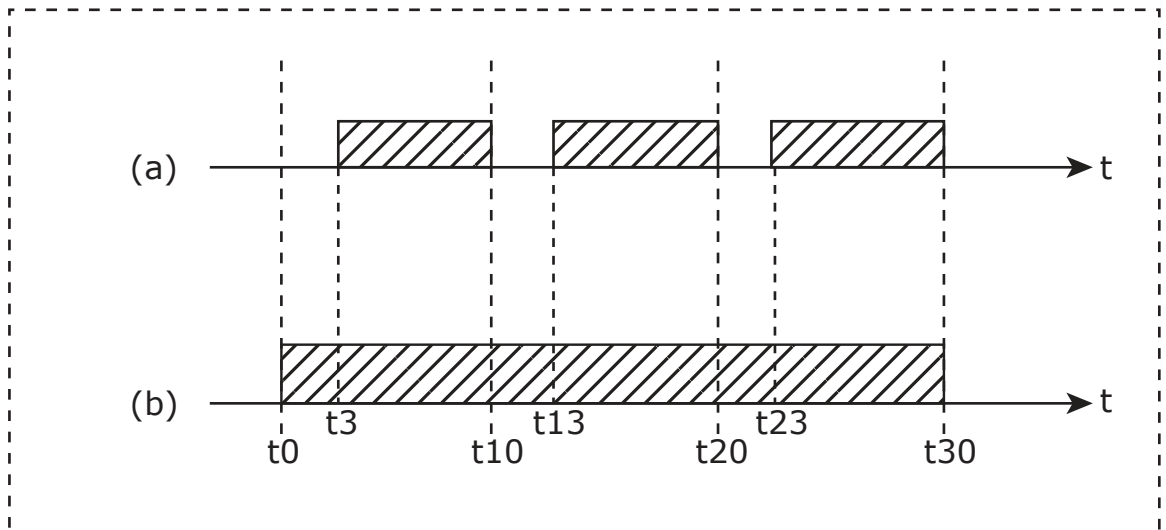


FIG. 180C

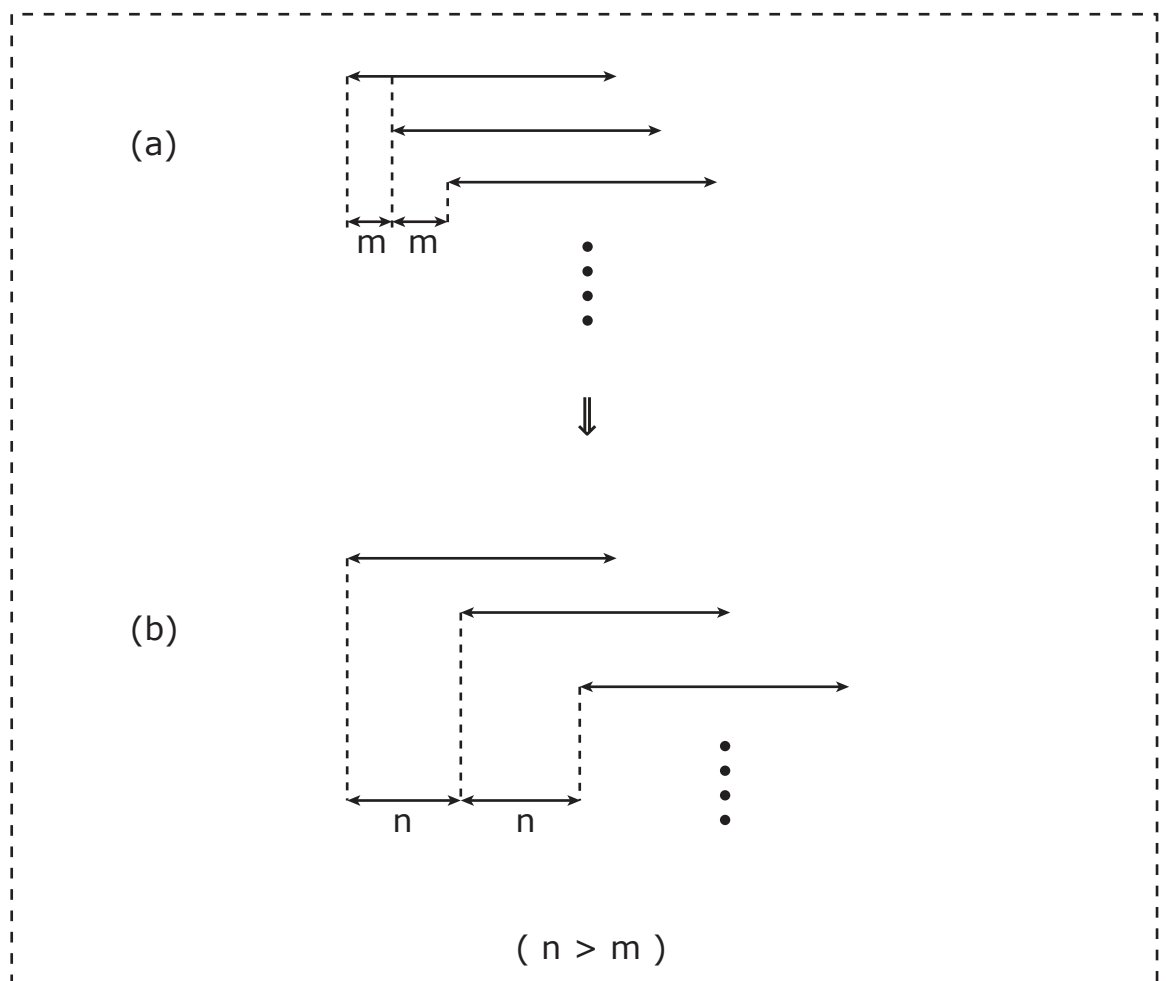


FIG. 180D

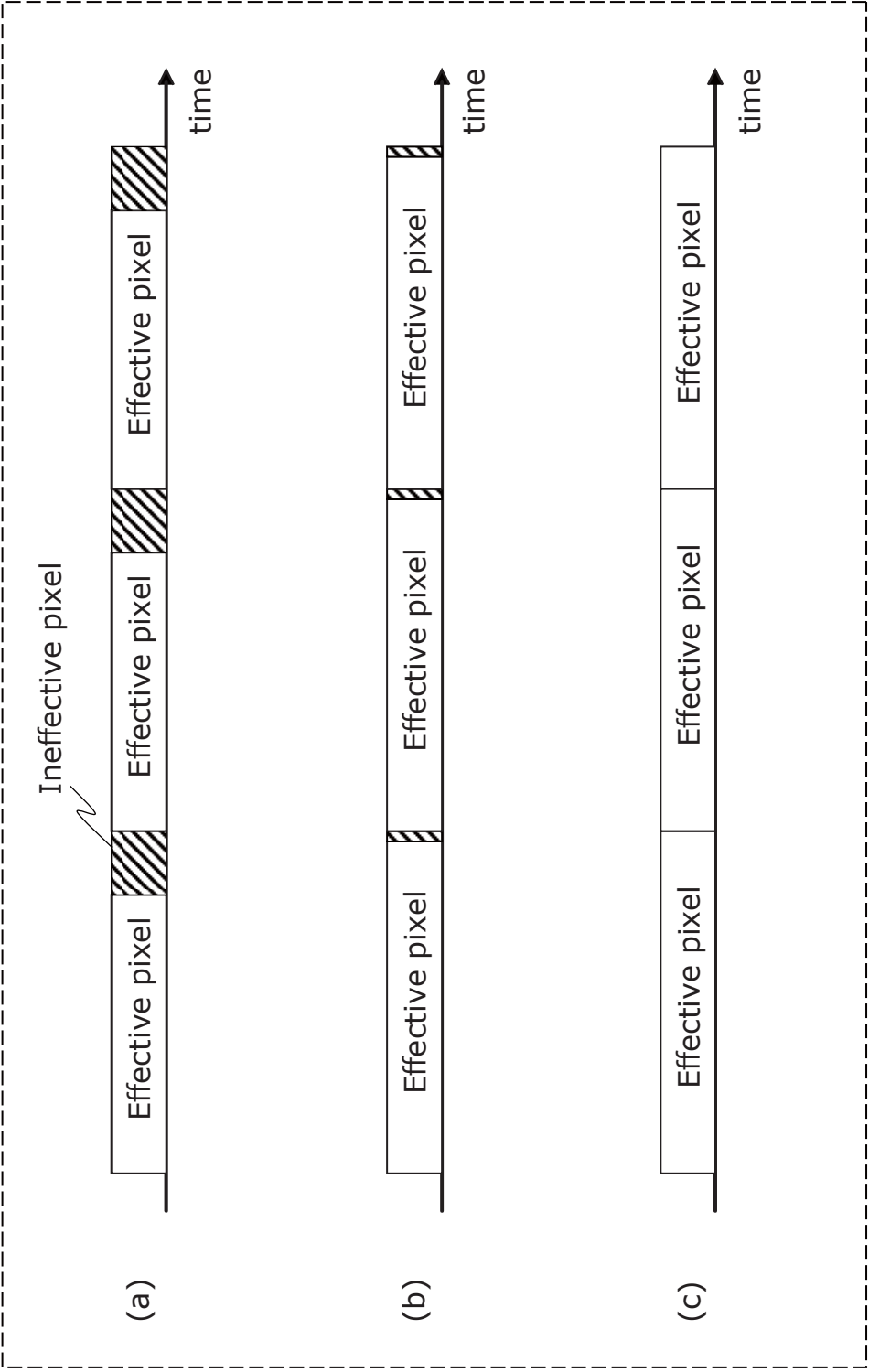


FIG. 180E

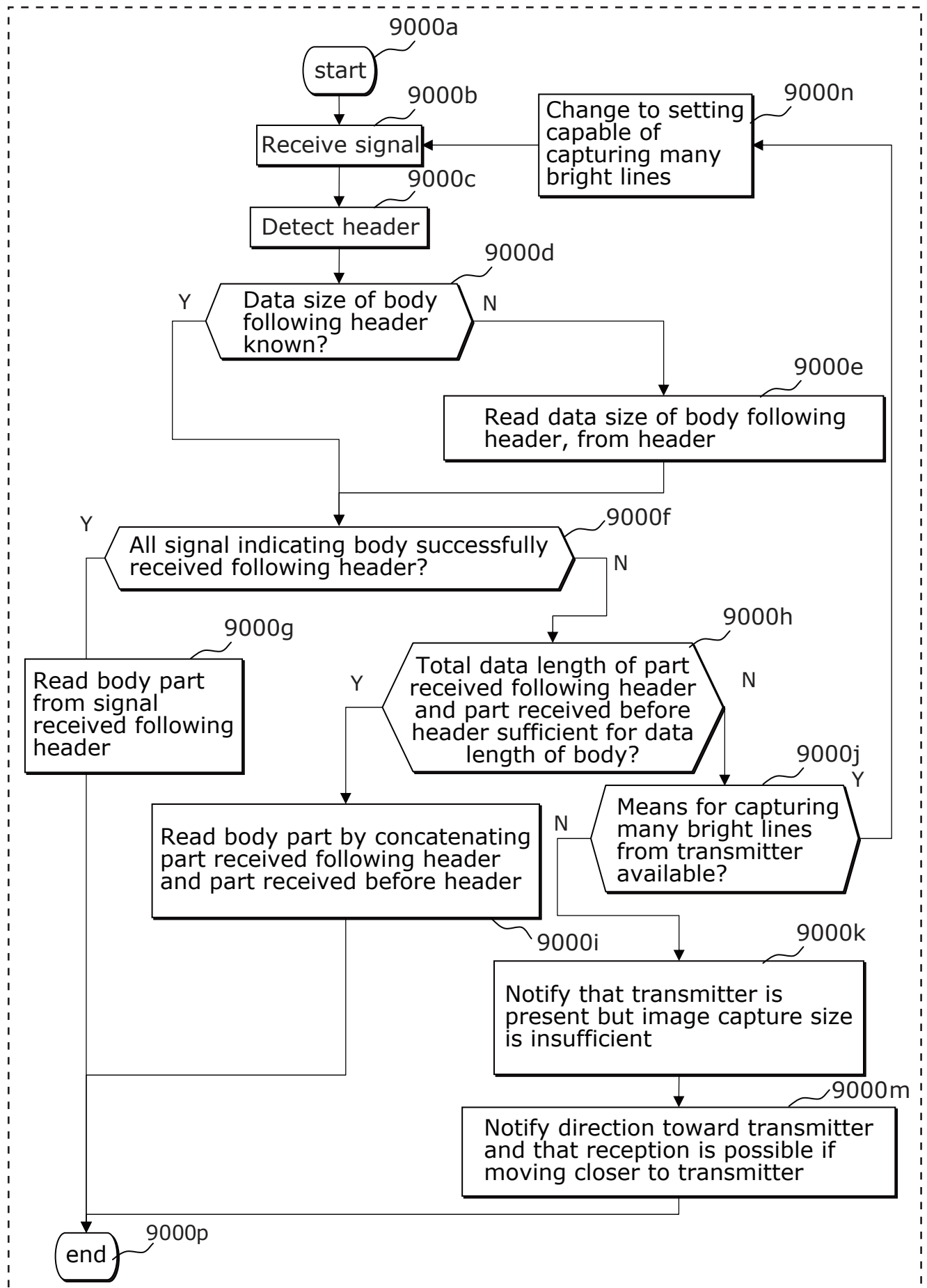


FIG. 181

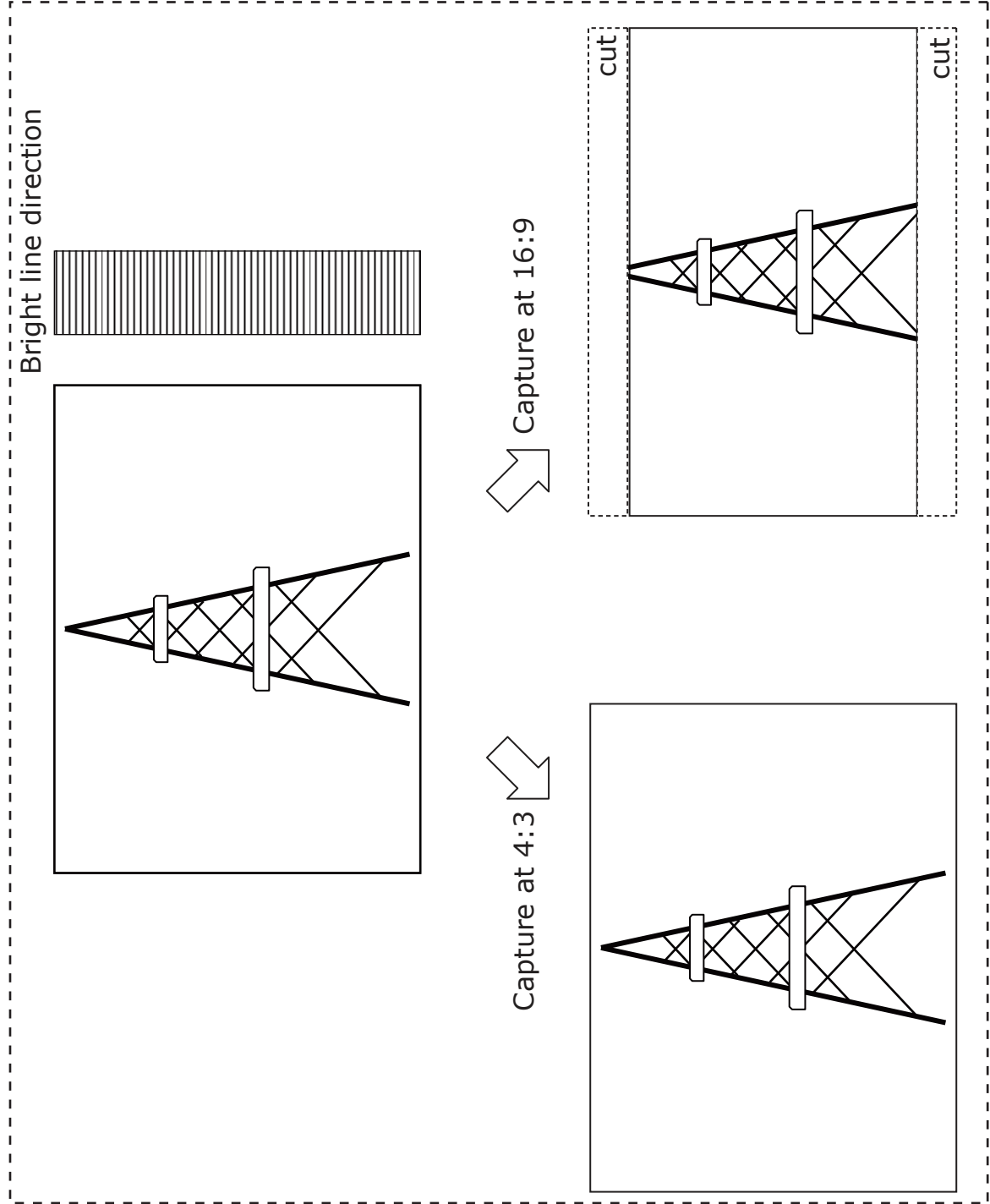


FIG. 182A

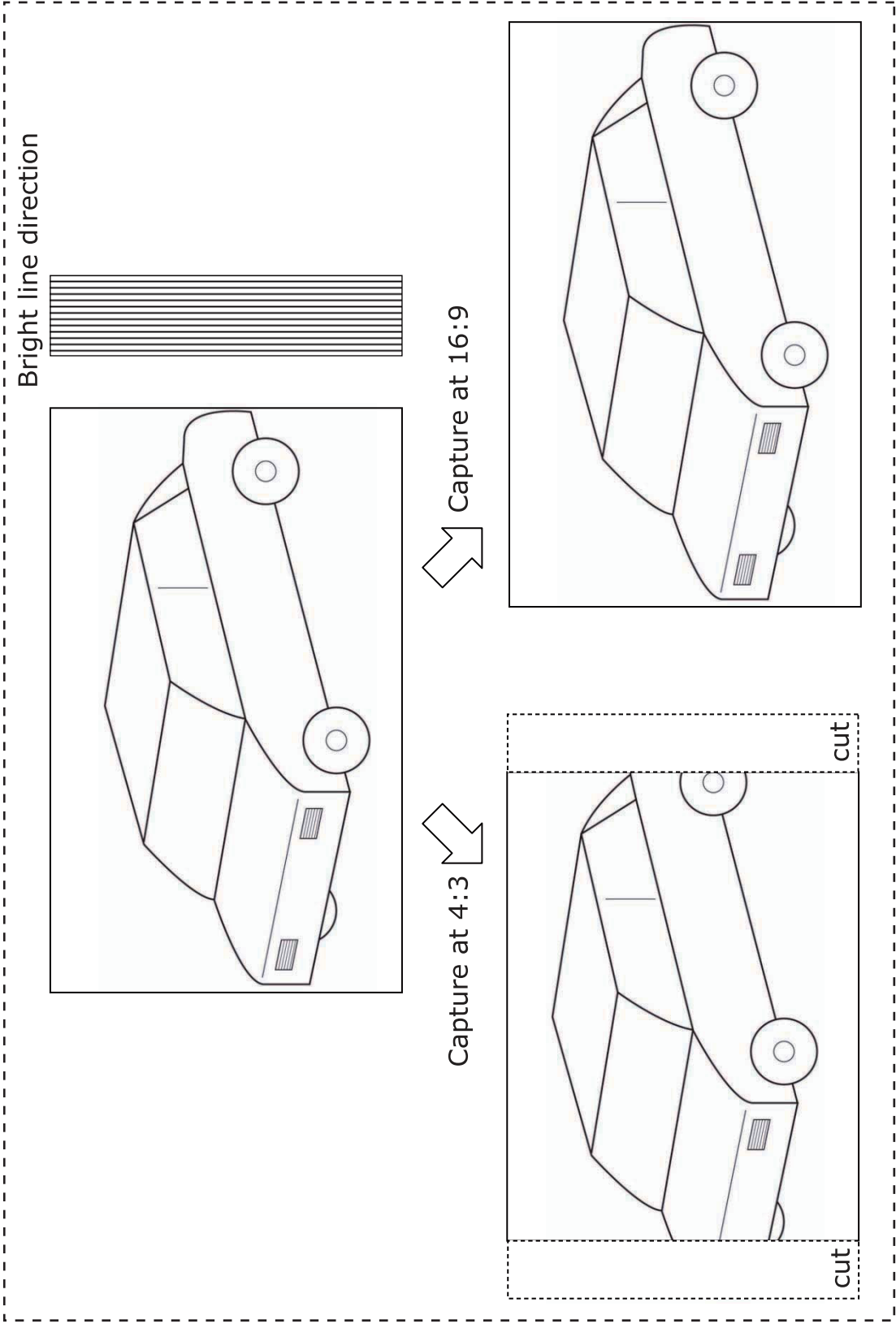


FIG. 182B

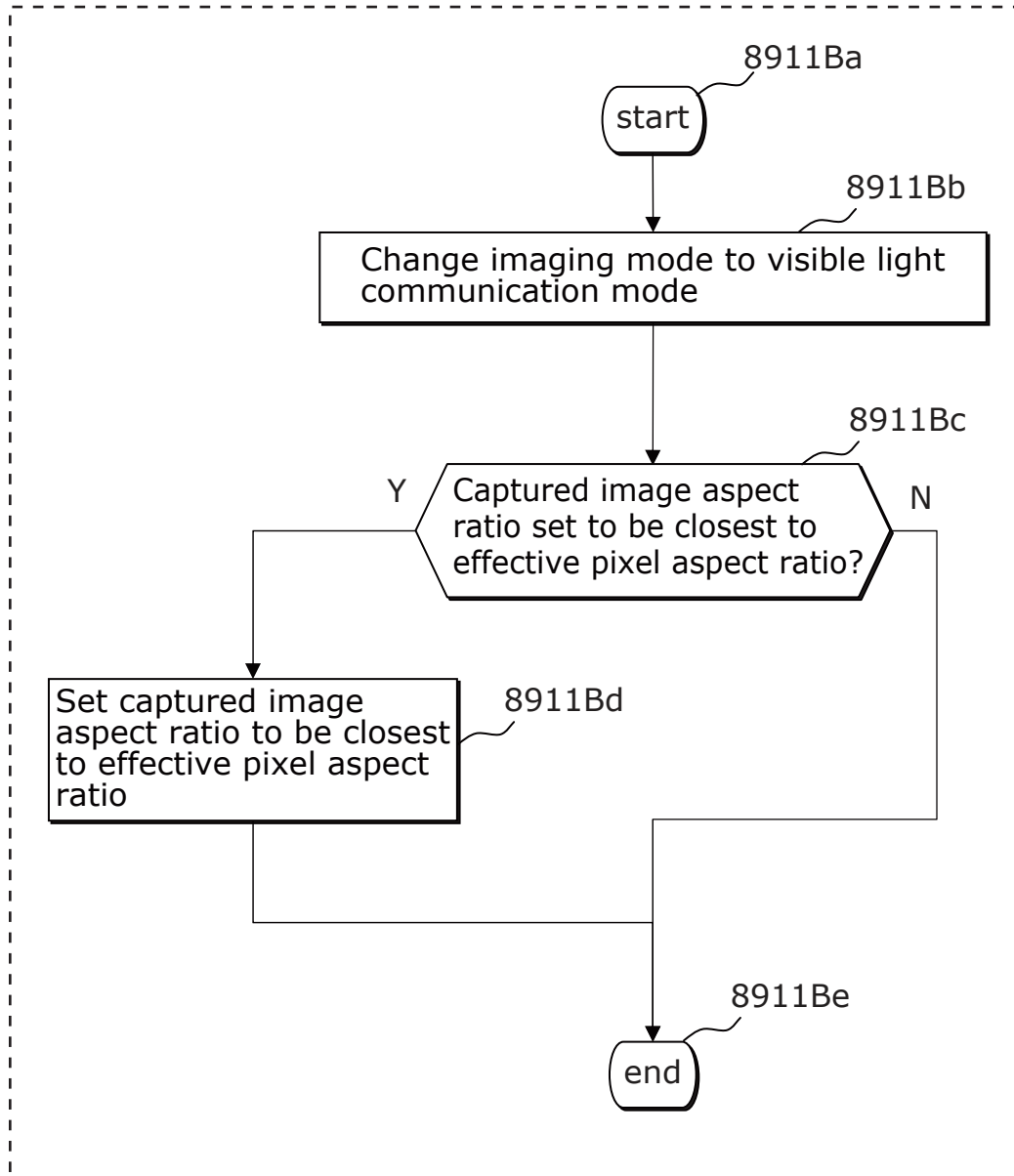


FIG. 182C

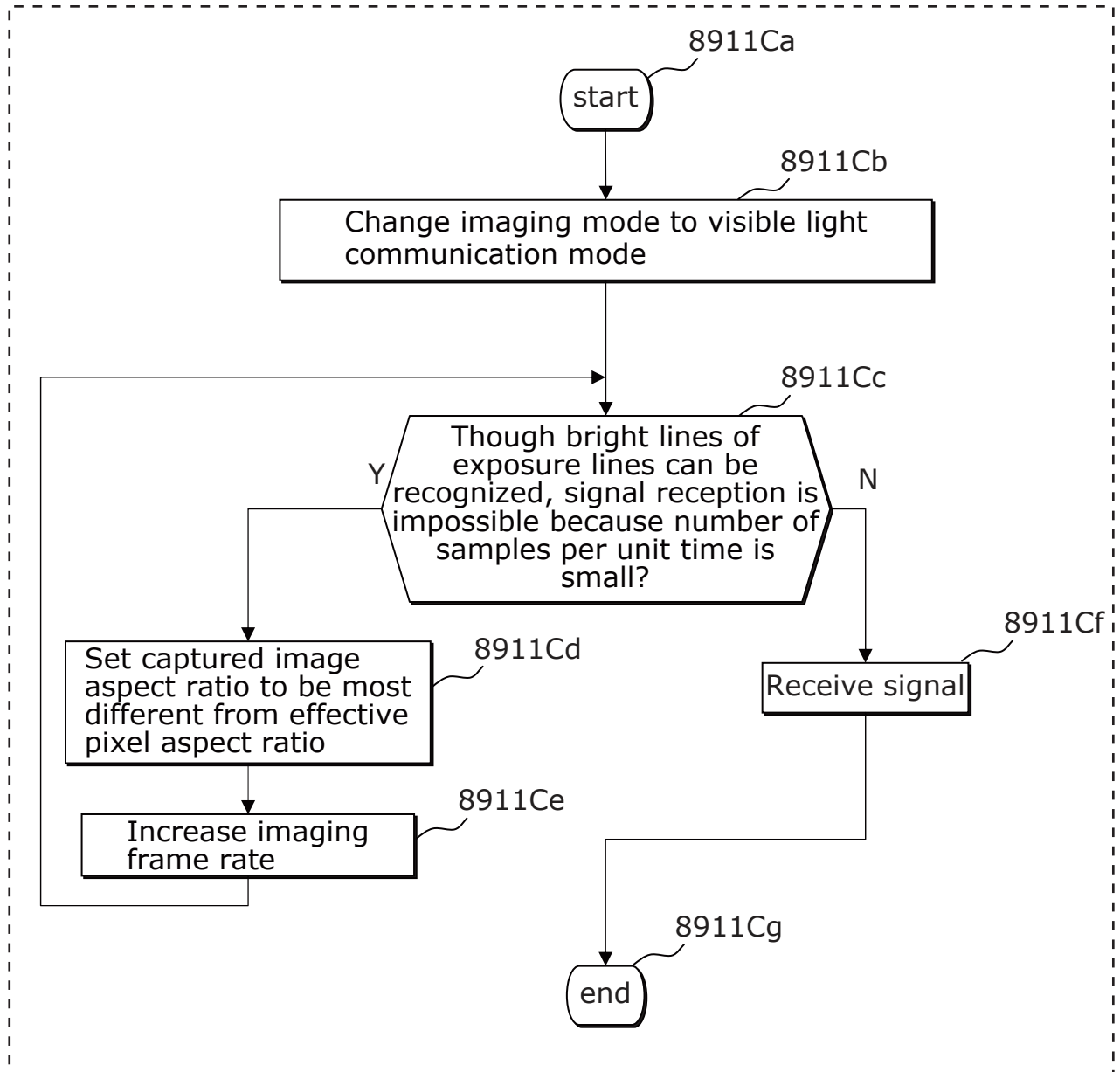


FIG. 183

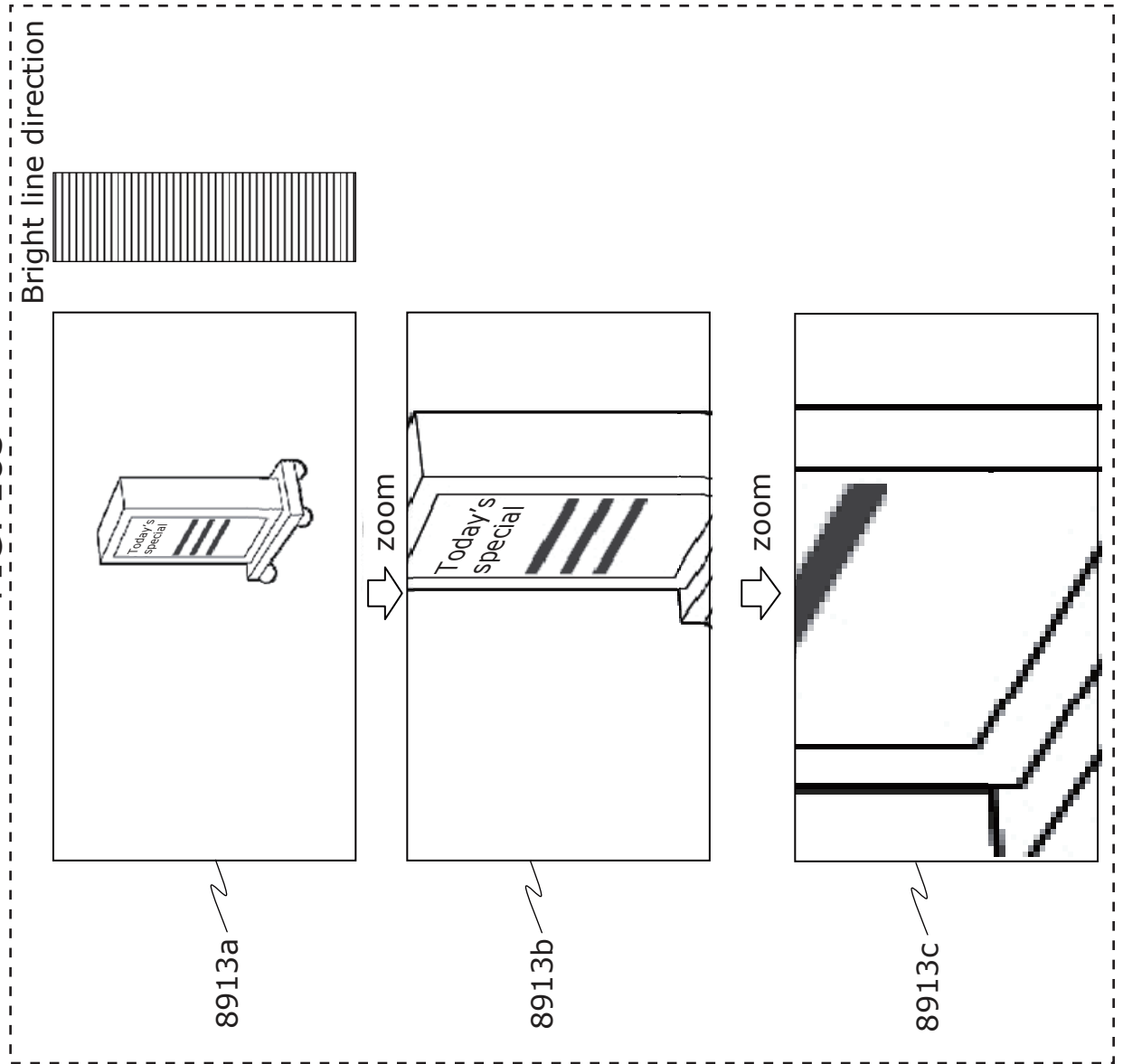


FIG. 184

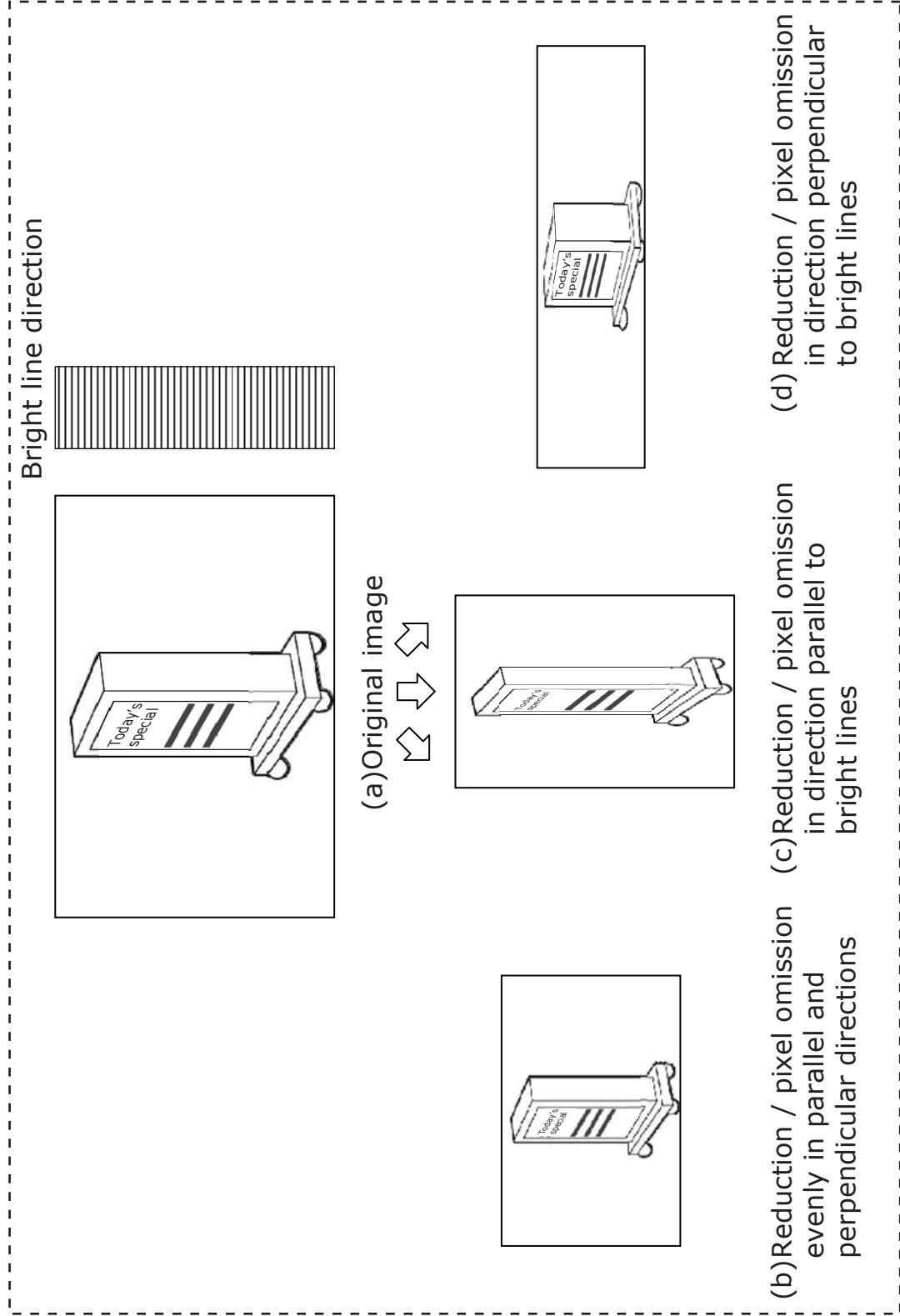


FIG. 185

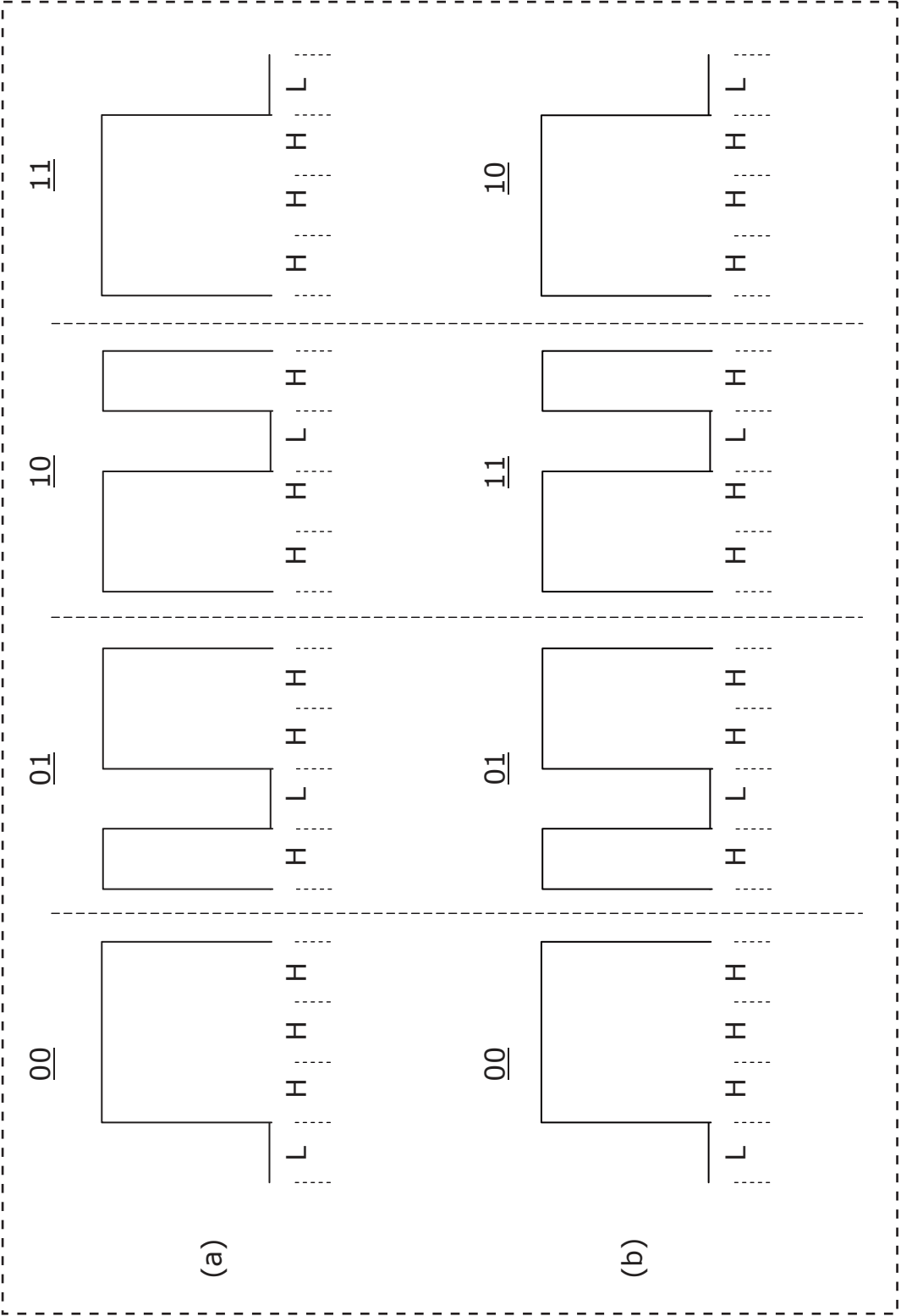


FIG. 186

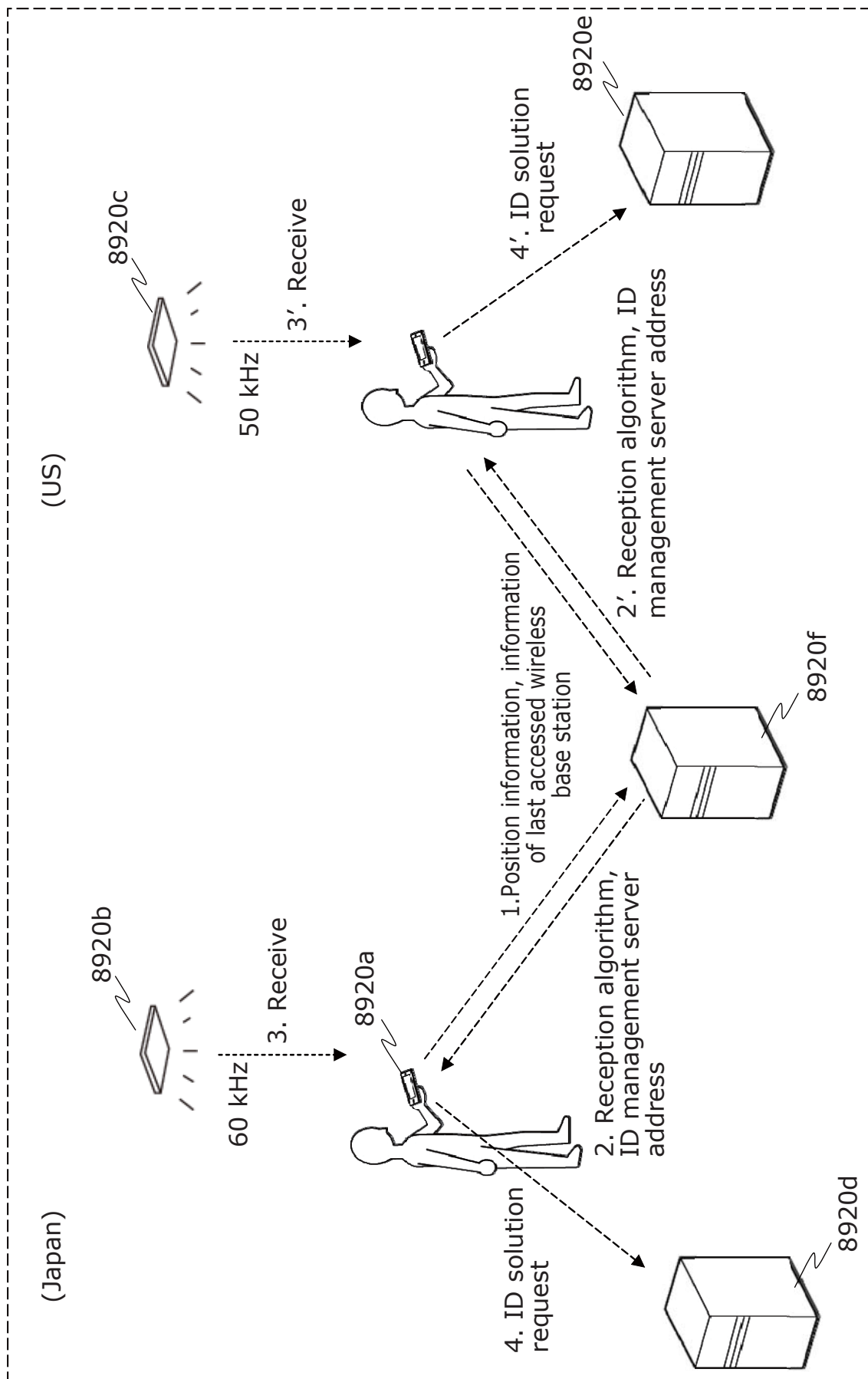


FIG. 187

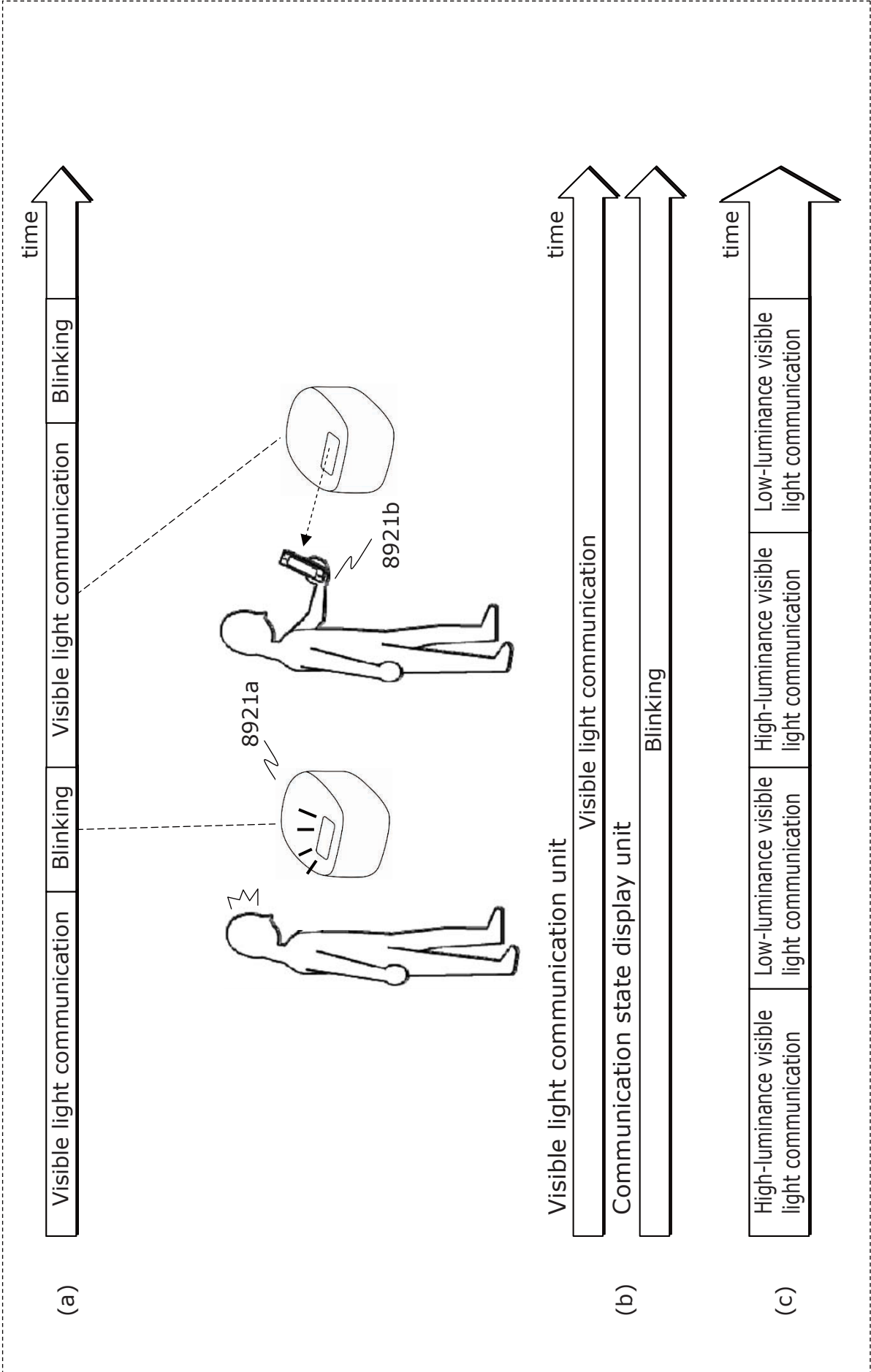


FIG. 188

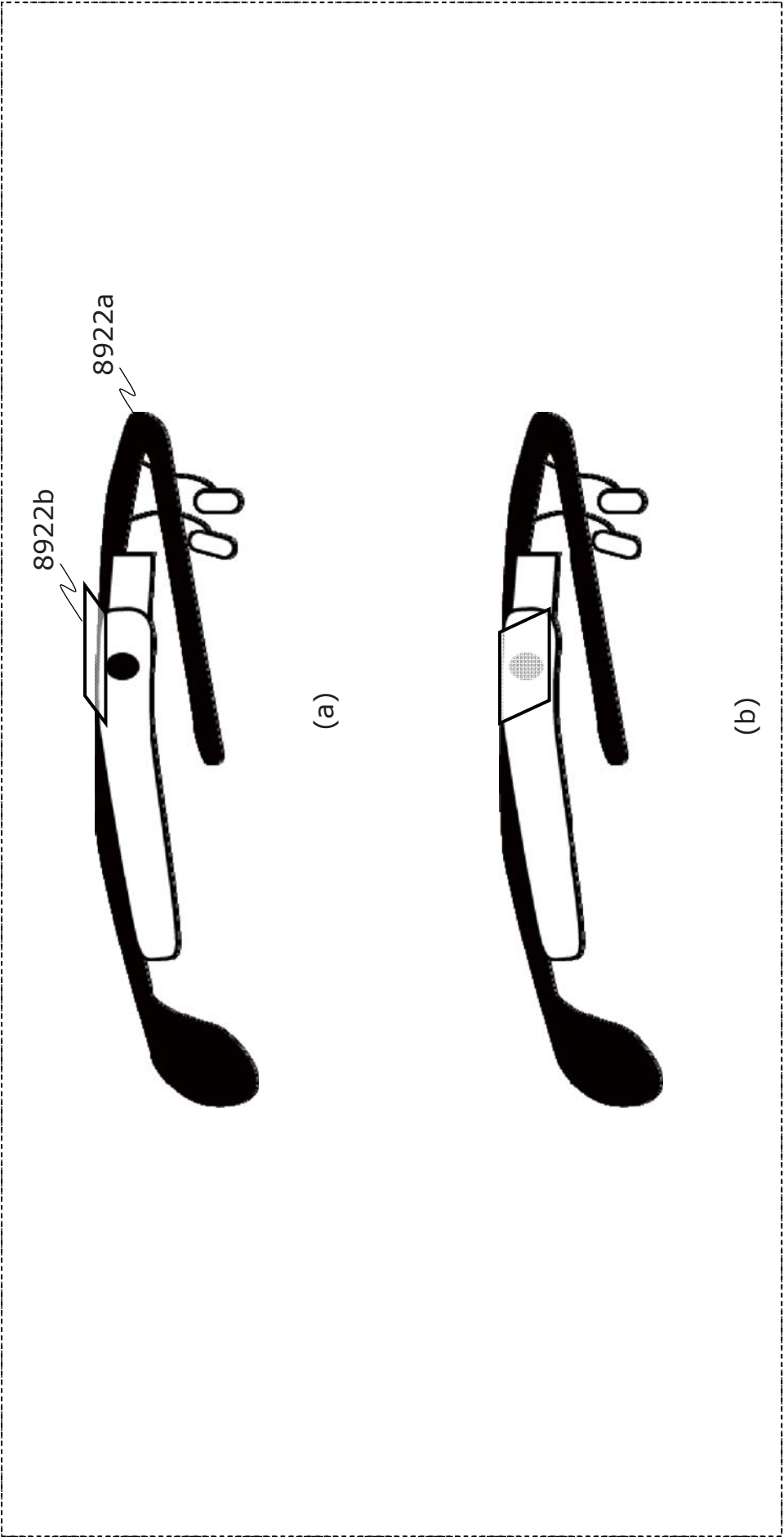


FIG. 189

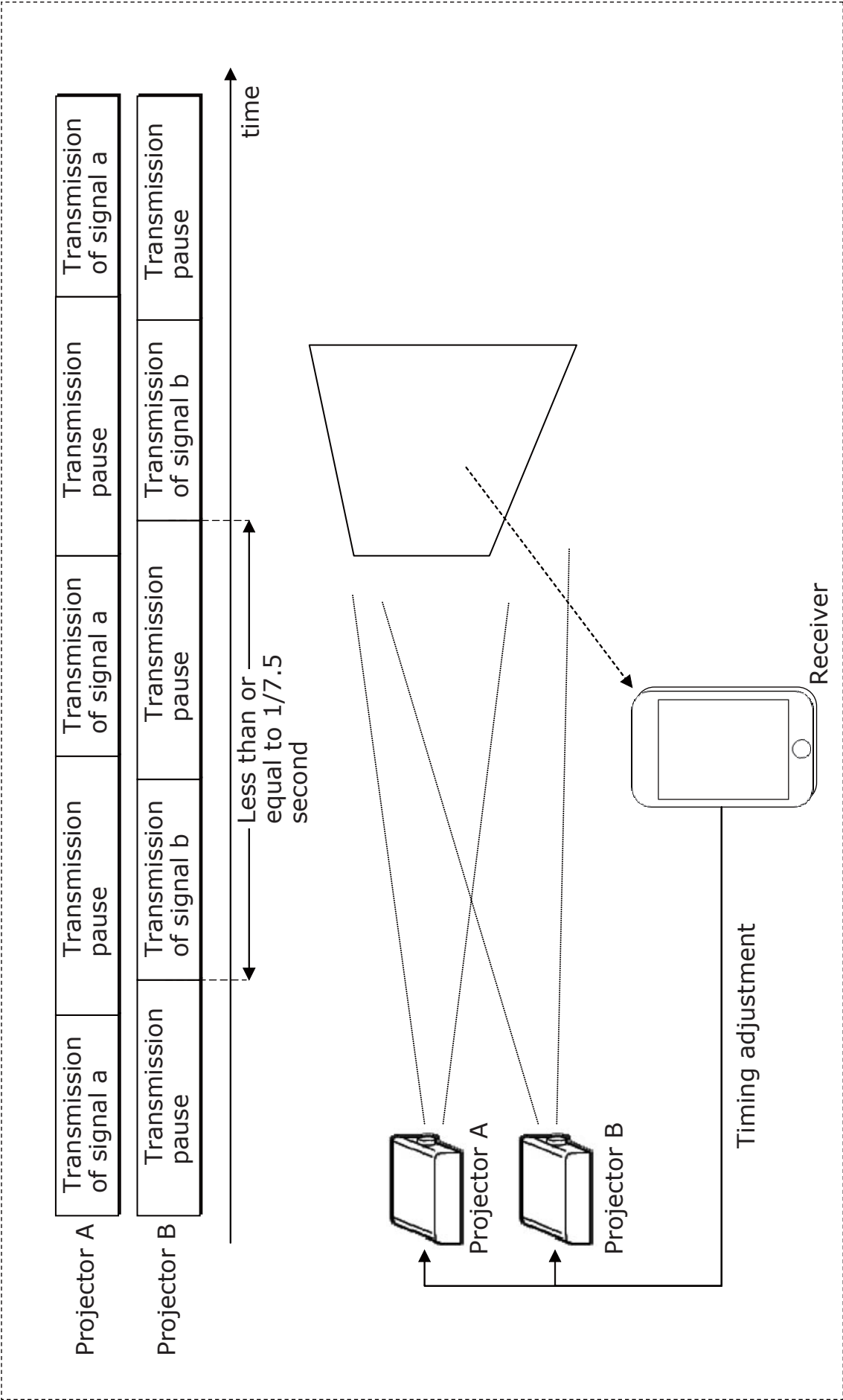


FIG. 190

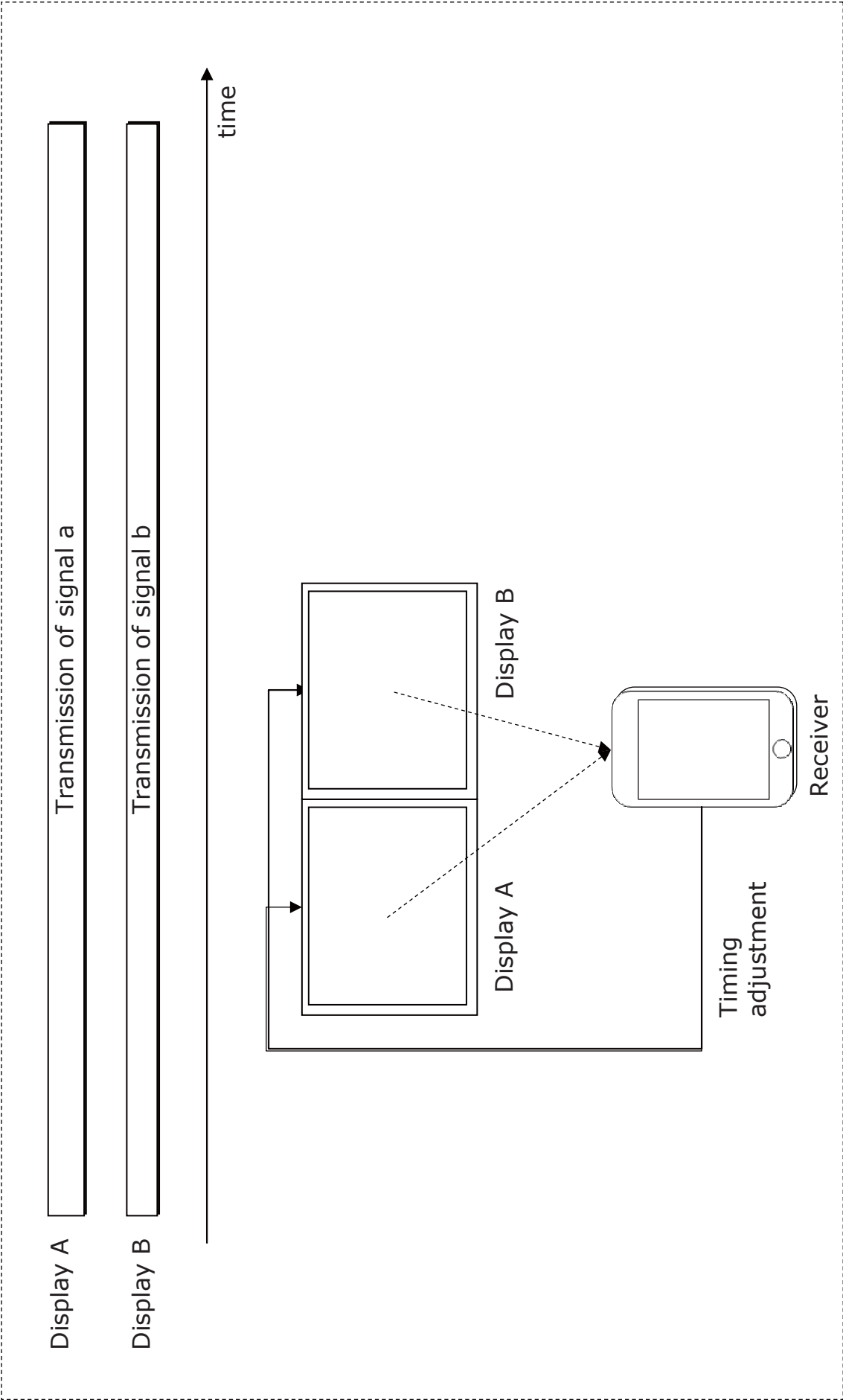


FIG. 191

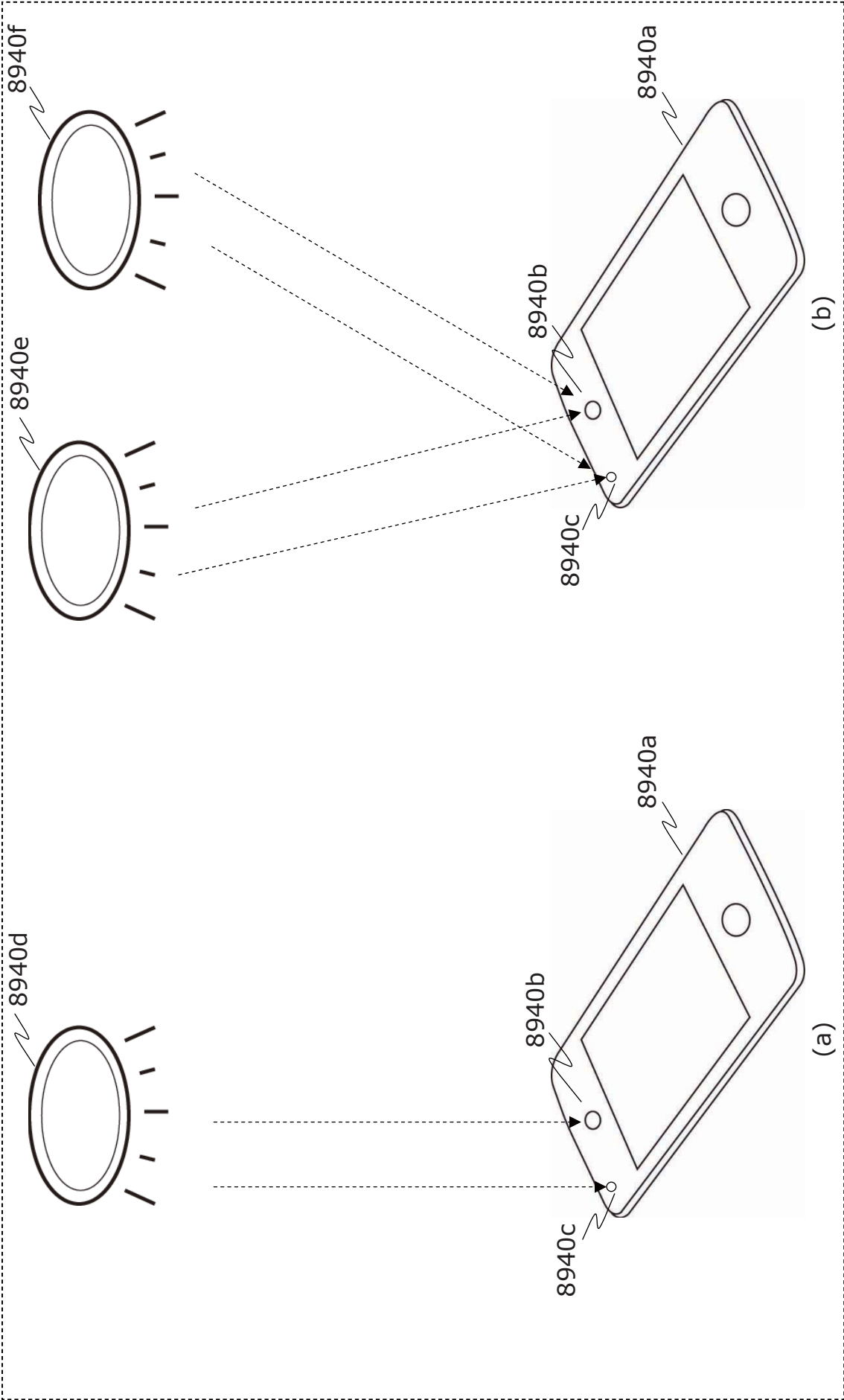


FIG. 192

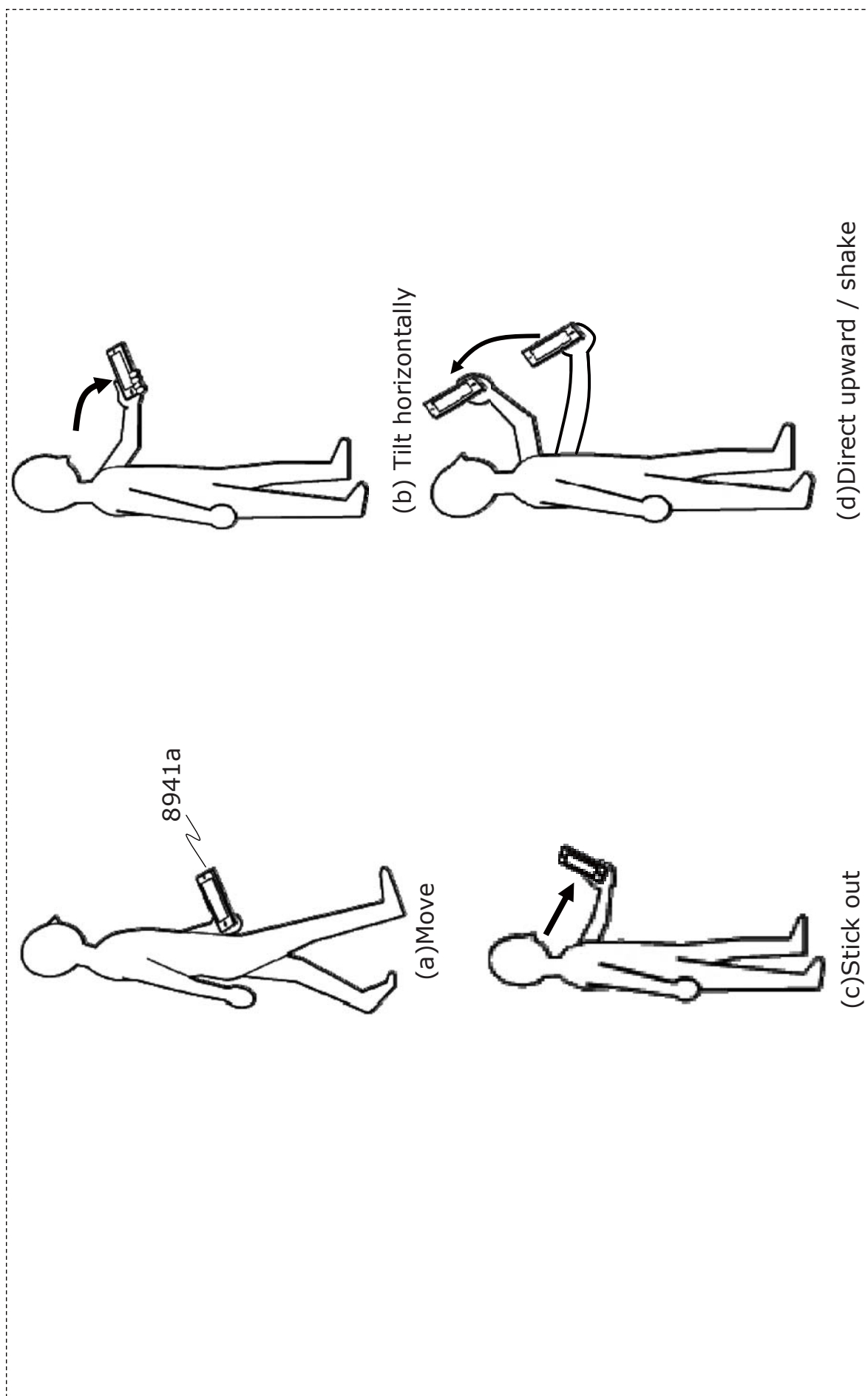


FIG. 193

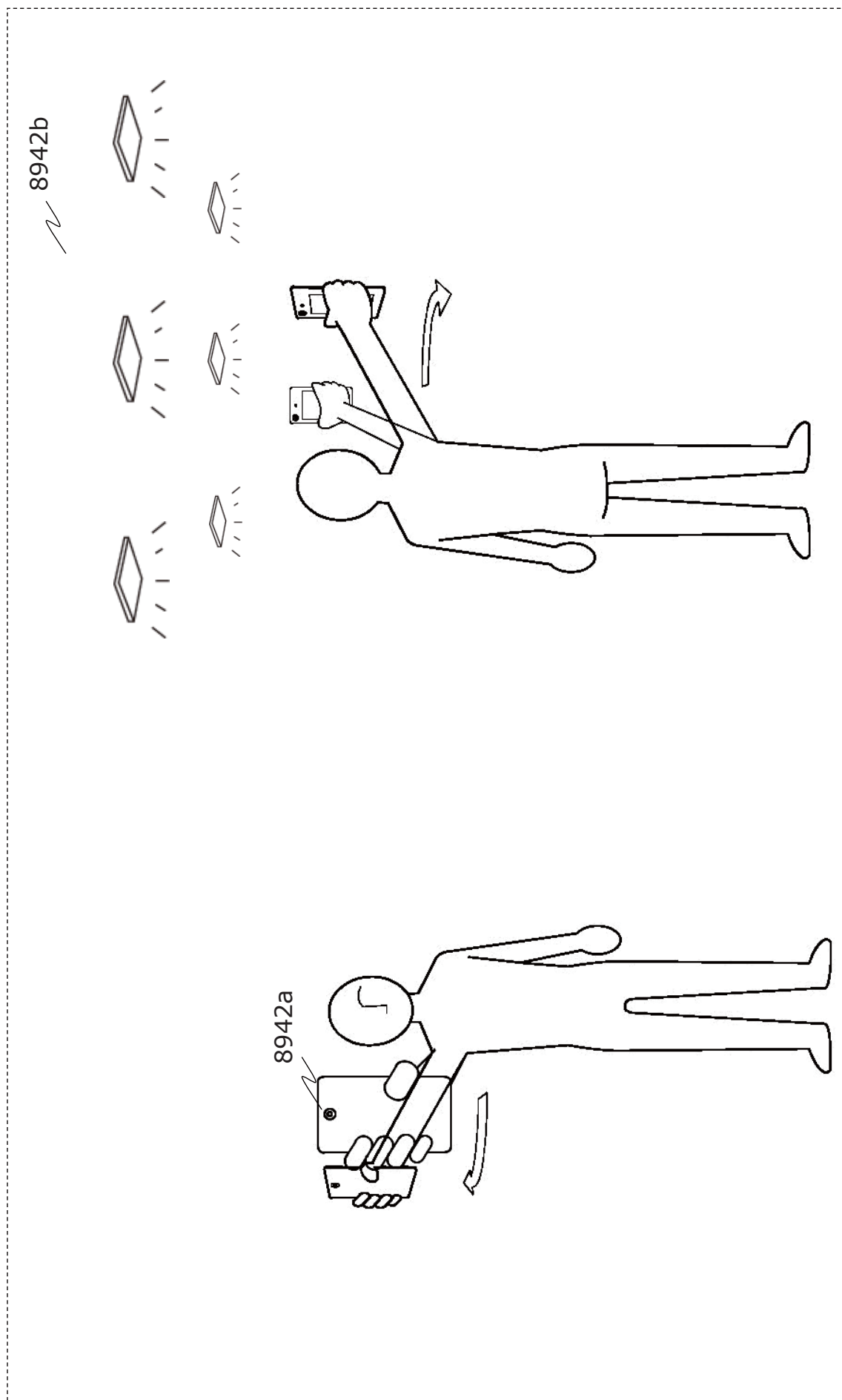


FIG. 194

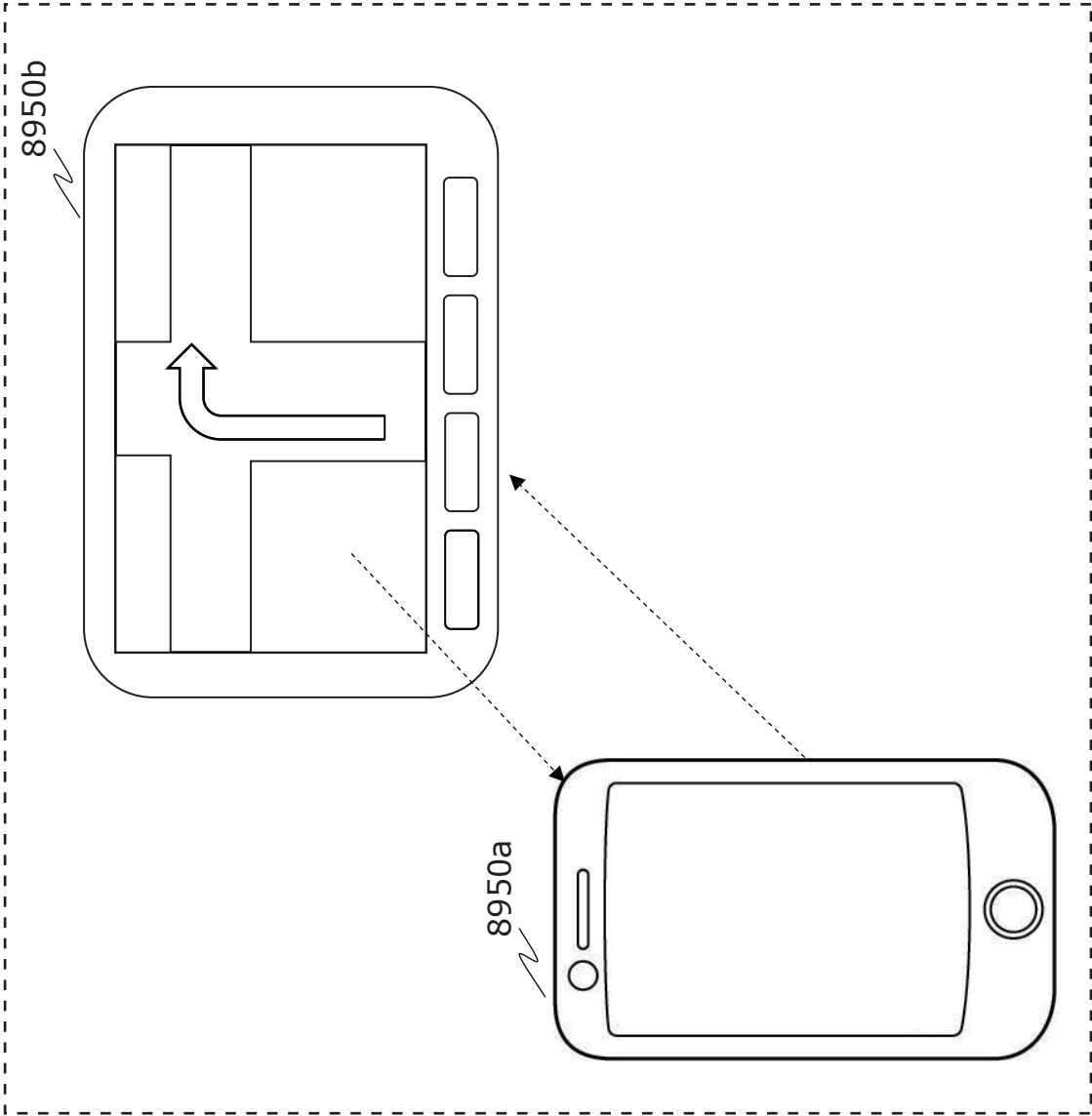


FIG. 195

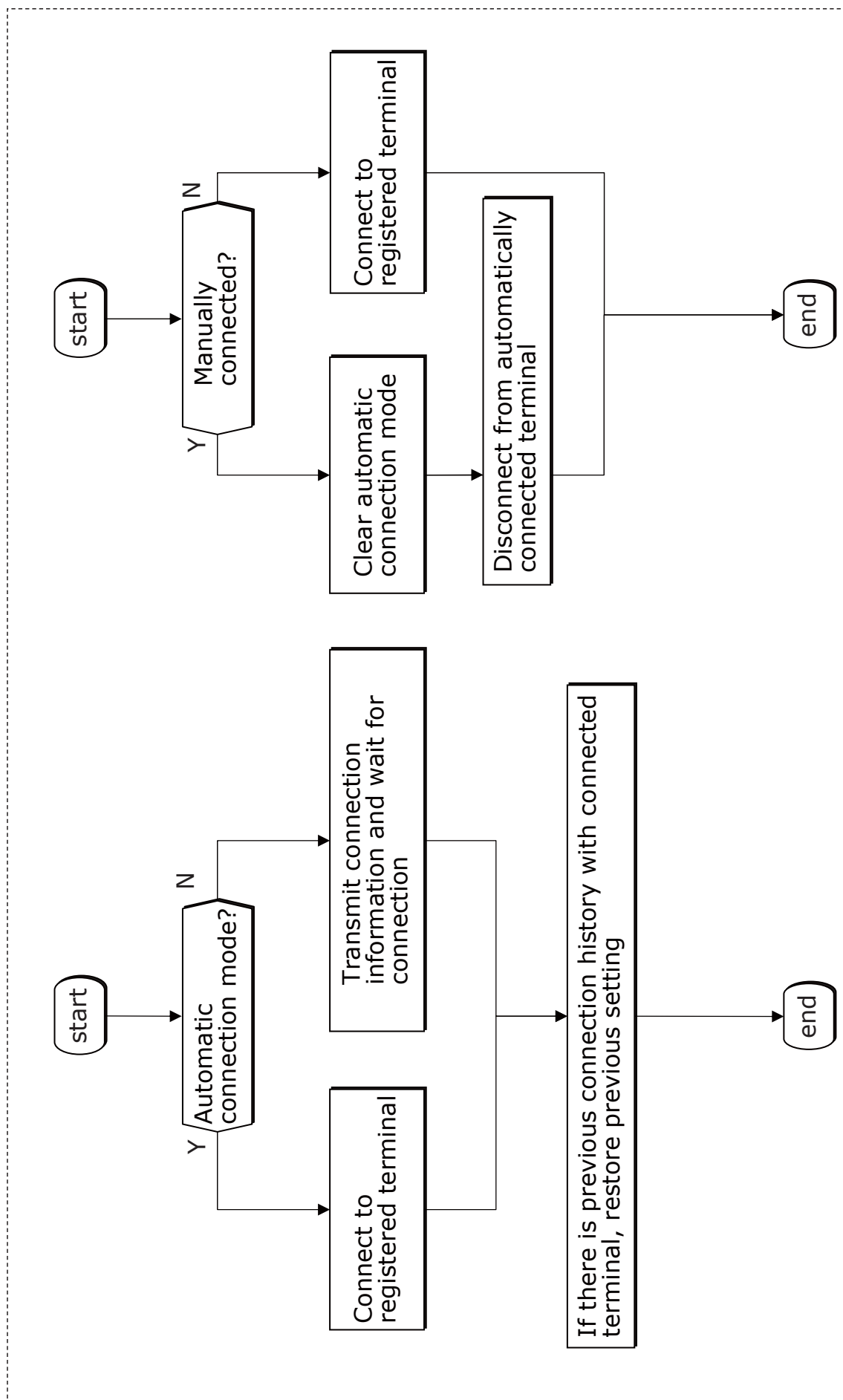


FIG. 196

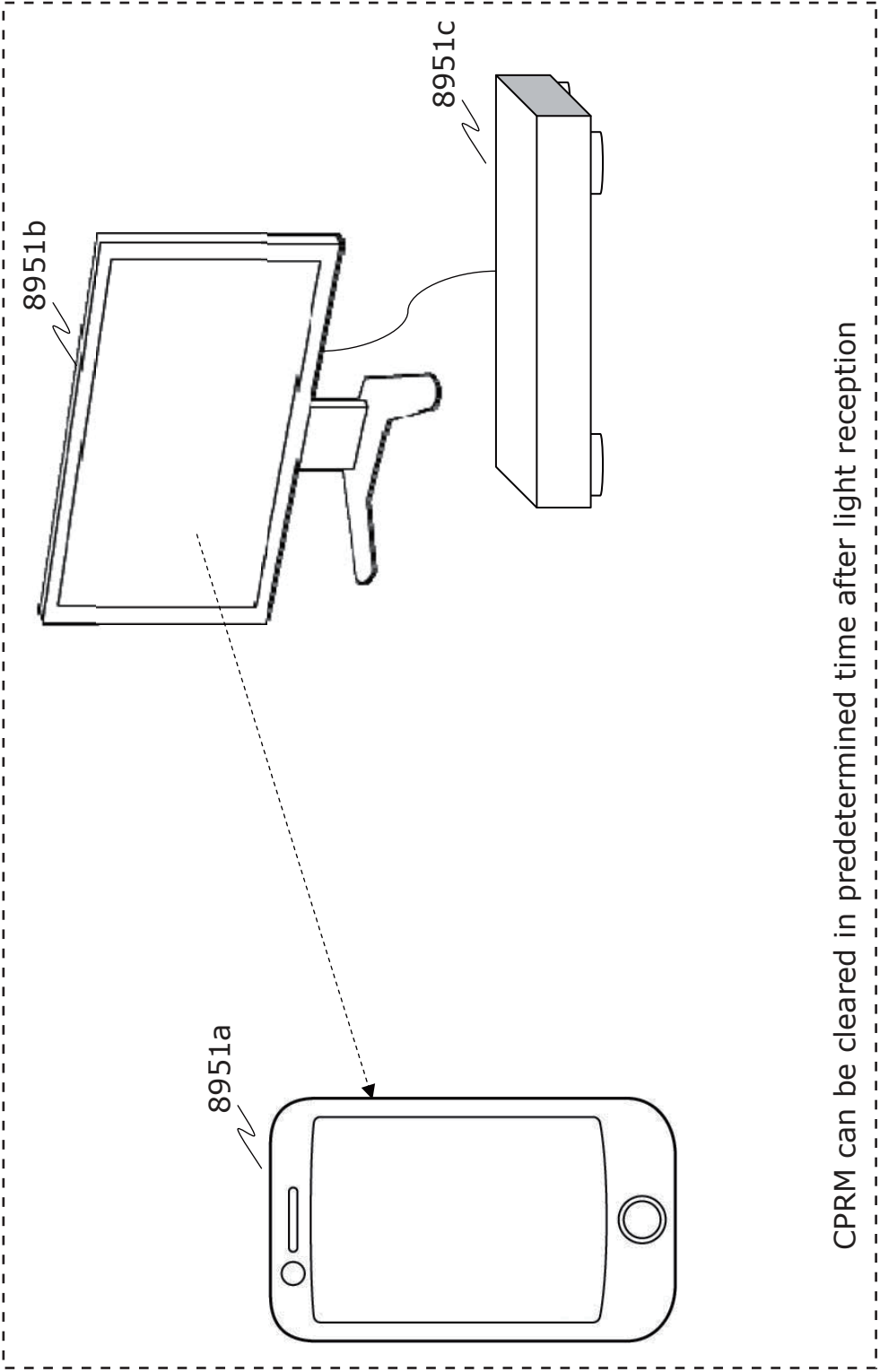


FIG. 197A

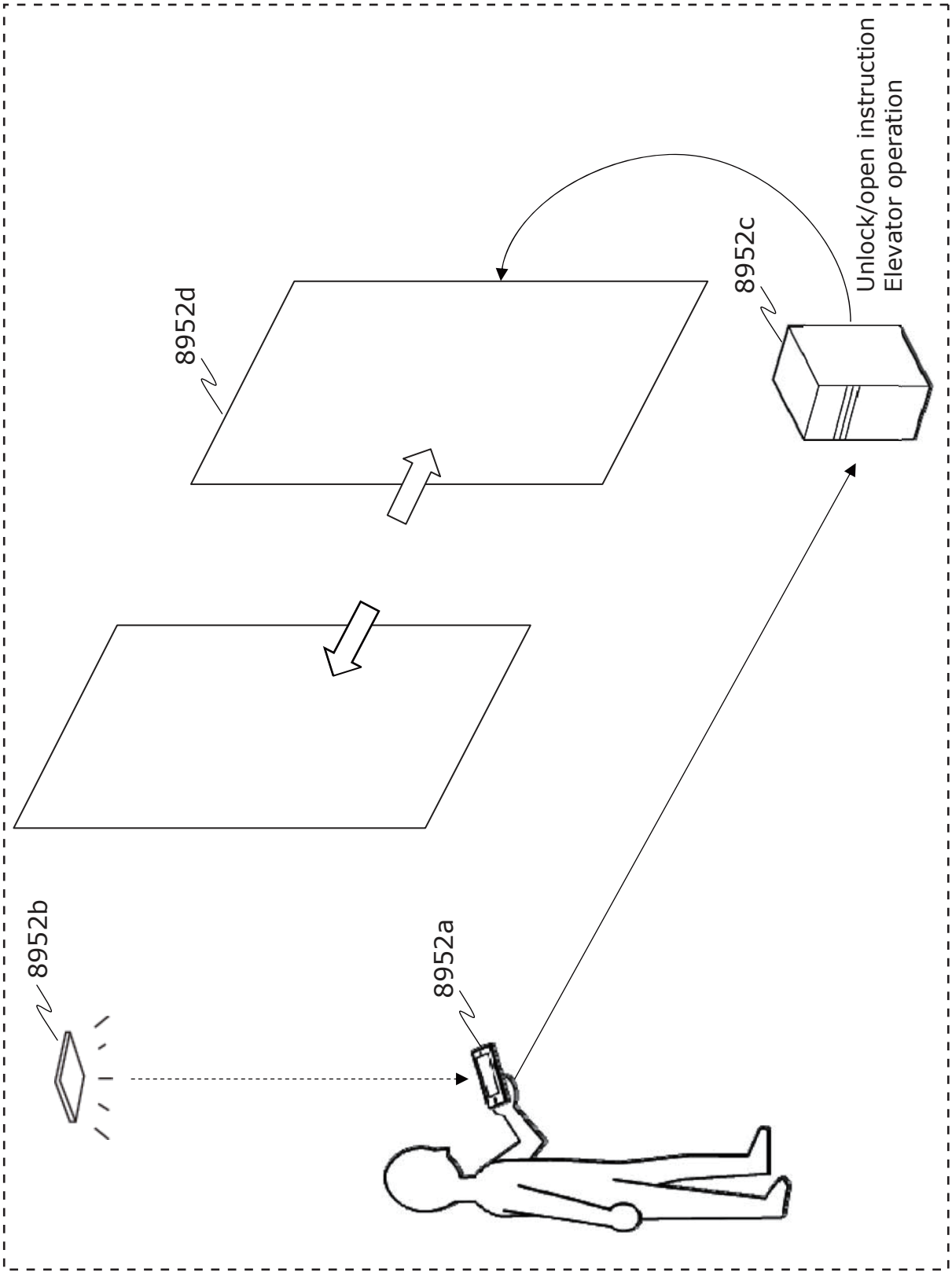


FIG. 197B

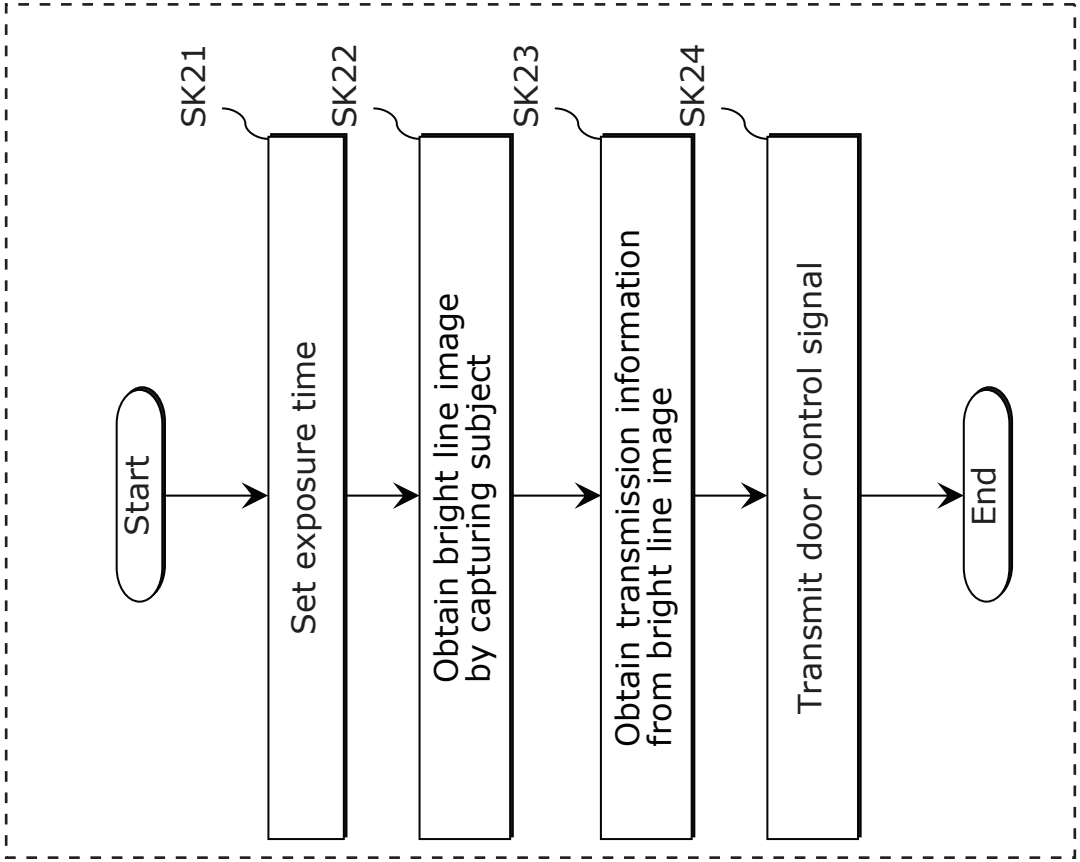


FIG. 197C

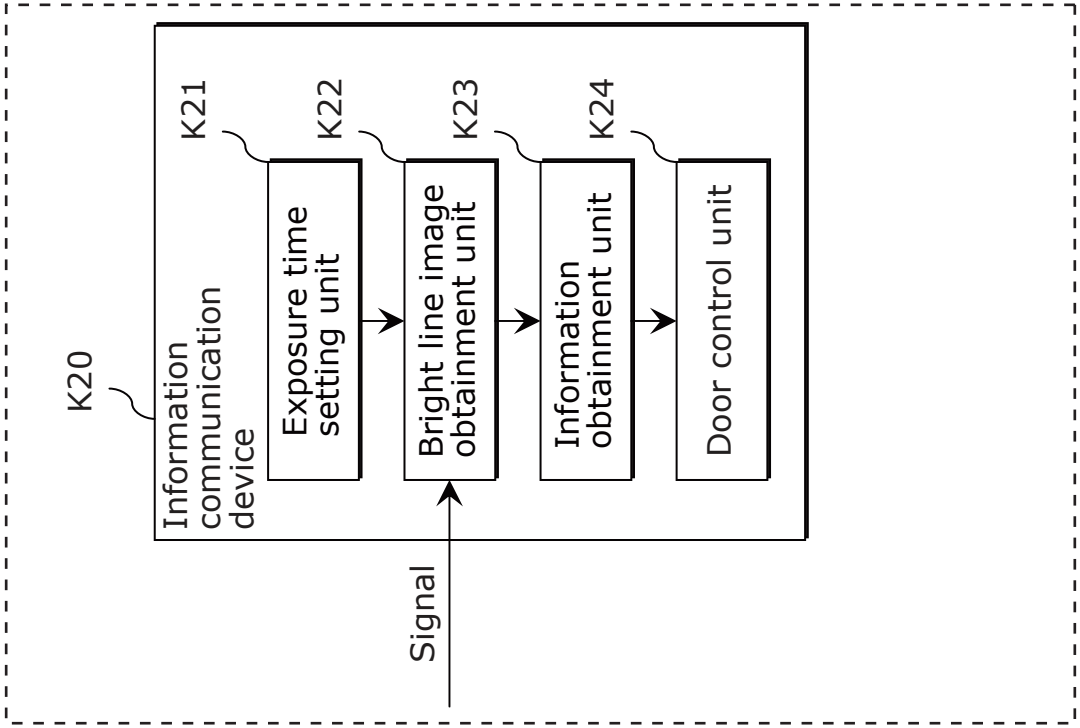


FIG. 198

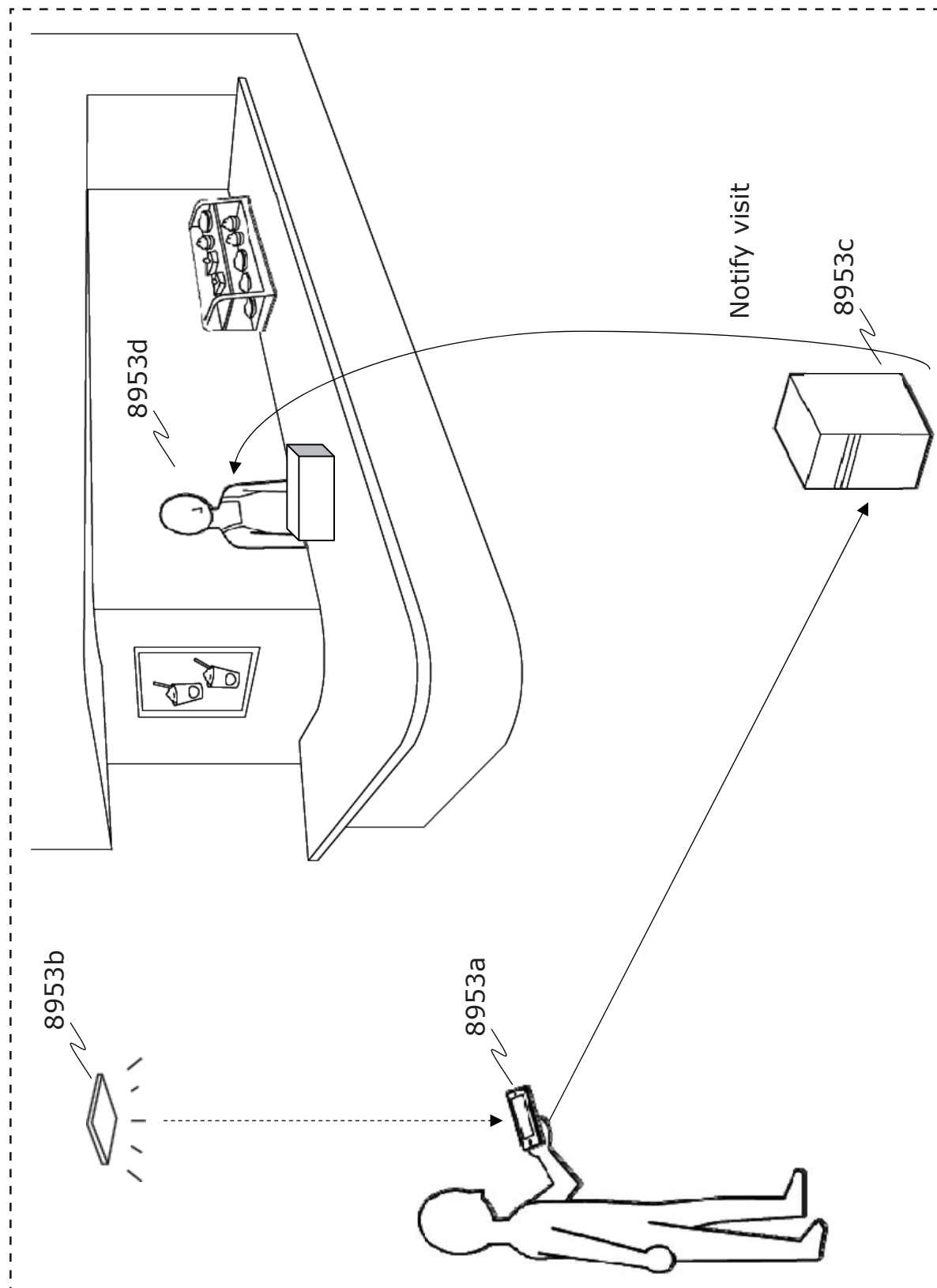


FIG. 199

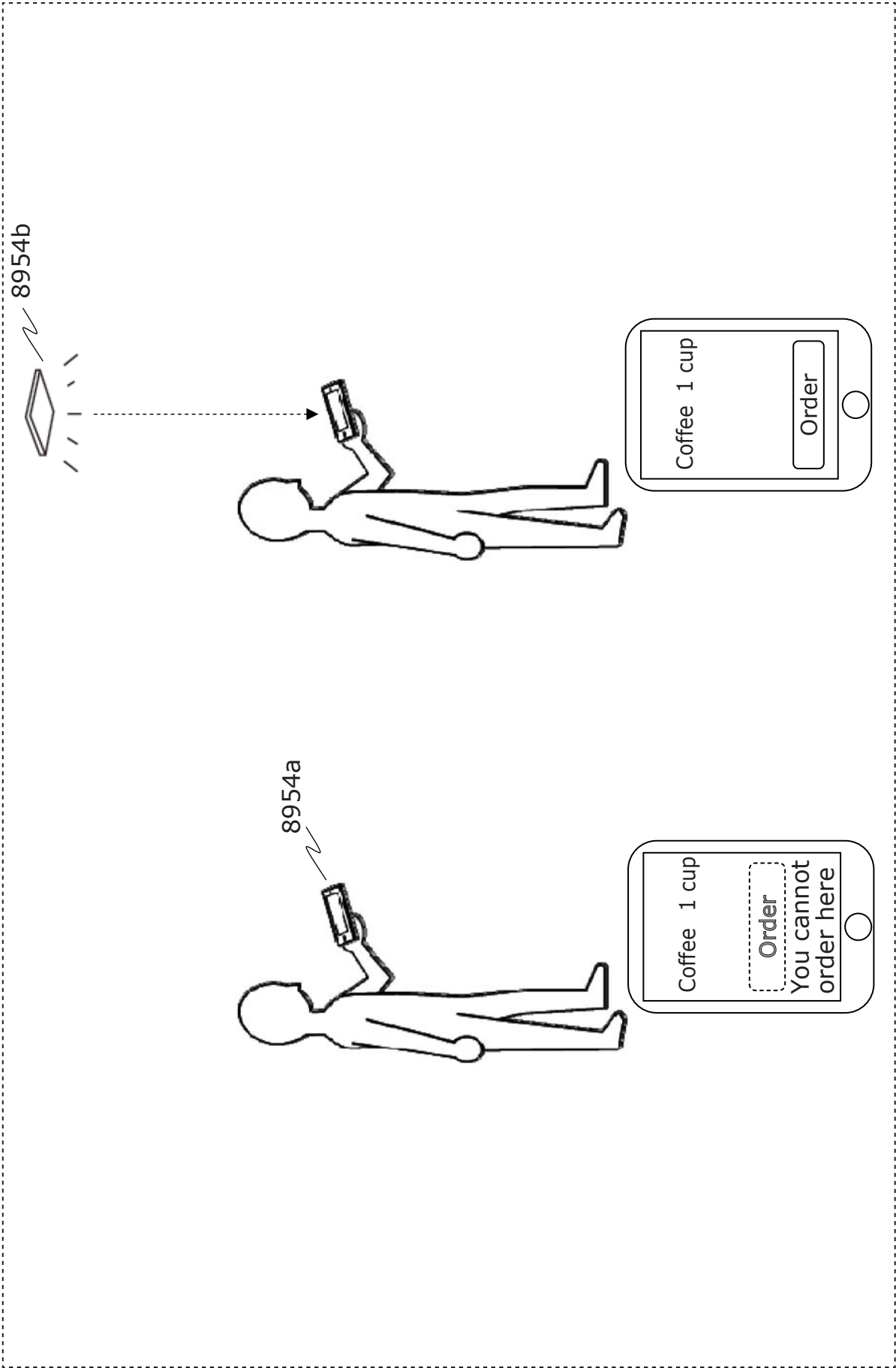


FIG. 200

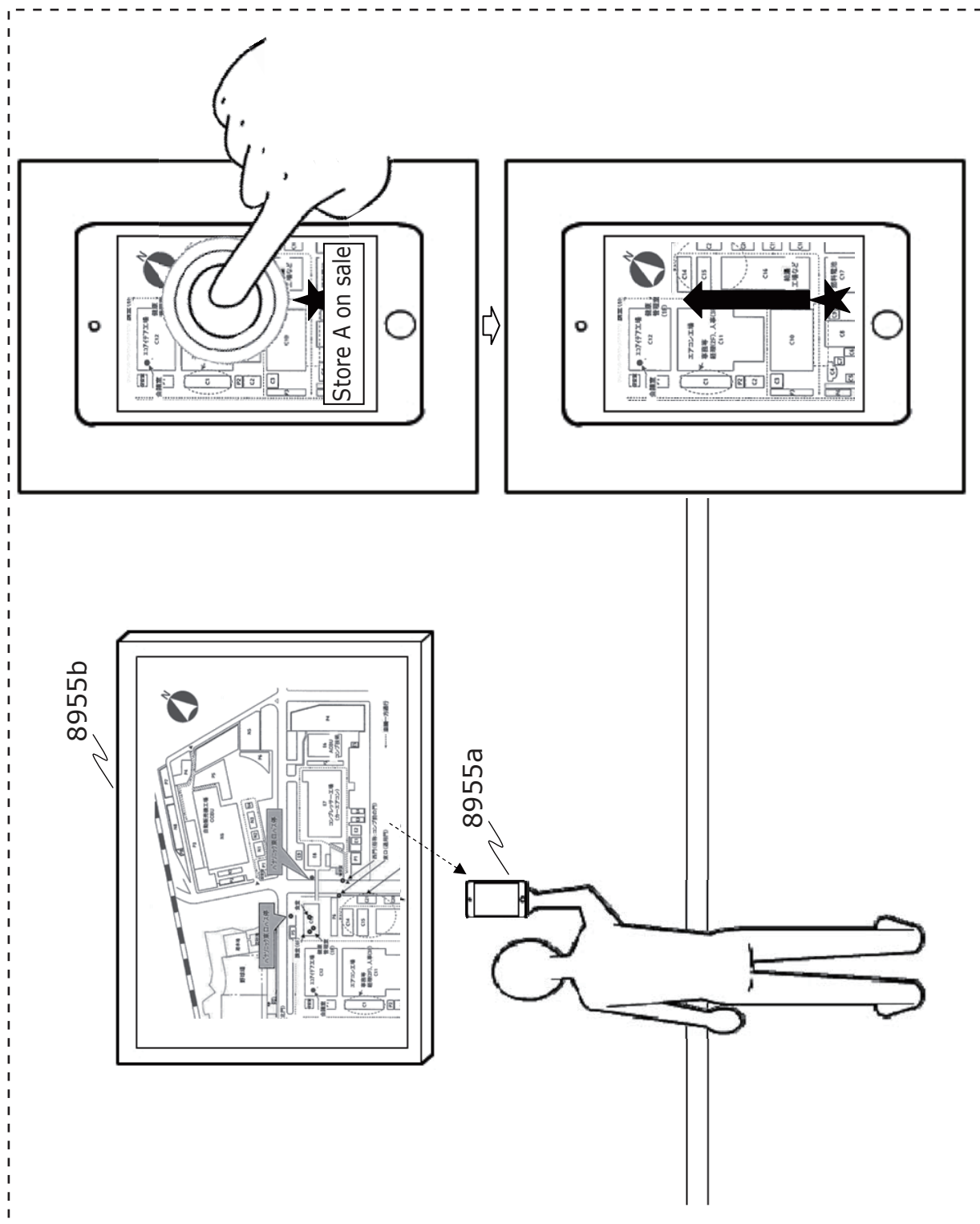


FIG. 201

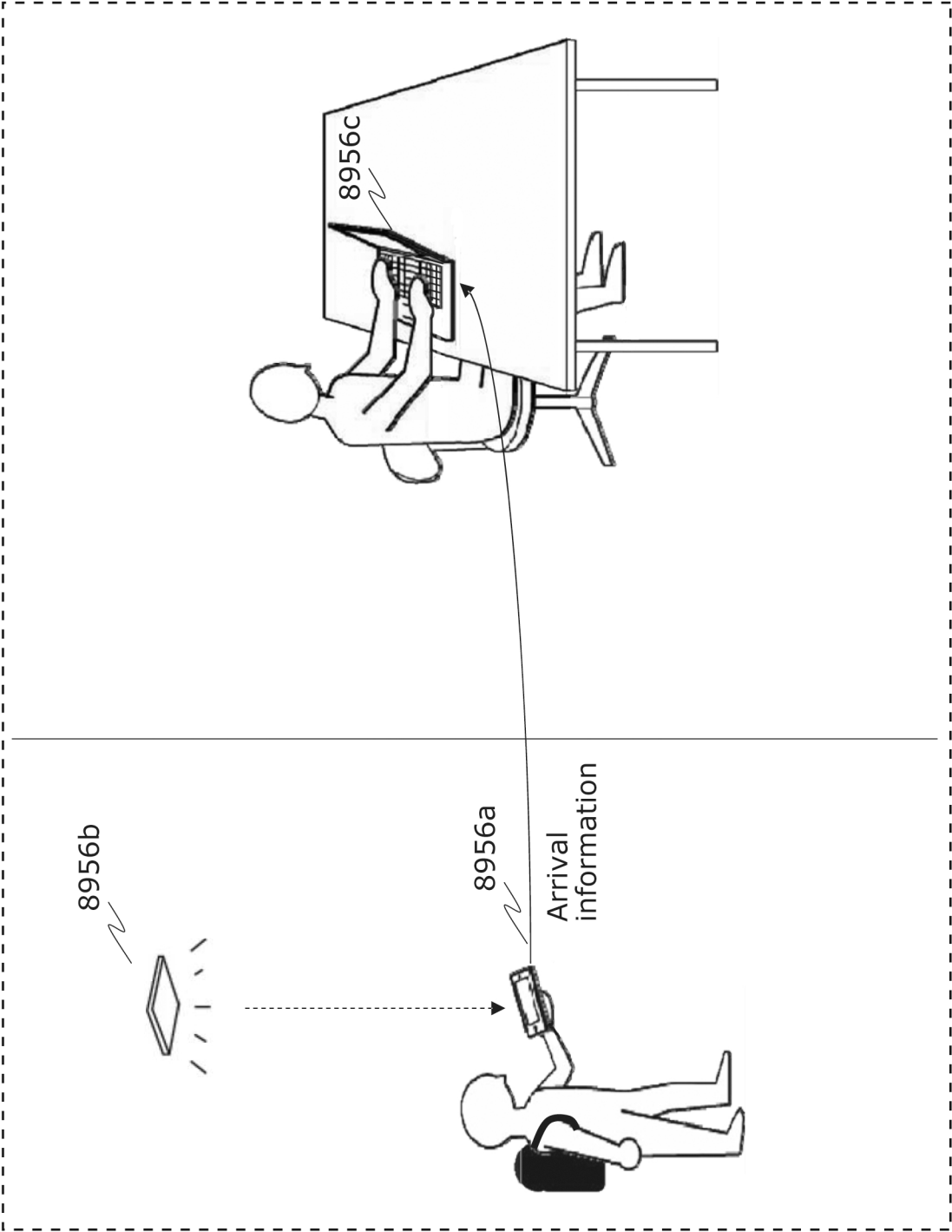


FIG. 202

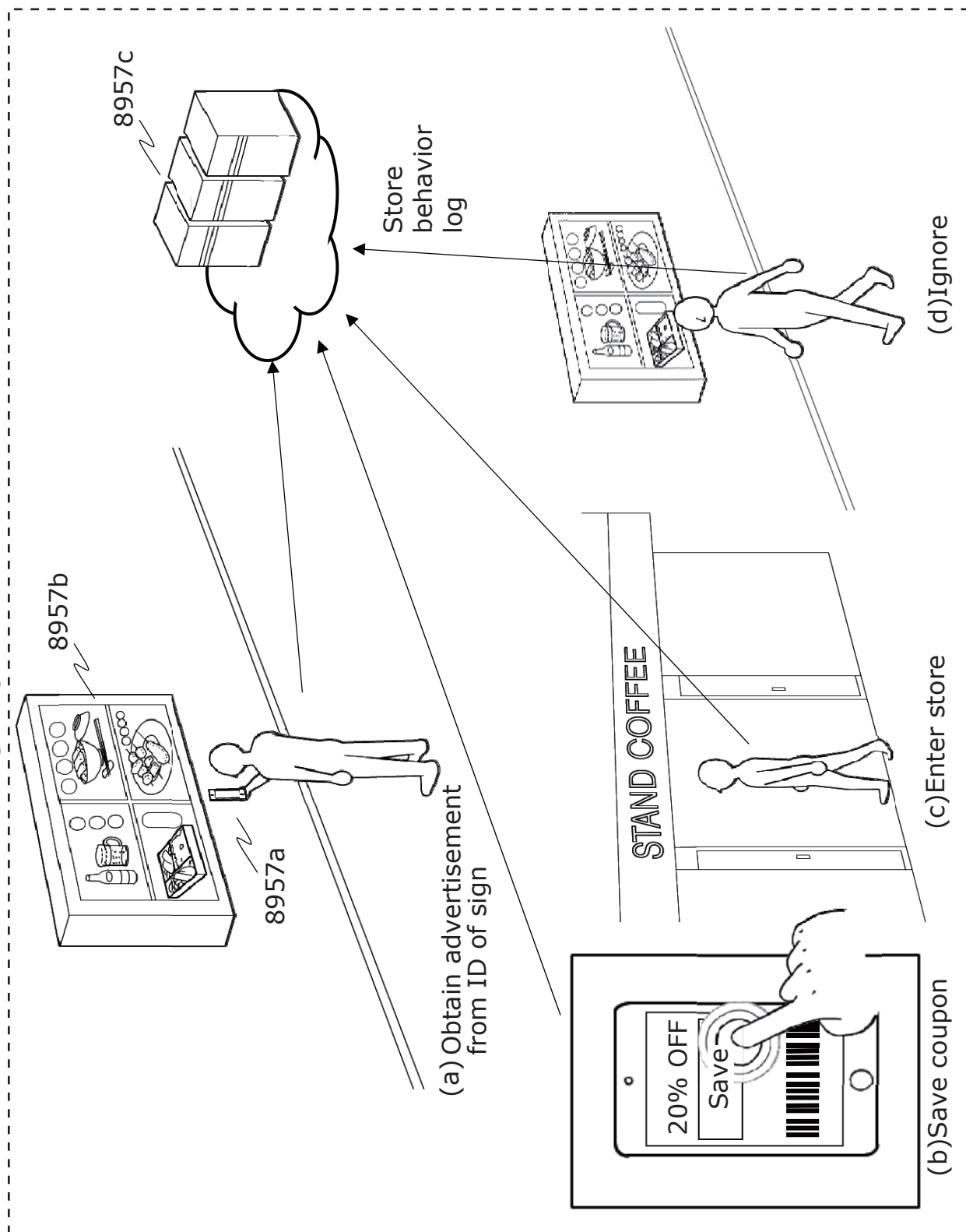


FIG. 203

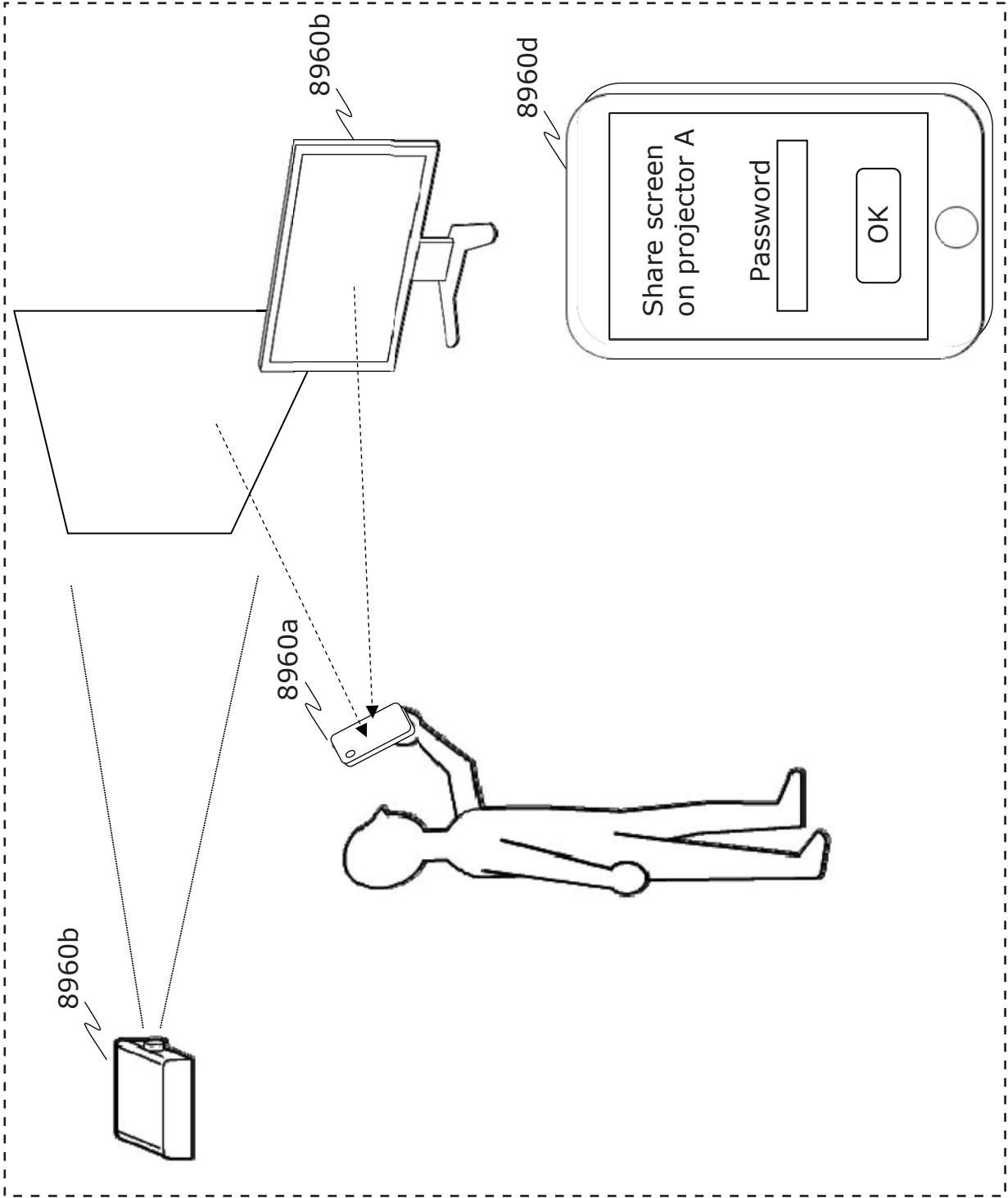


FIG. 204

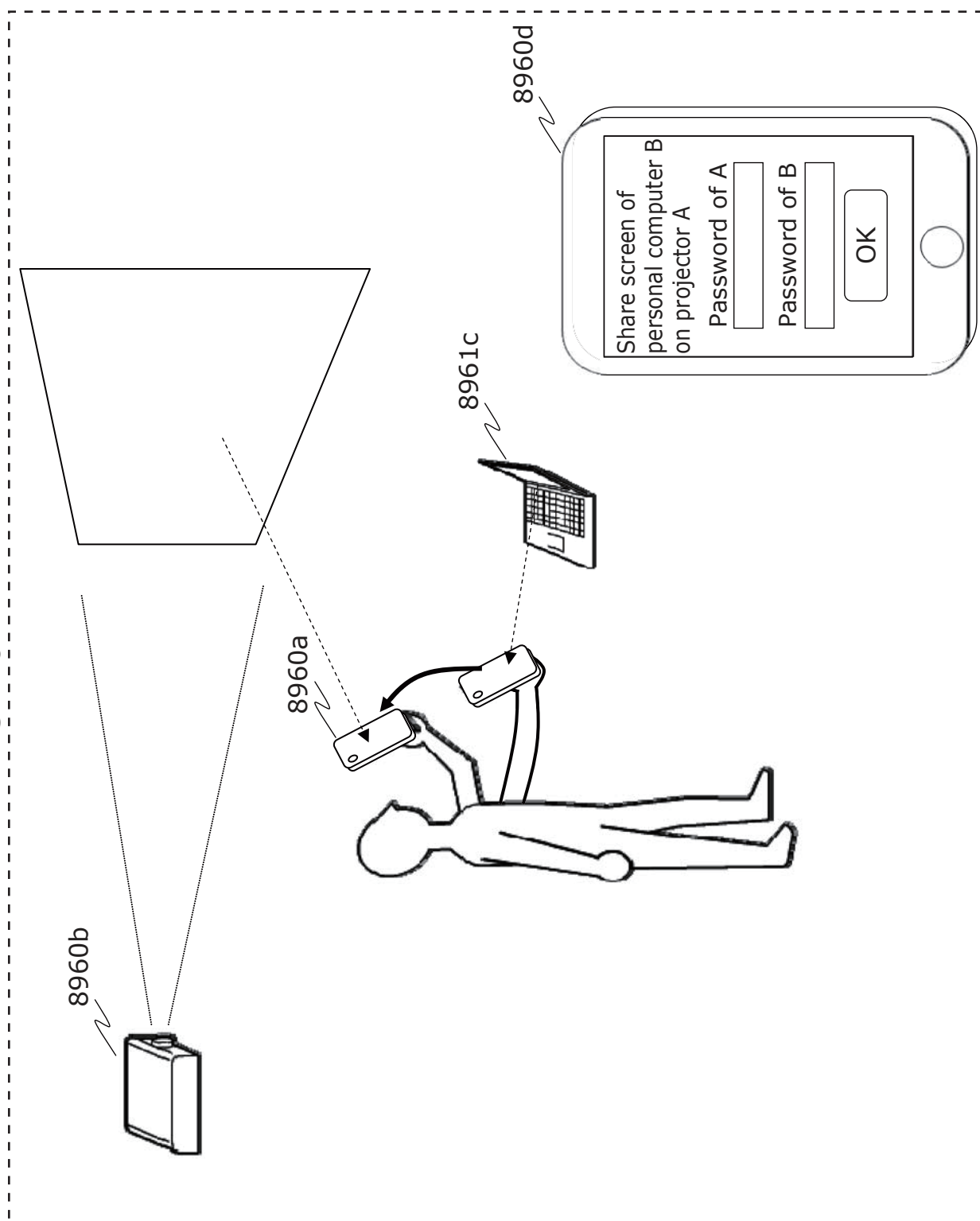


FIG. 205

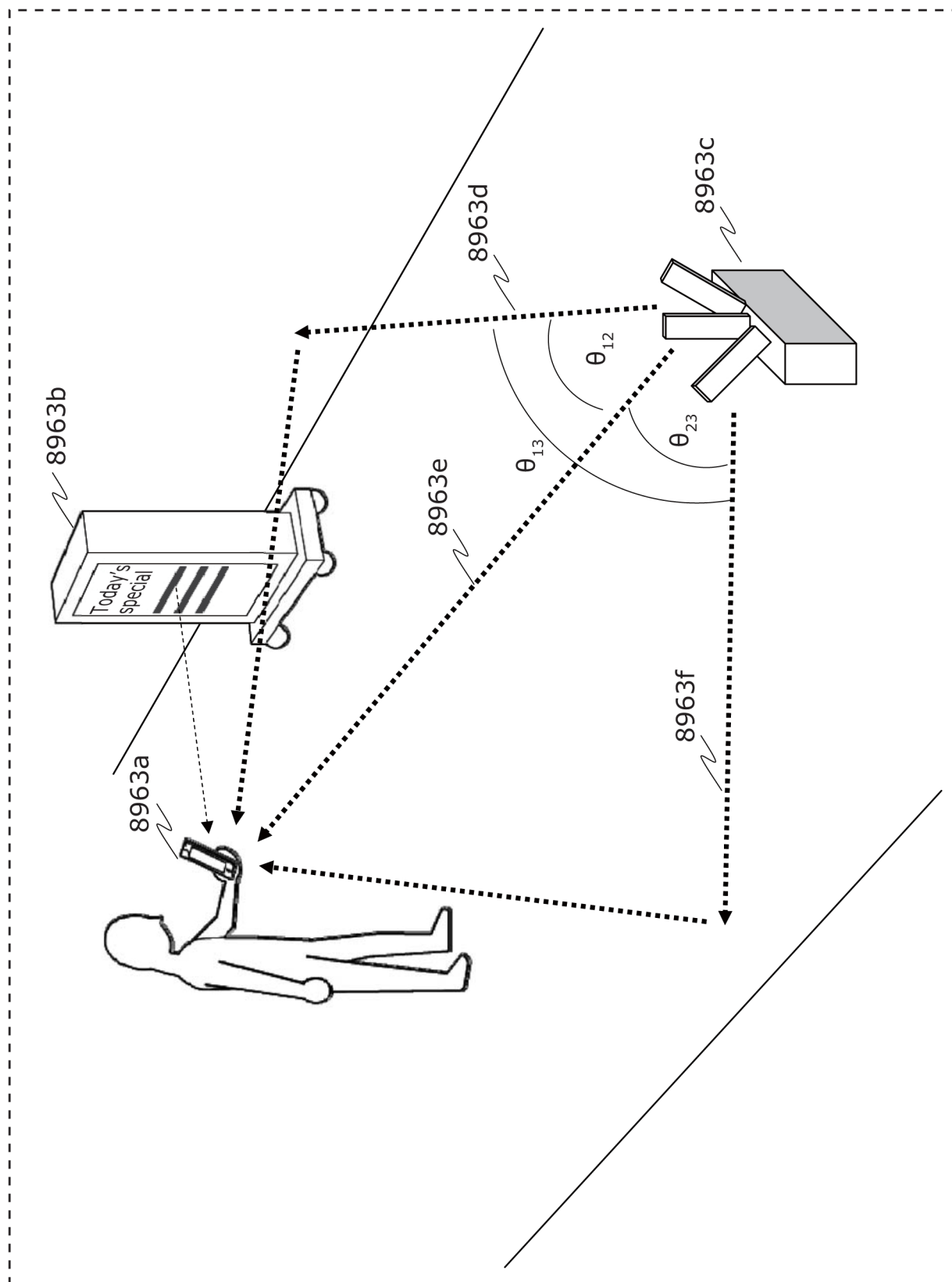


FIG. 206

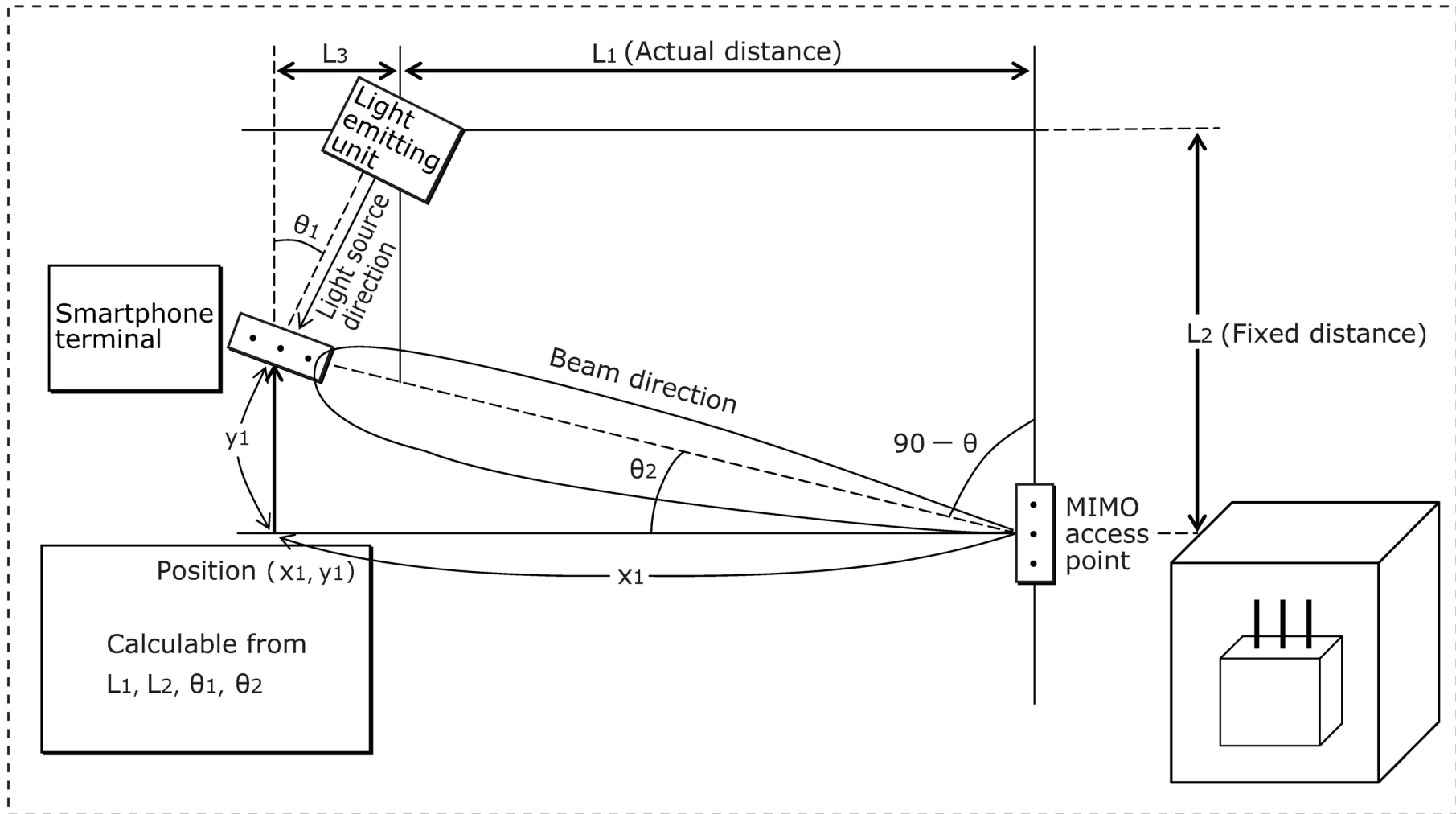


FIG. 207

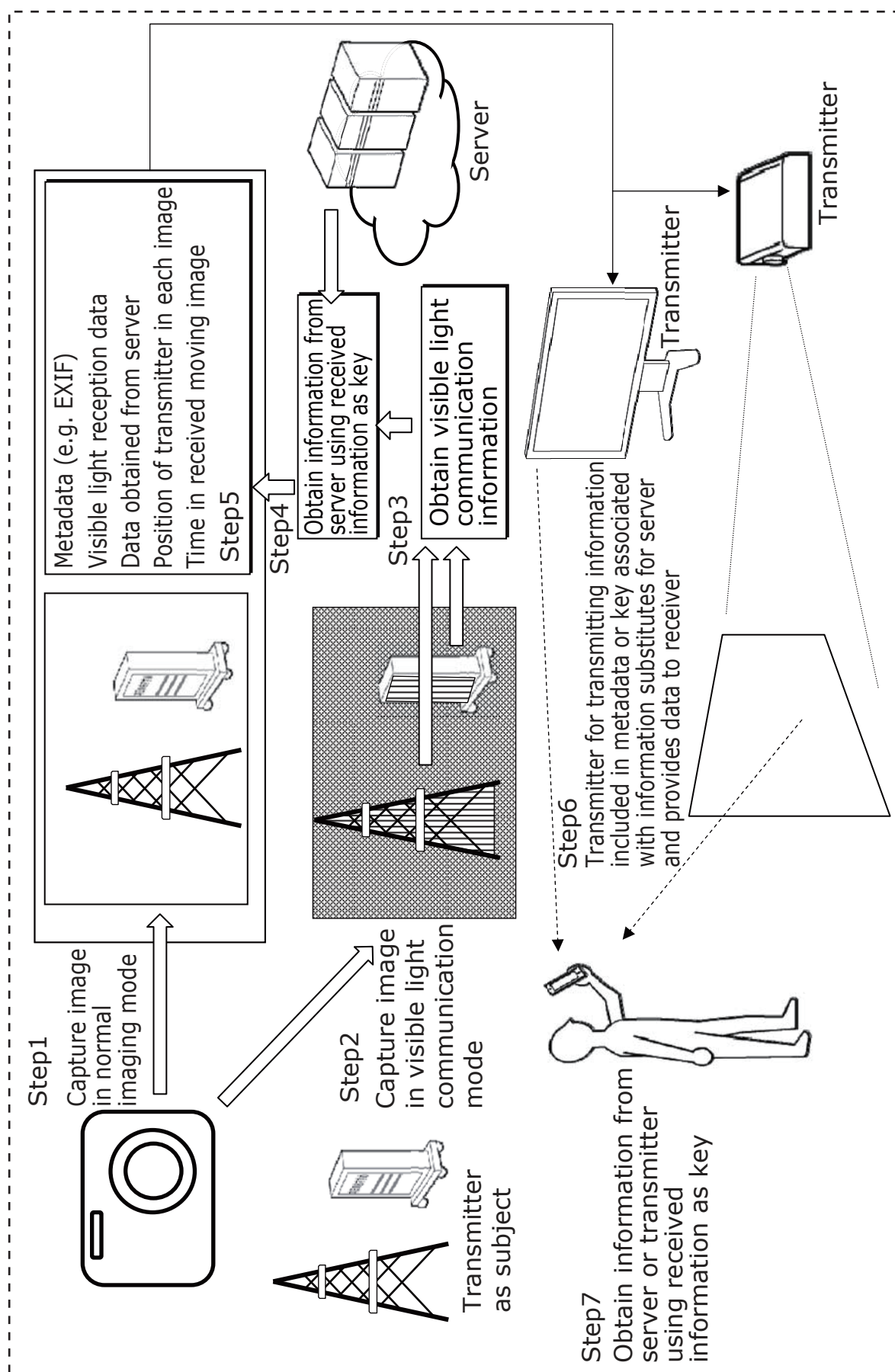


FIG. 208

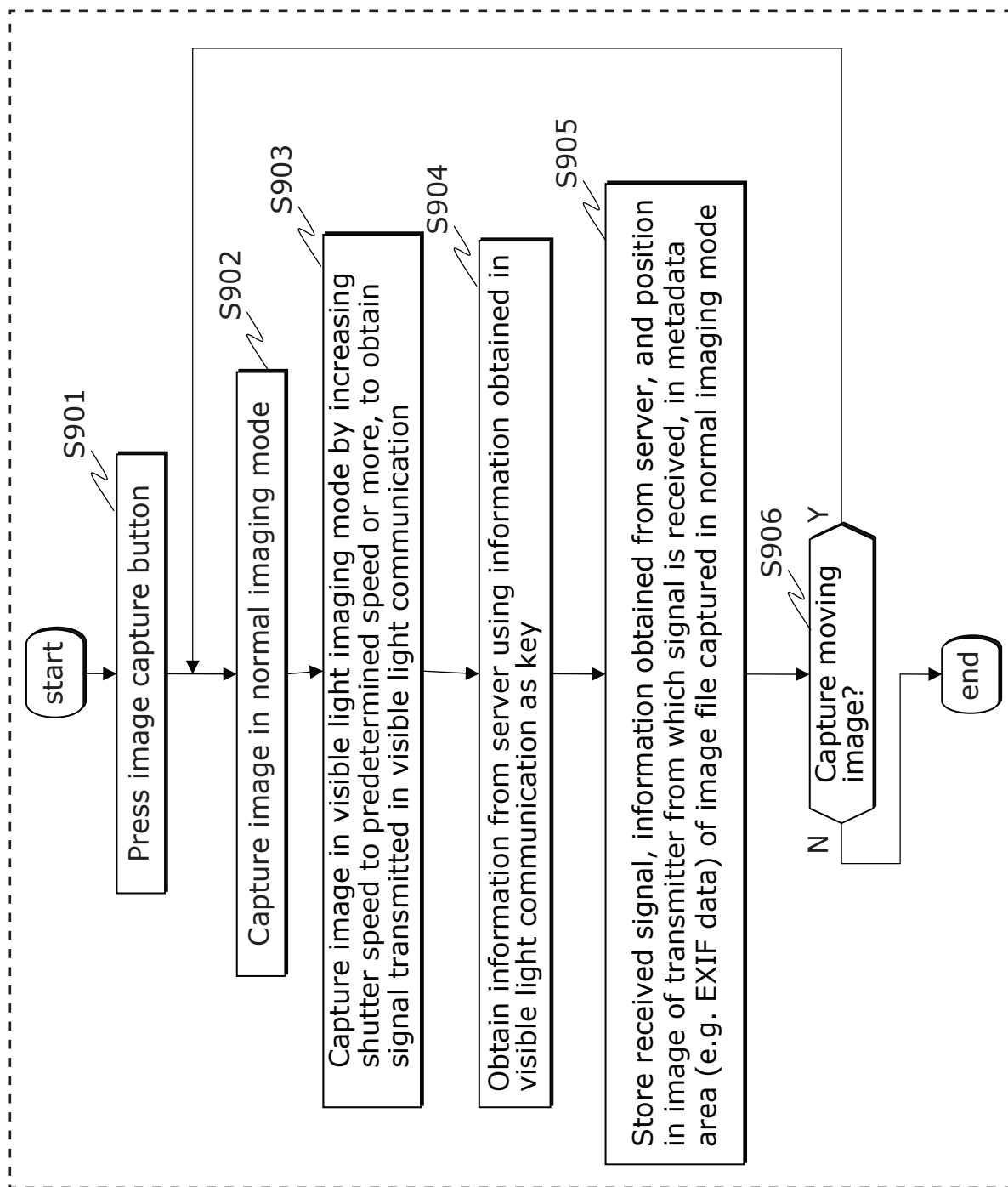


FIG. 209

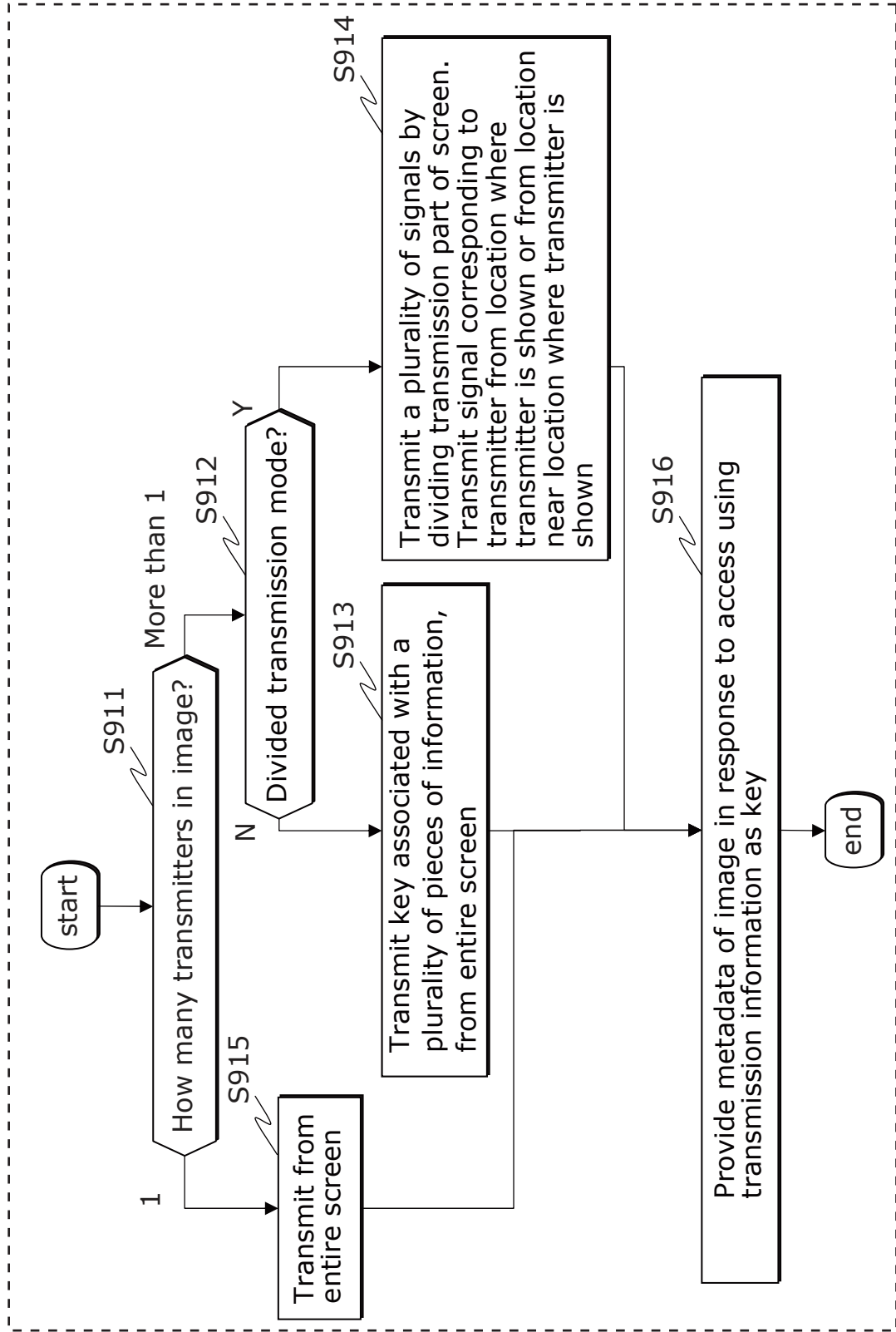


FIG. 210

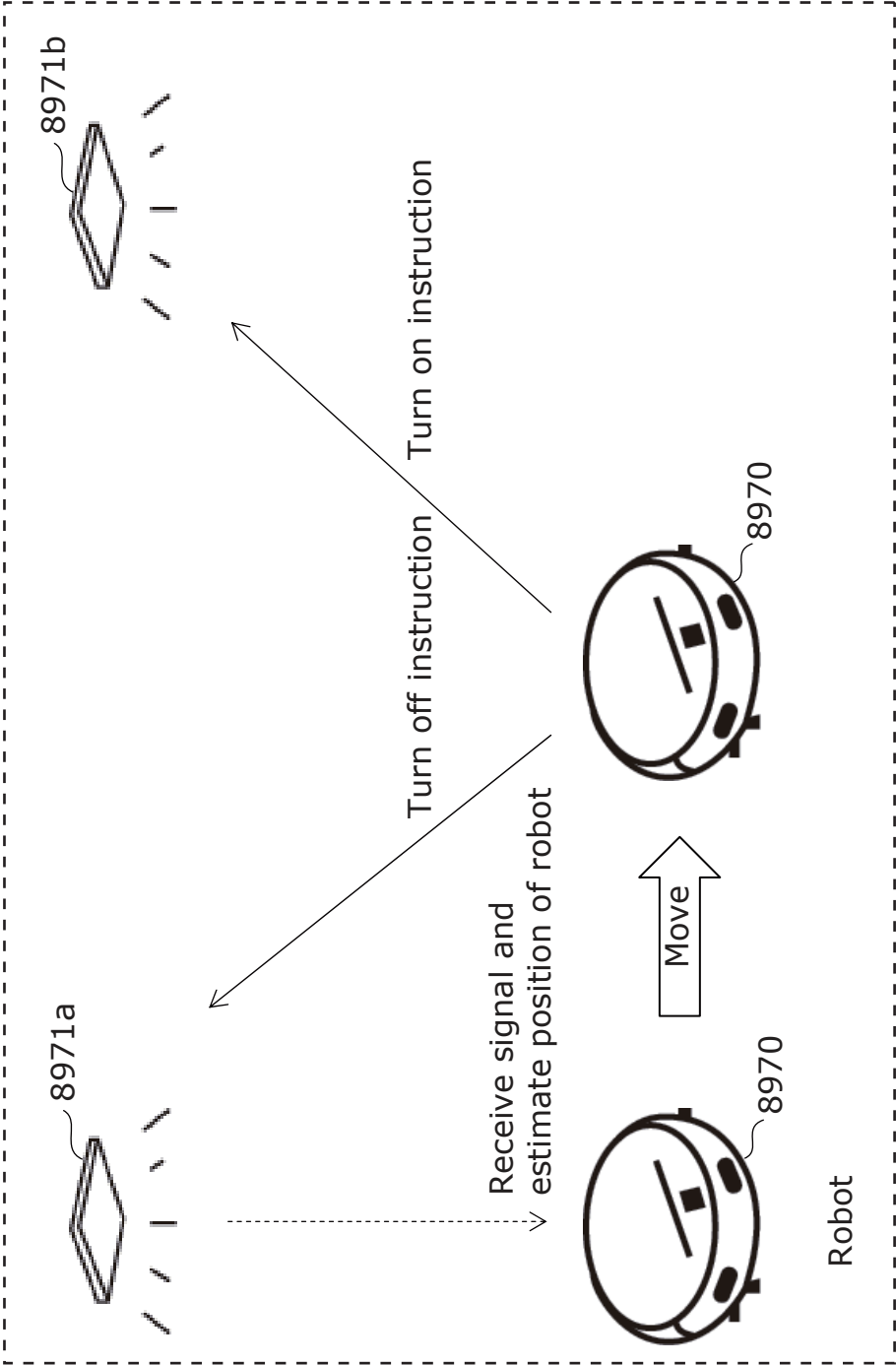


FIG. 211

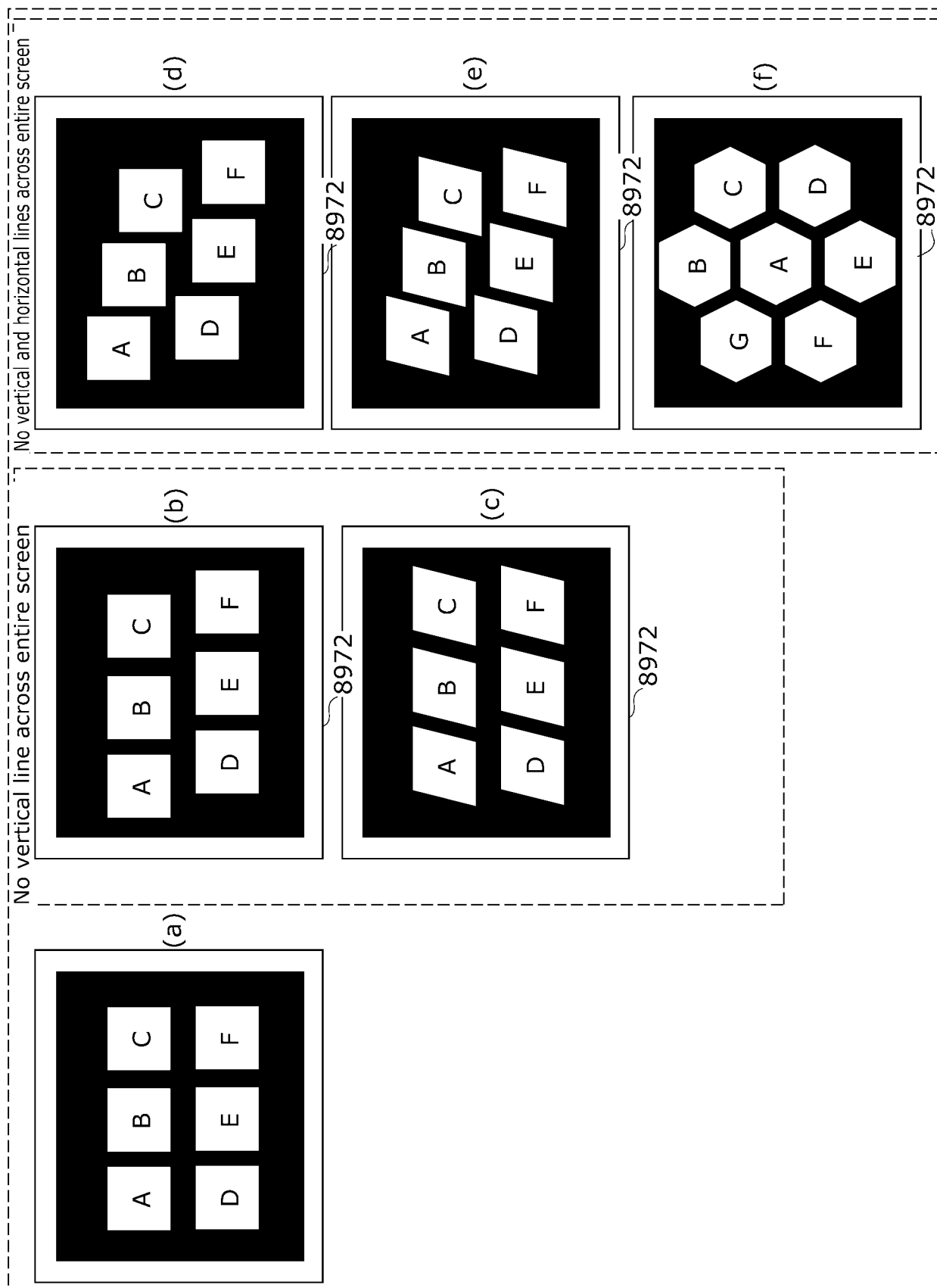


FIG. 212

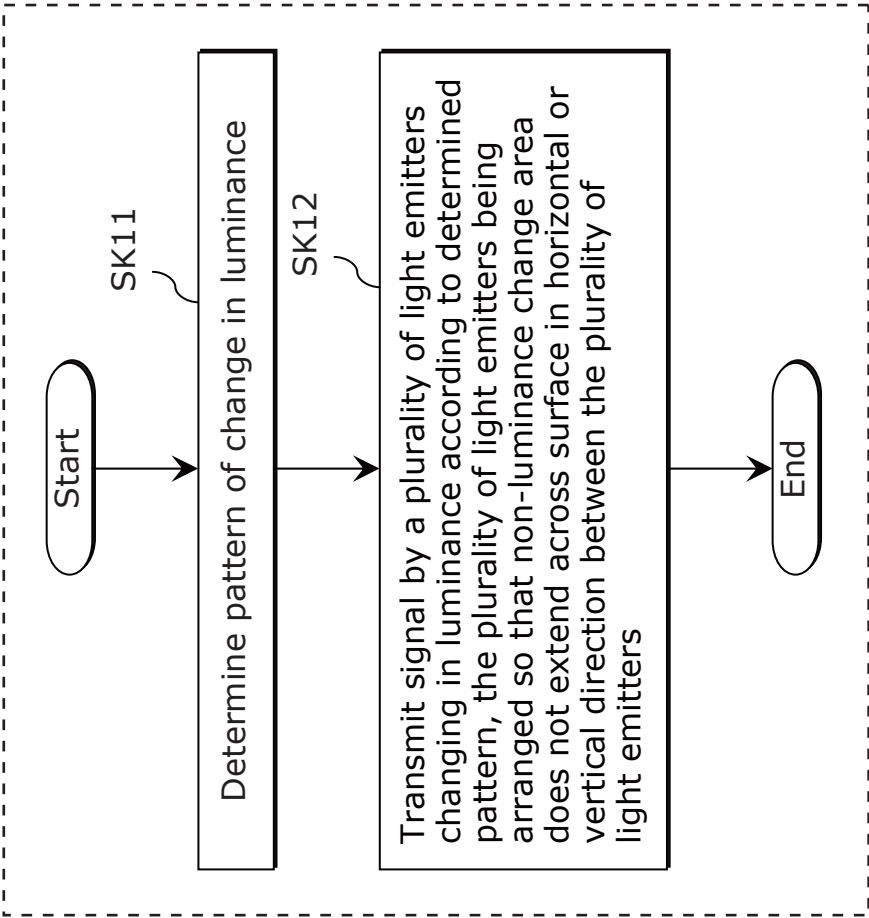


FIG. 213

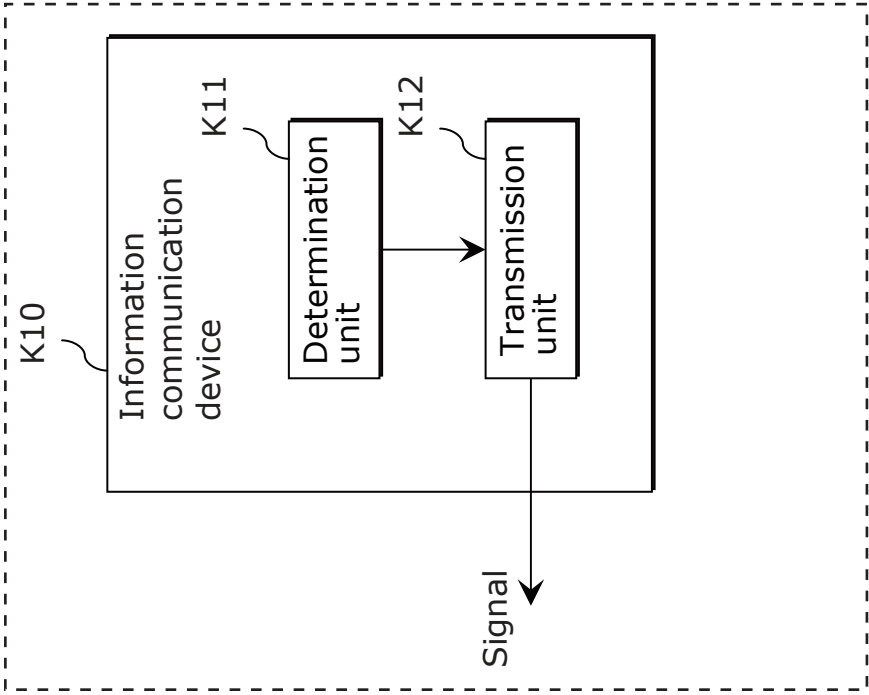


FIG. 214A

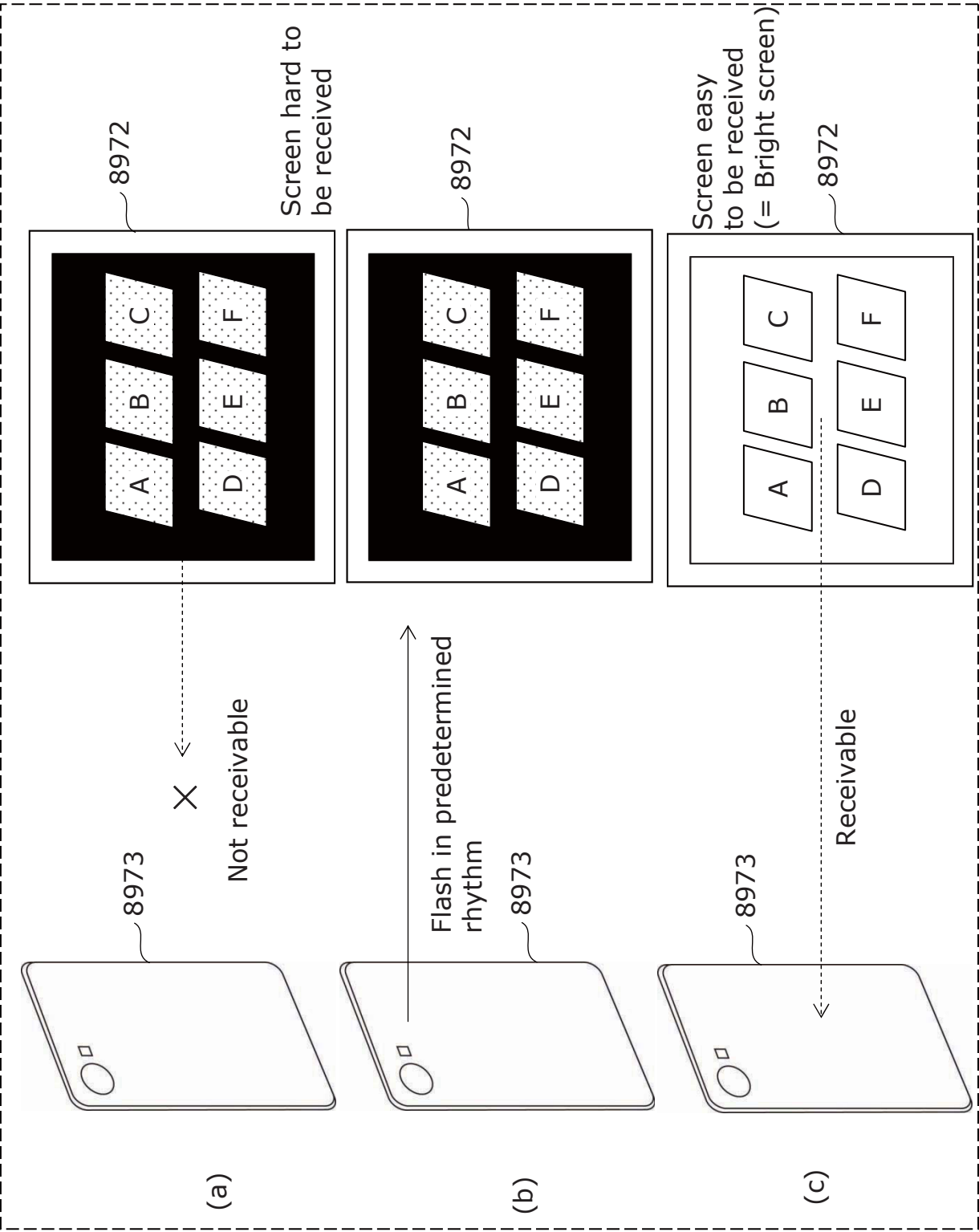


FIG. 214B

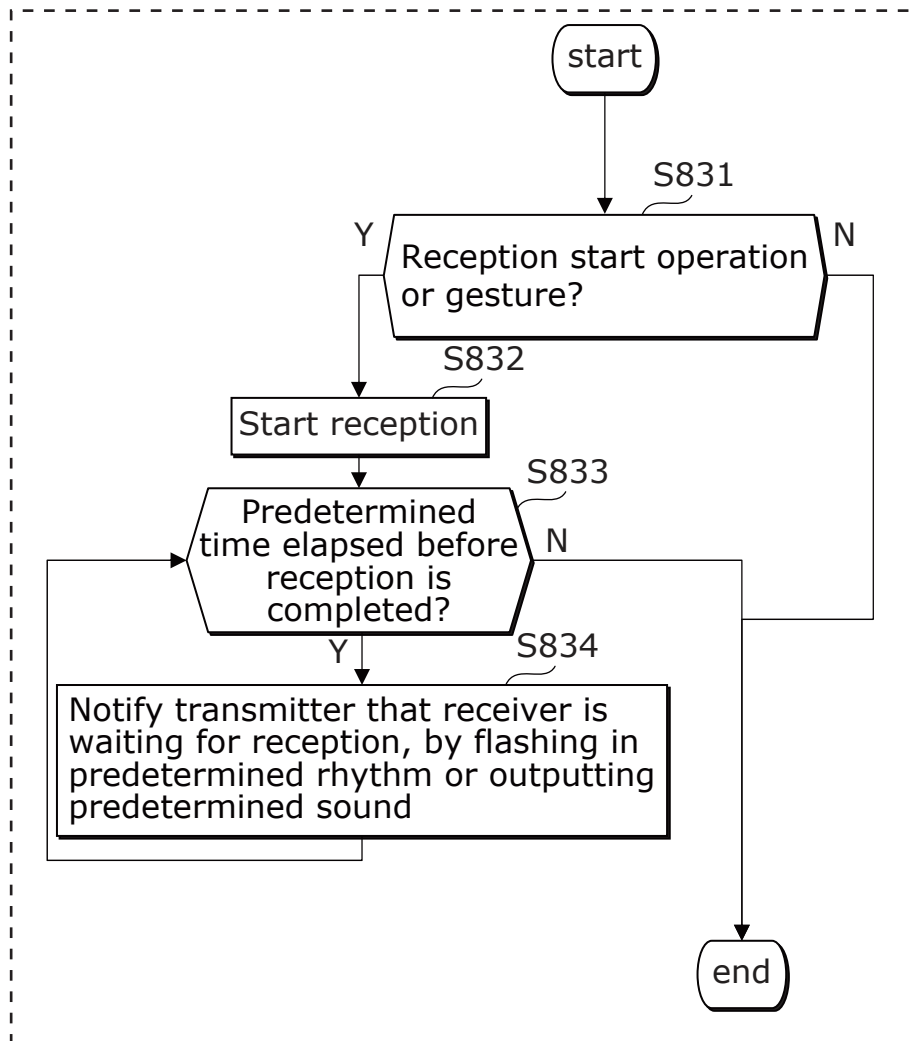


FIG. 215

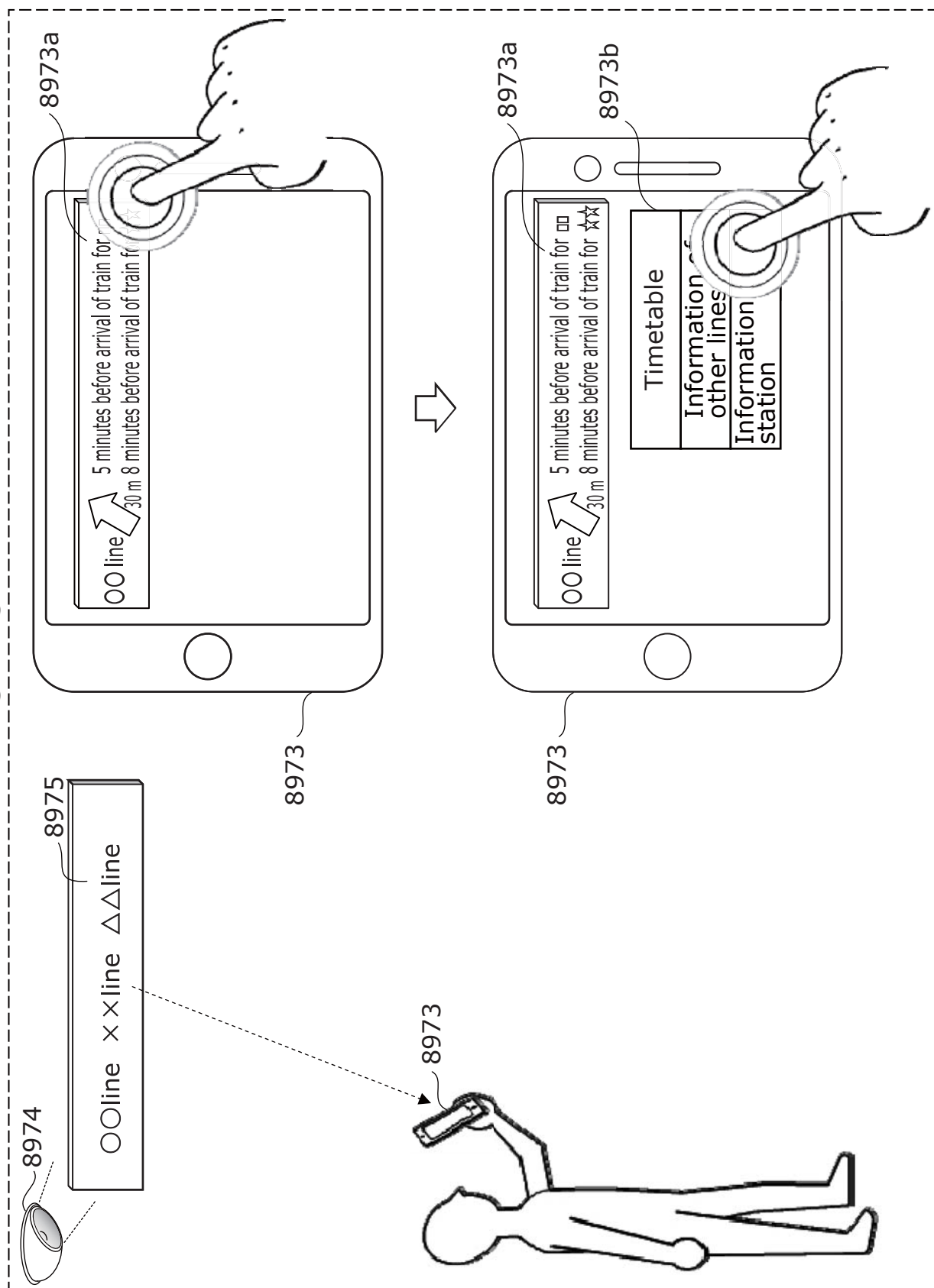


FIG. 216

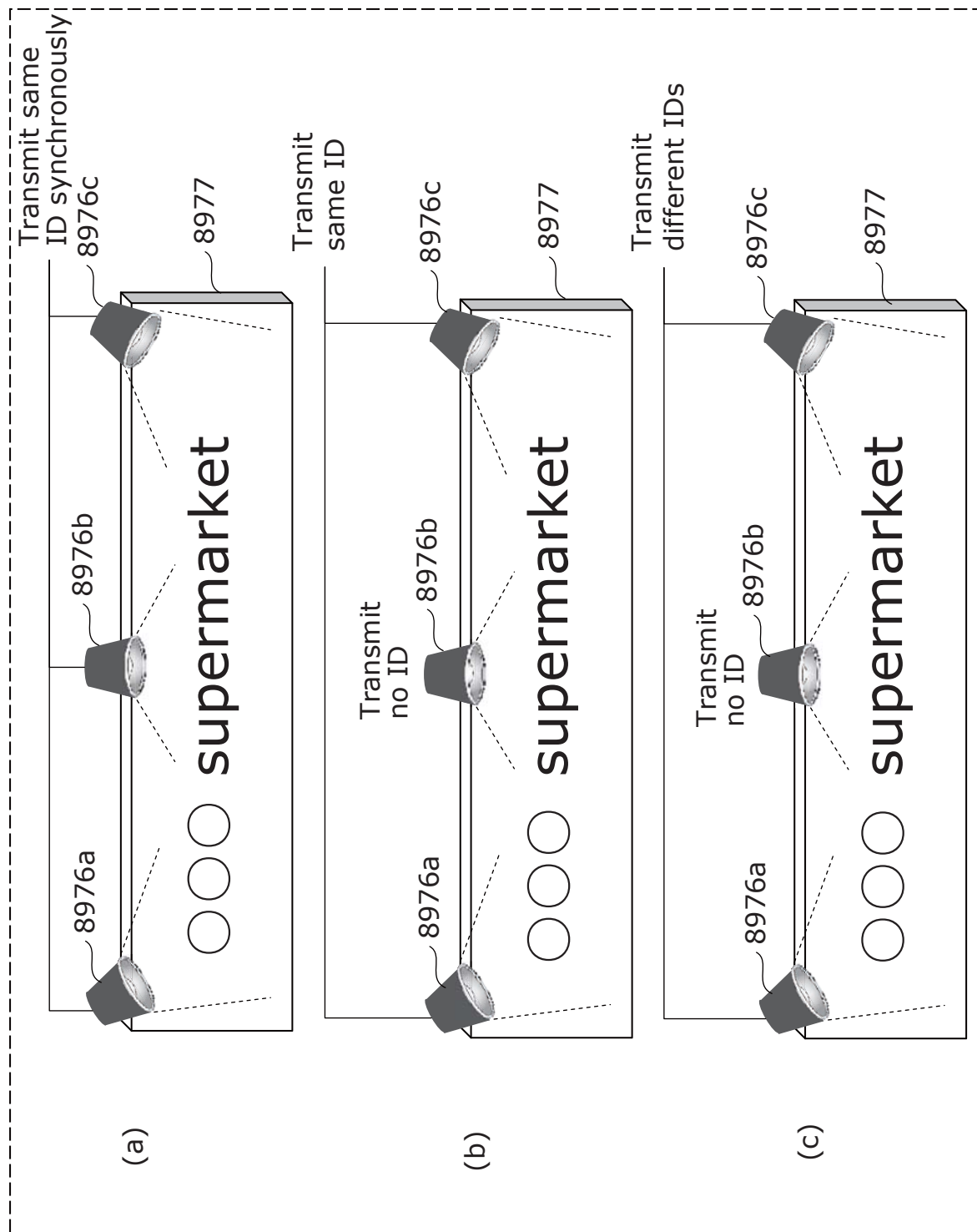


FIG. 217A

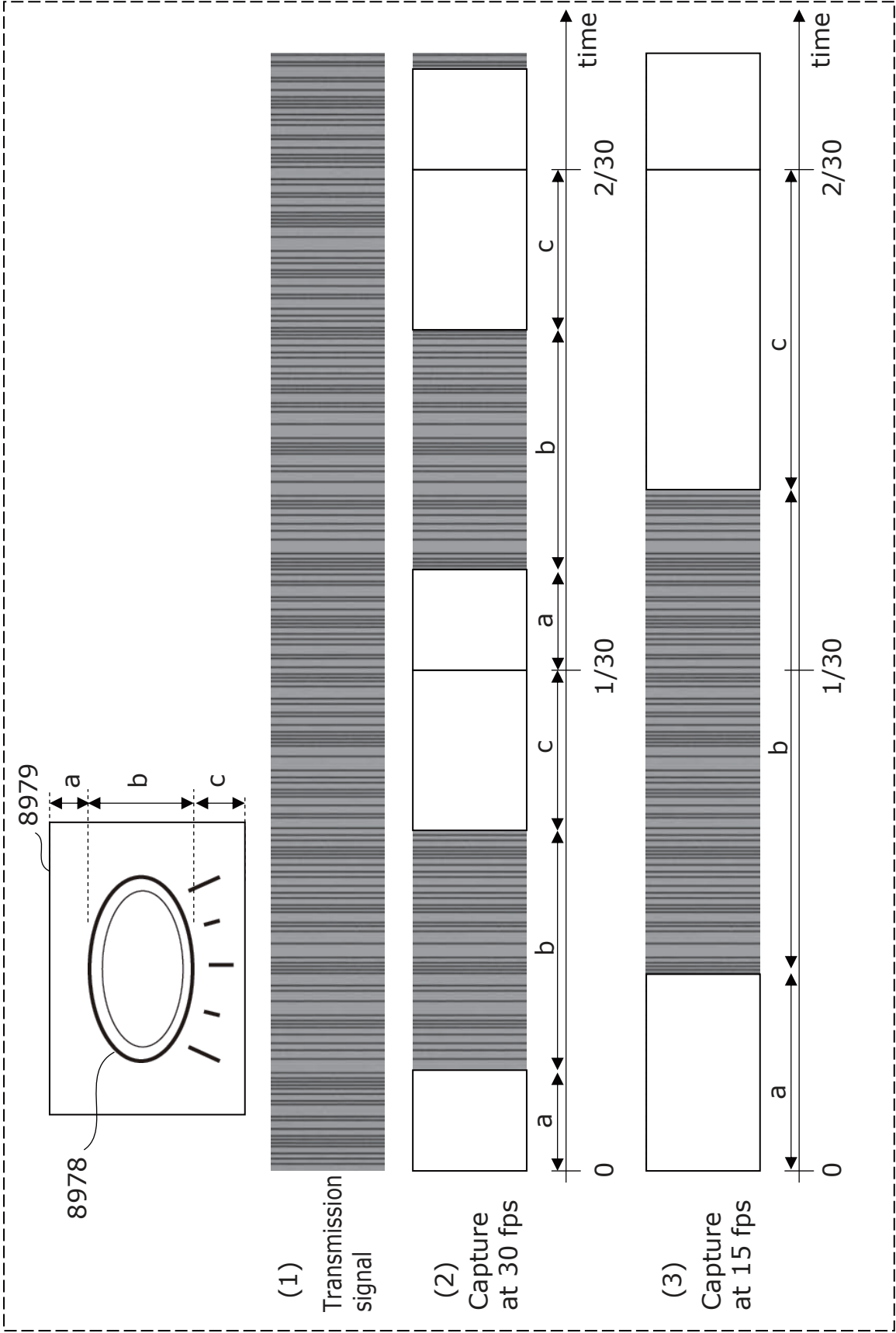


FIG. 217B

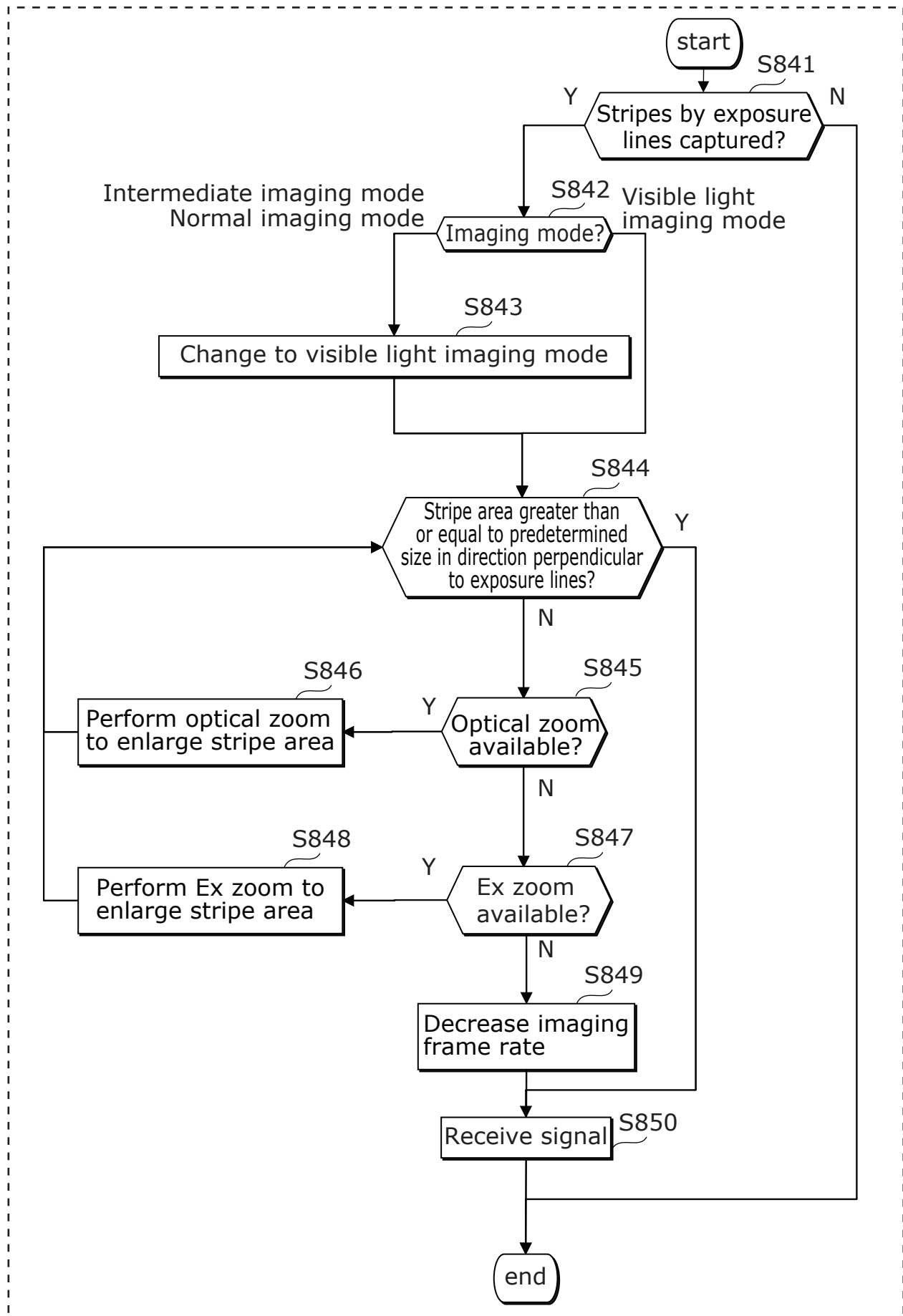


FIG. 218

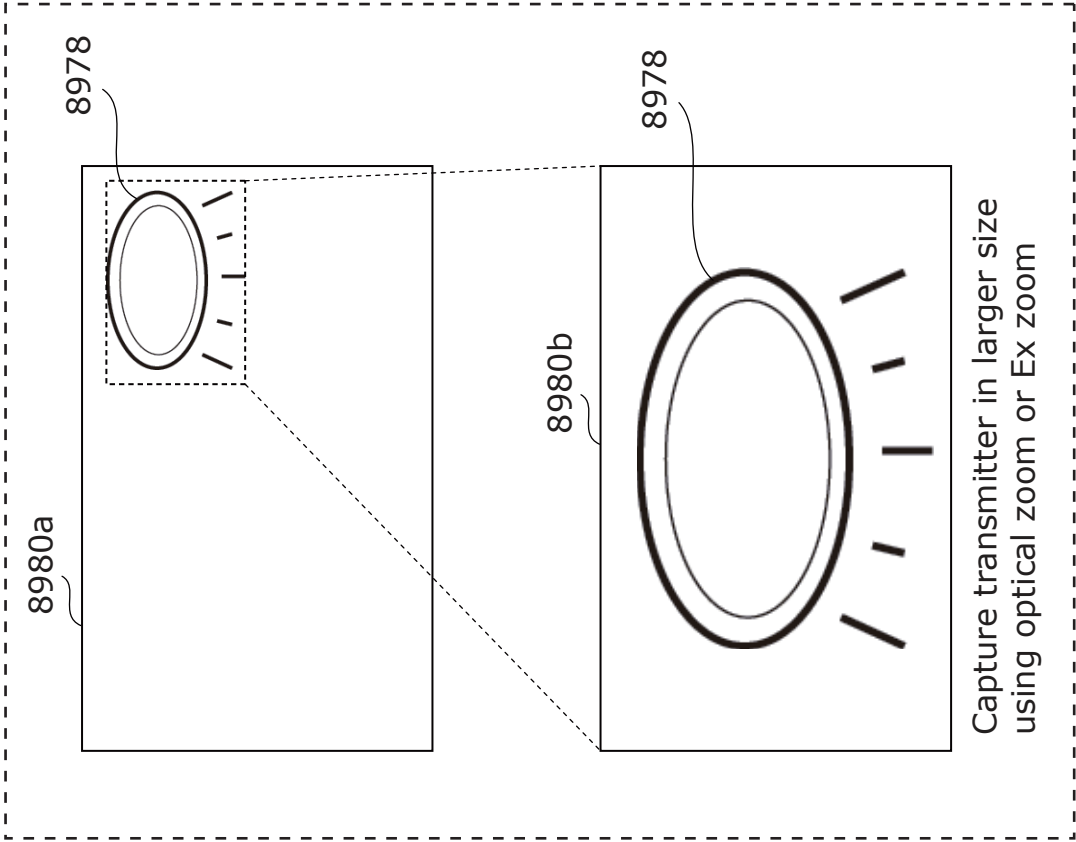


FIG. 219

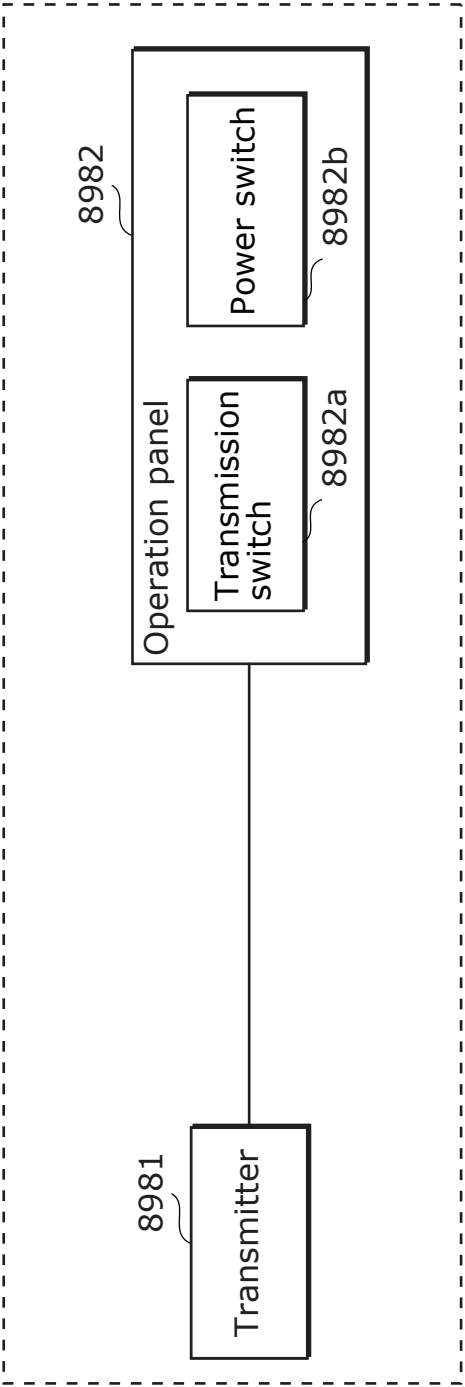


FIG. 220

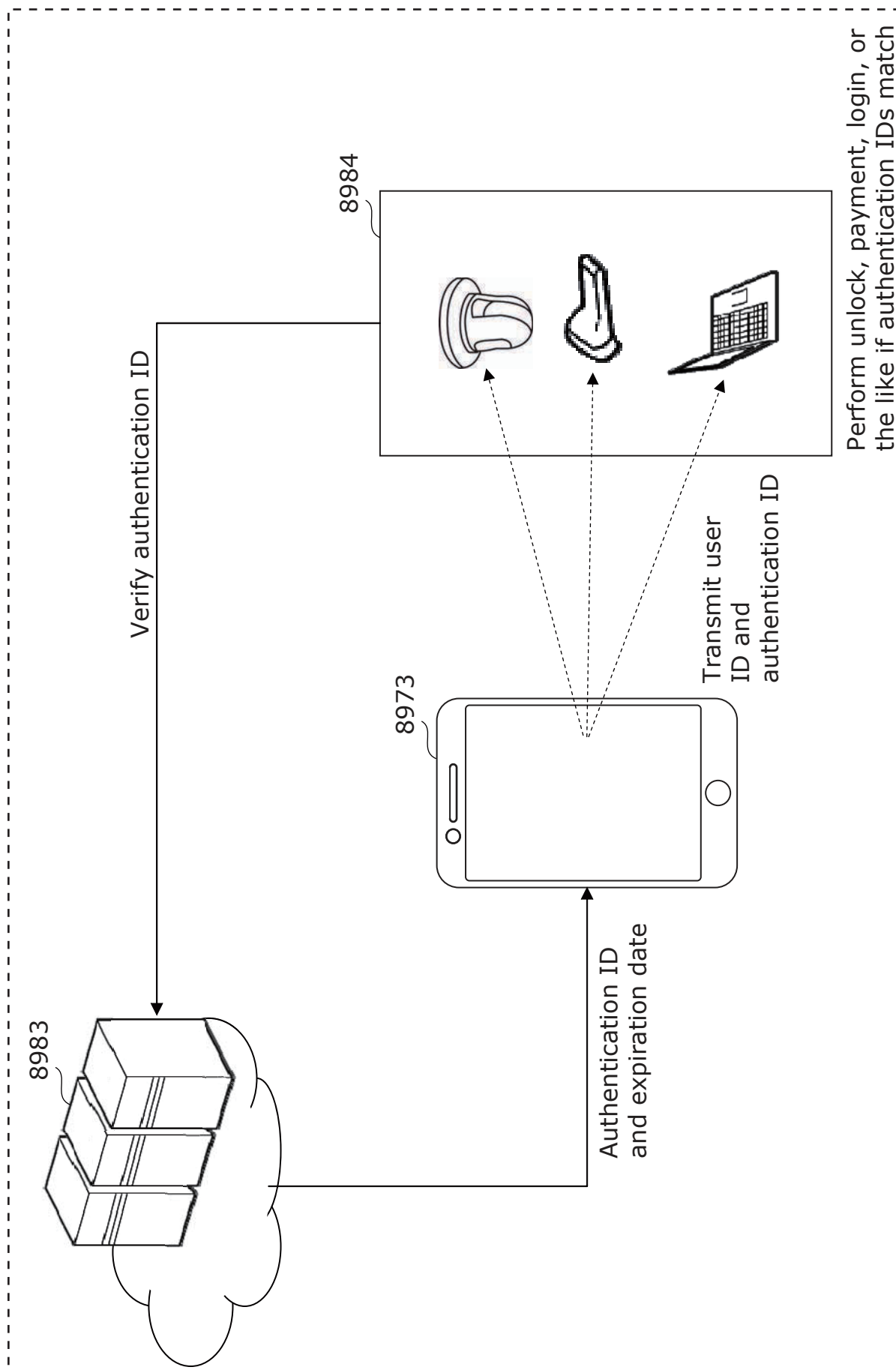


FIG. 221A

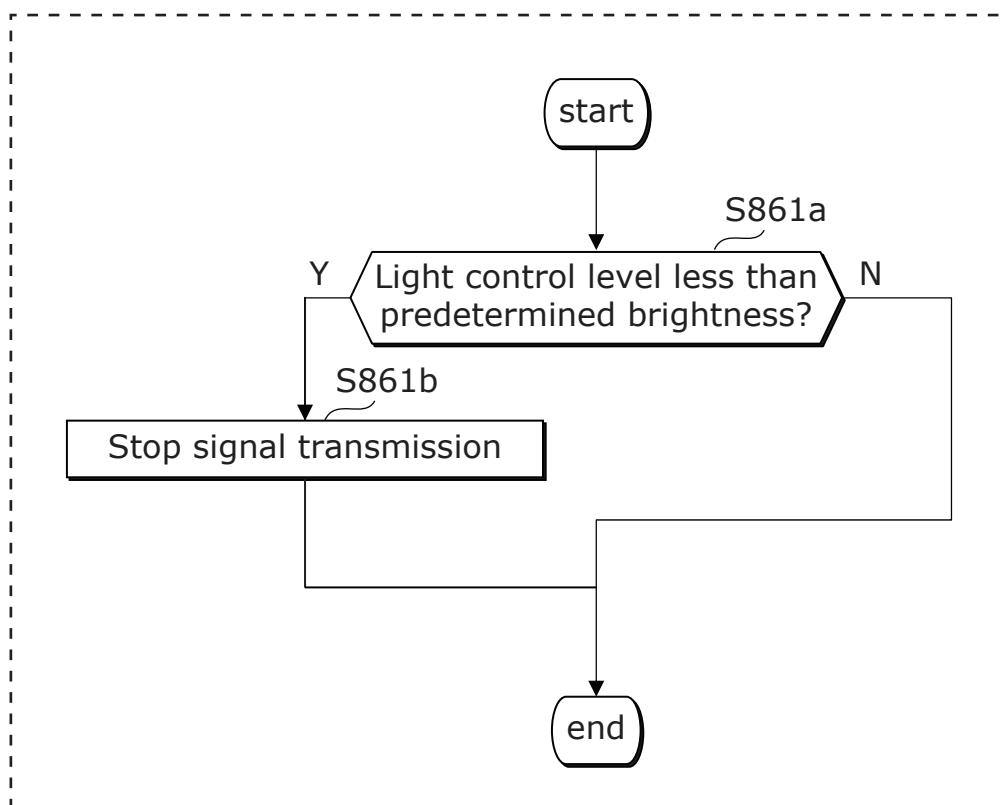


FIG. 221B

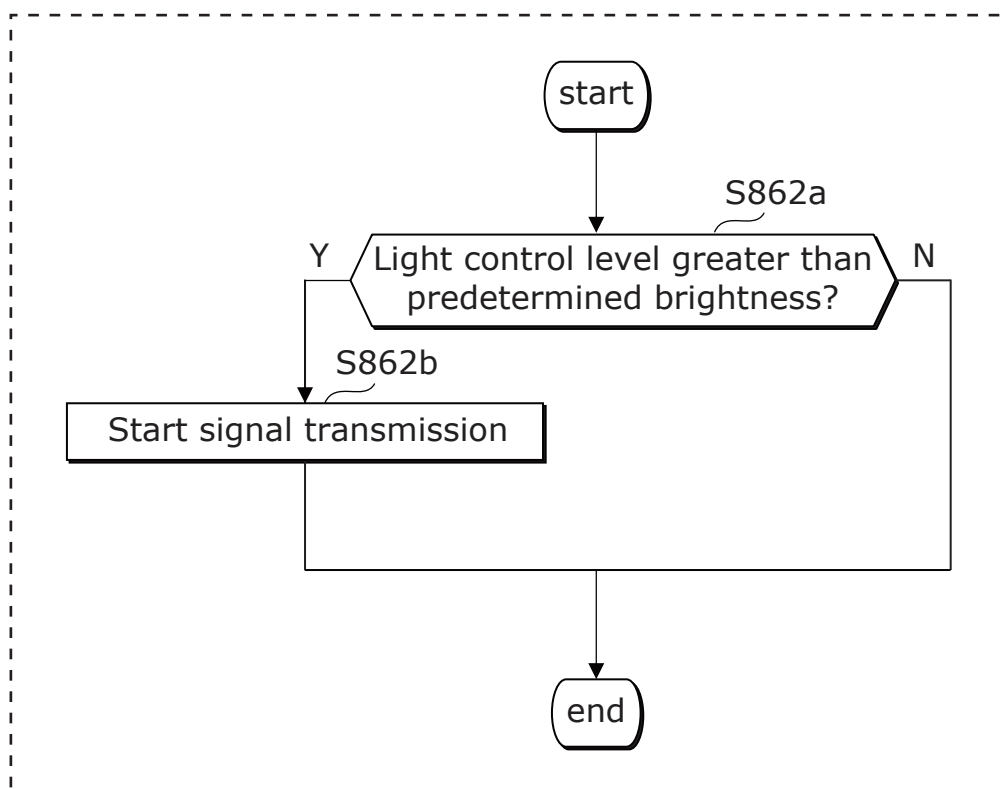


FIG. 222

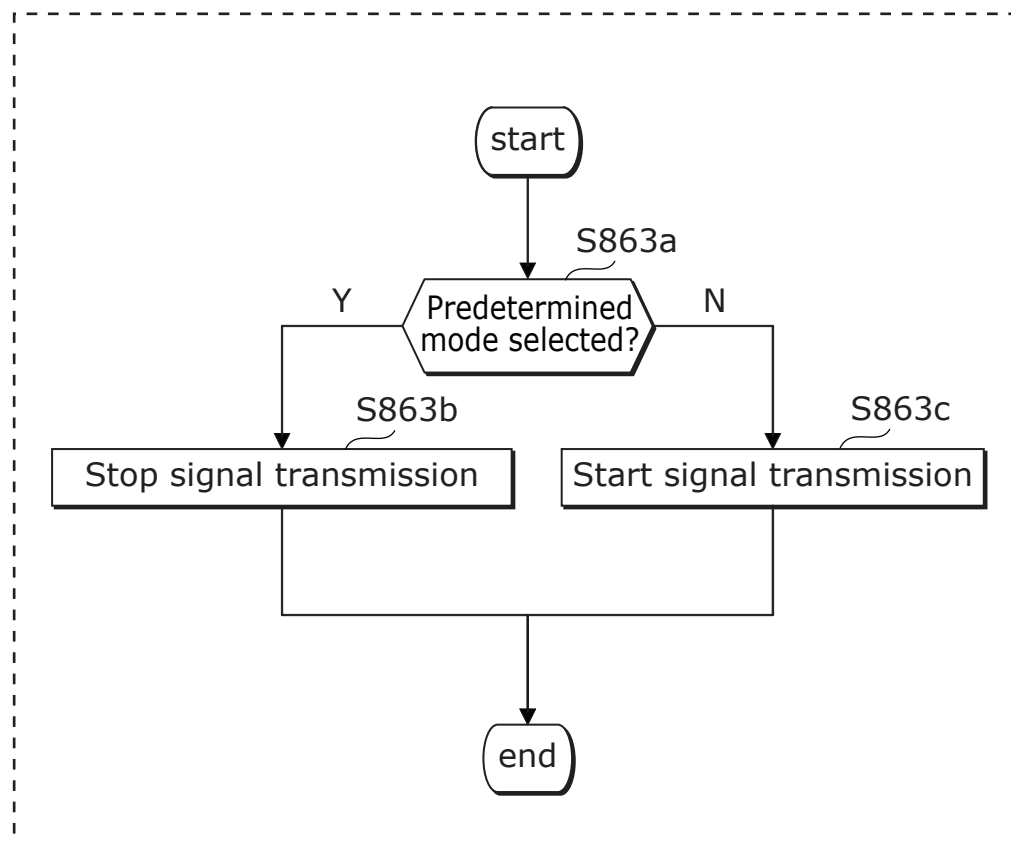


FIG. 223

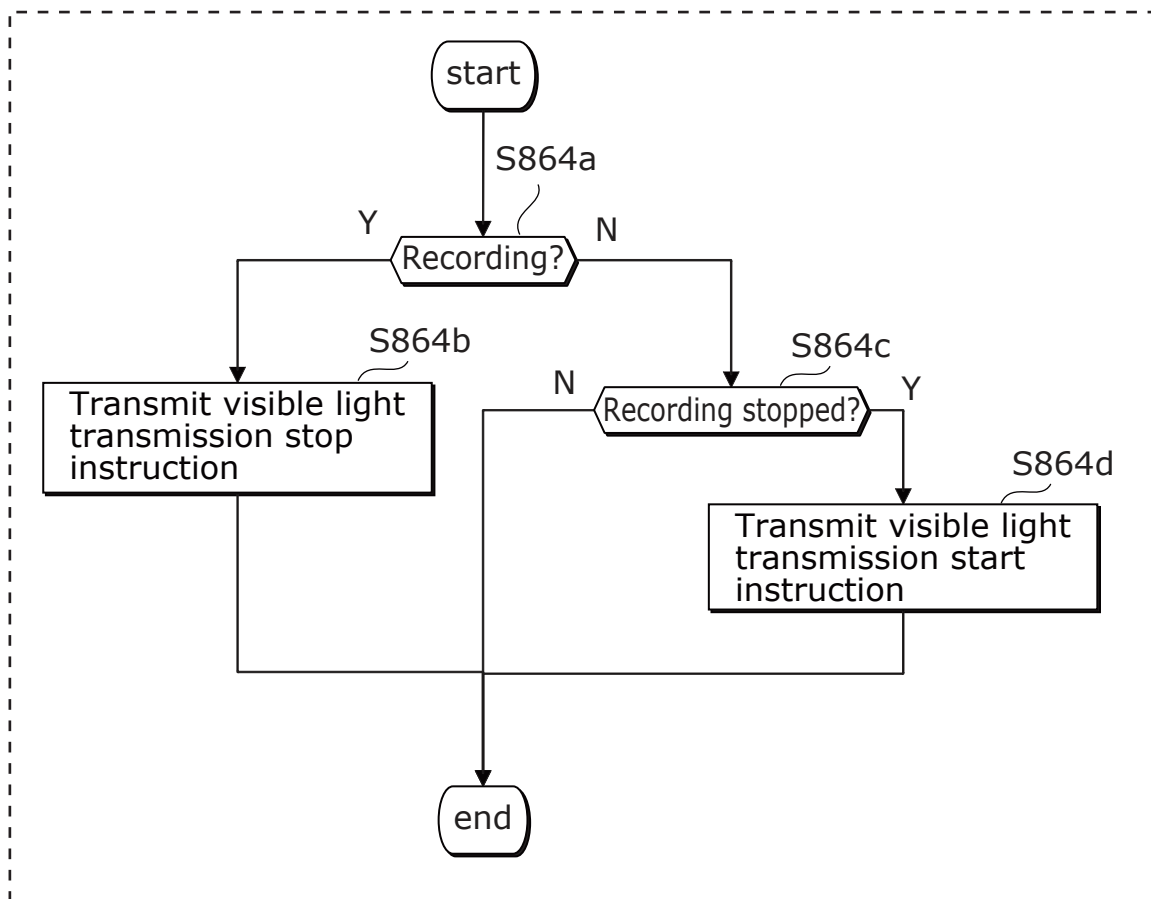


FIG. 224

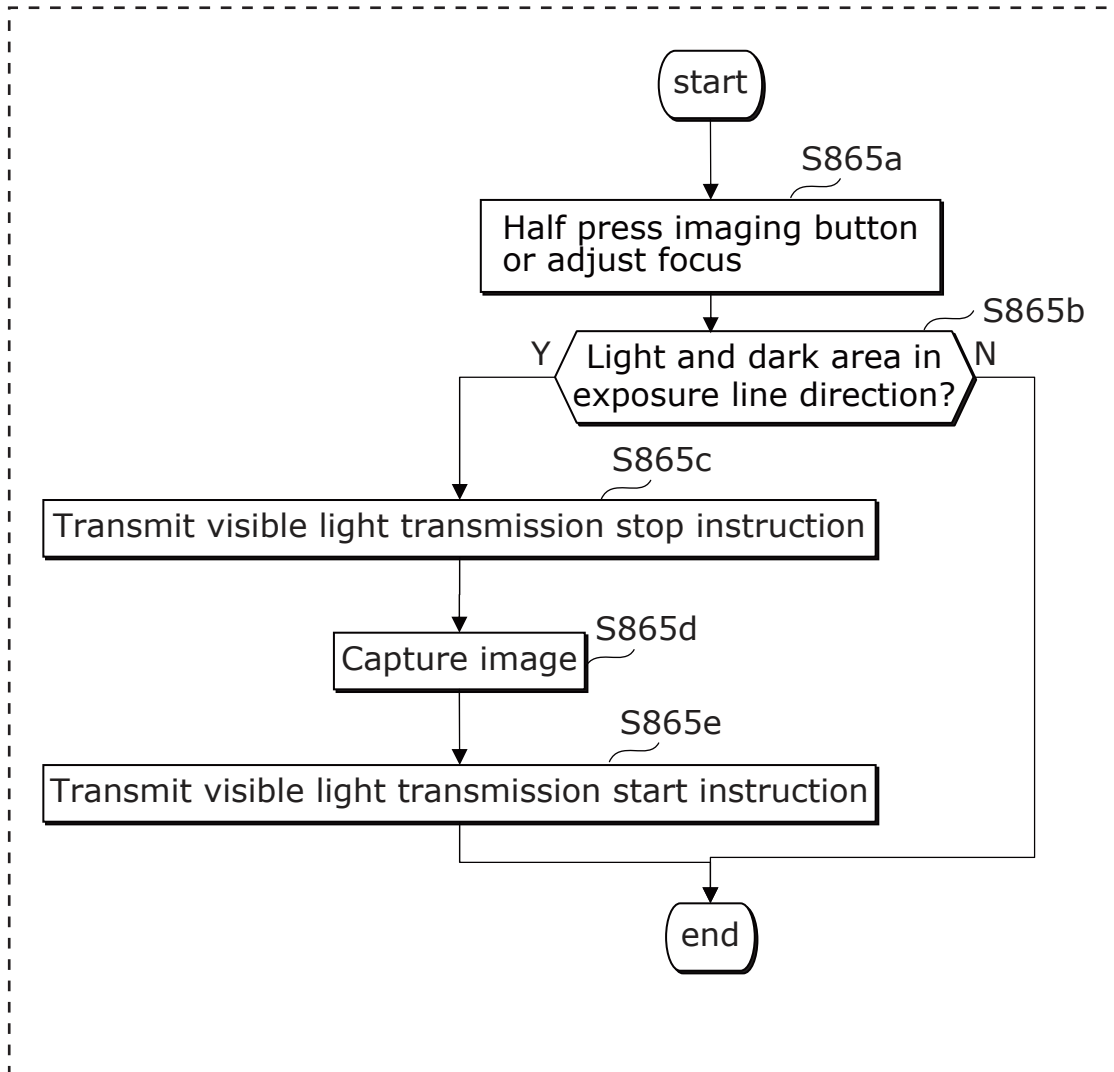


FIG. 225

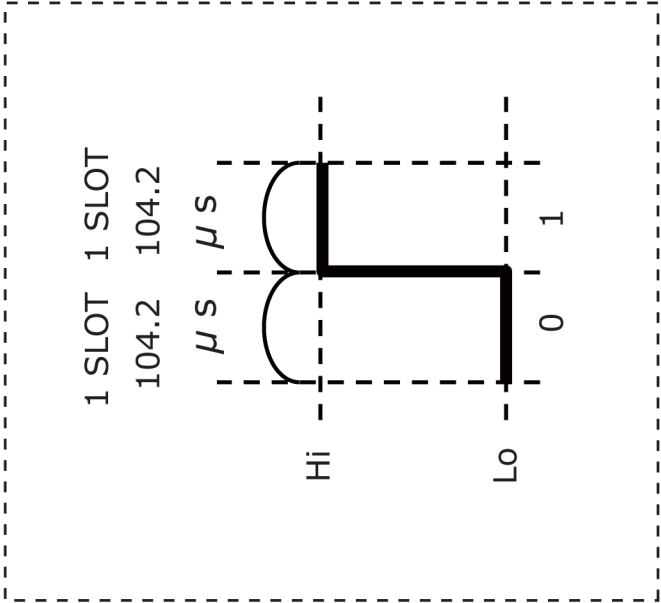


FIG. 226

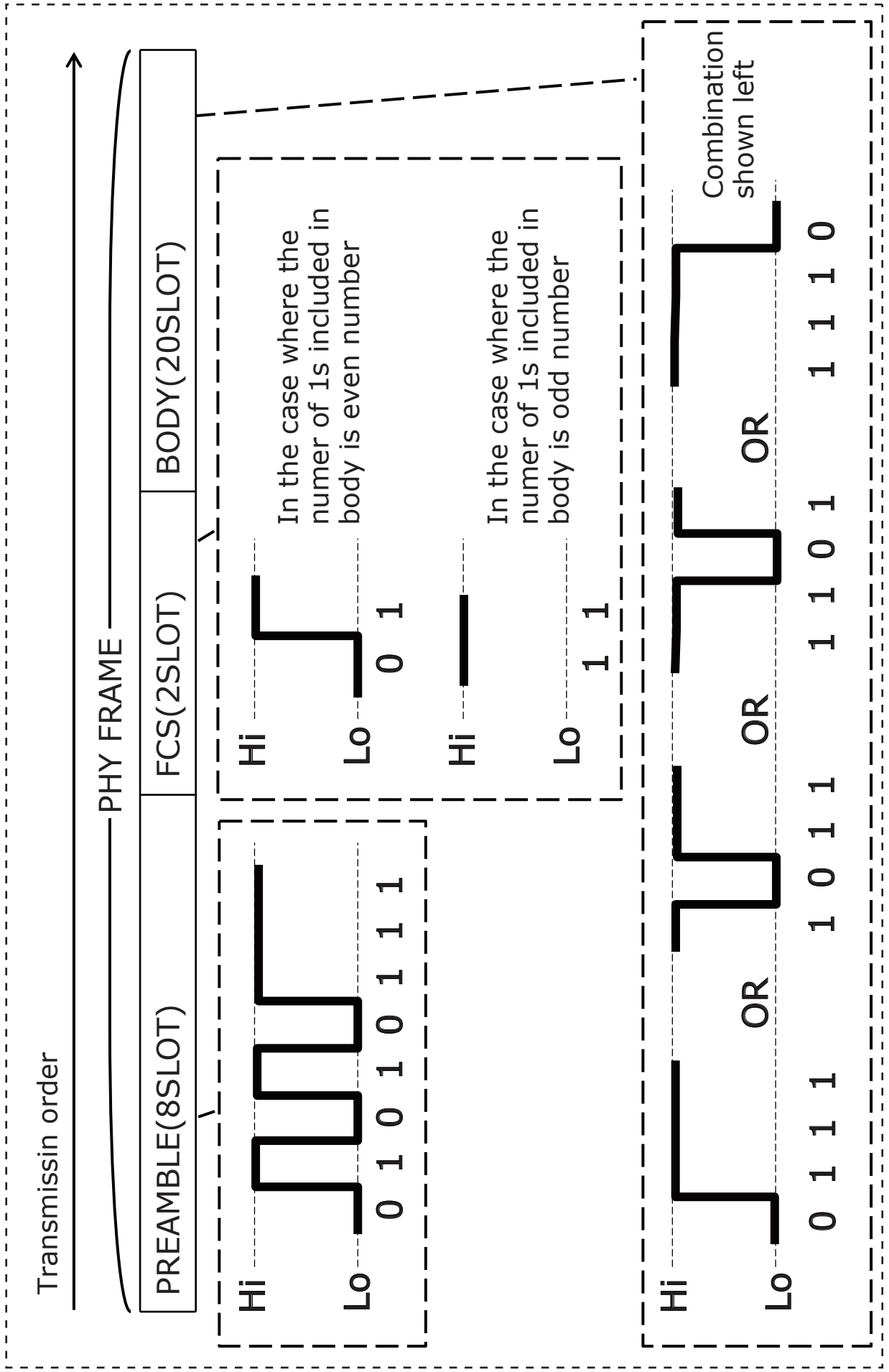


FIG. 227

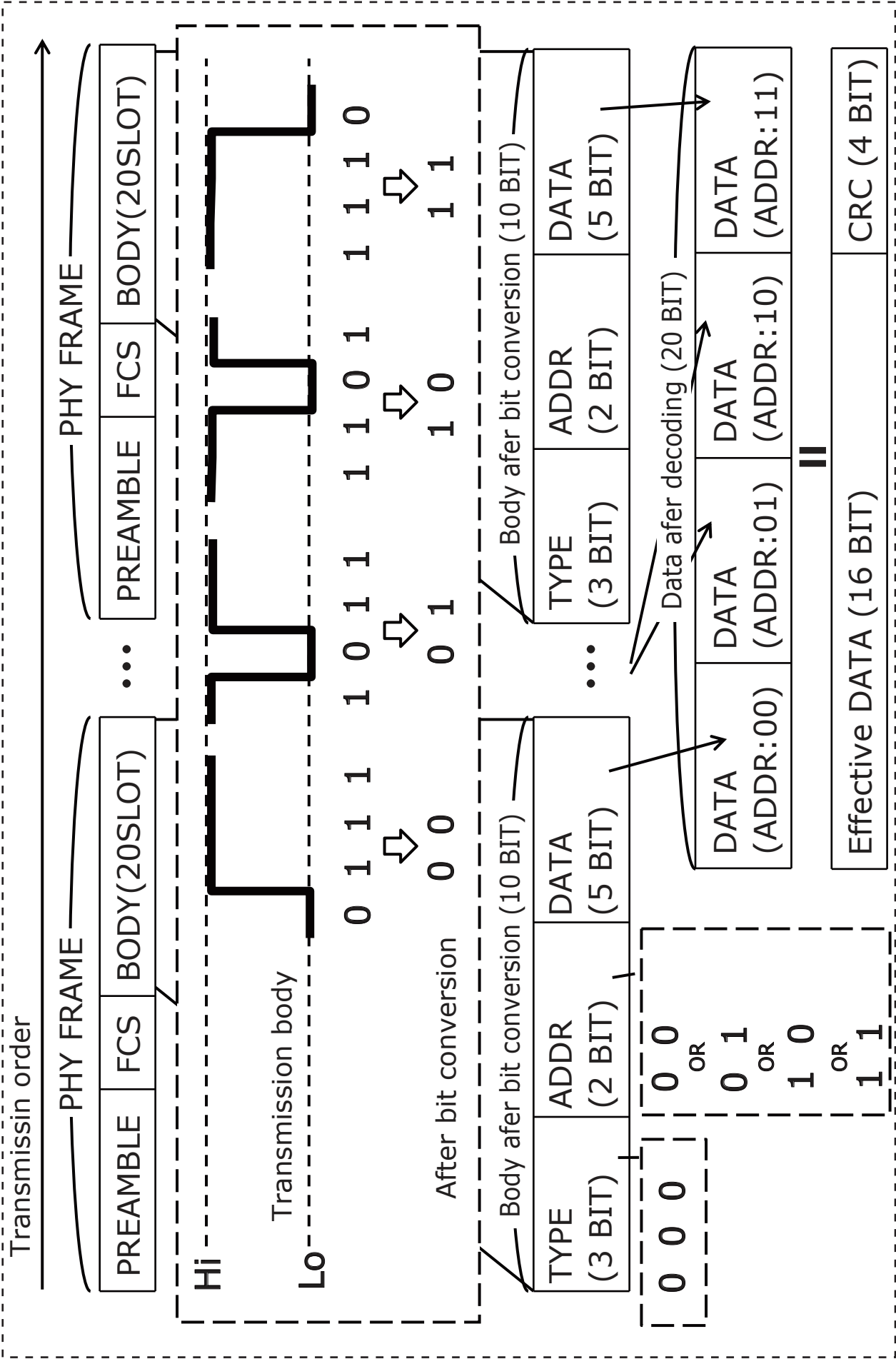


FIG. 228

TYPE definition	TYPEBIT	BODY length	ADDR length	DATA length	Concatenation number	Effective DATA length	CRC type
TYPE1	000	20 SLOT	2 BIT	5 BIT	4	16 BIT	CRC-4
TYPE2	001	24 SLOT	4 BIT	5 BIT	8	32 BIT	CRC-8
TYPE3	010	24 SLOT	5 BIT	4 BIT	19	64 BIT	CRC-12
TYPE4	011	28 SLOT	5 BIT	6 BIT	24	128 BIT	CRC-16

FIG. 229

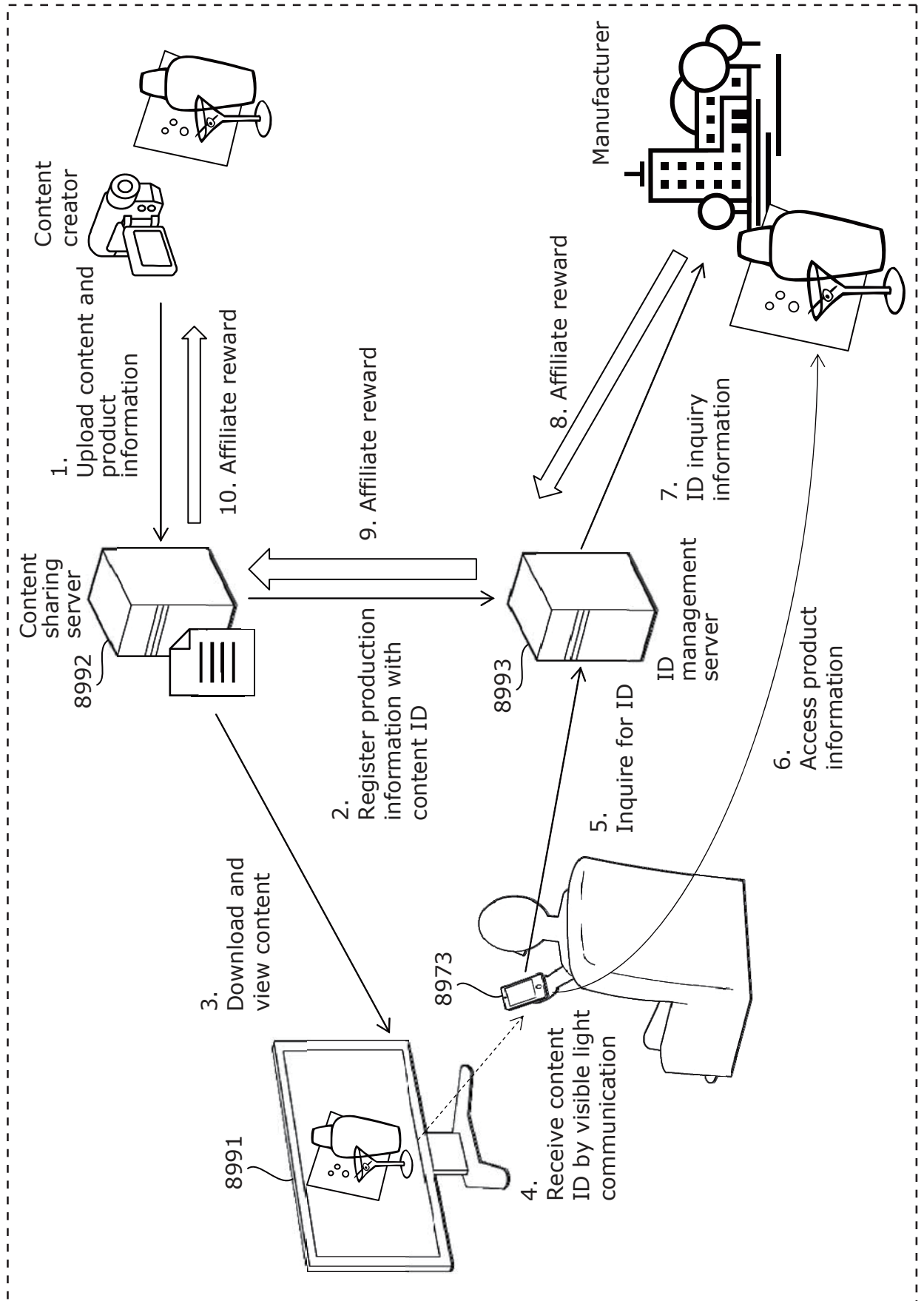


FIG. 230

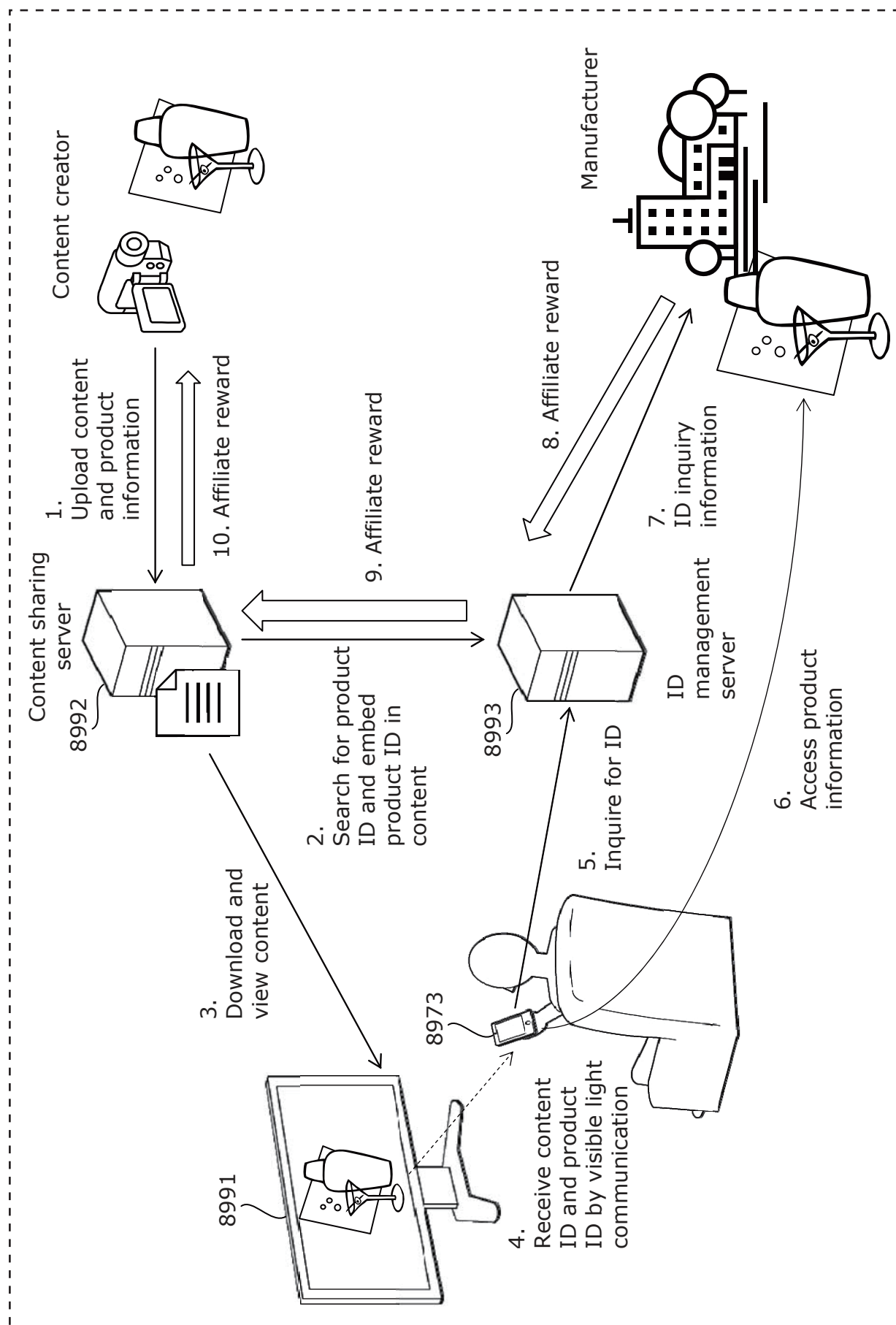


FIG. 231

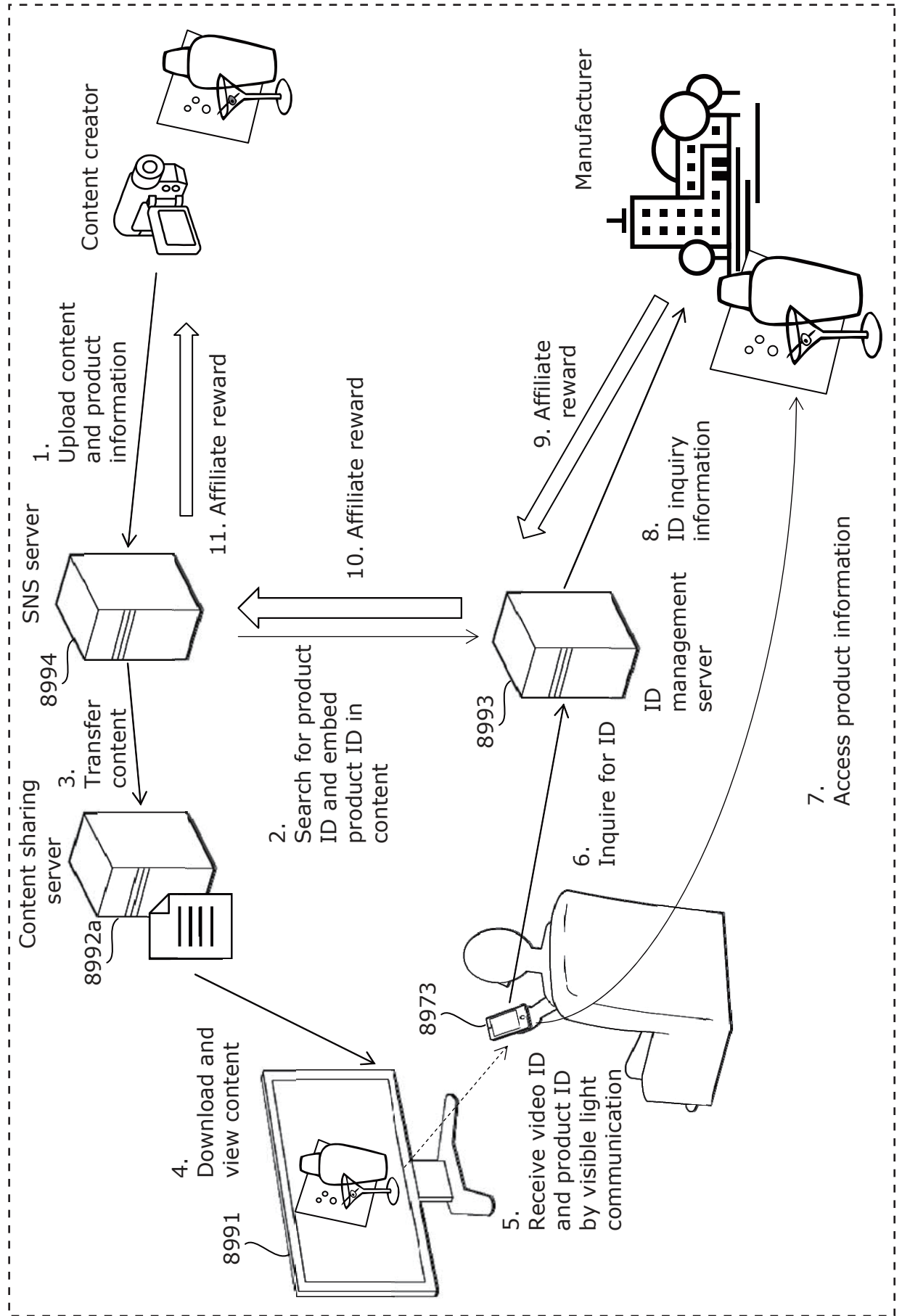


FIG. 232

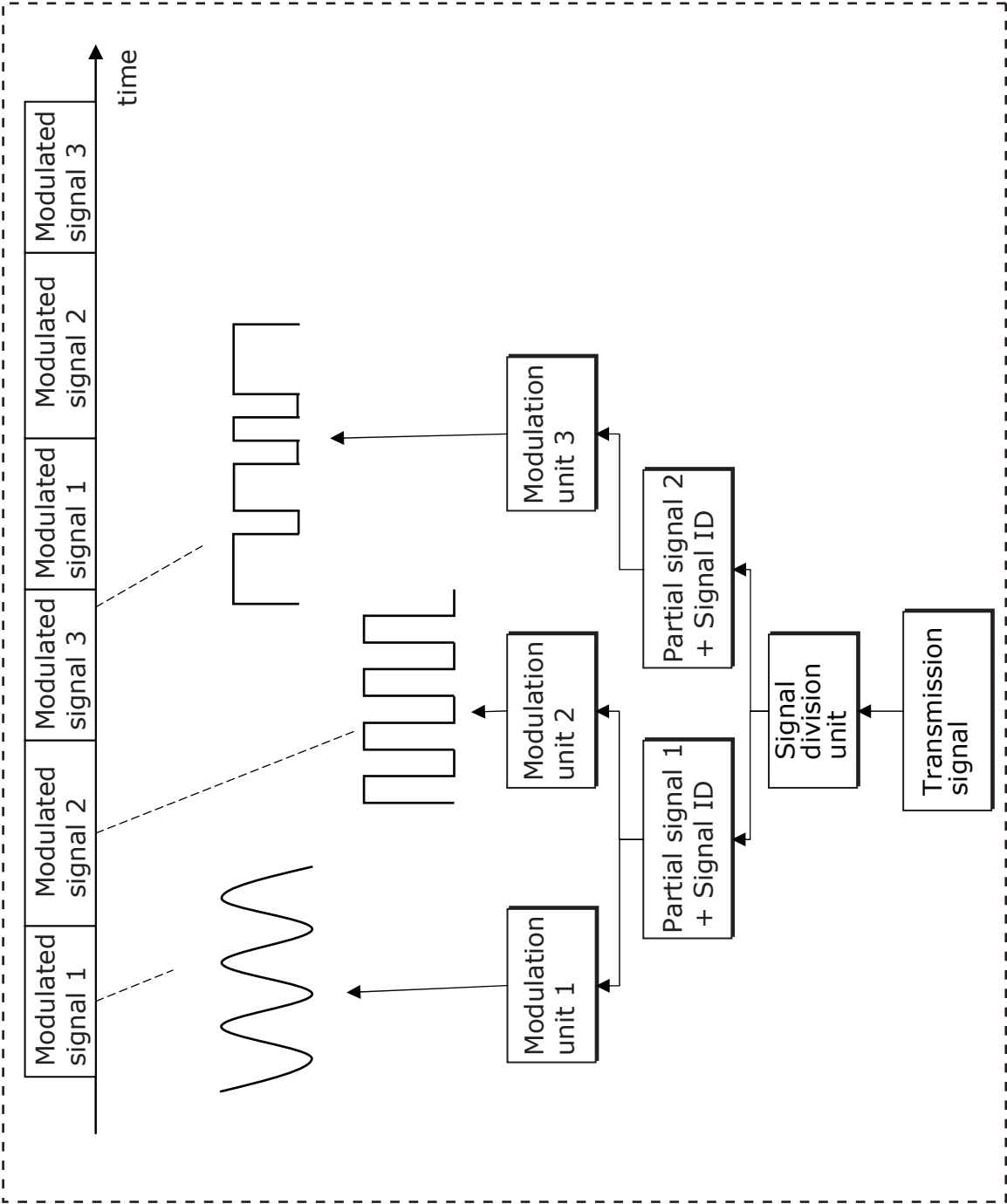


FIG. 233

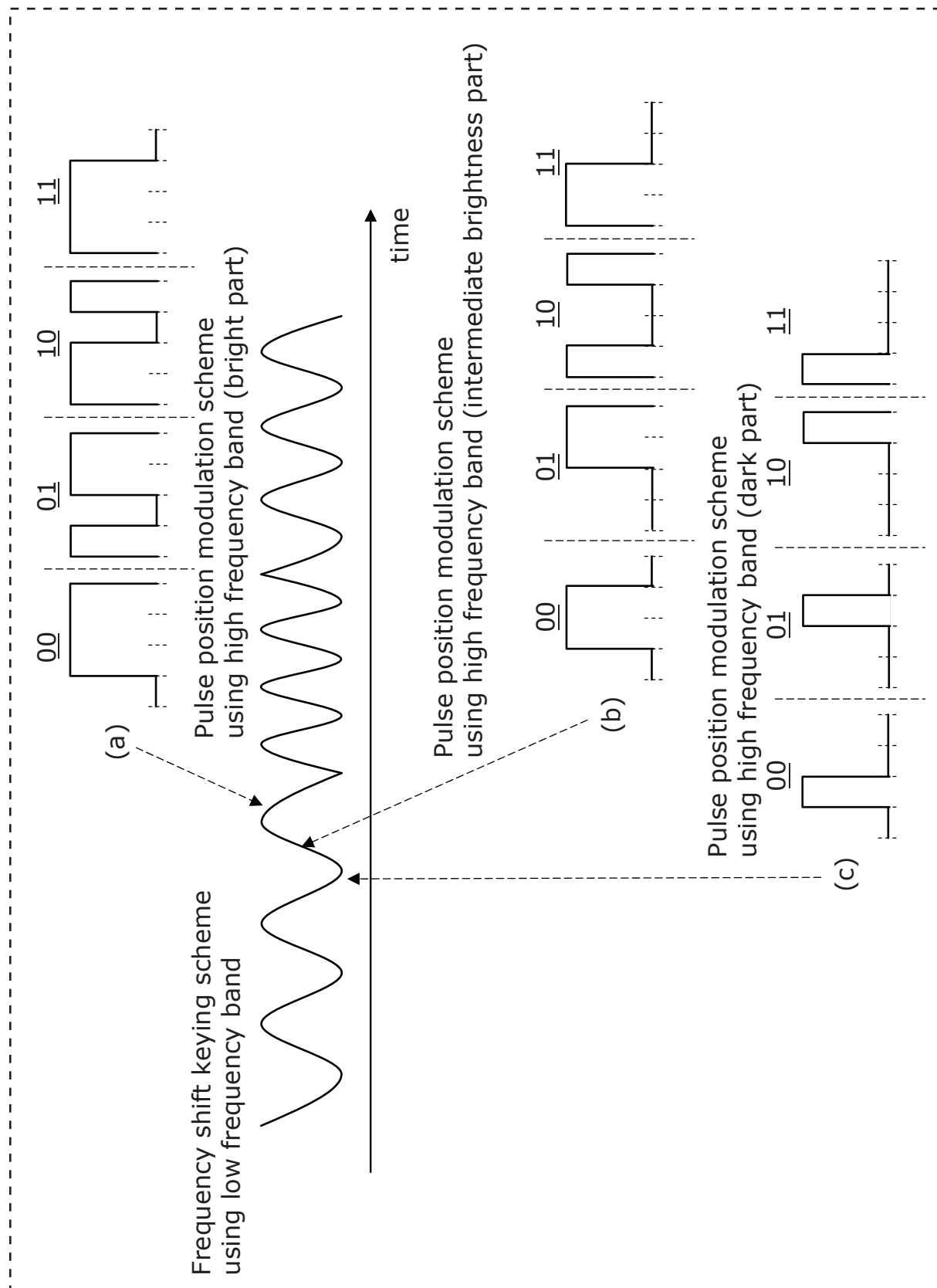


FIG. 234

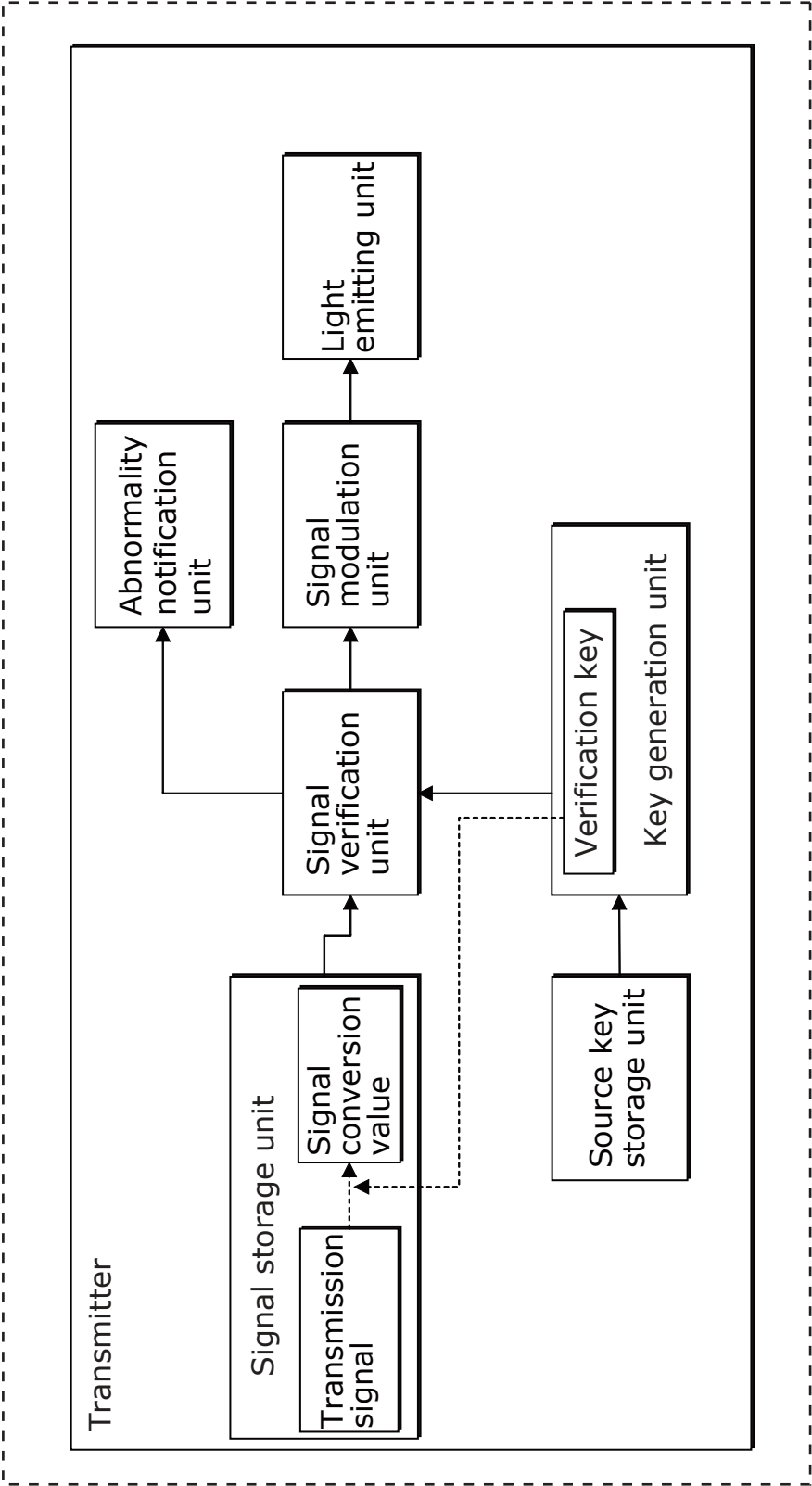


FIG. 235

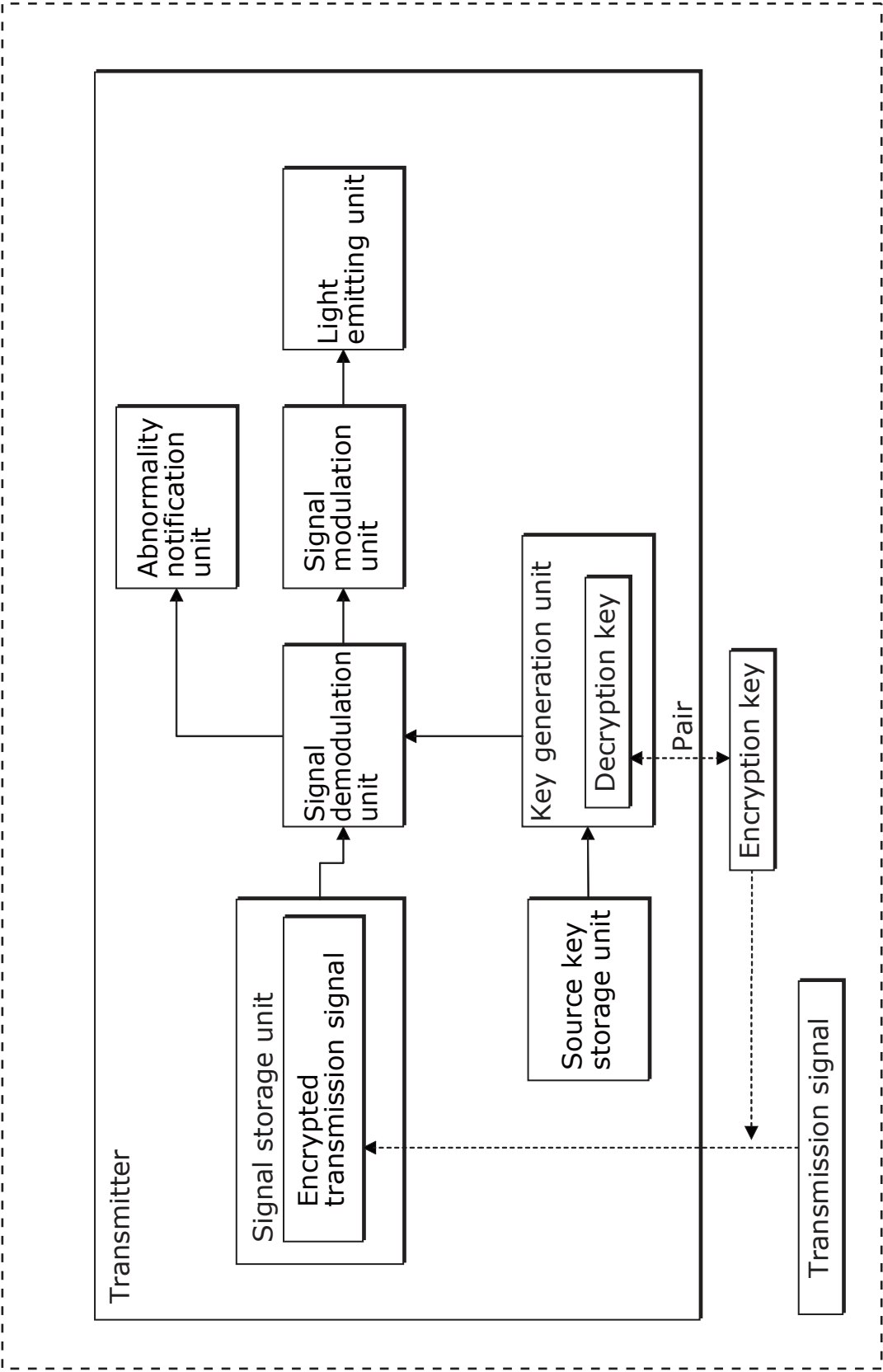


FIG. 236

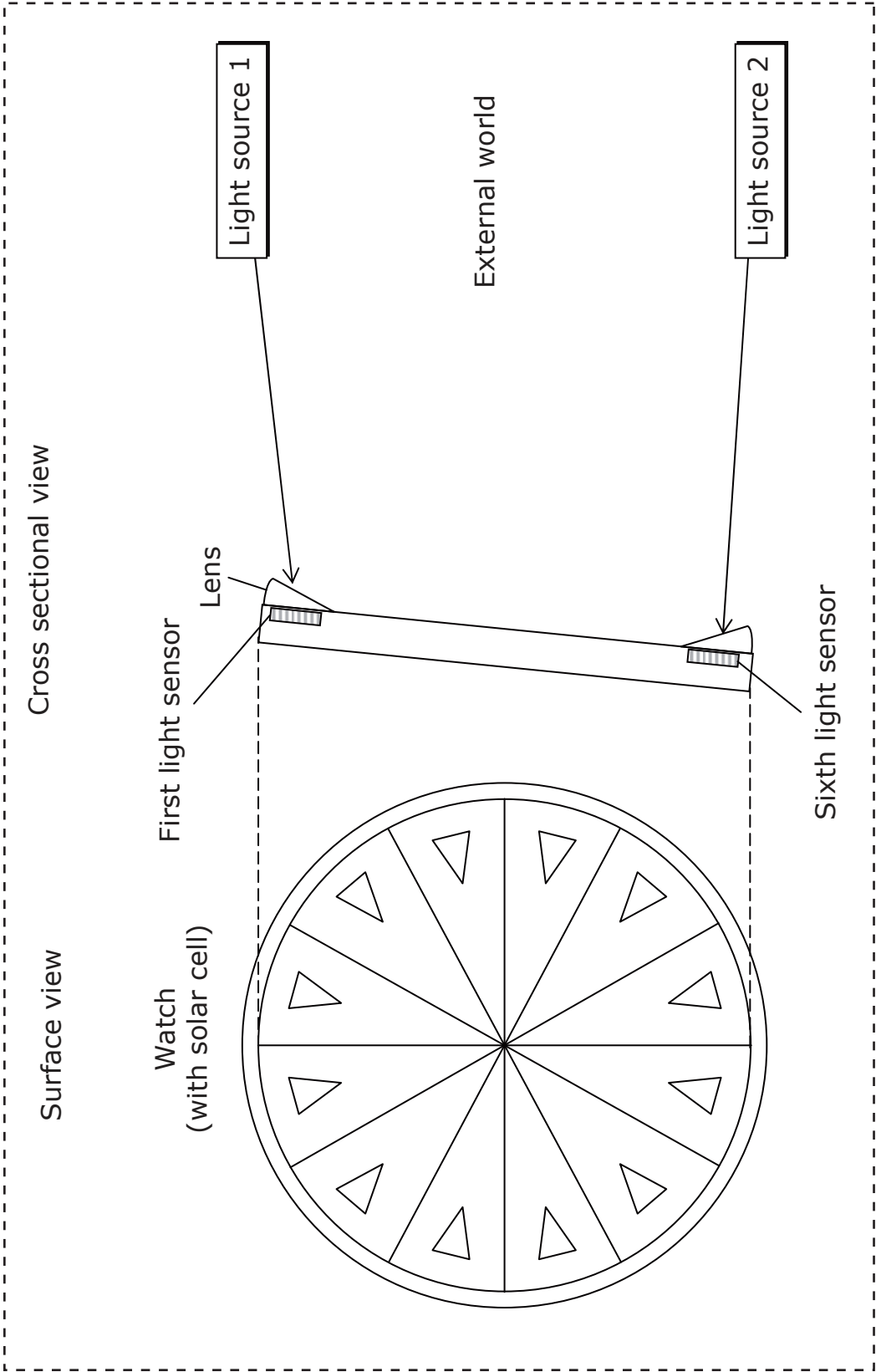


FIG. 237

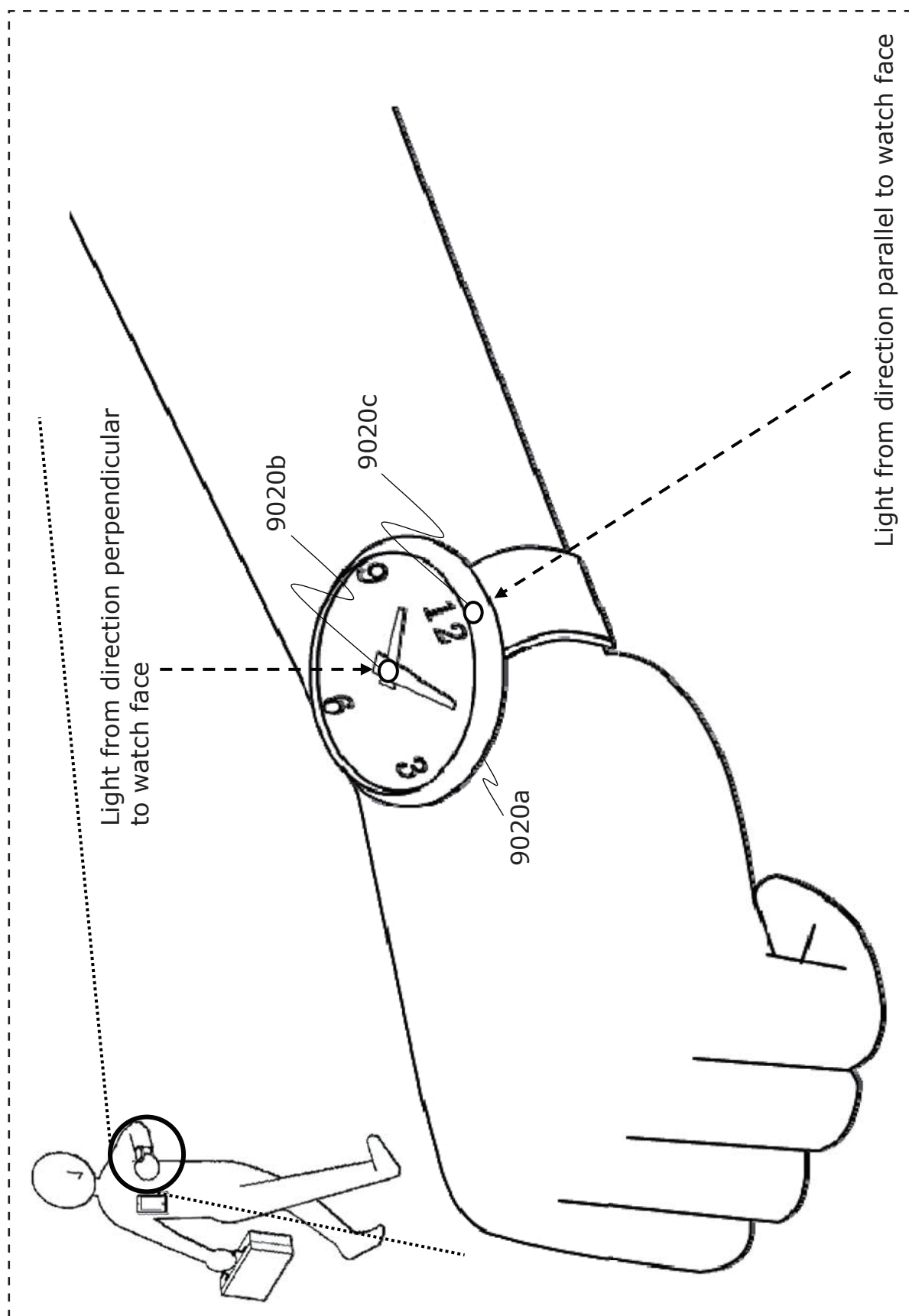


FIG. 239A

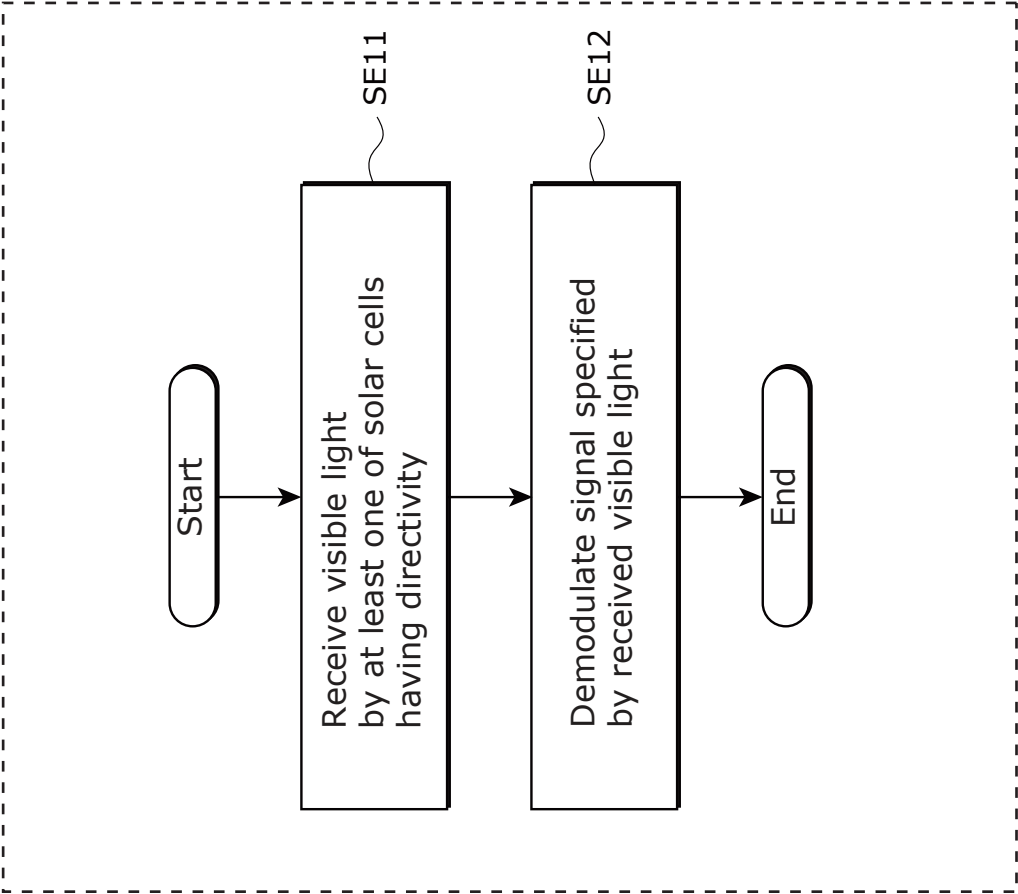


FIG. 239B

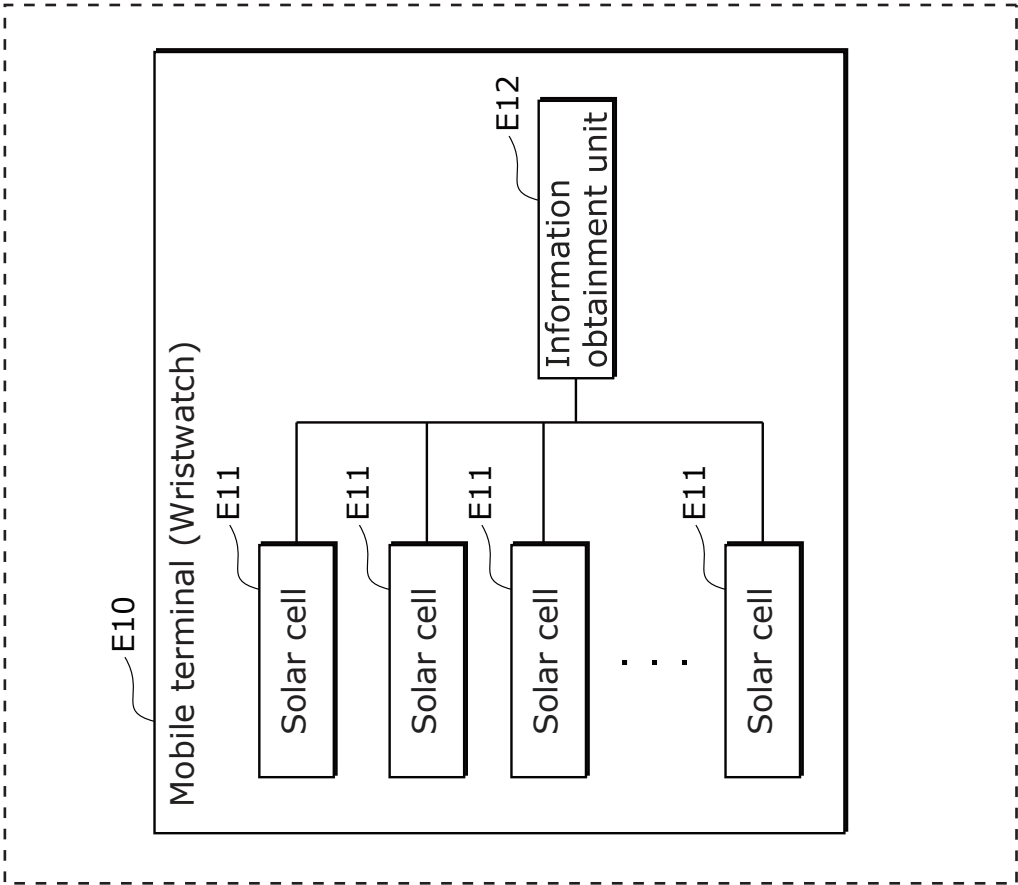


FIG. 241

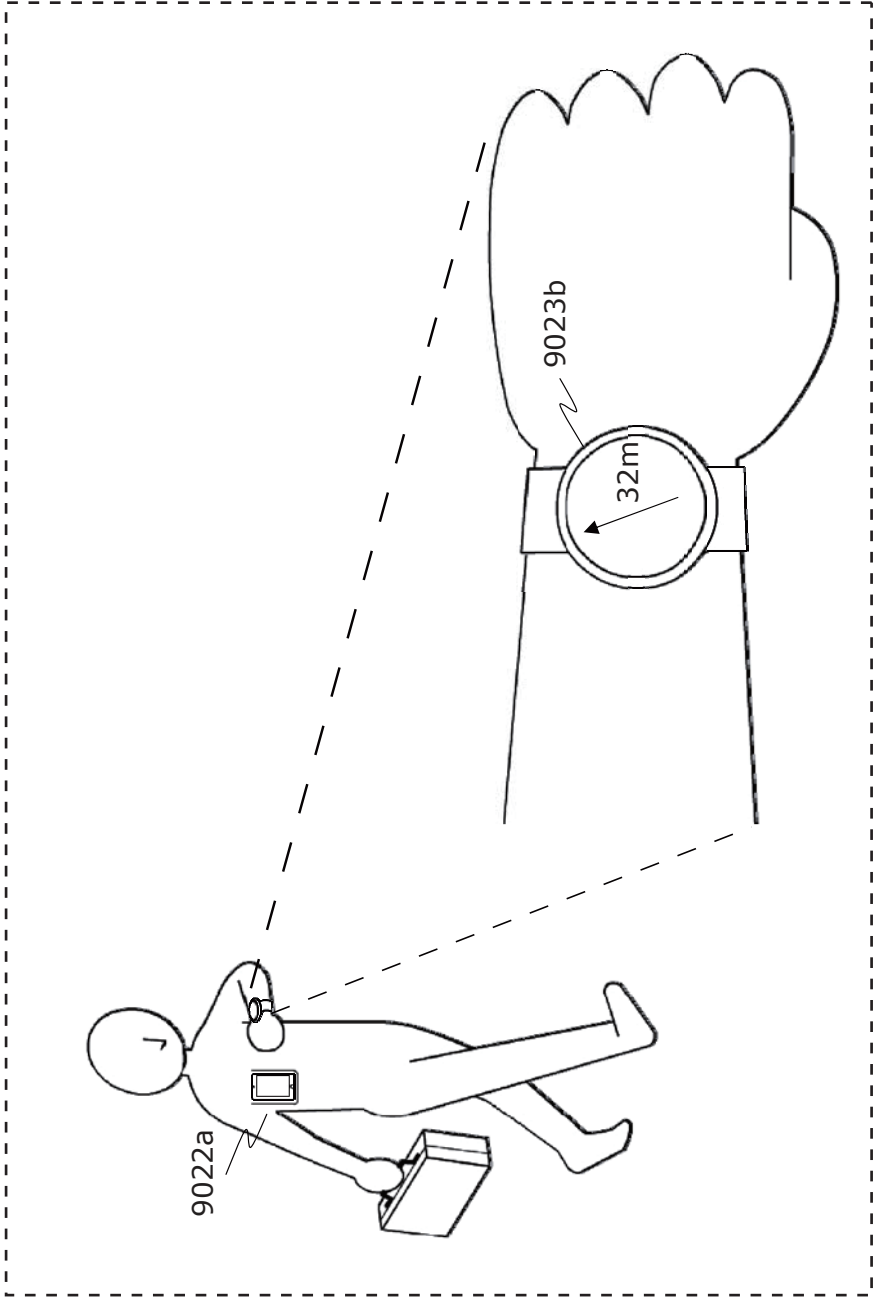


FIG. 242A

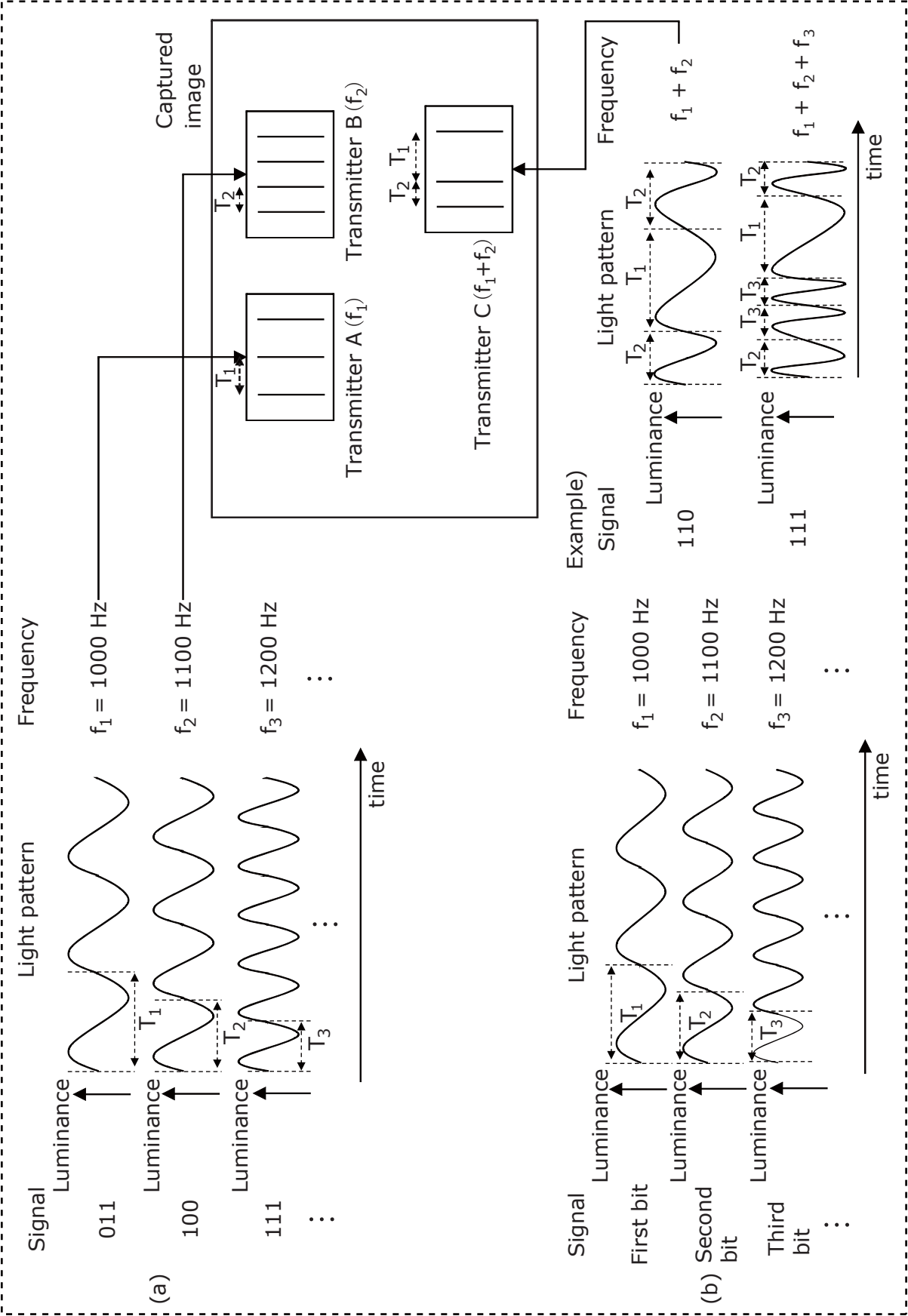


FIG. 242B

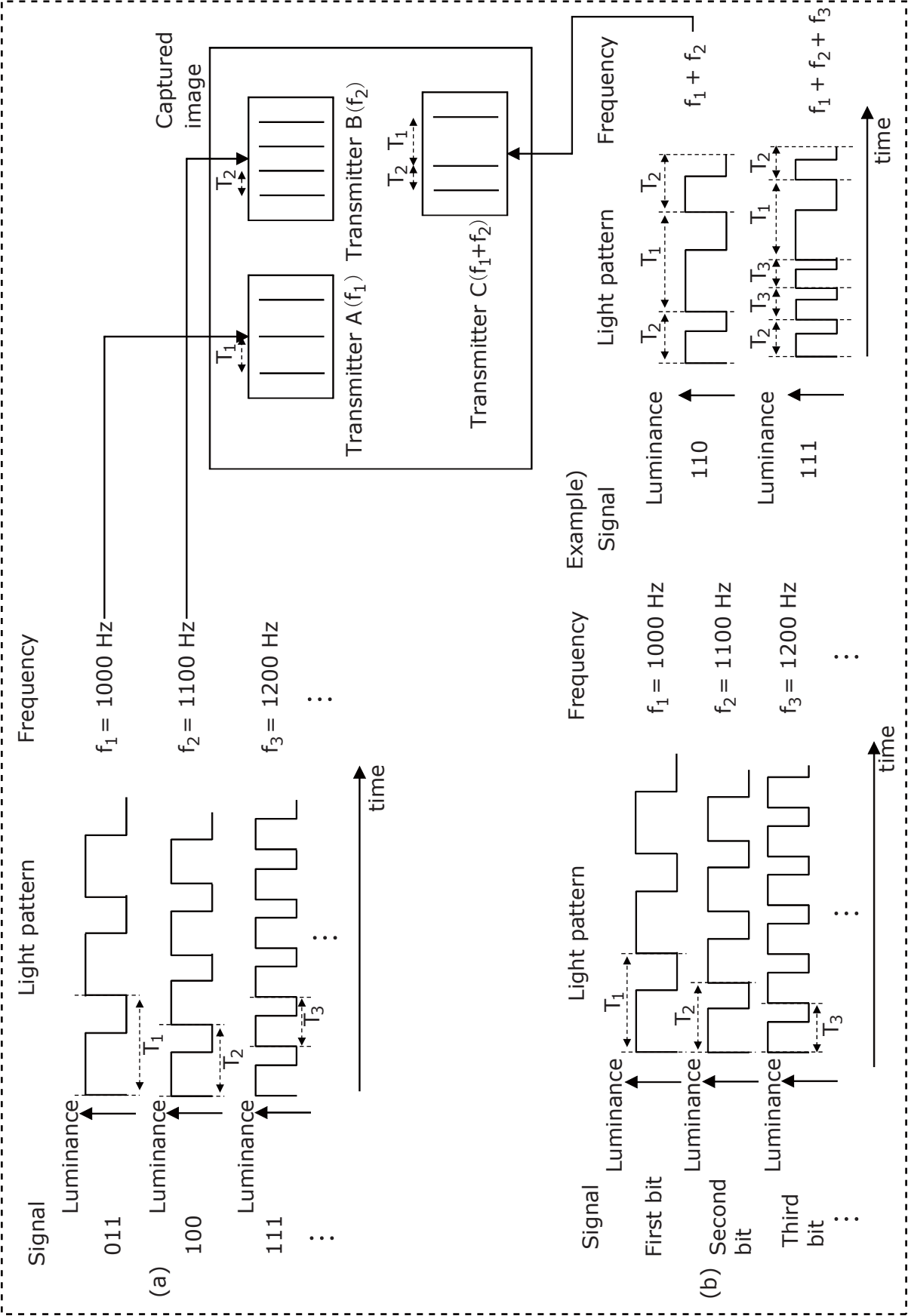


FIG. 242C

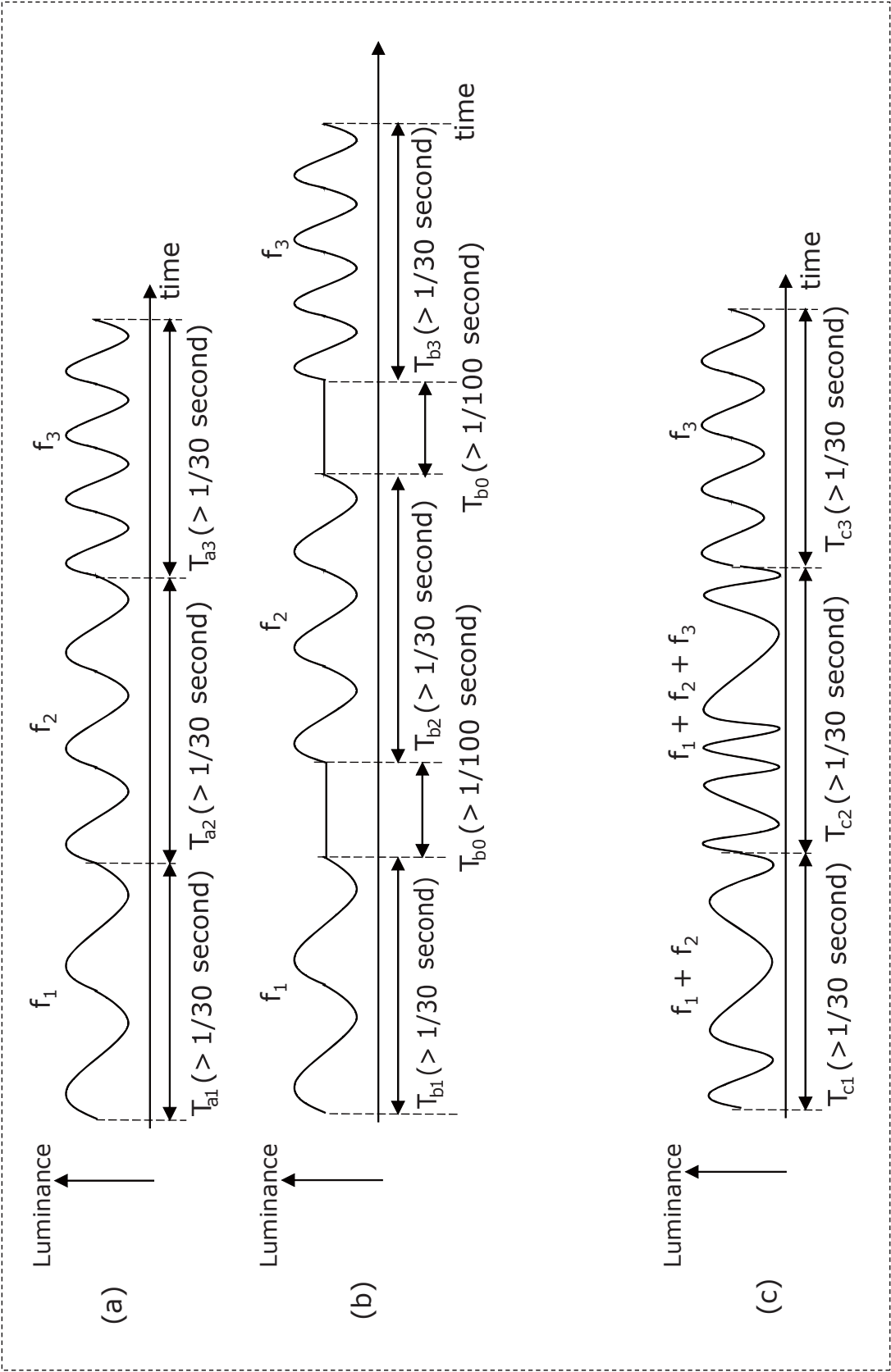


FIG. 242D

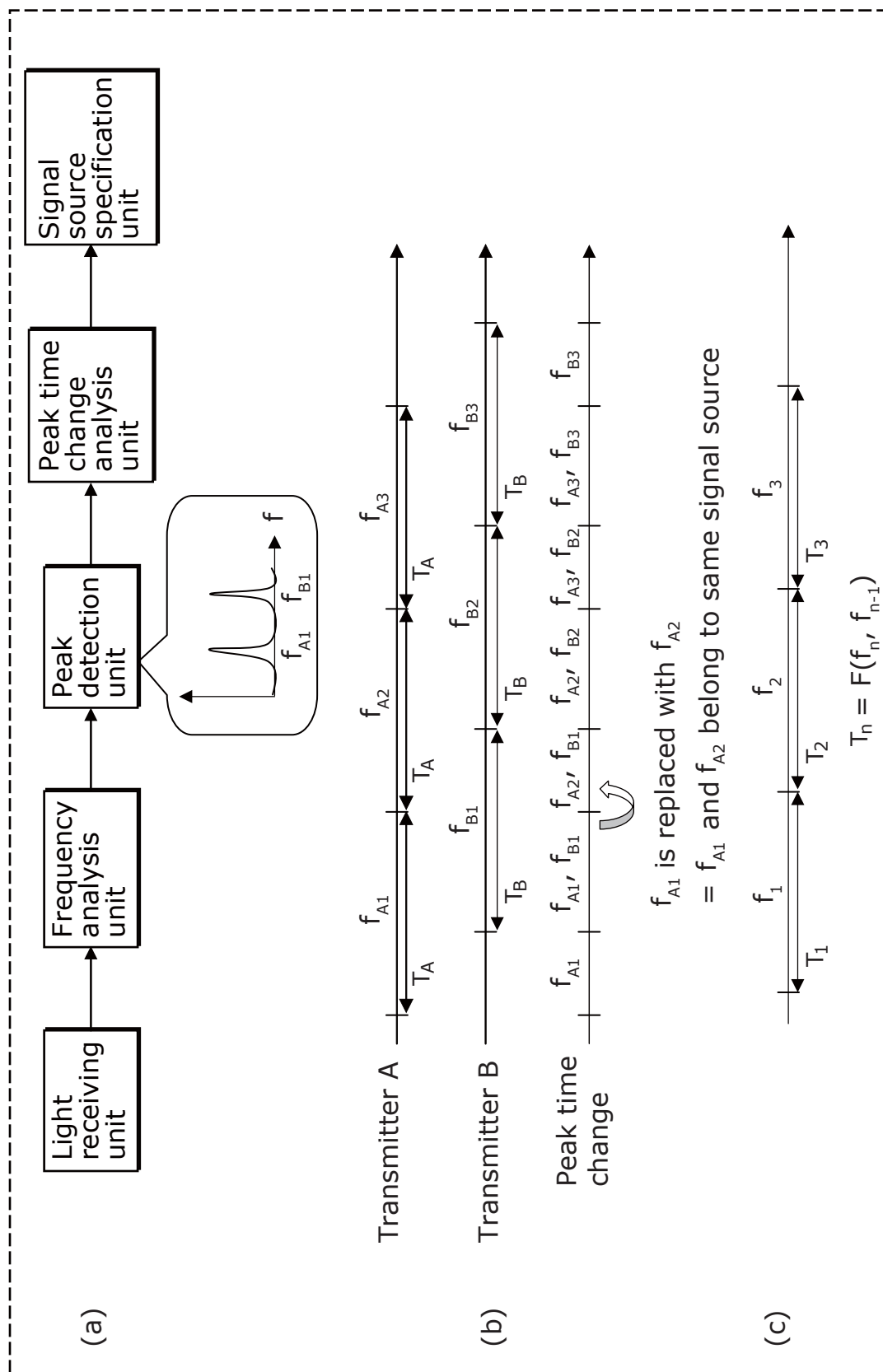


FIG. 242E

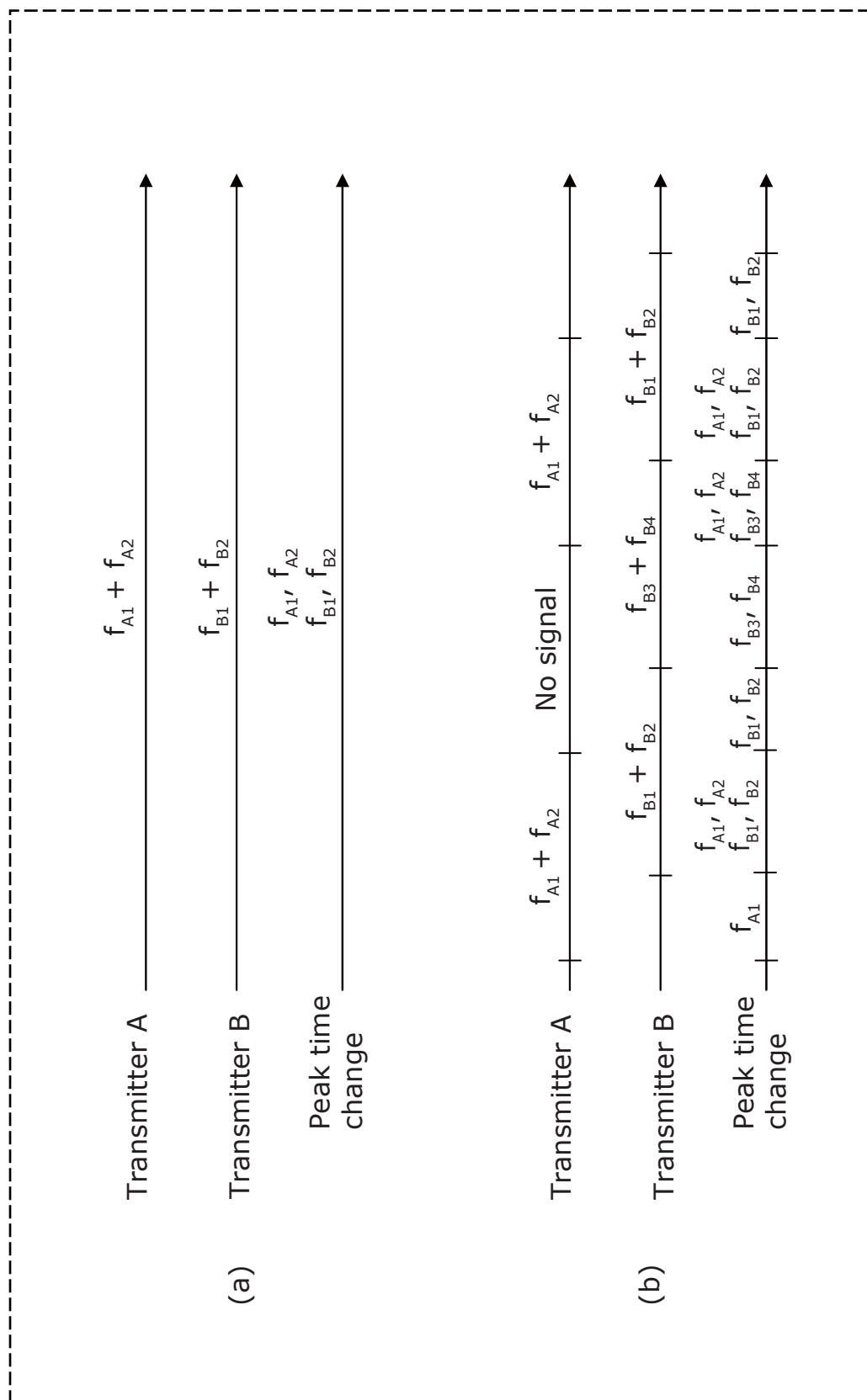


FIG. 242F

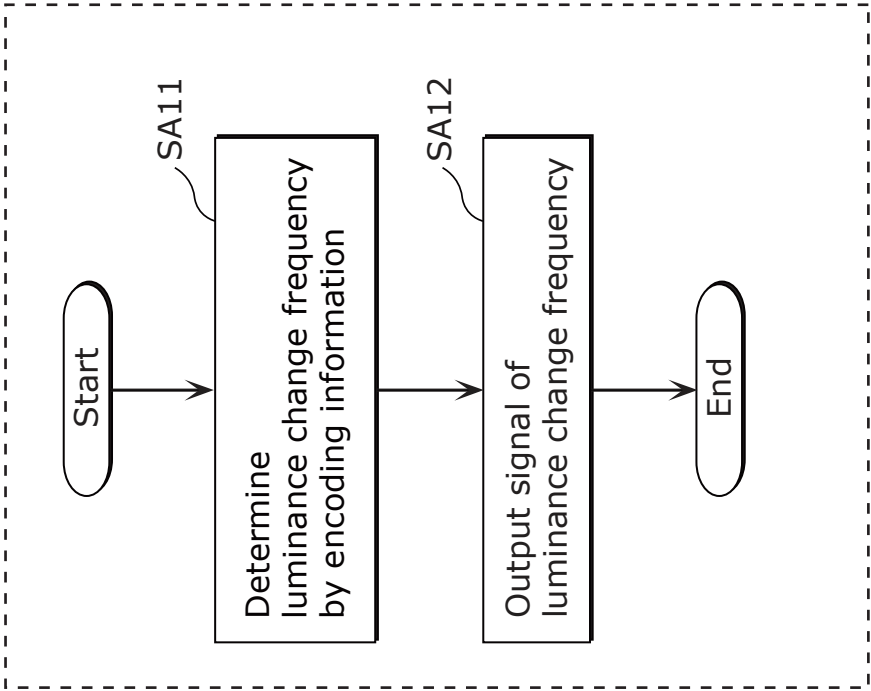


FIG. 242G

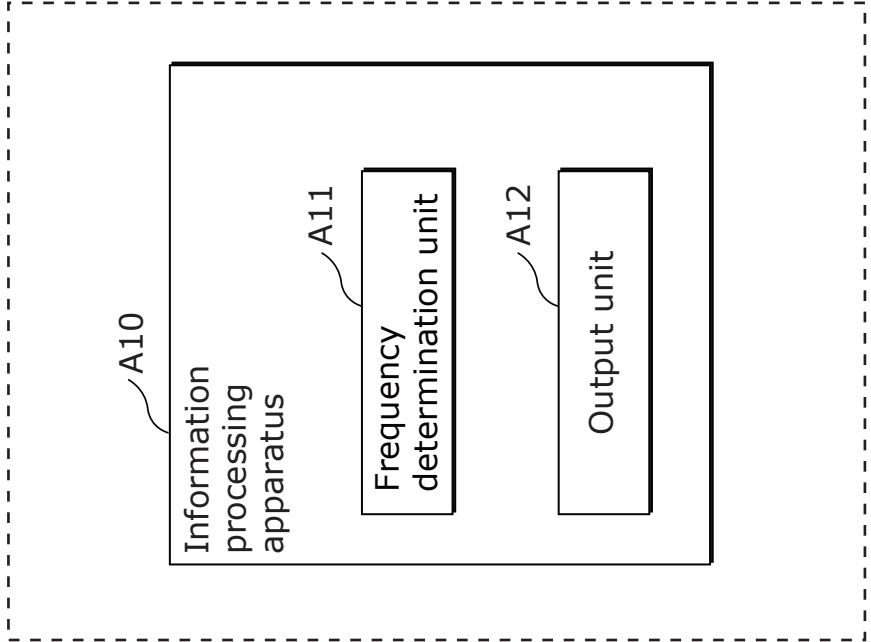


FIG. 243B

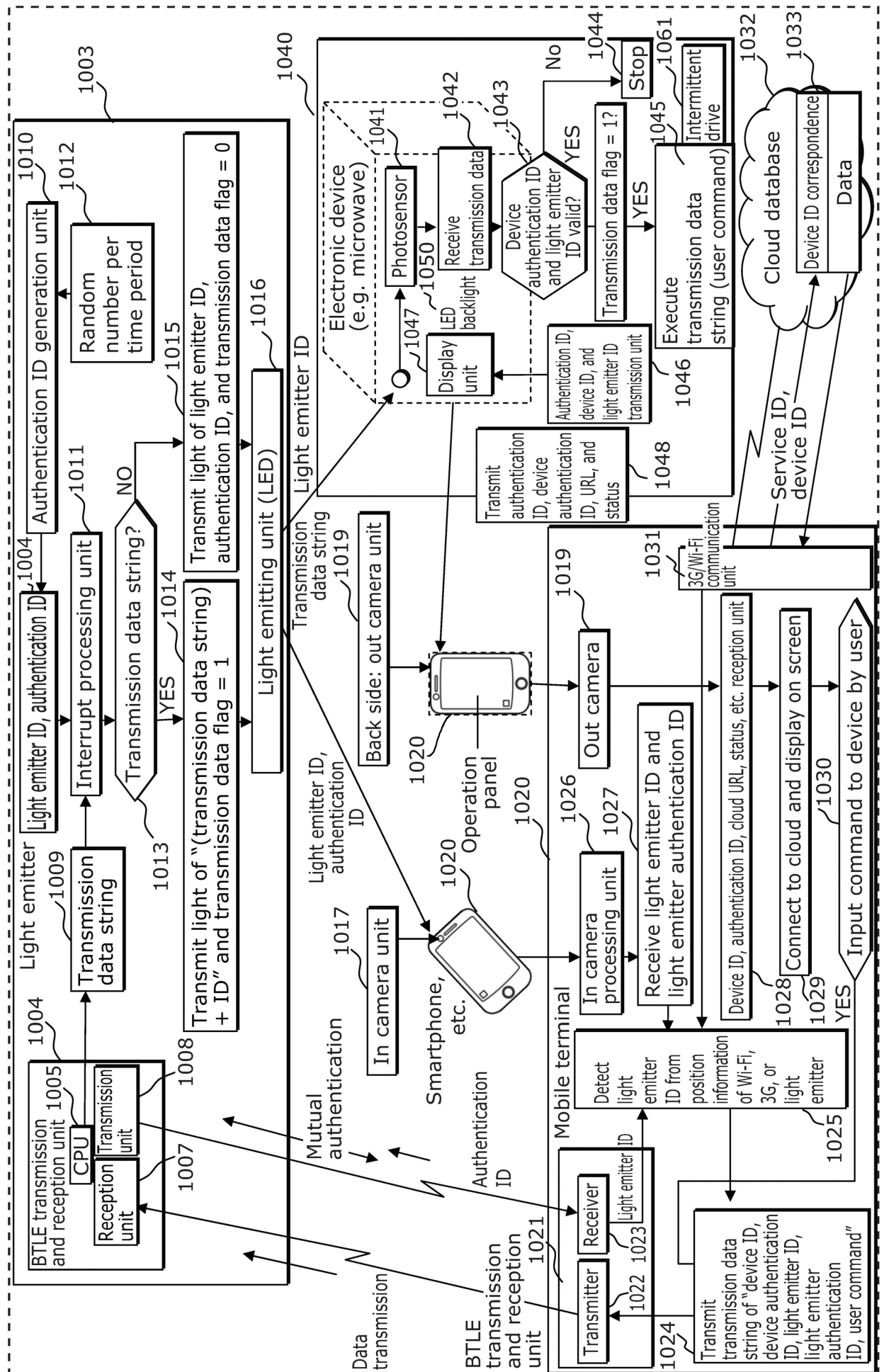


FIG. 243C

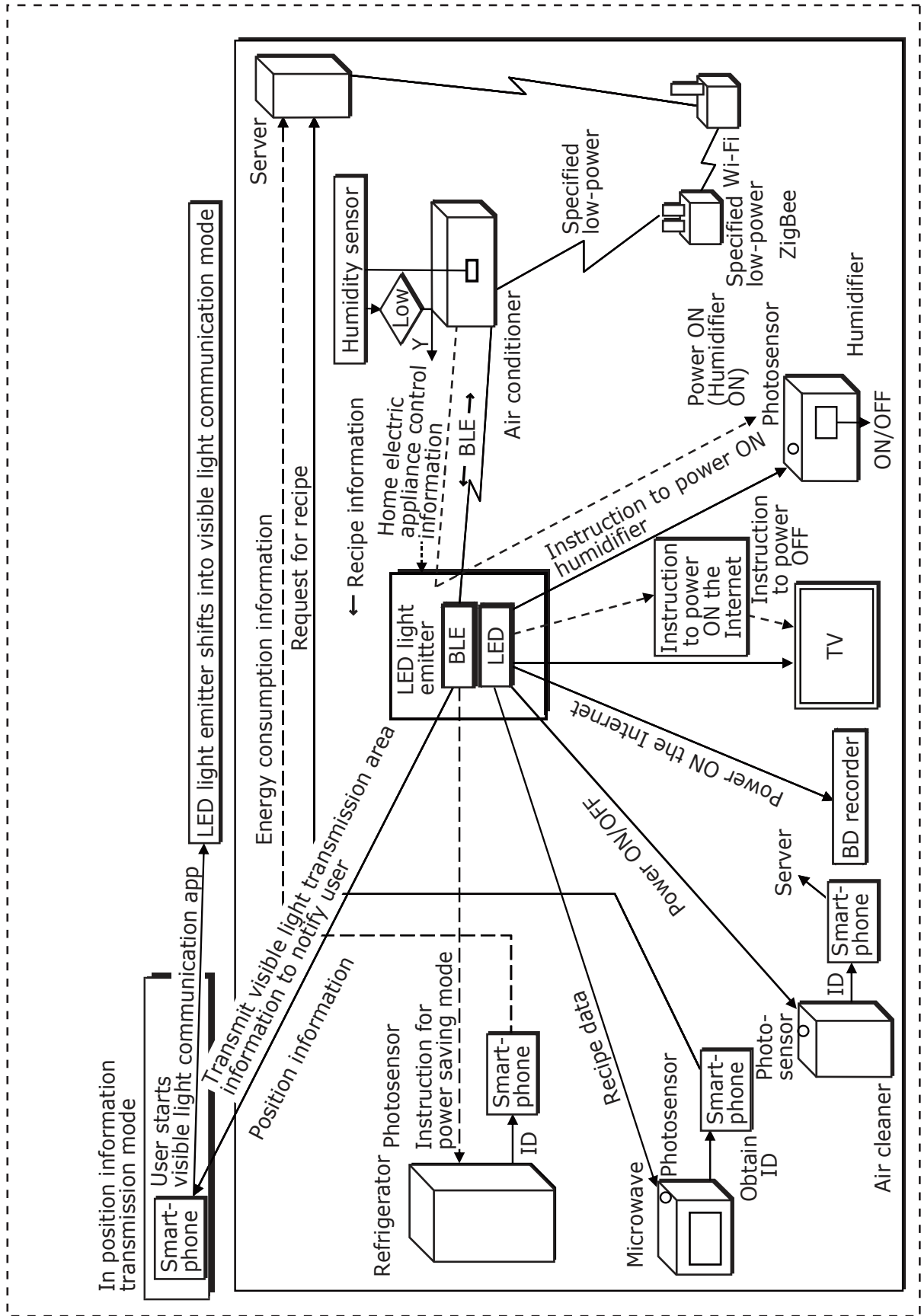


FIG. 244

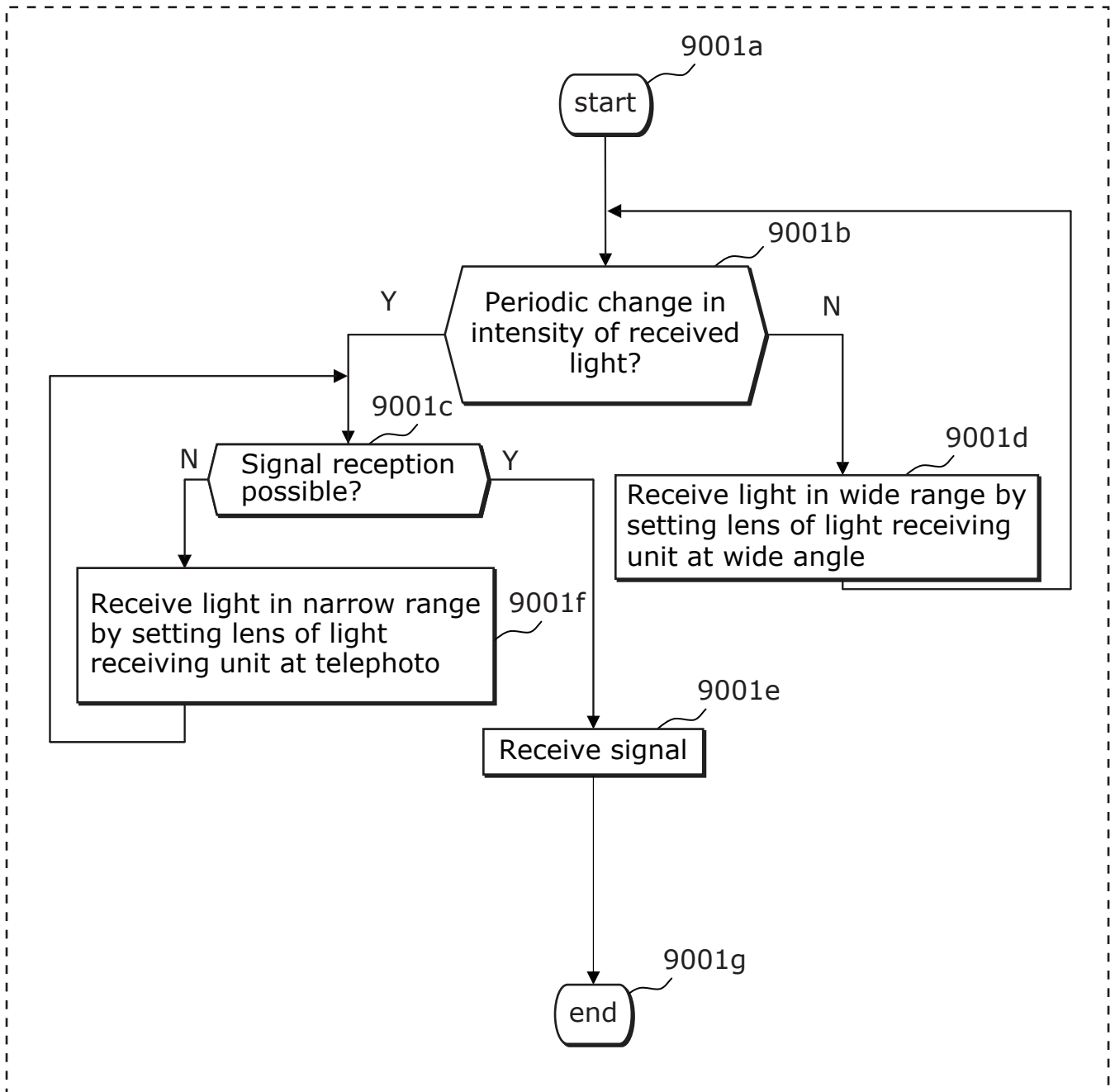


FIG. 245

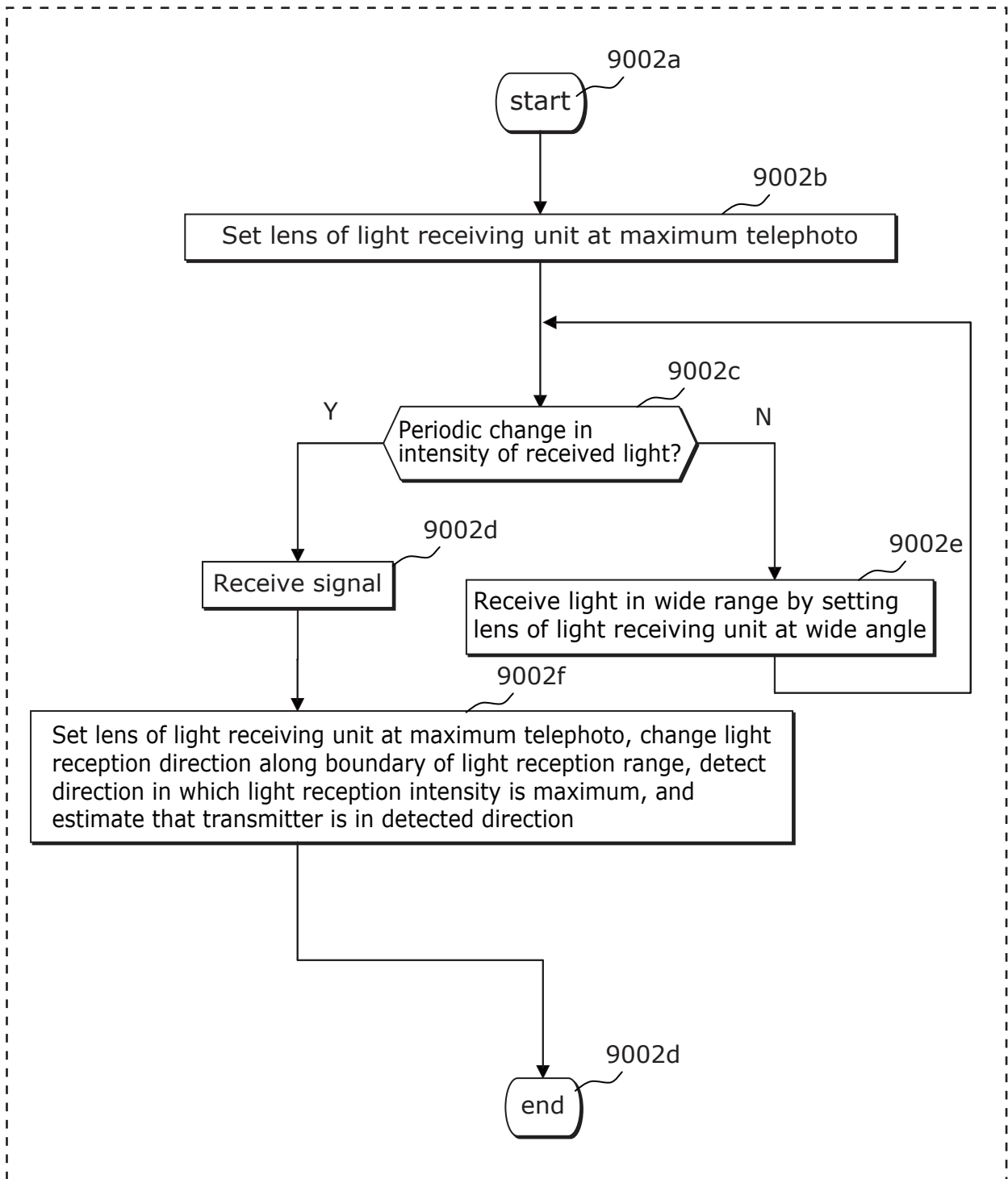


FIG. 246

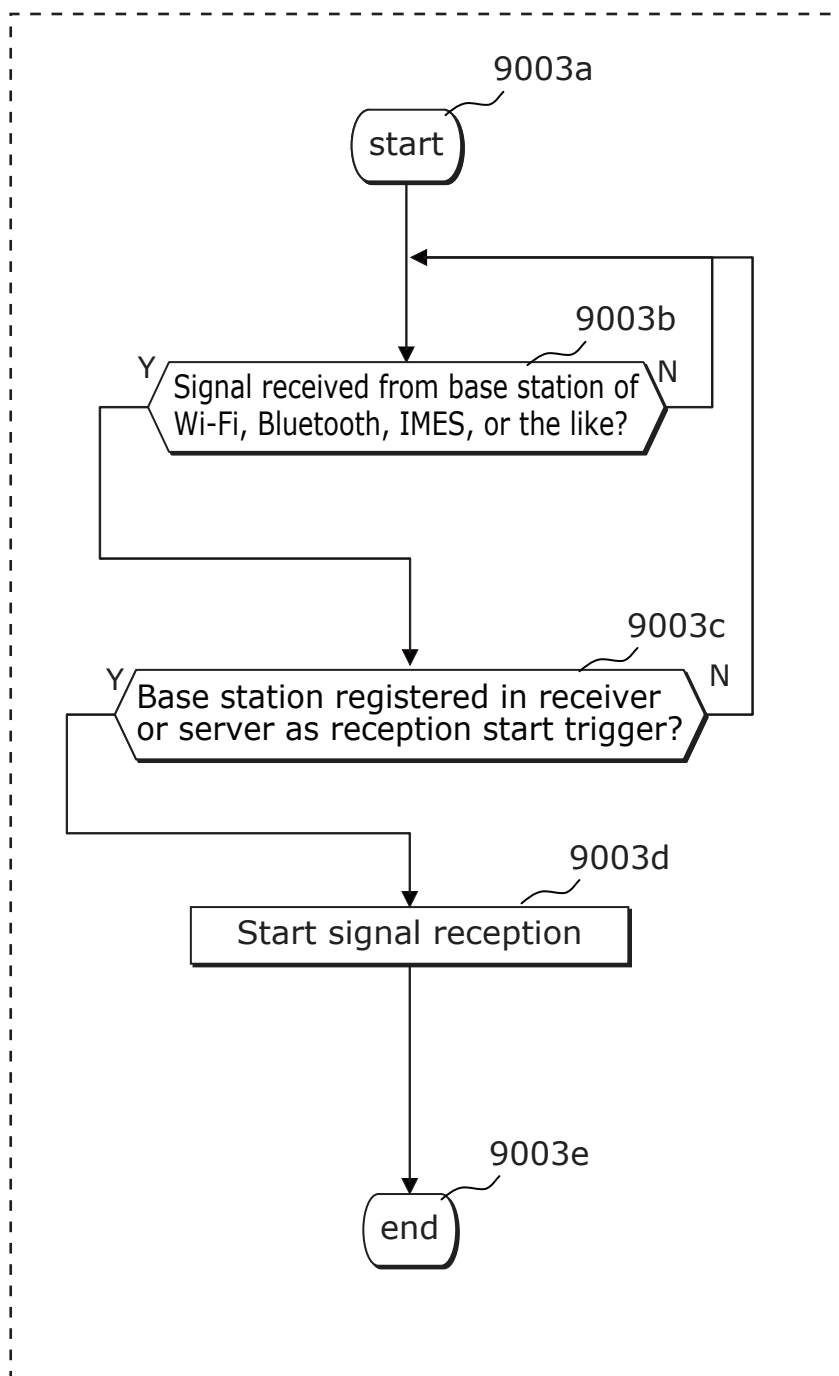


FIG. 247

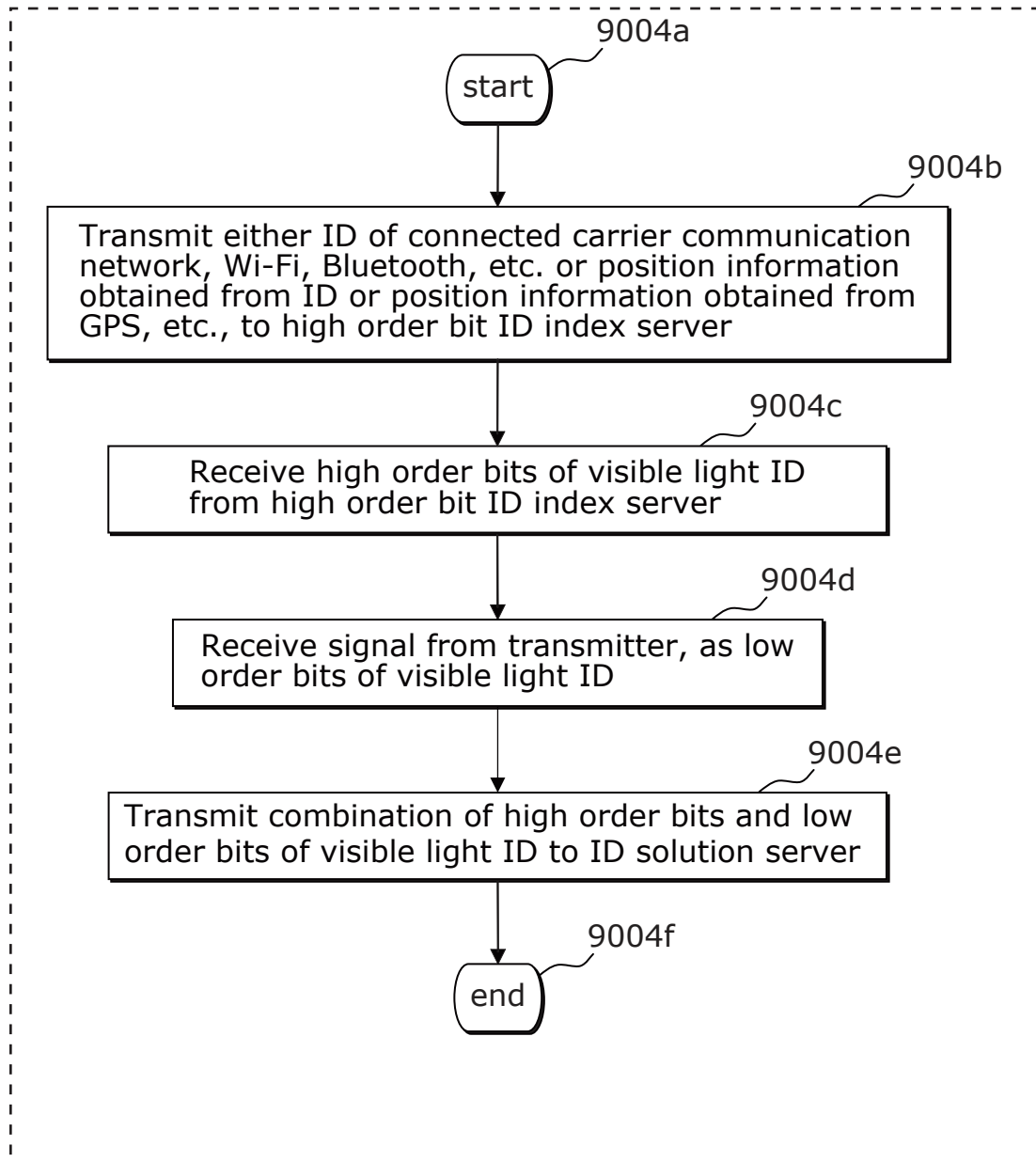


FIG. 248

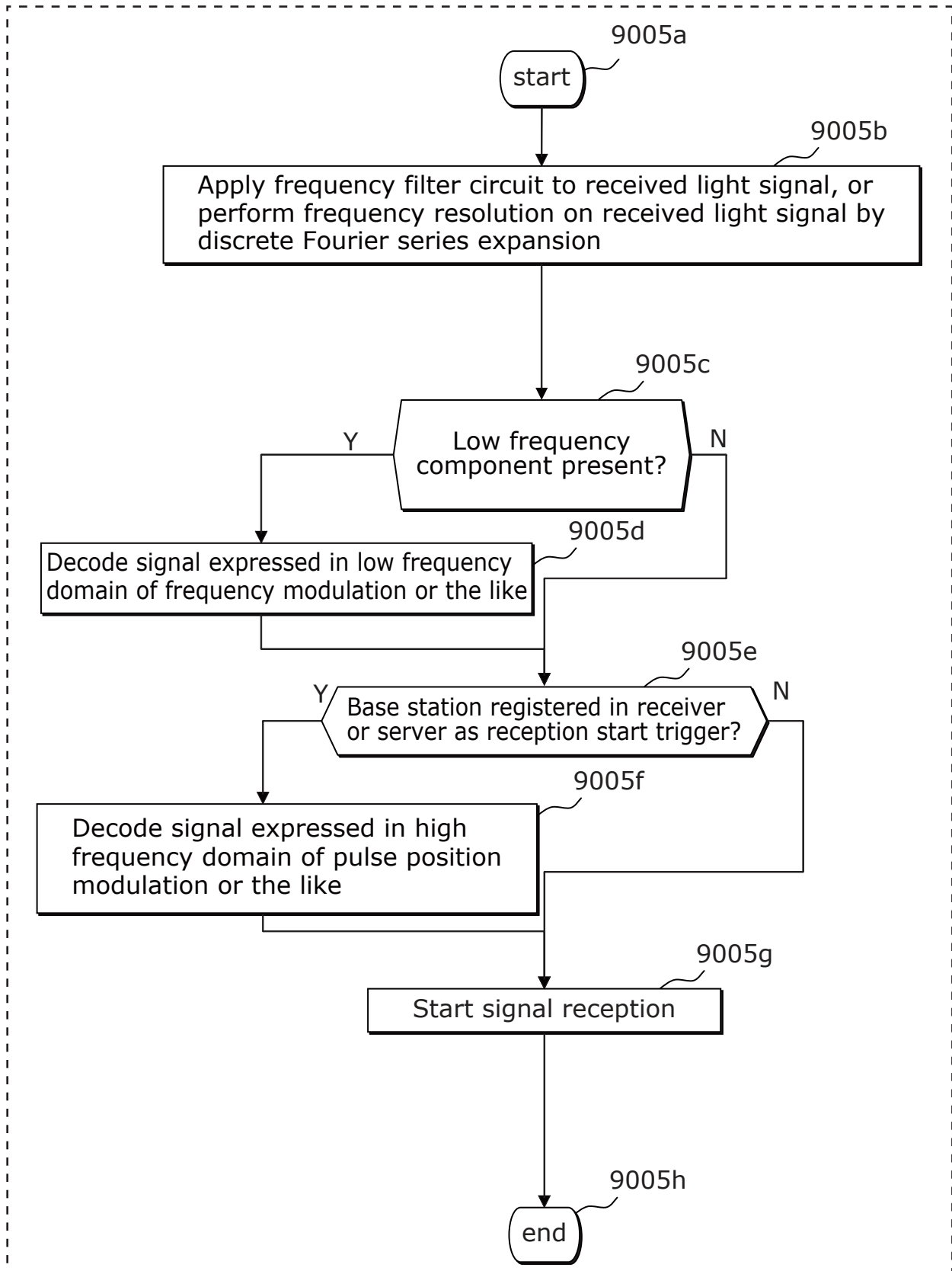


FIG. 249

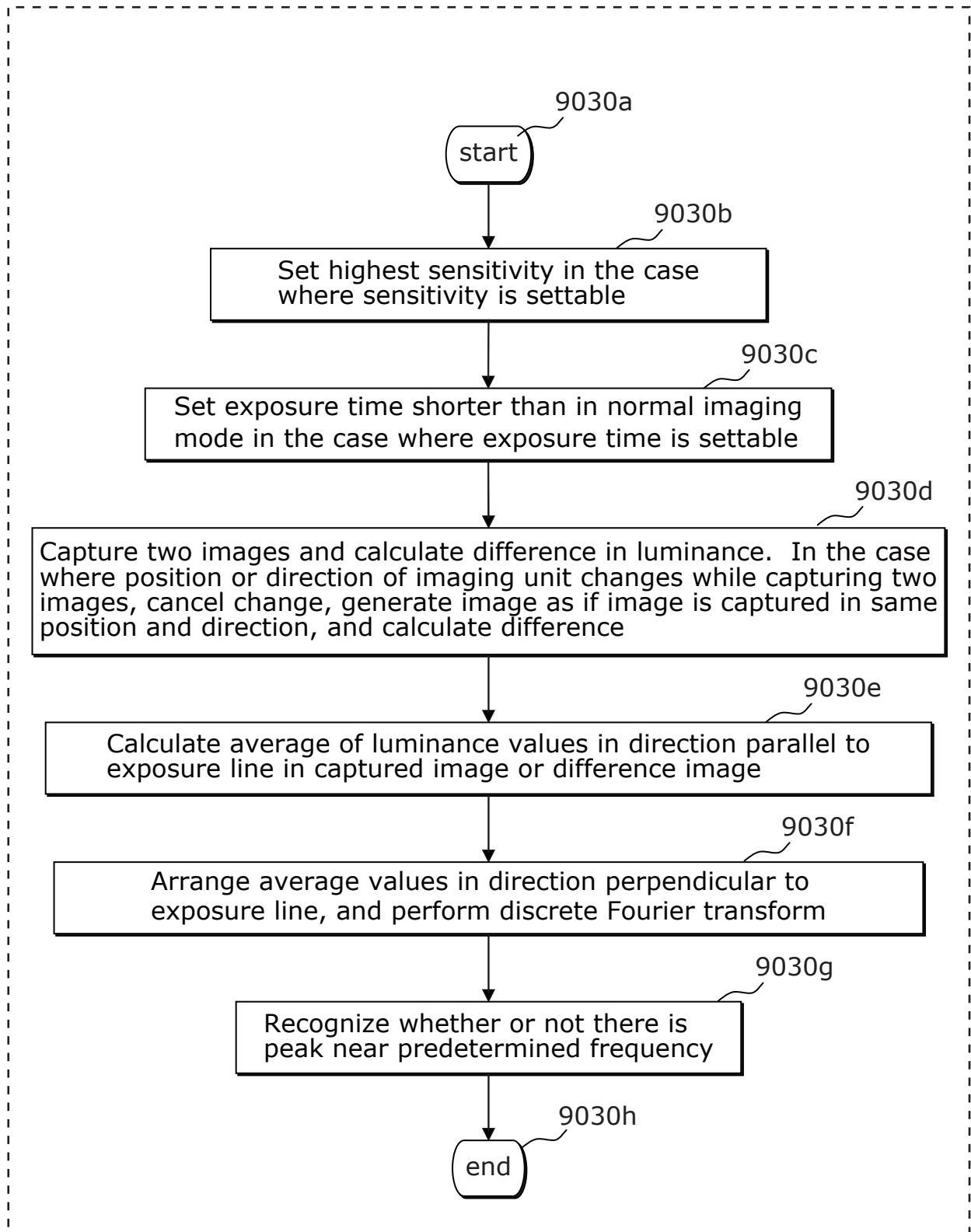


FIG. 250

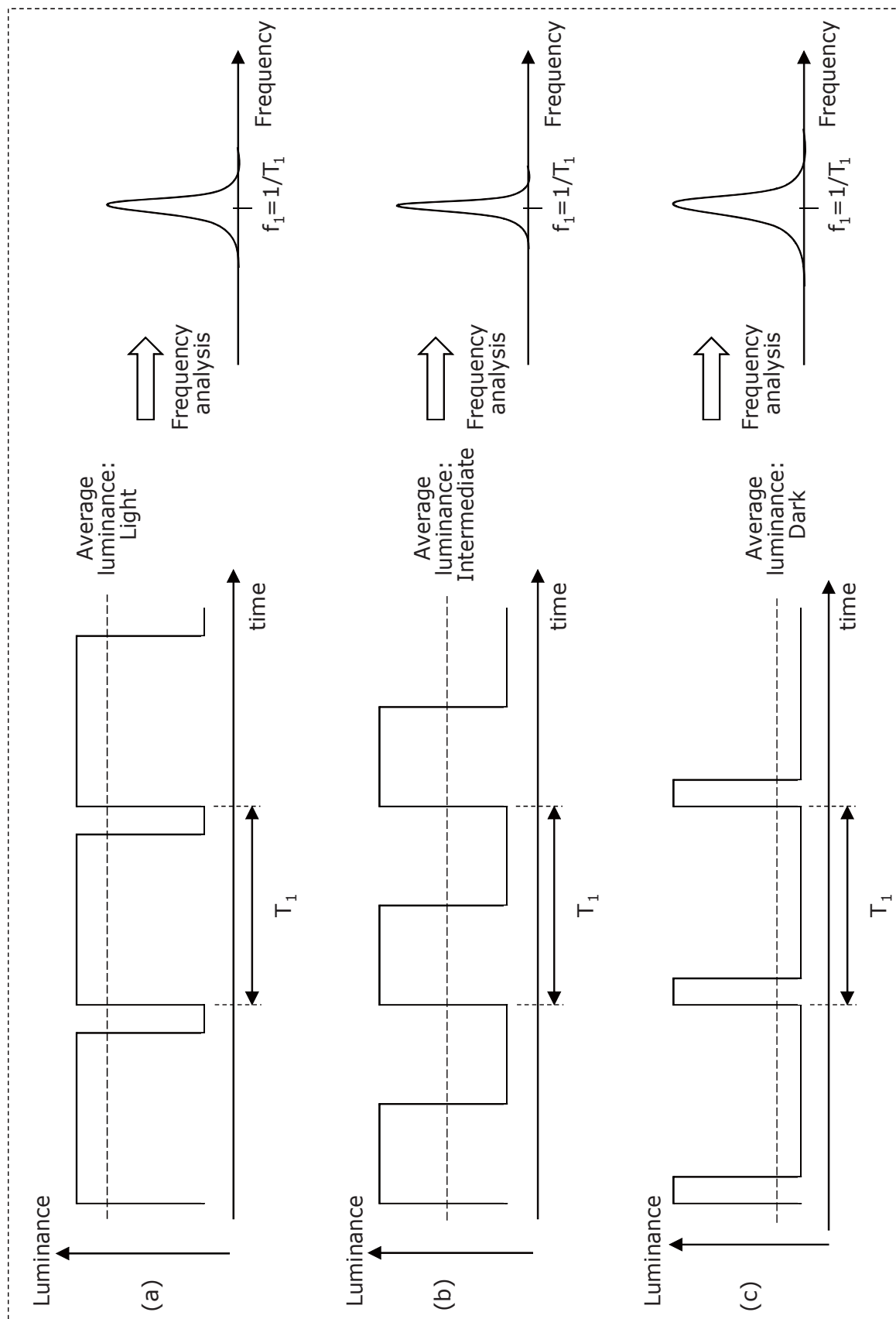


FIG. 251

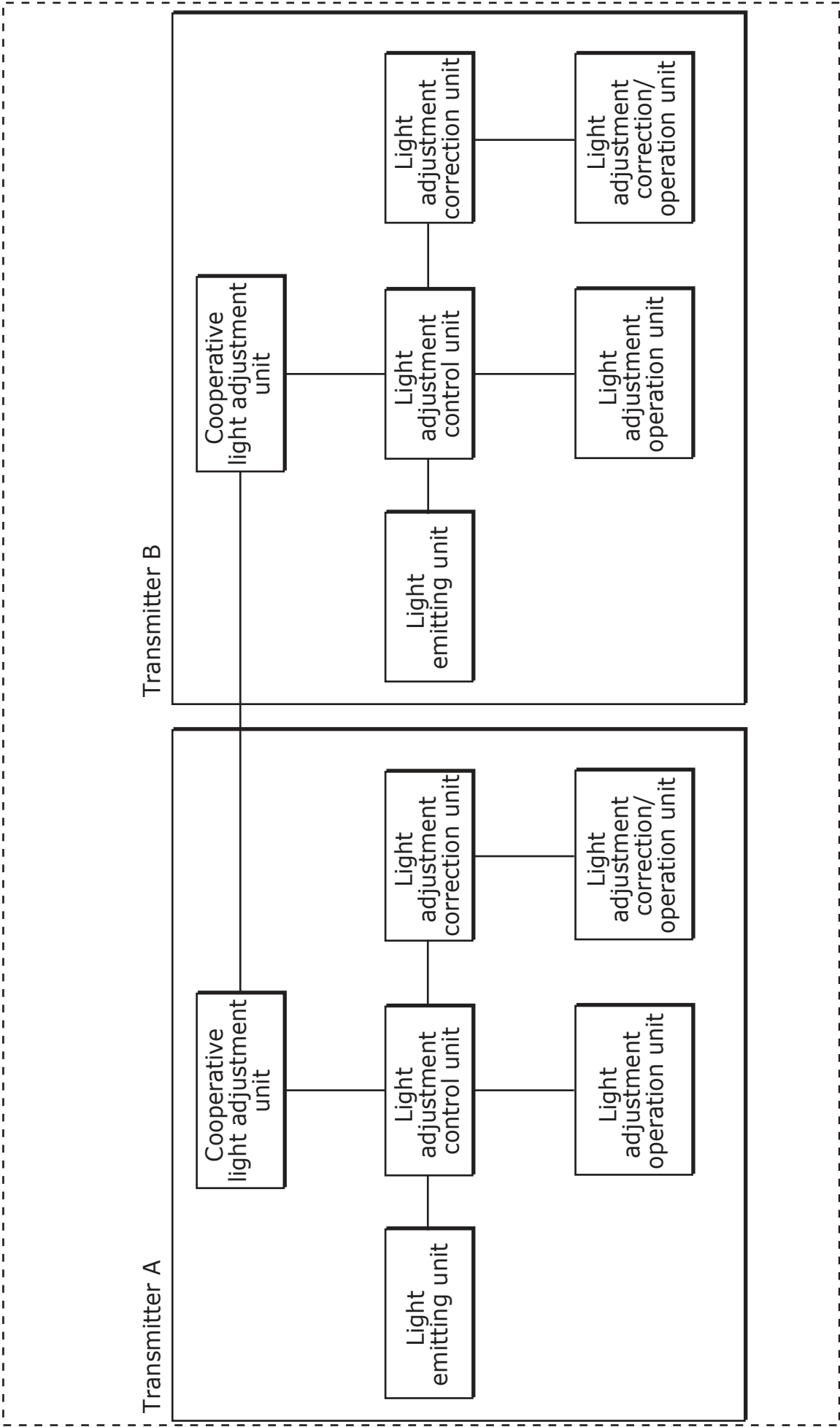


FIG. 252A

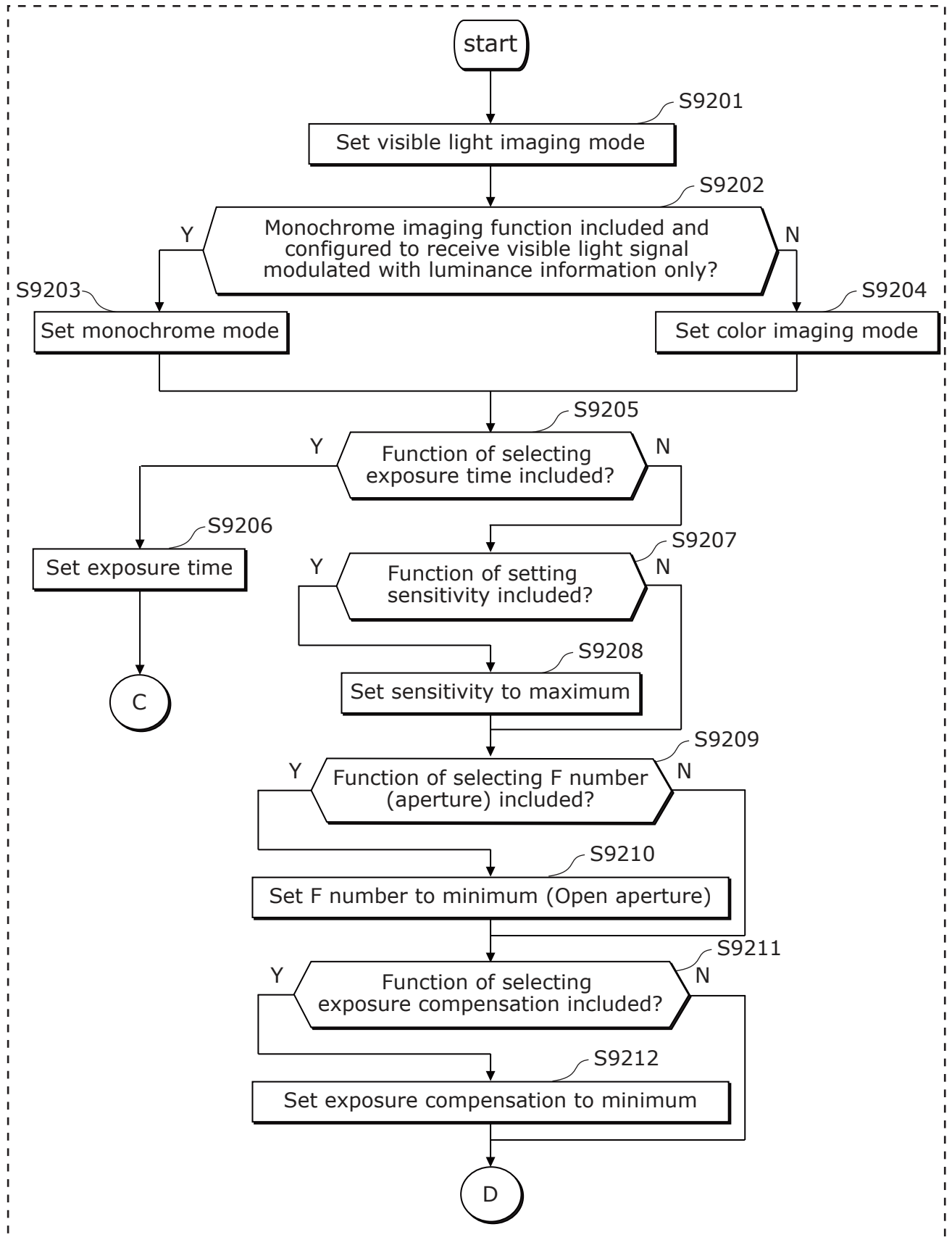


FIG. 252B

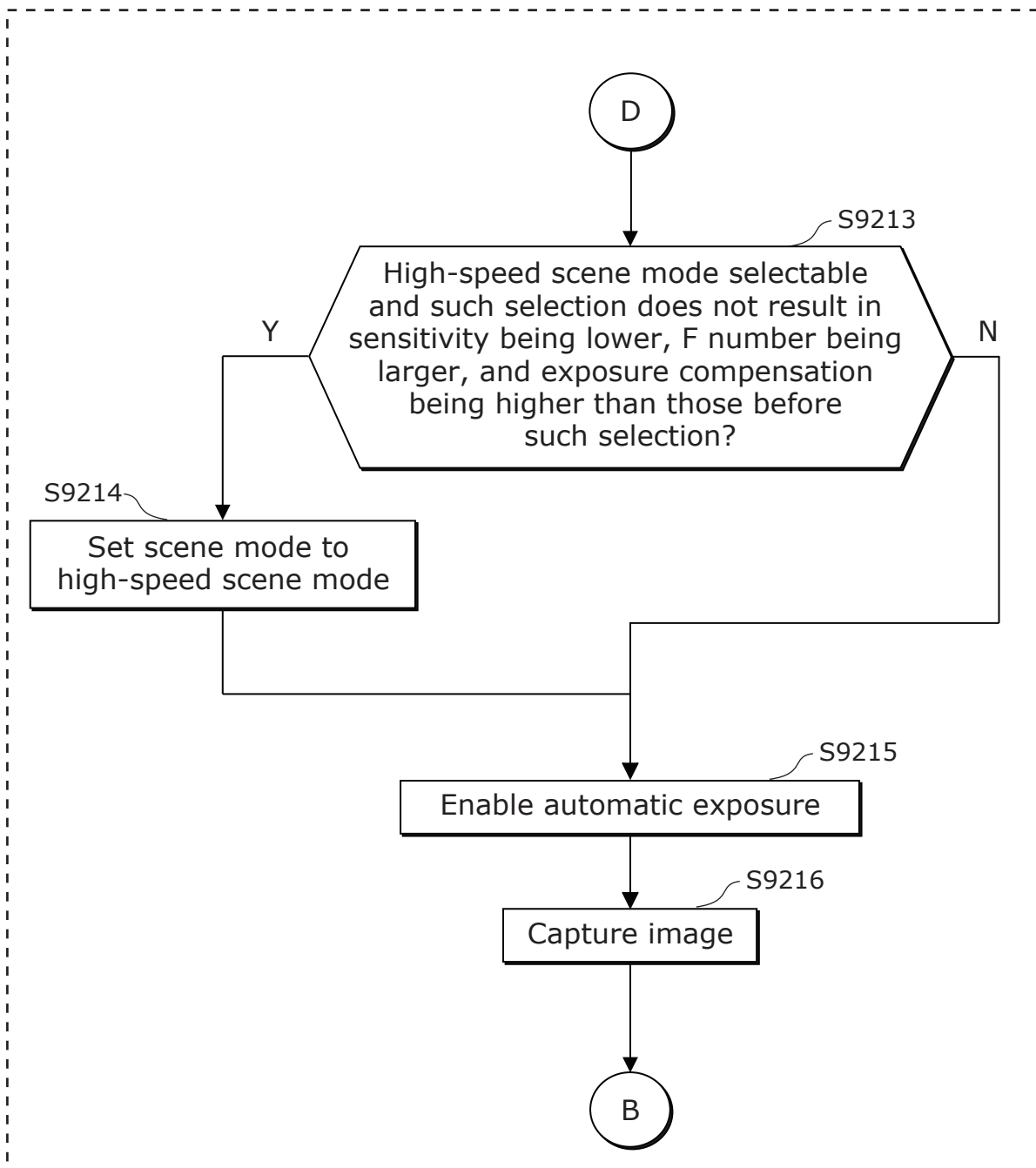


FIG. 252C

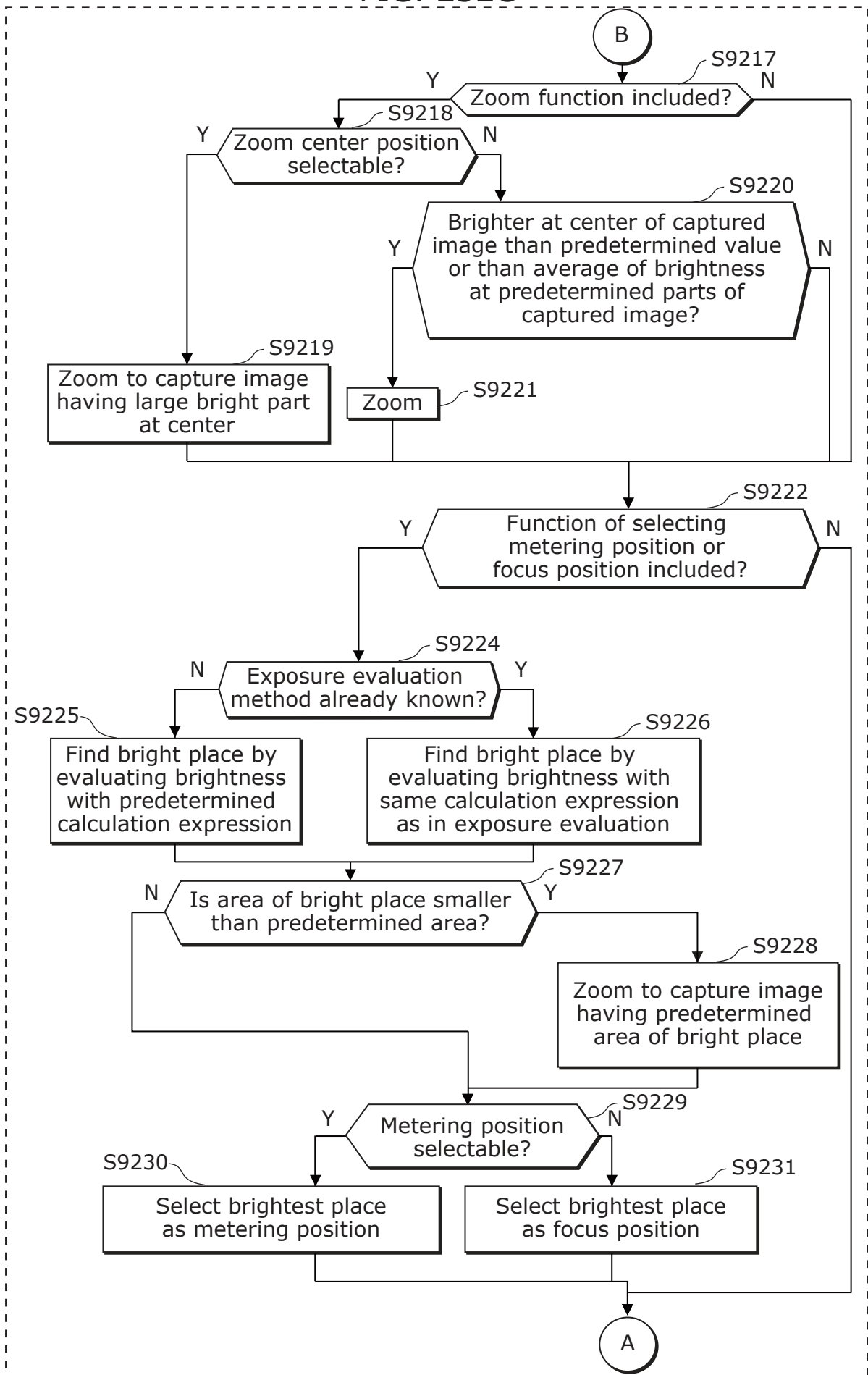


FIG. 252D

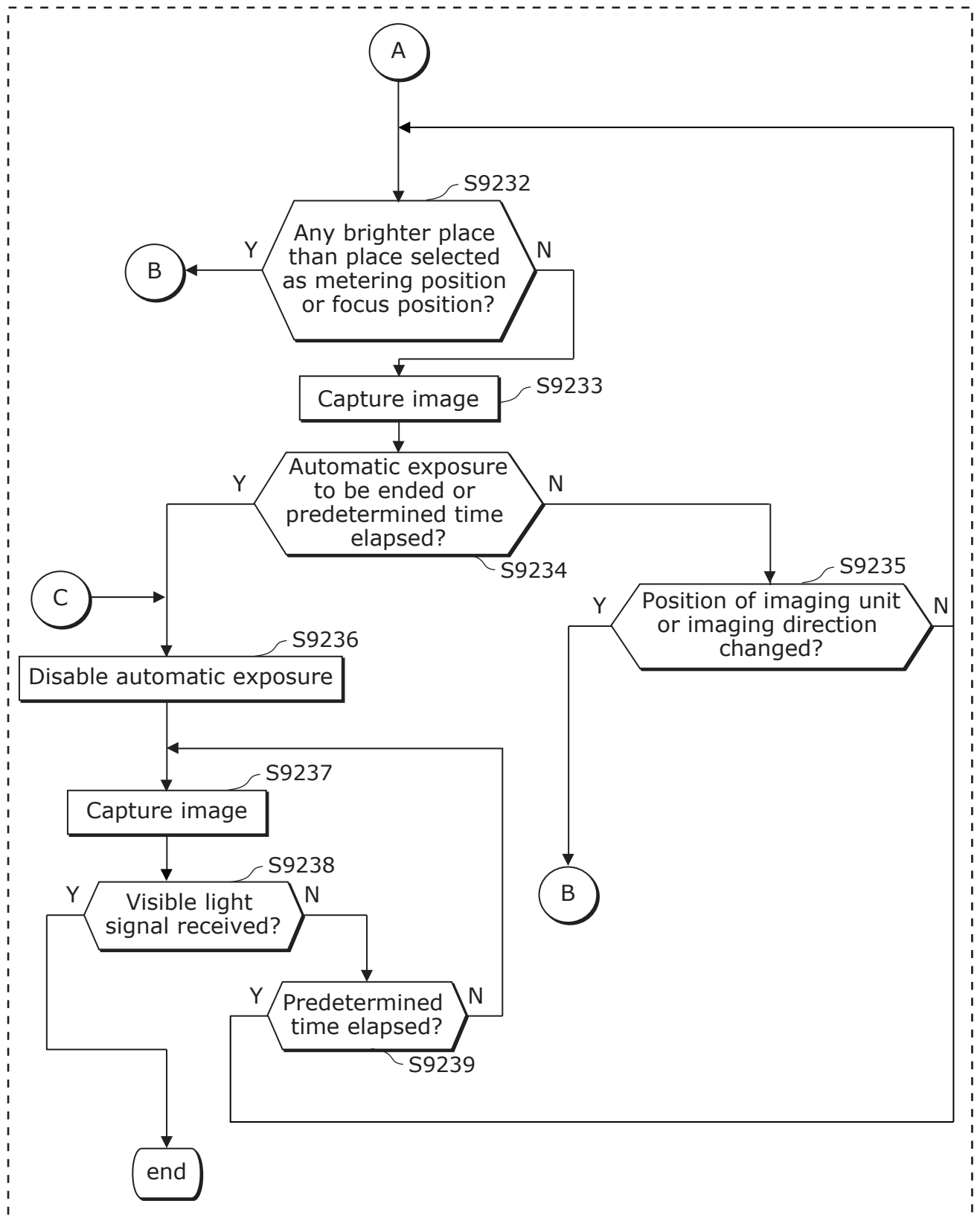


FIG. 253

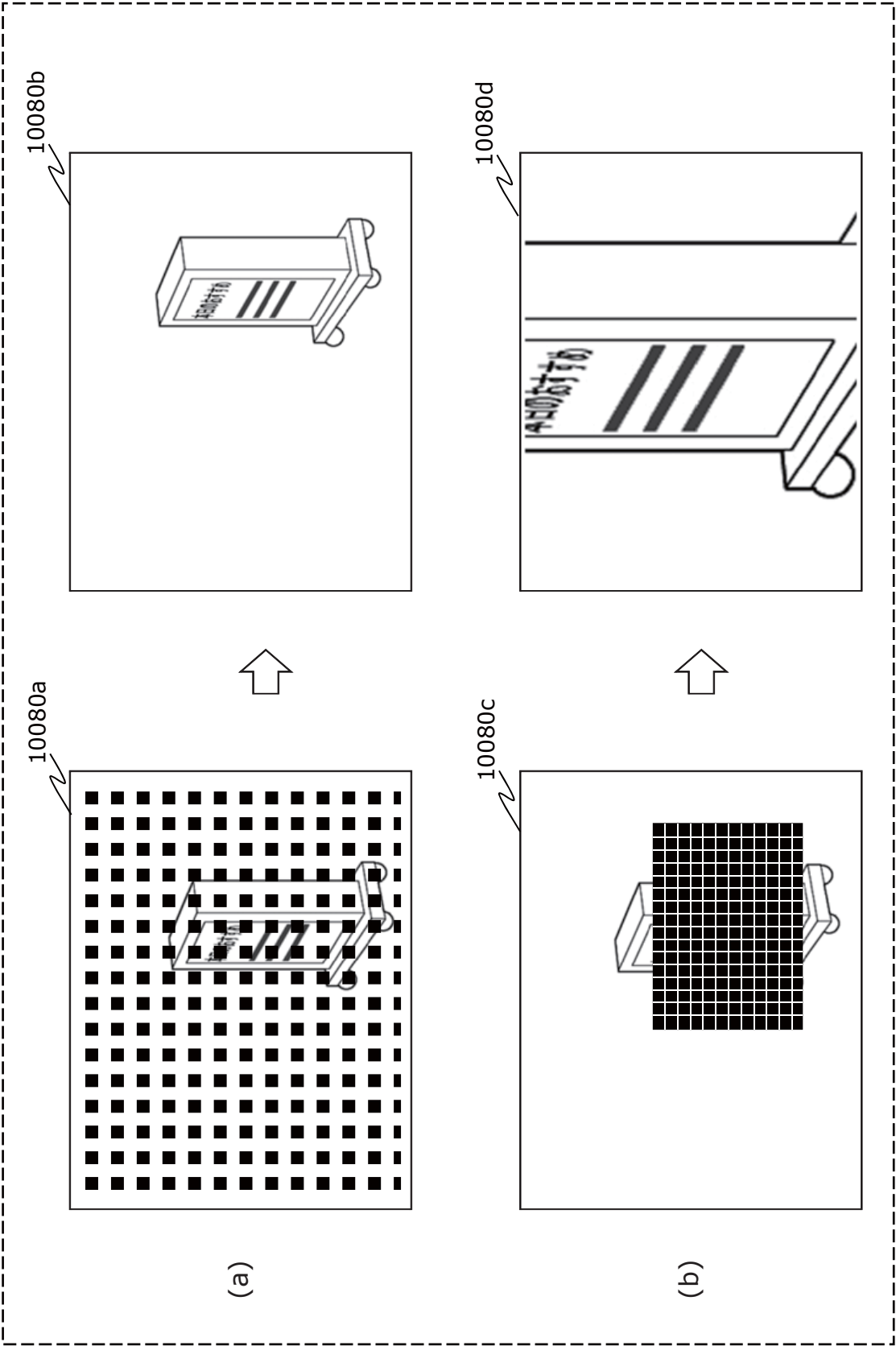


FIG. 254A

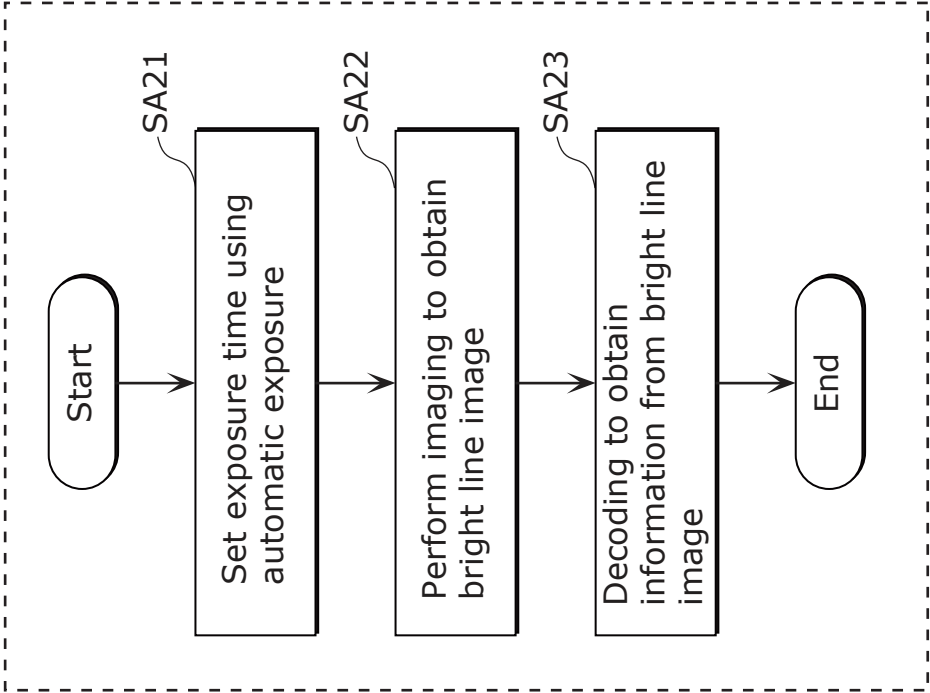


FIG. 254B

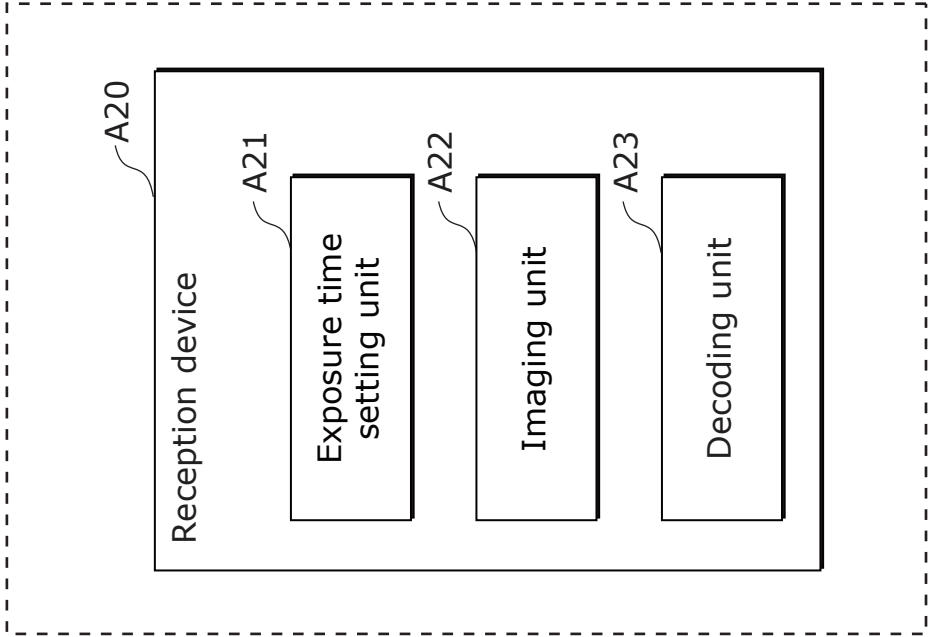


FIG. 255

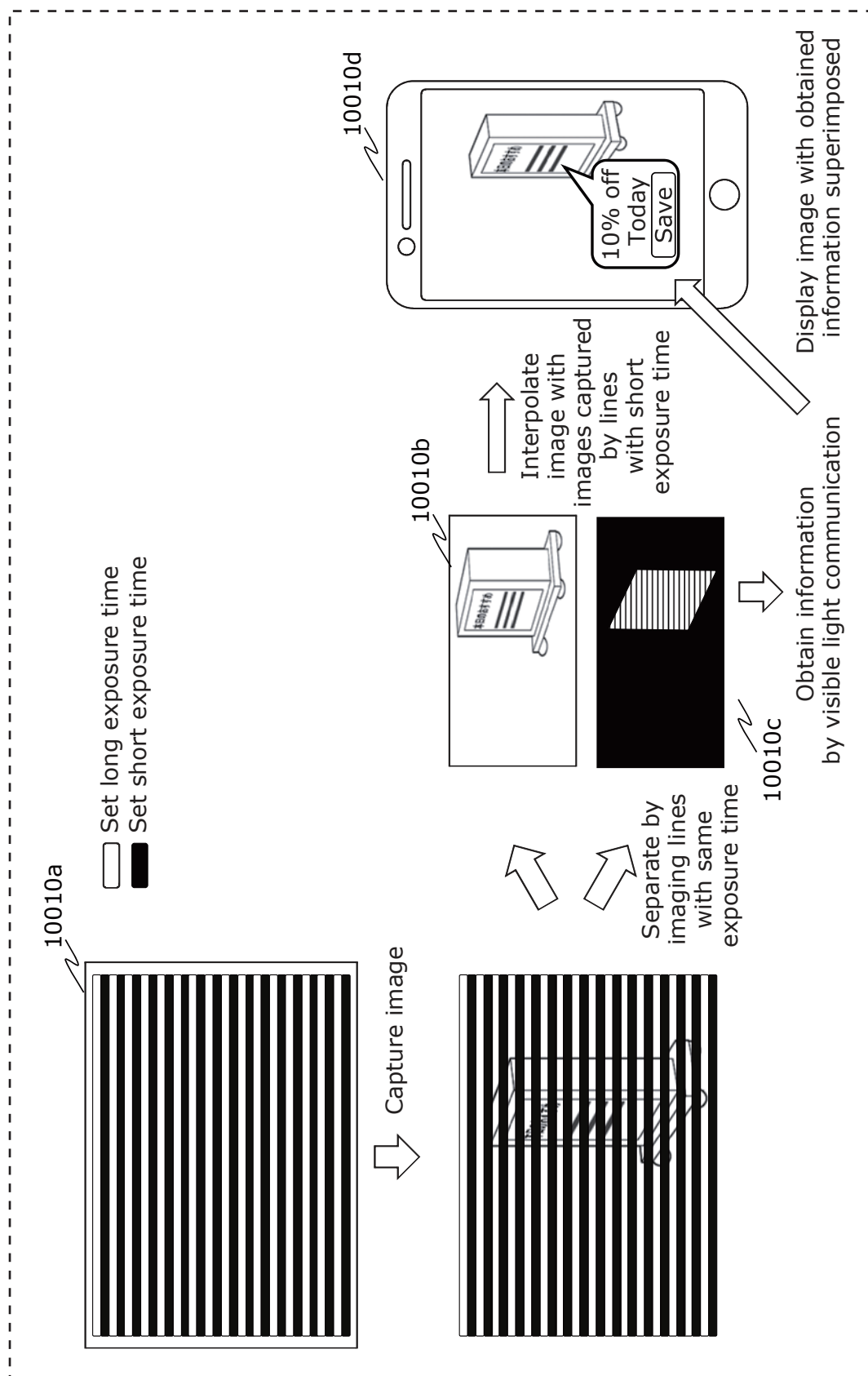


FIG. 256

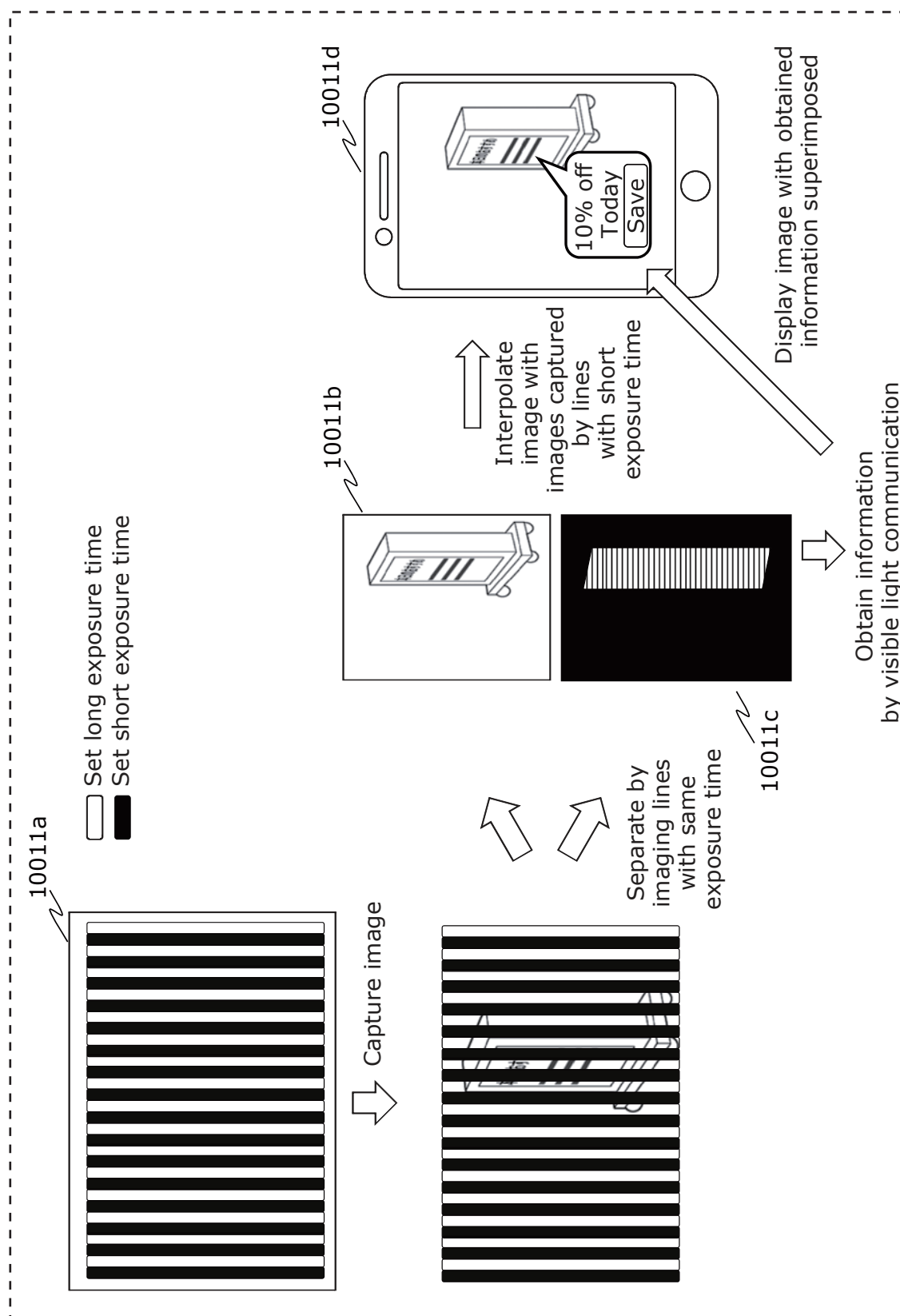


FIG. 257

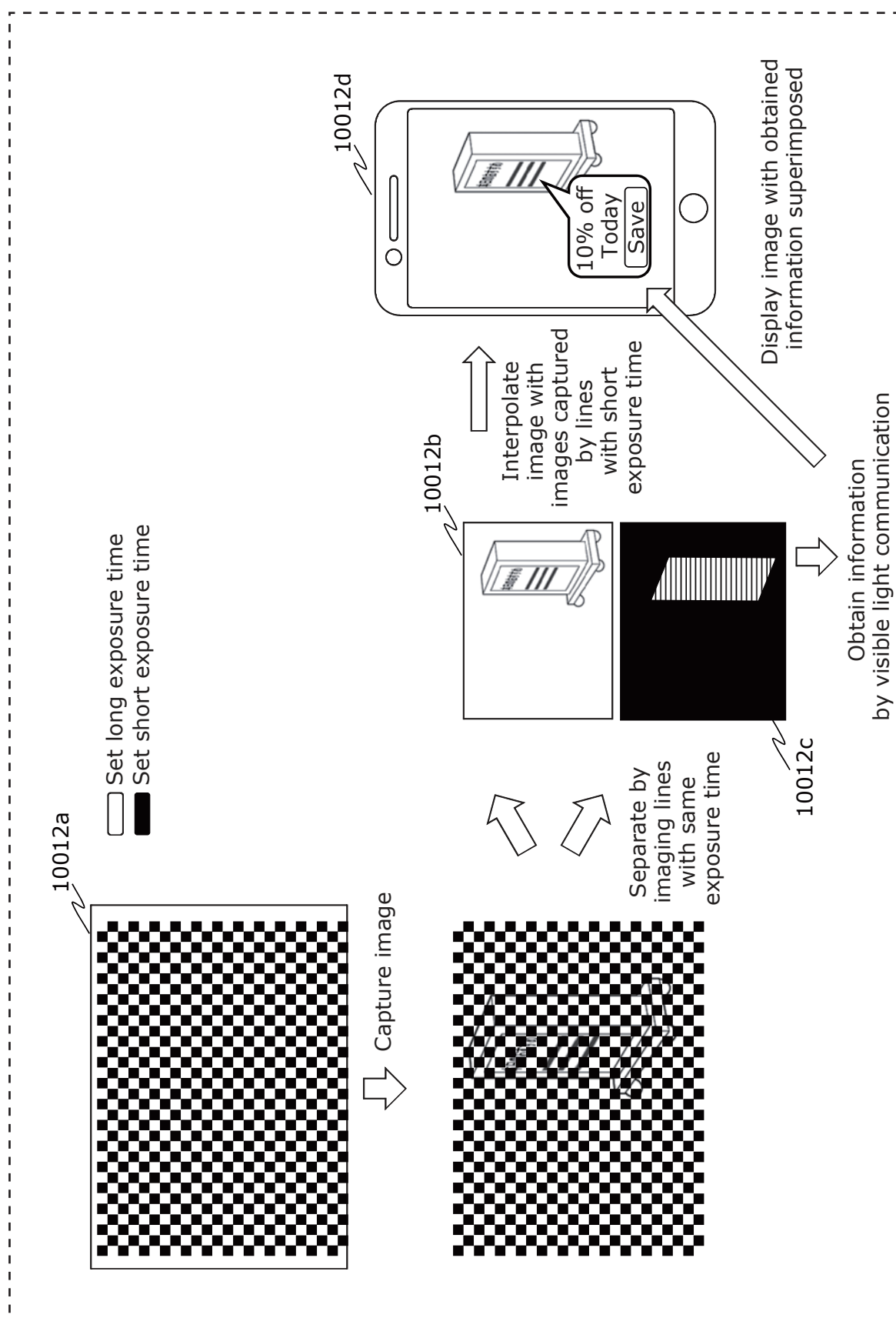


FIG. 258

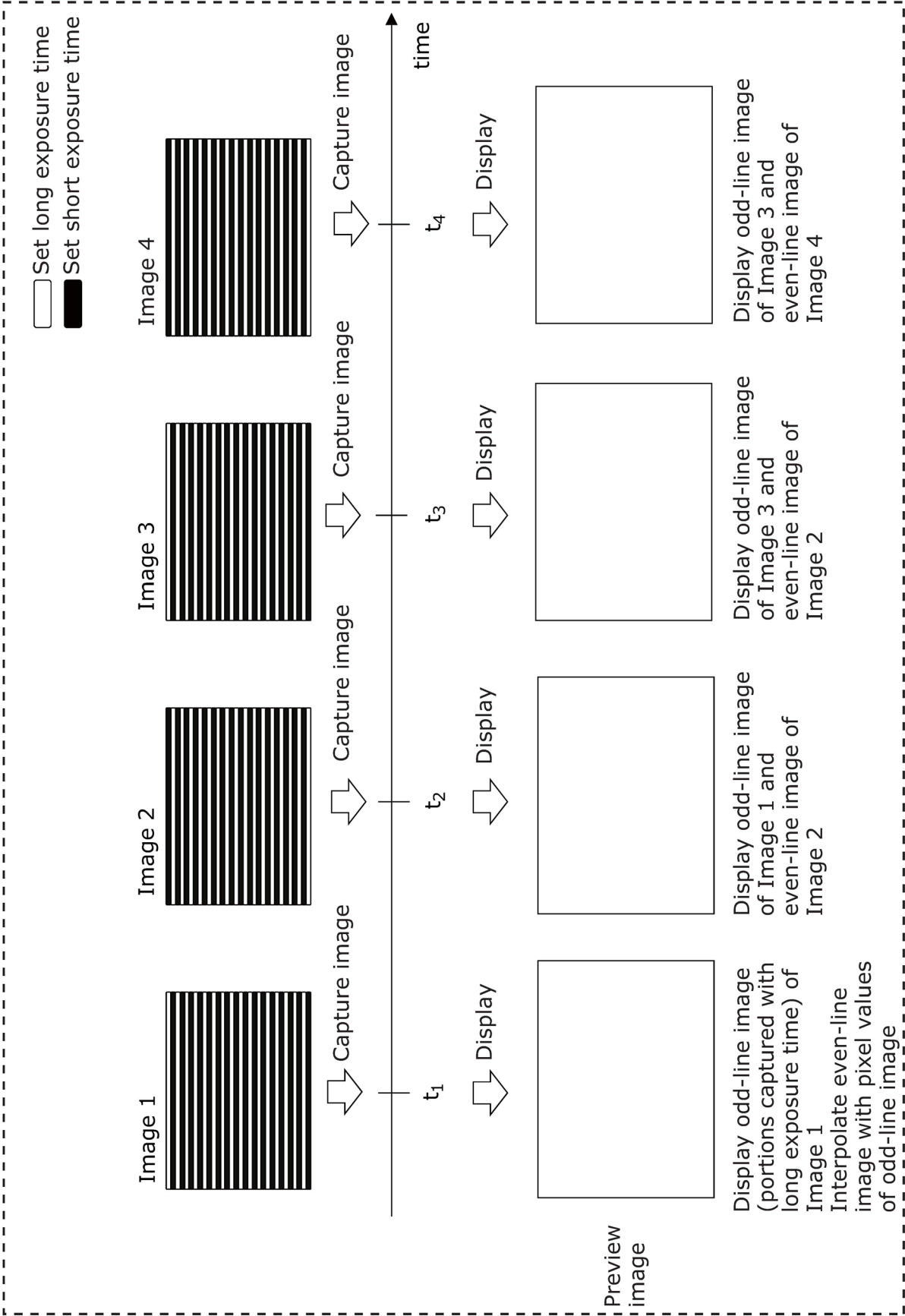


FIG. 259

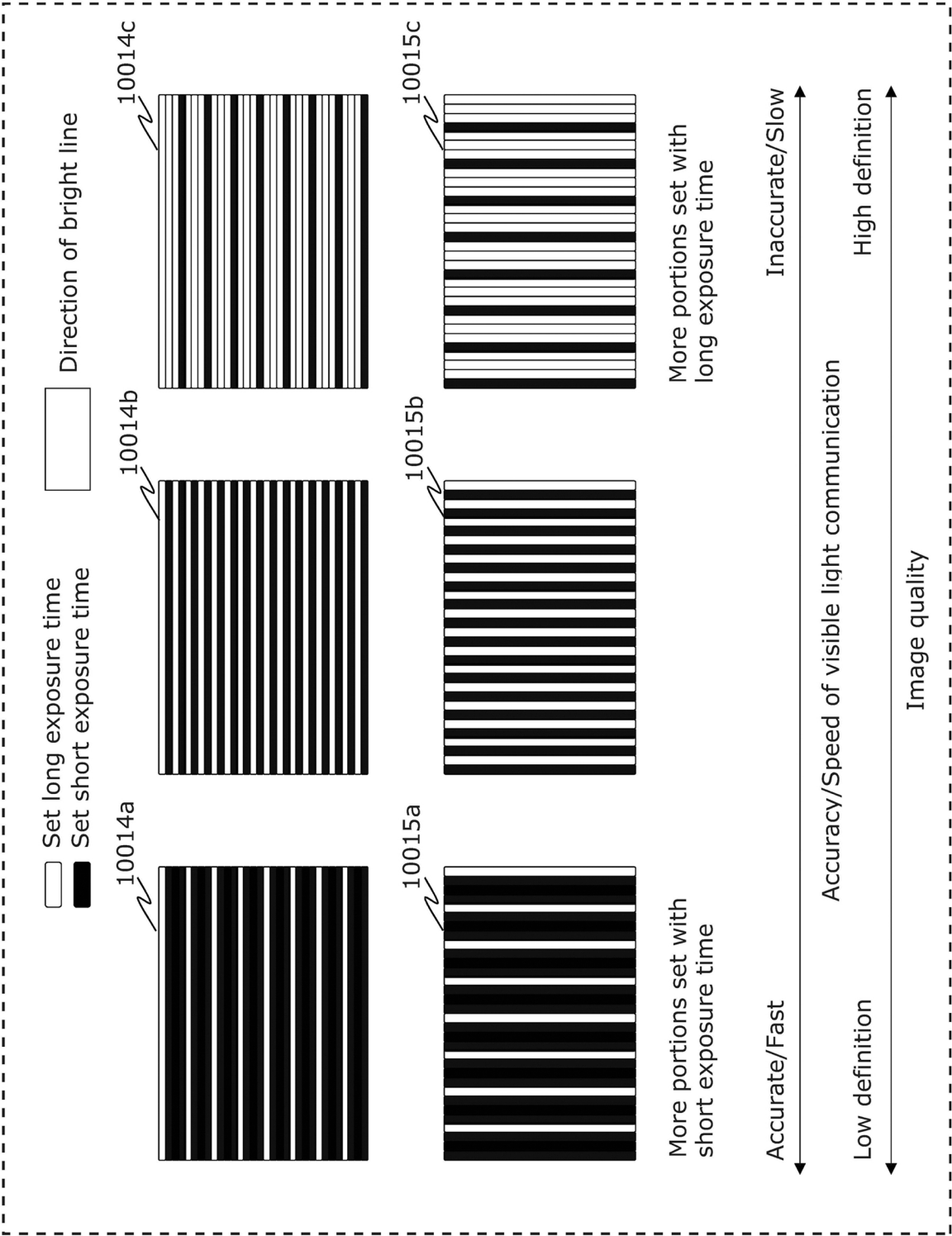


FIG. 260

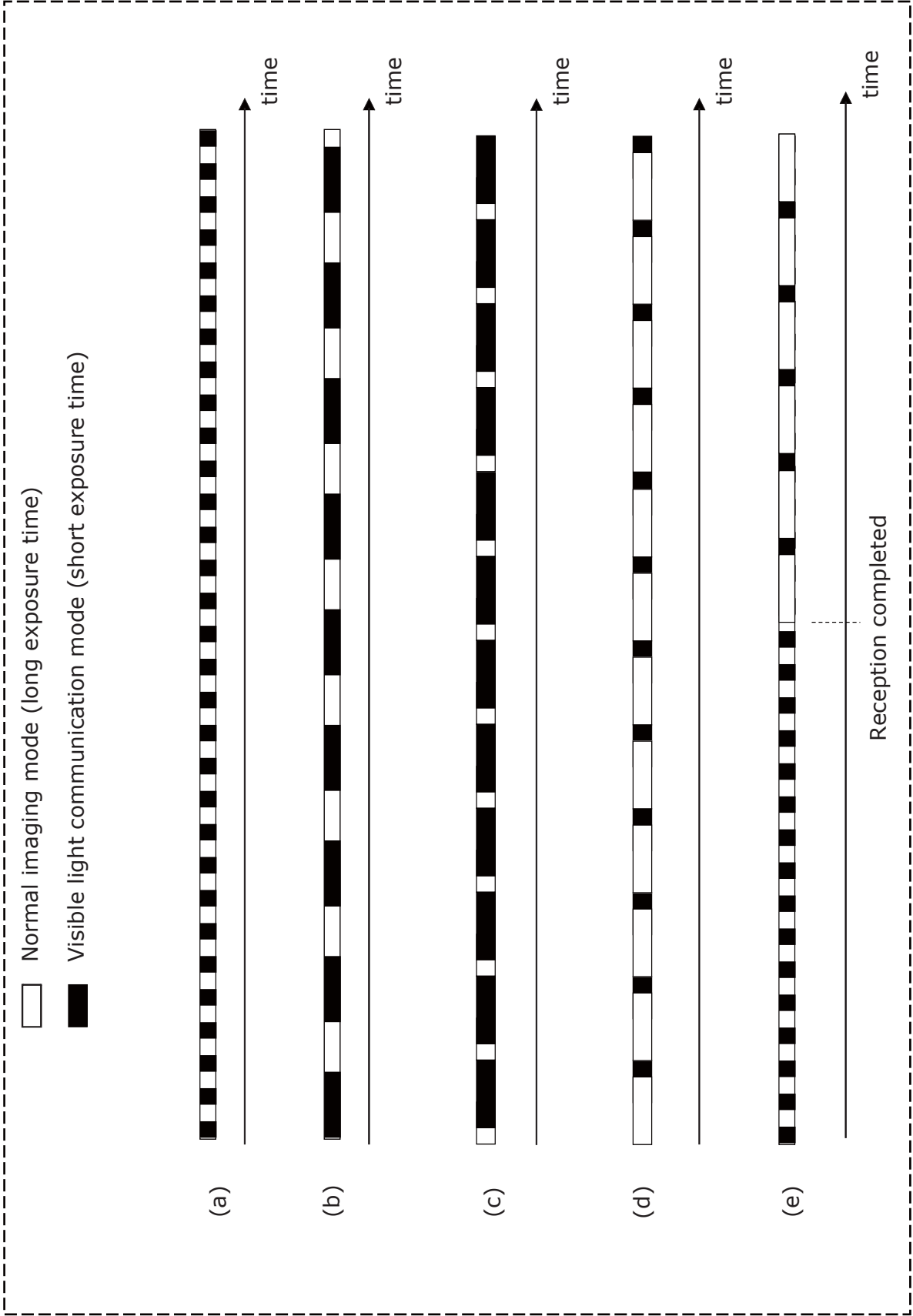


FIG. 261

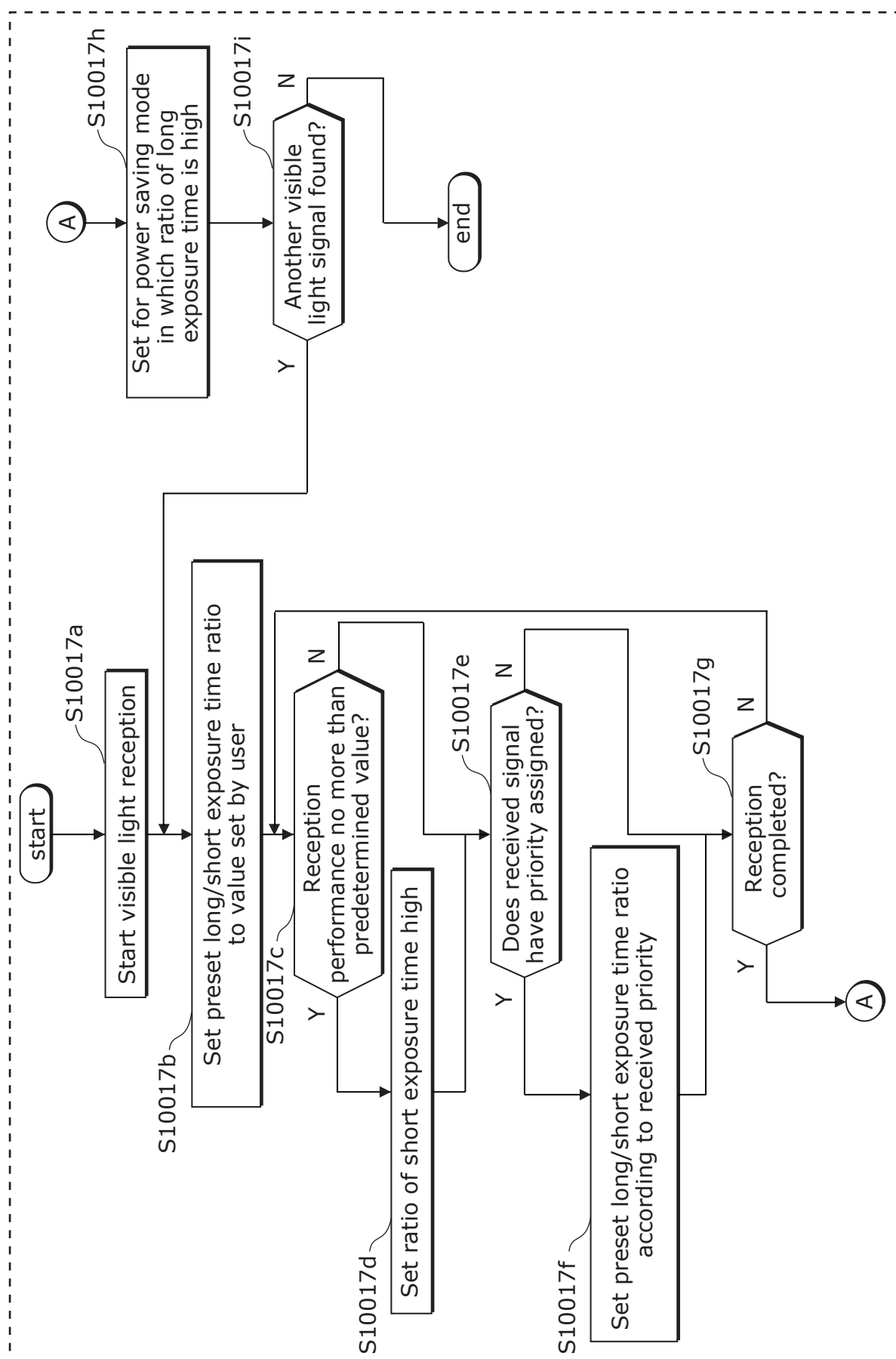


FIG. 262

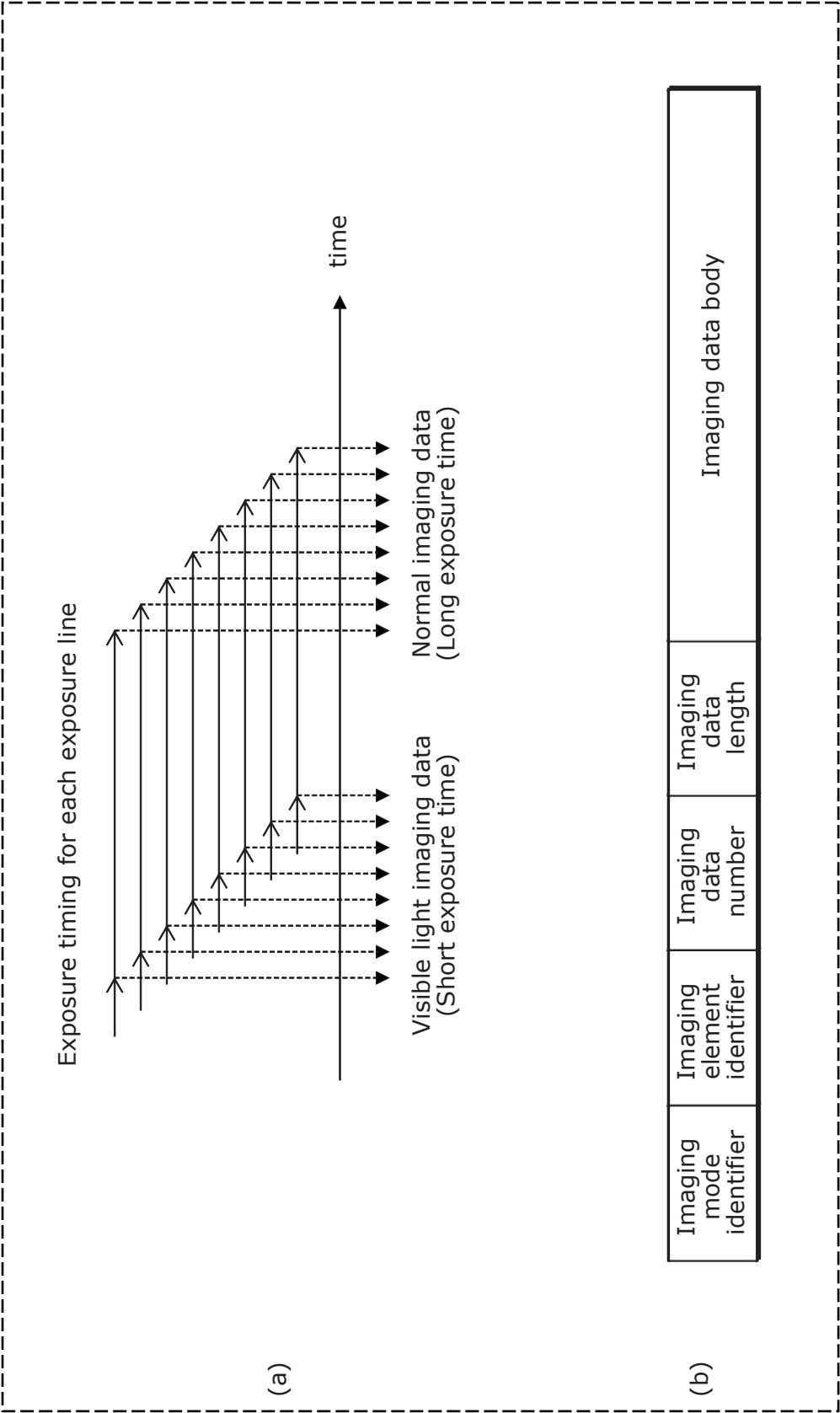


FIG. 263A

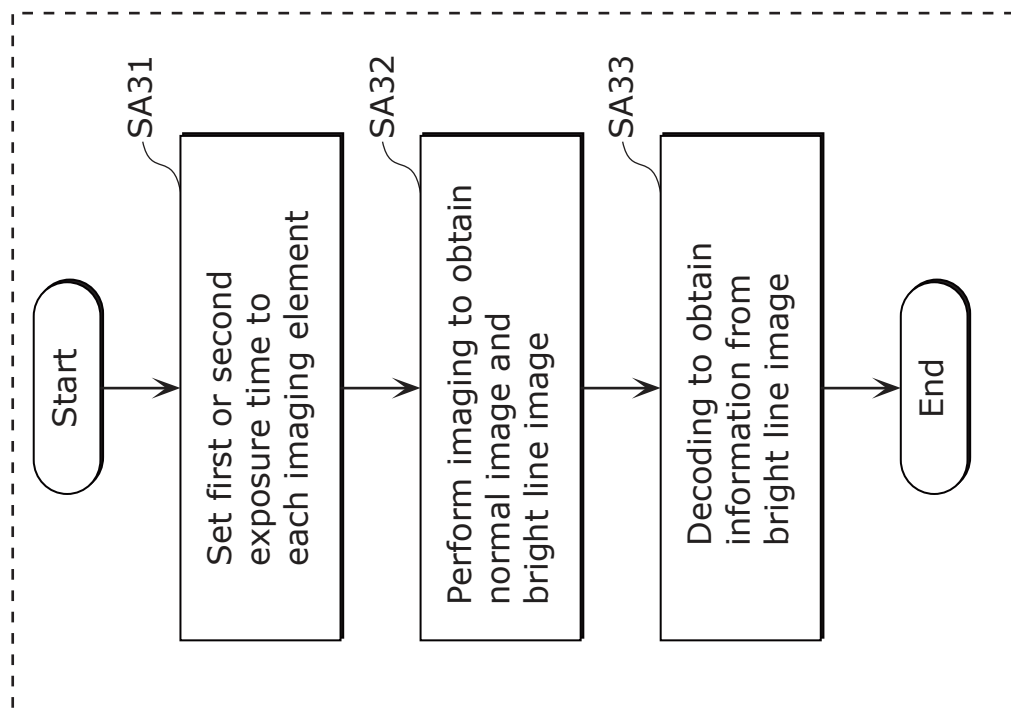


FIG. 263B

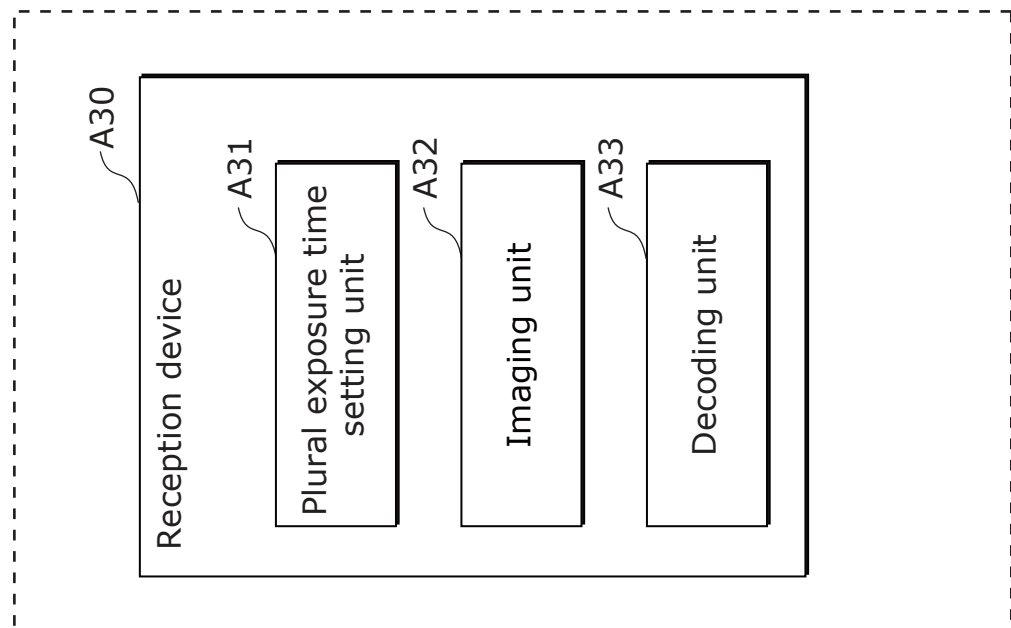


FIG. 264

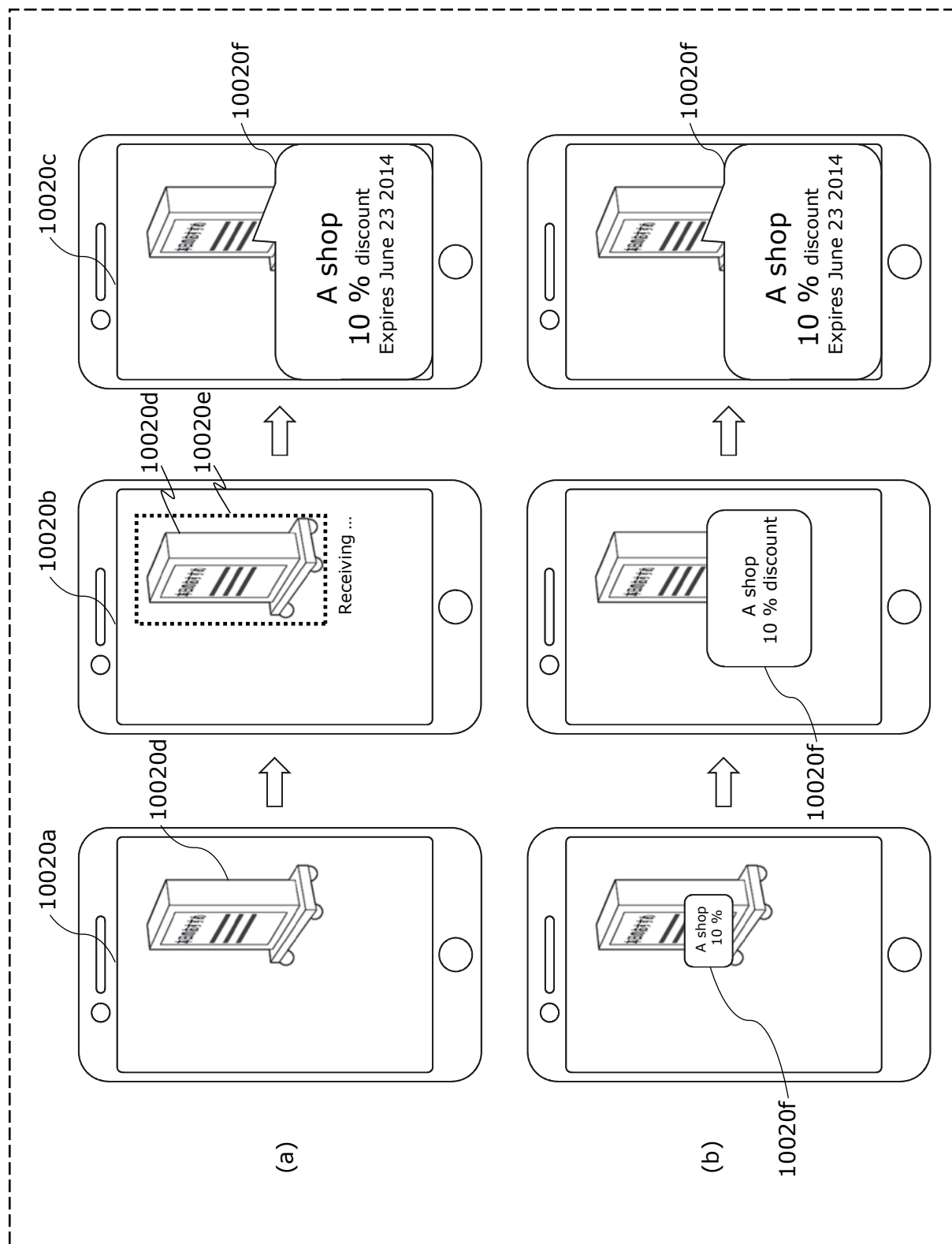


FIG. 265

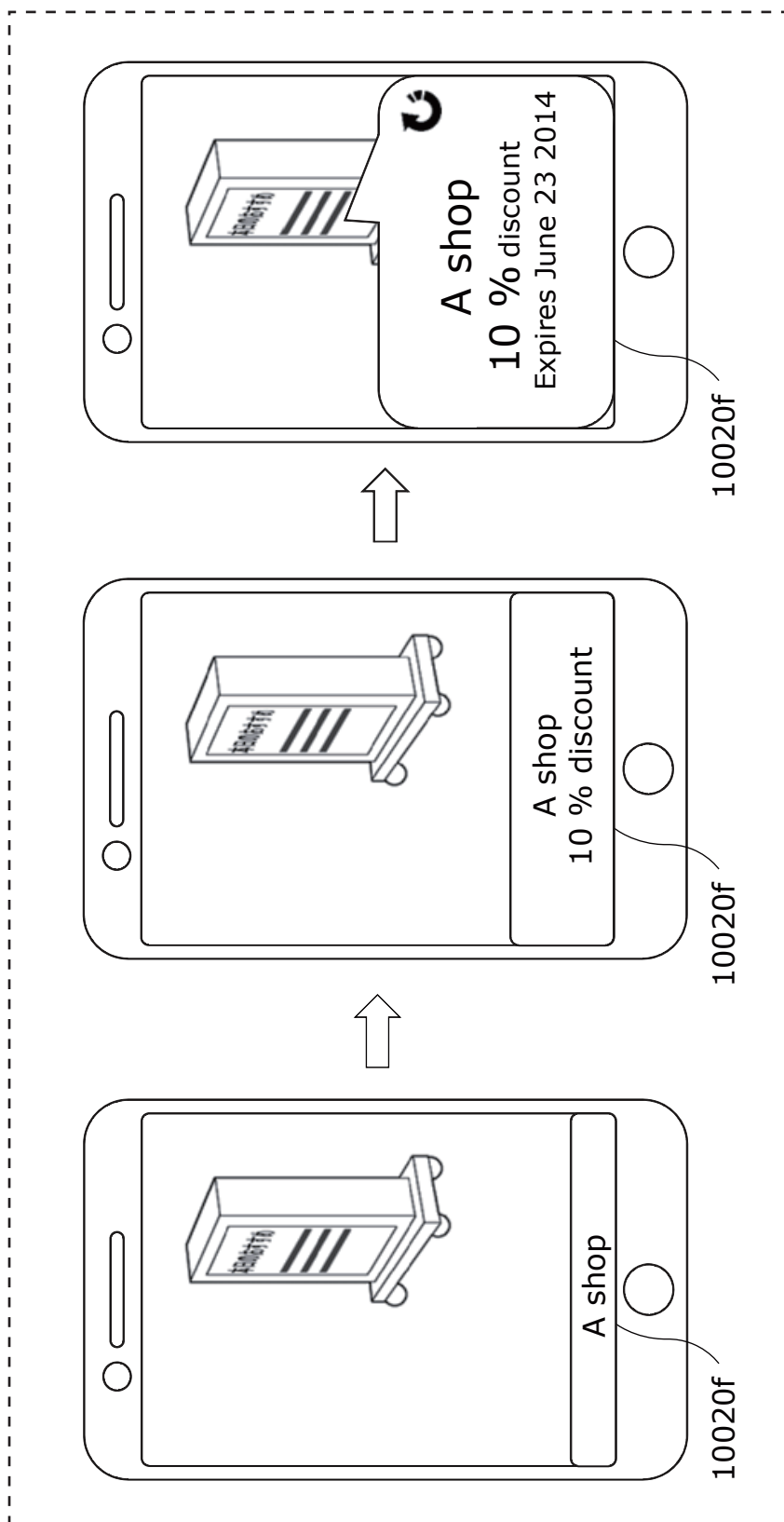


FIG. 266

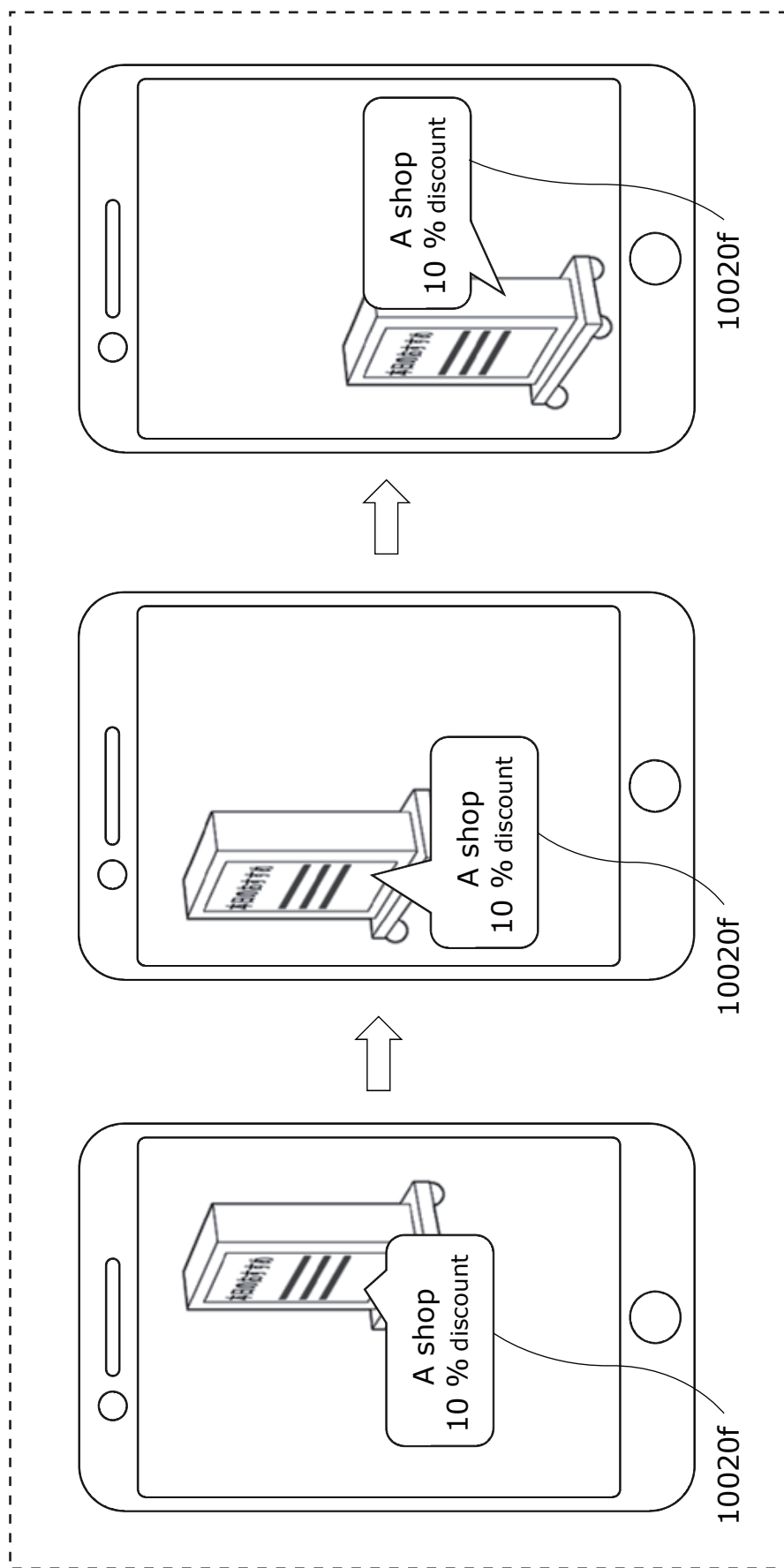


FIG. 267

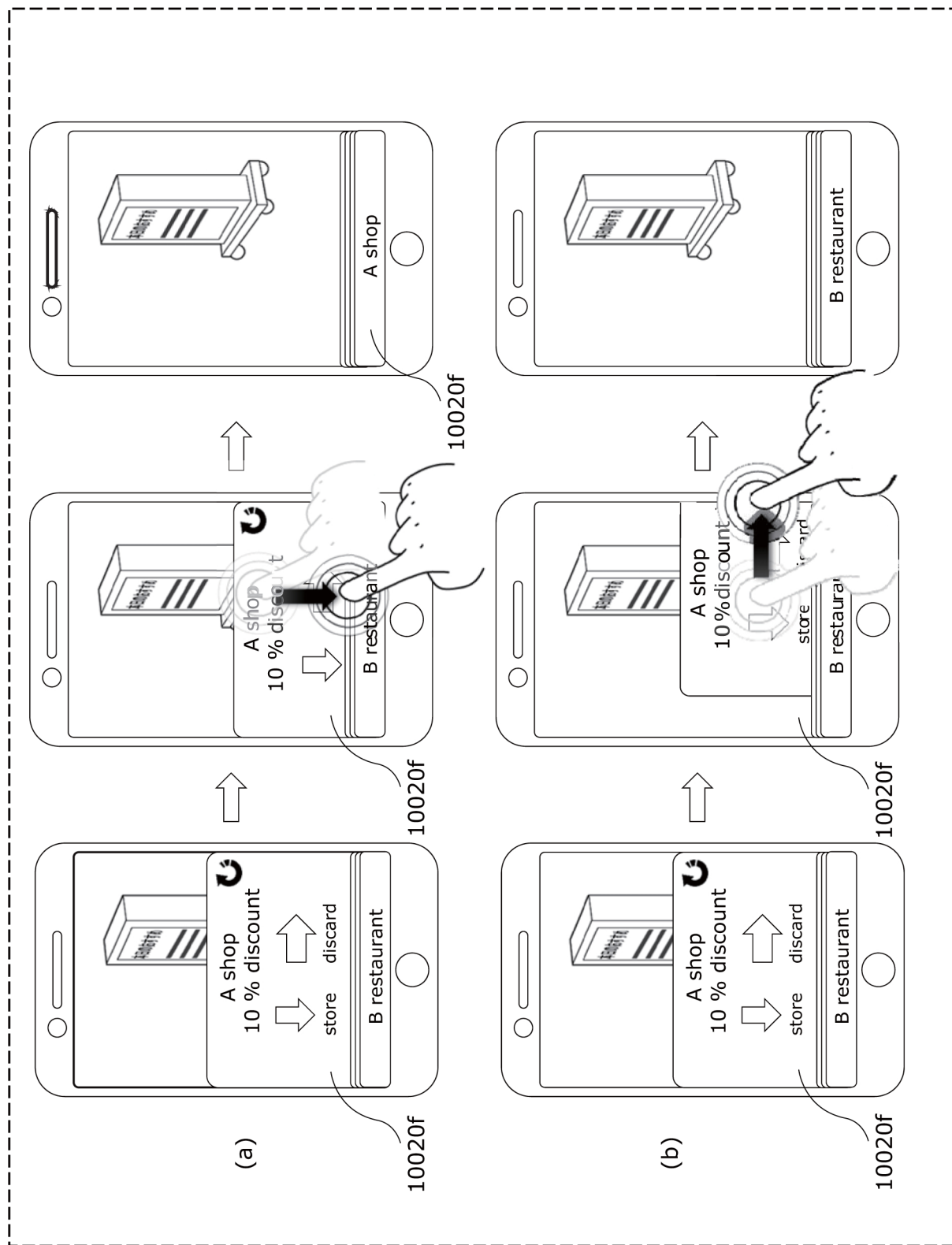


FIG. 268

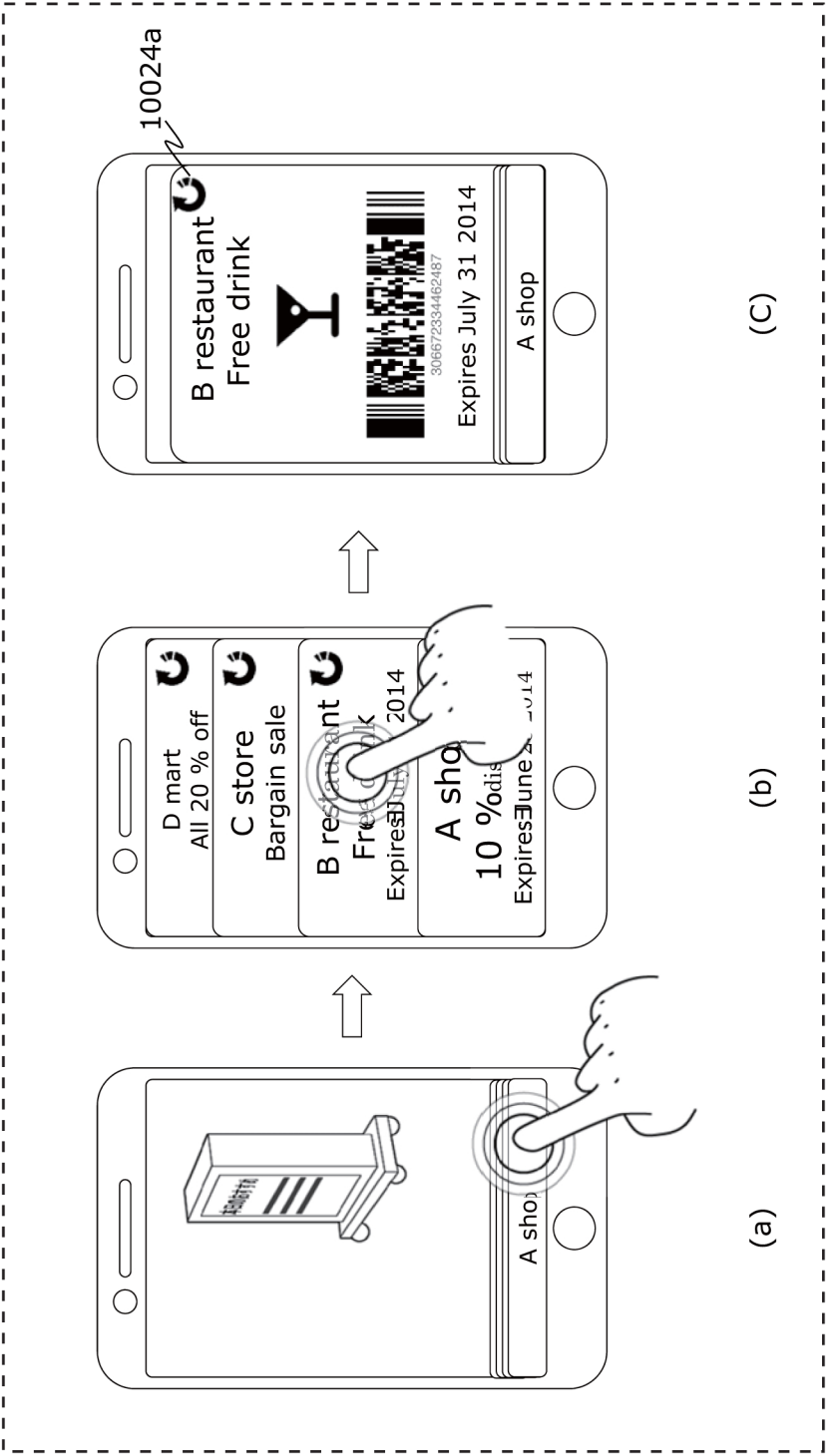


FIG. 269

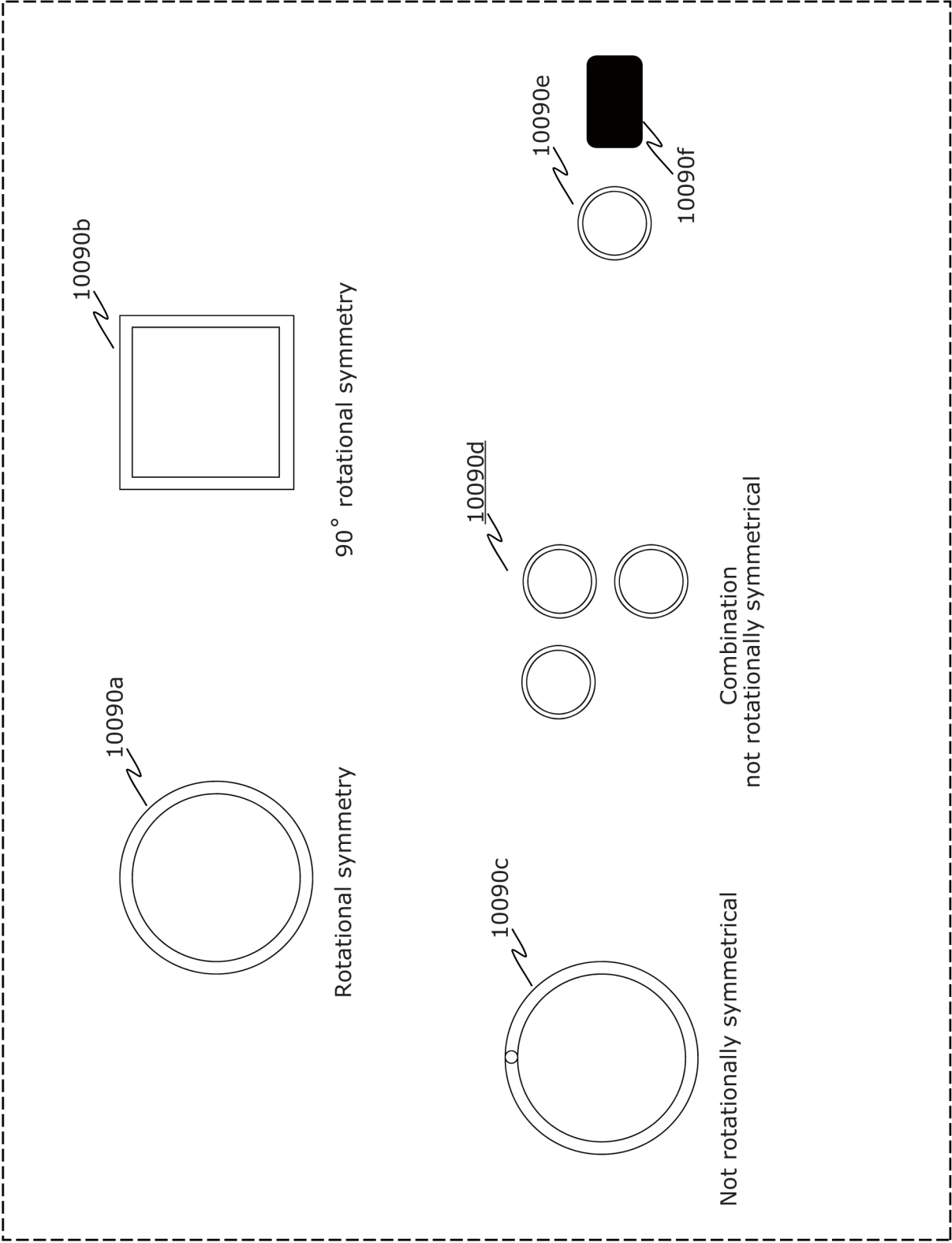


FIG. 270

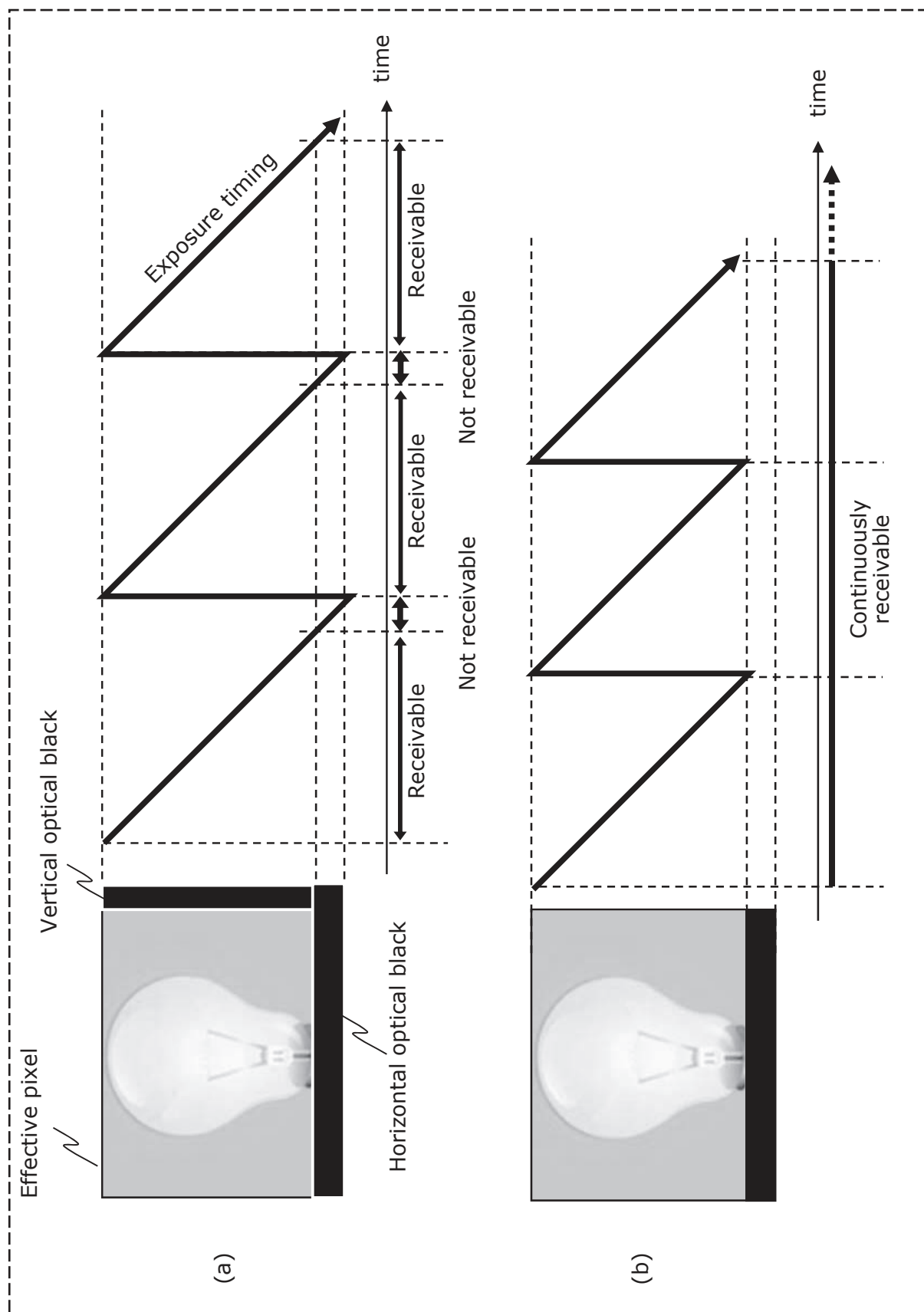


FIG. 271

	Header	Body (4PPM)
(a)	Header pattern 11111111000	Greatest number of continuous 0 slots 4
(b)	111111110001	3
(c)	111111101001	2
(d)	111111100101	2

FIG. 272

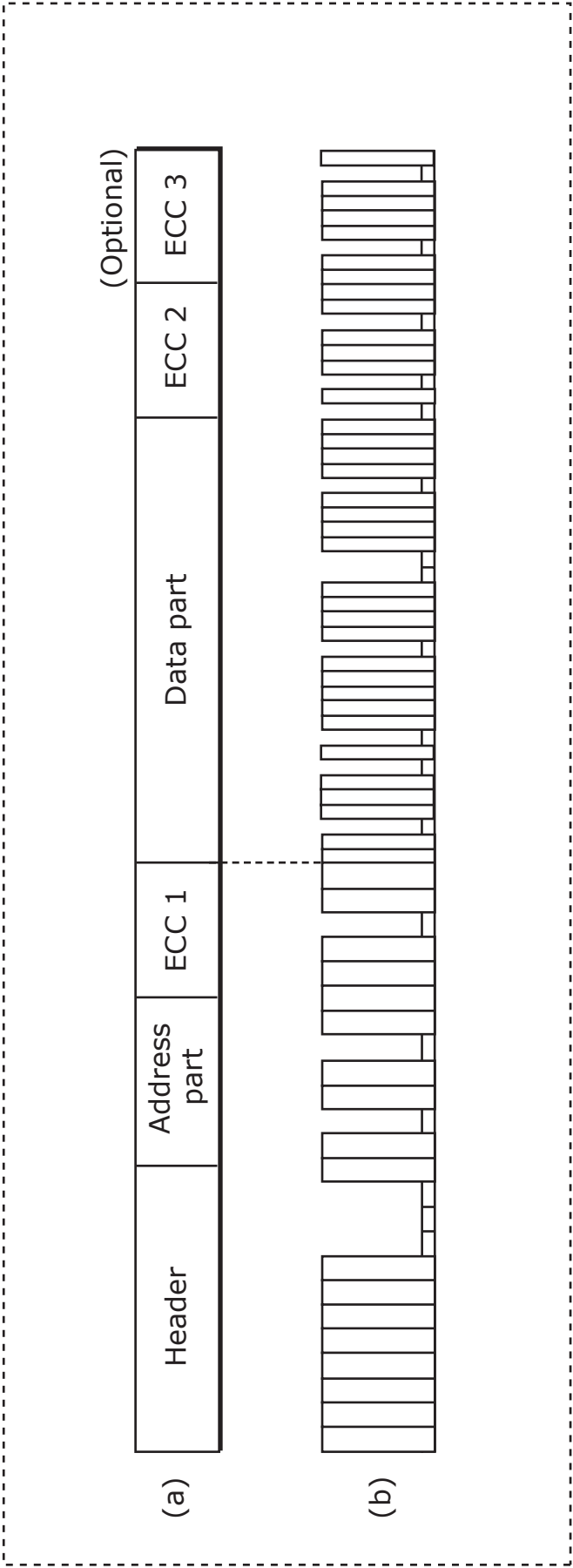


FIG. 273

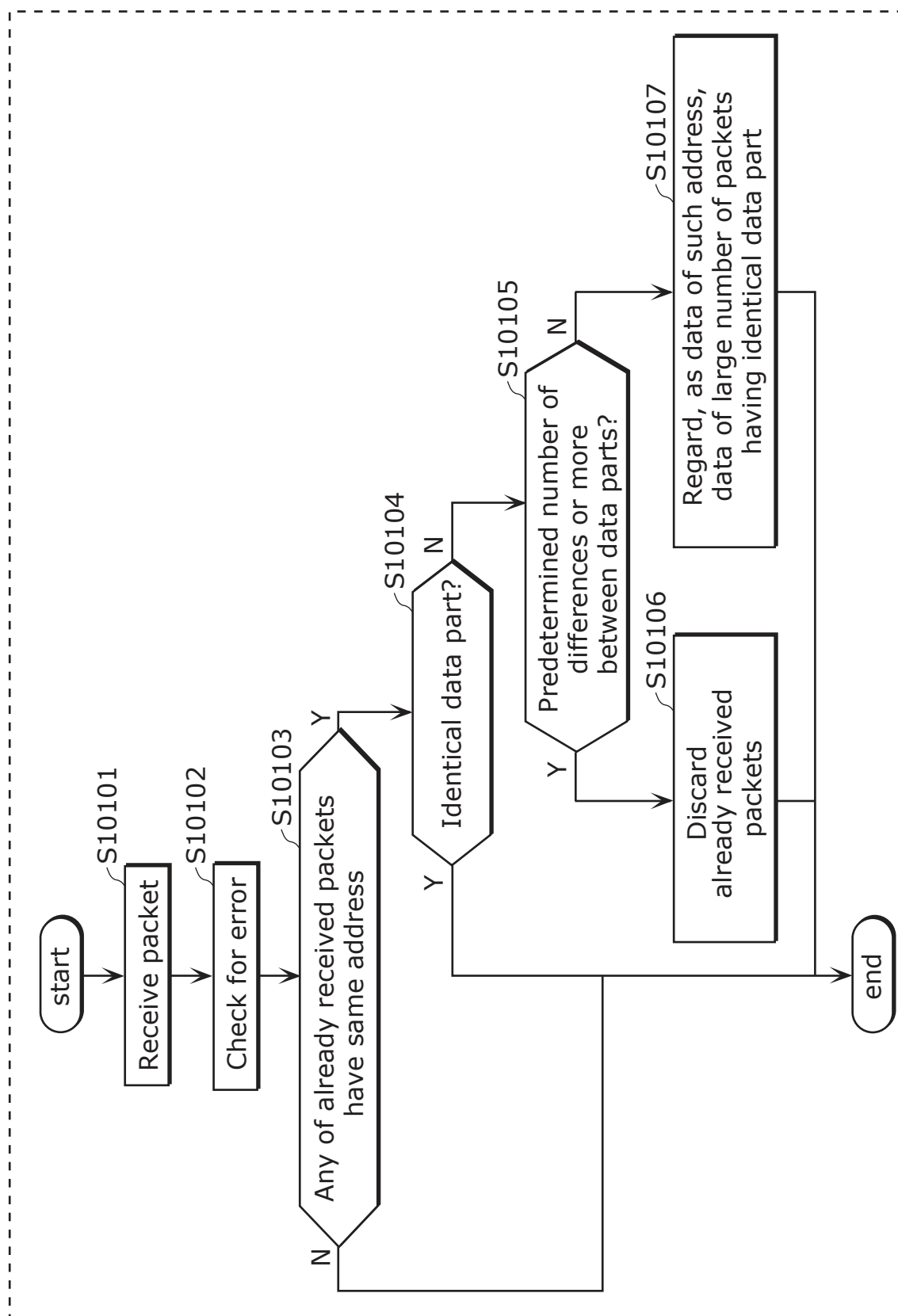


FIG. 274

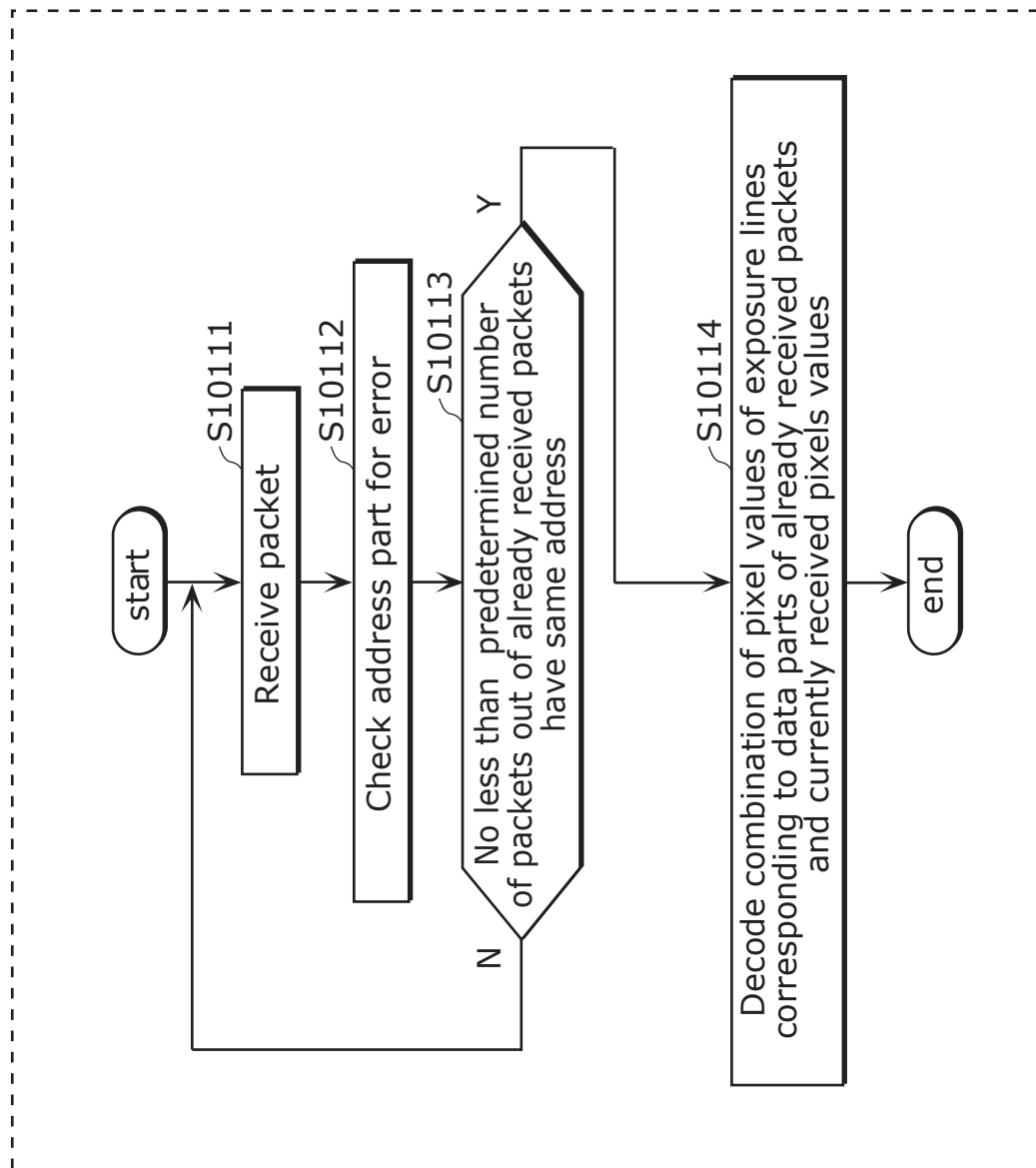


FIG. 275

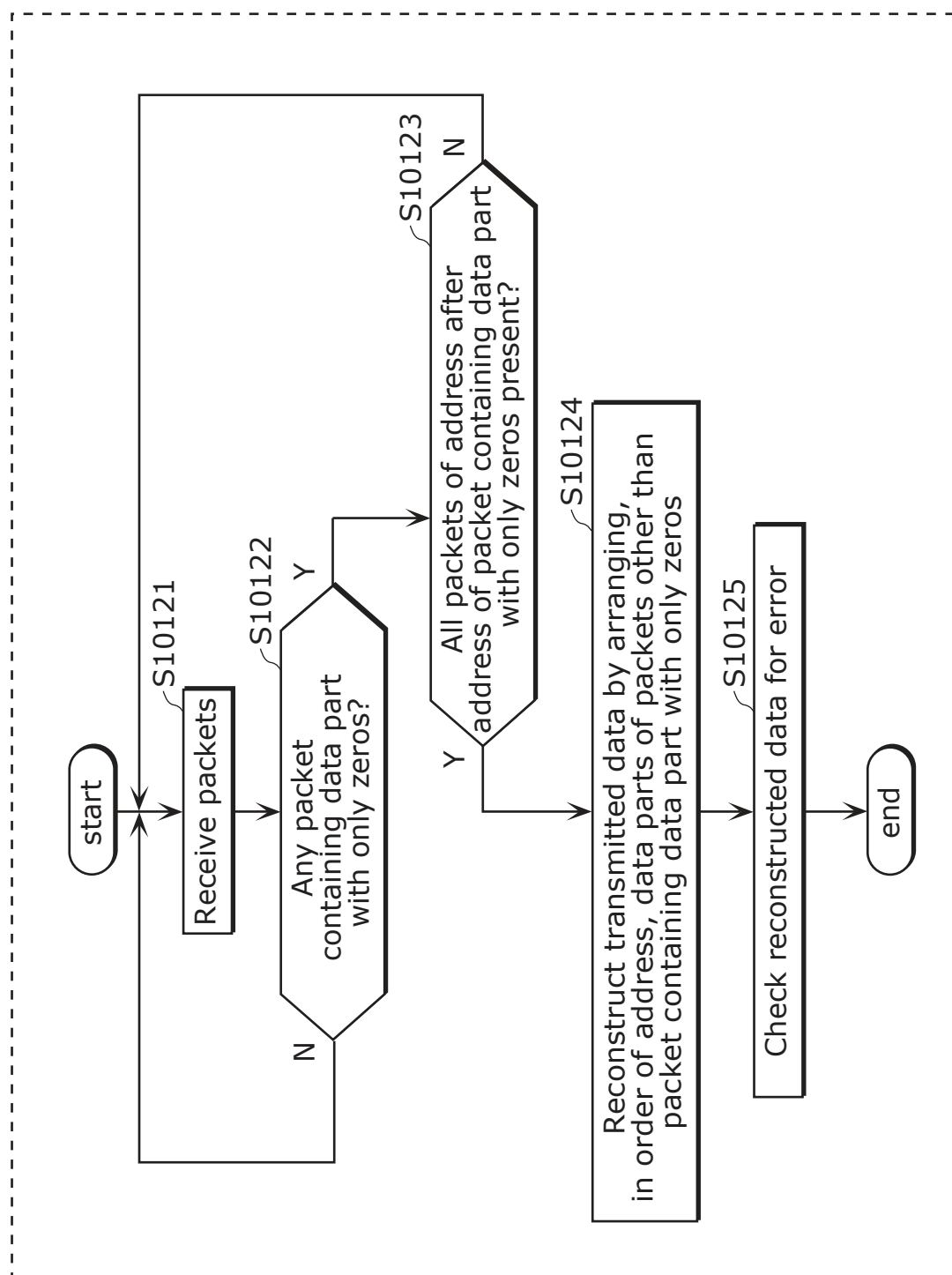


FIG. 276

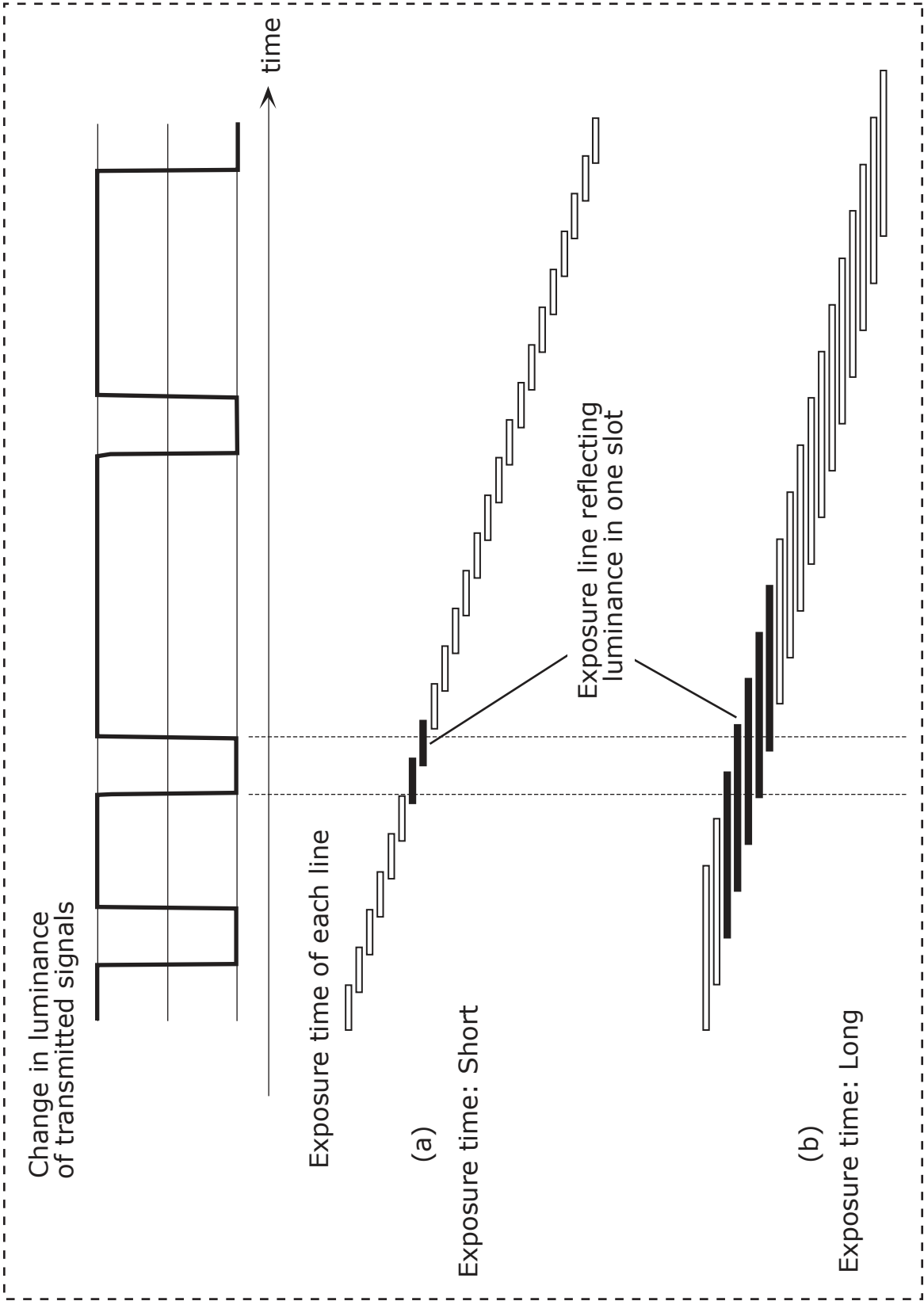


FIG. 277

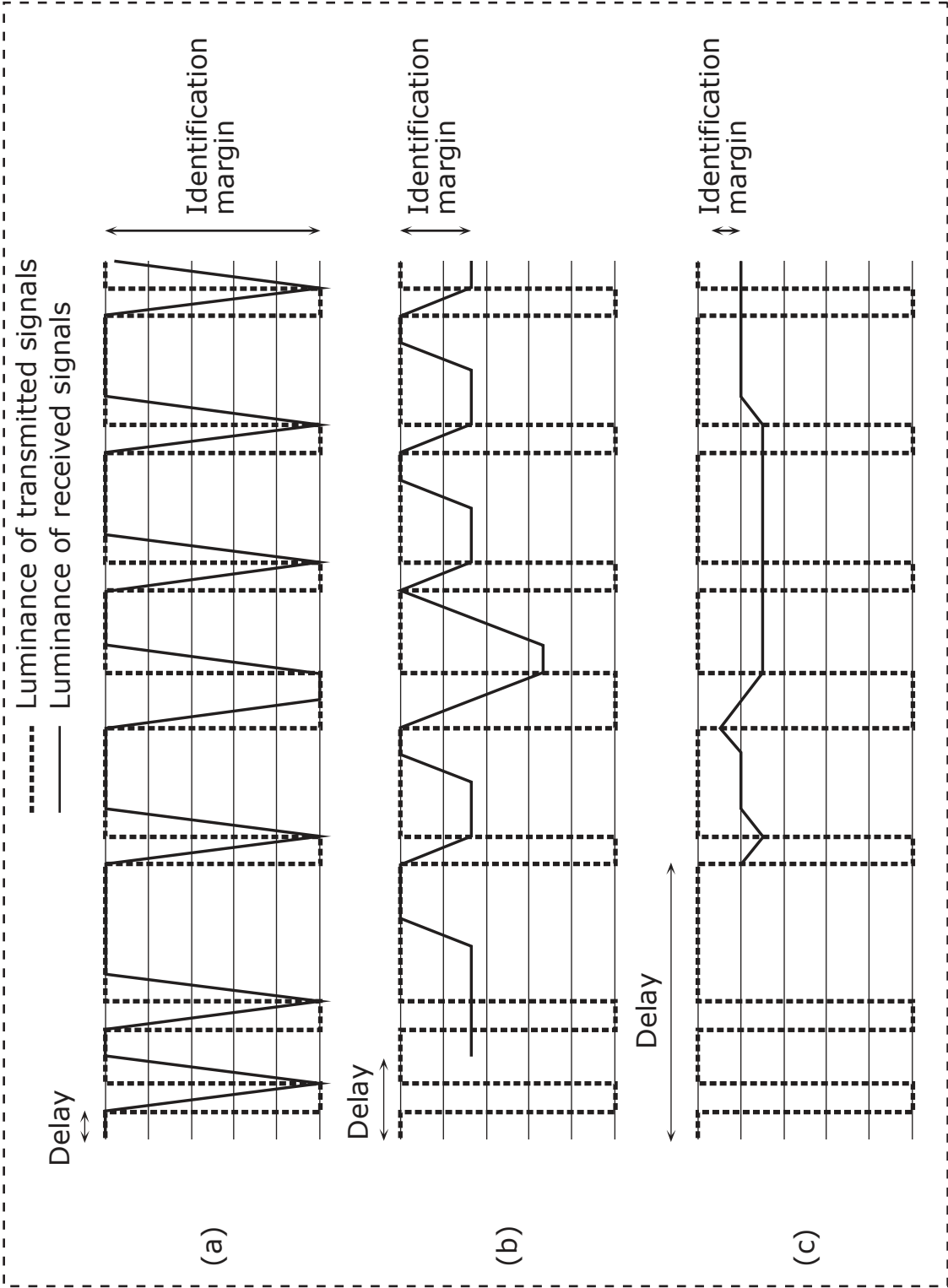
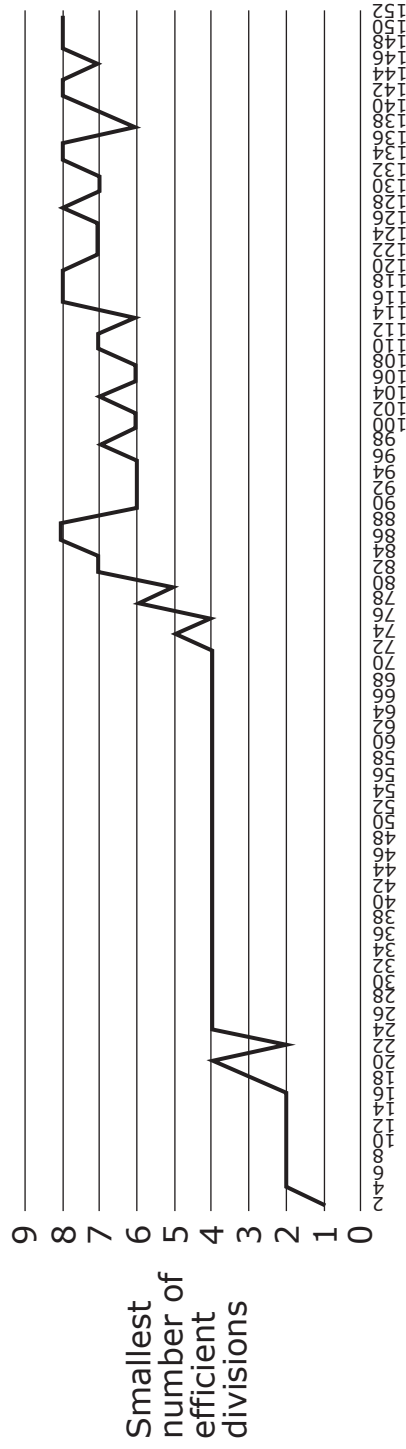
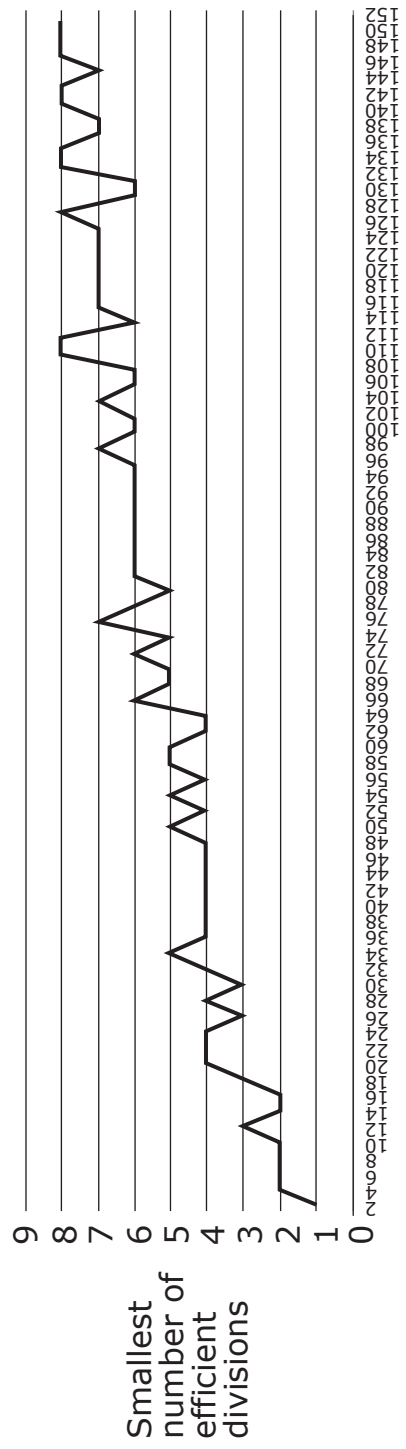


FIG. 278



Transmission data size [bit]

(a) When address size is variable (When transmission data size is not variable)



Transmission data size [bit]

(b) When address size is fixed at 4 bits (When transmission data size is variable)

FIG. 279A

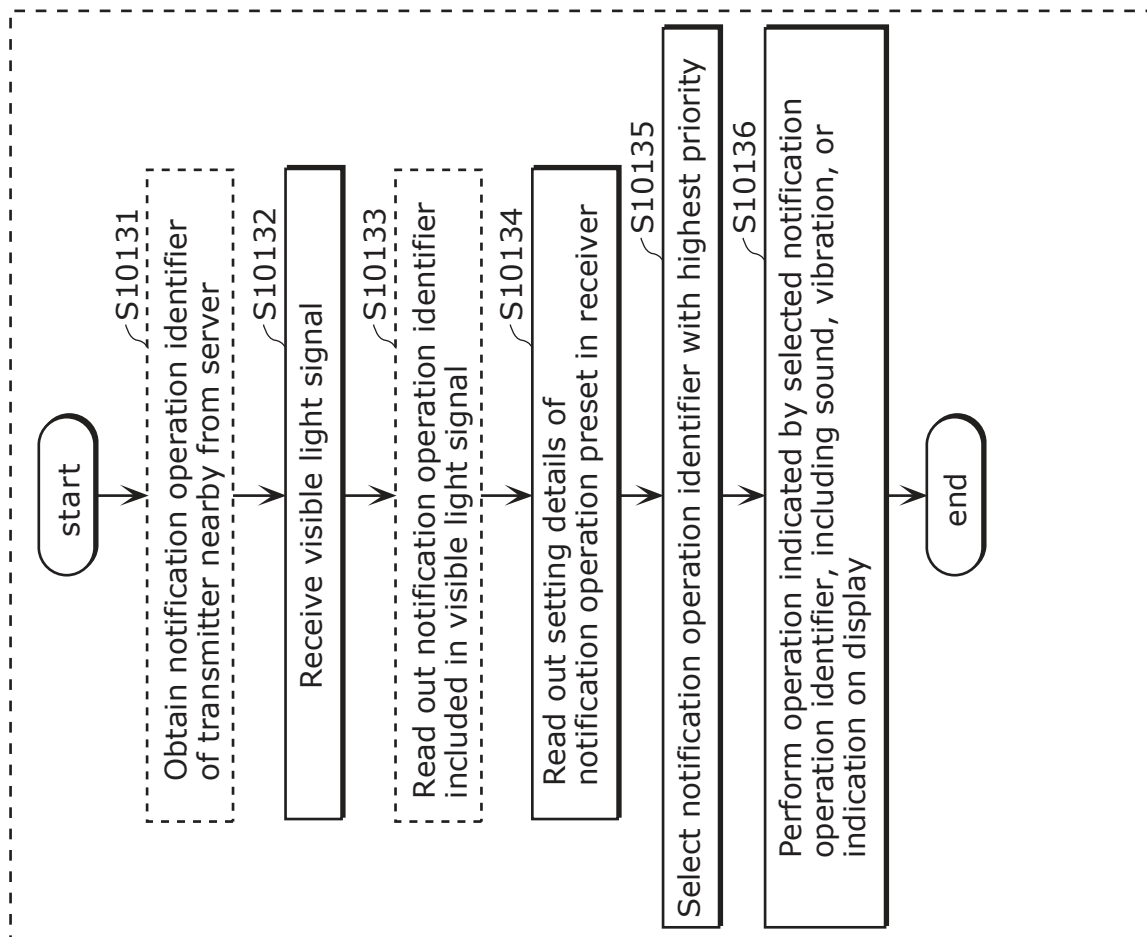


FIG. 279B

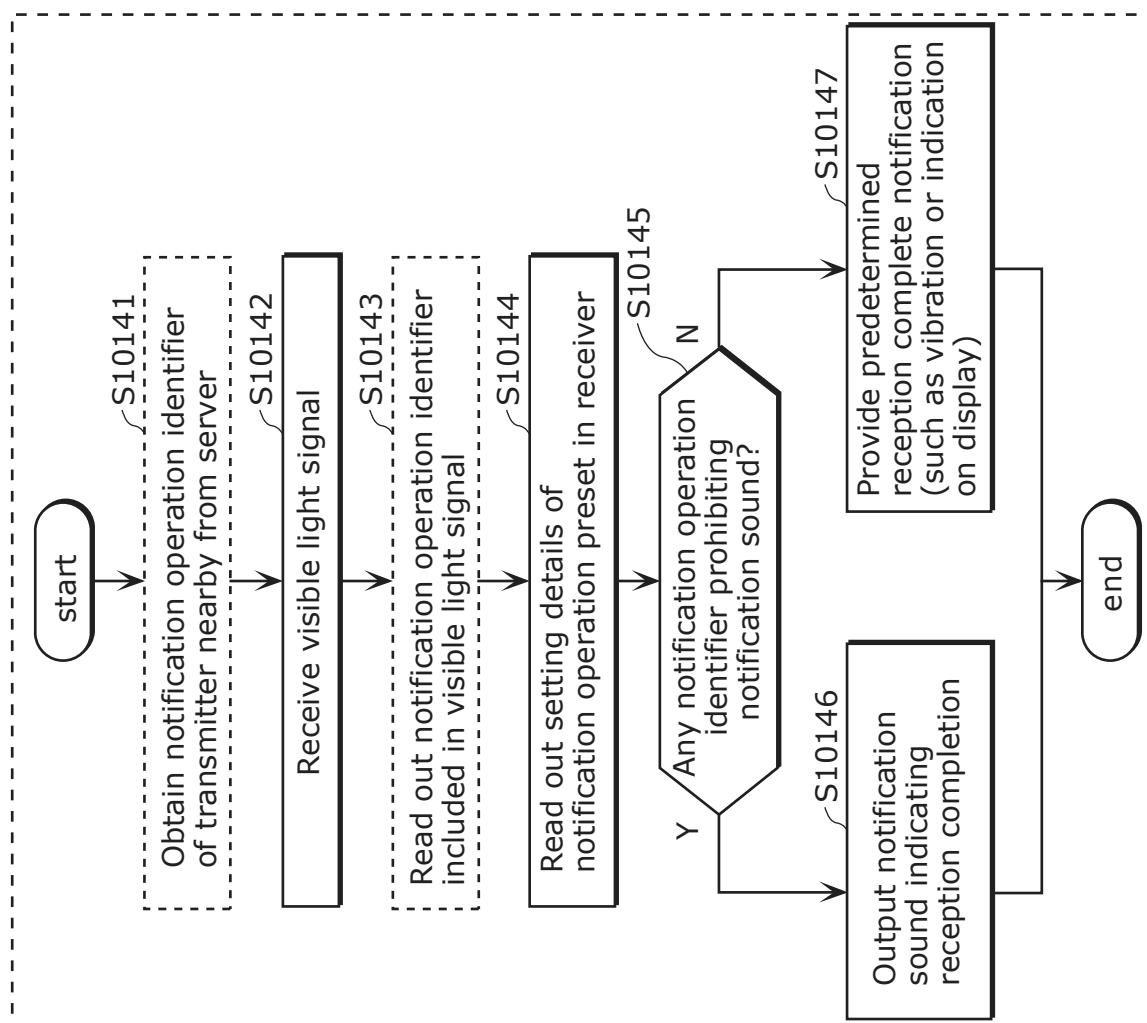


FIG. 280A

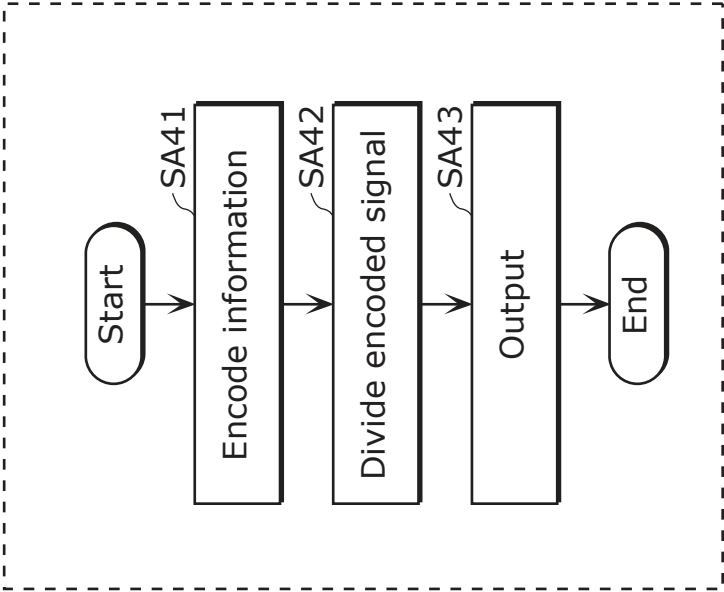


FIG. 280B

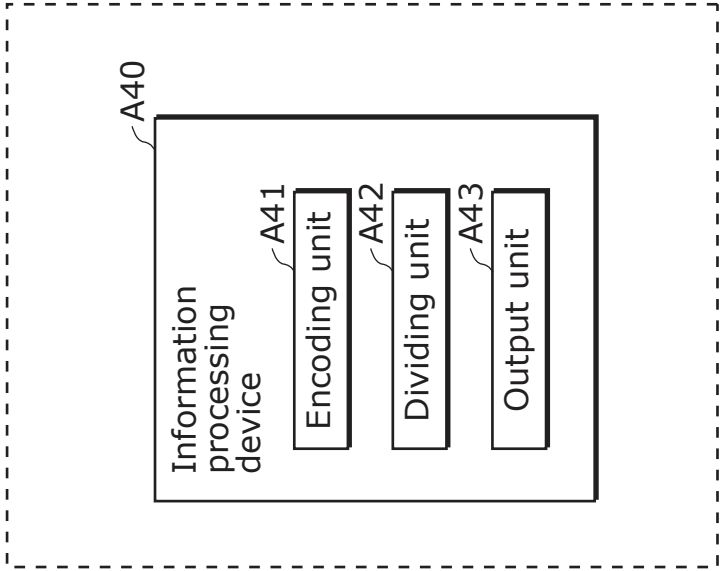


FIG. 281

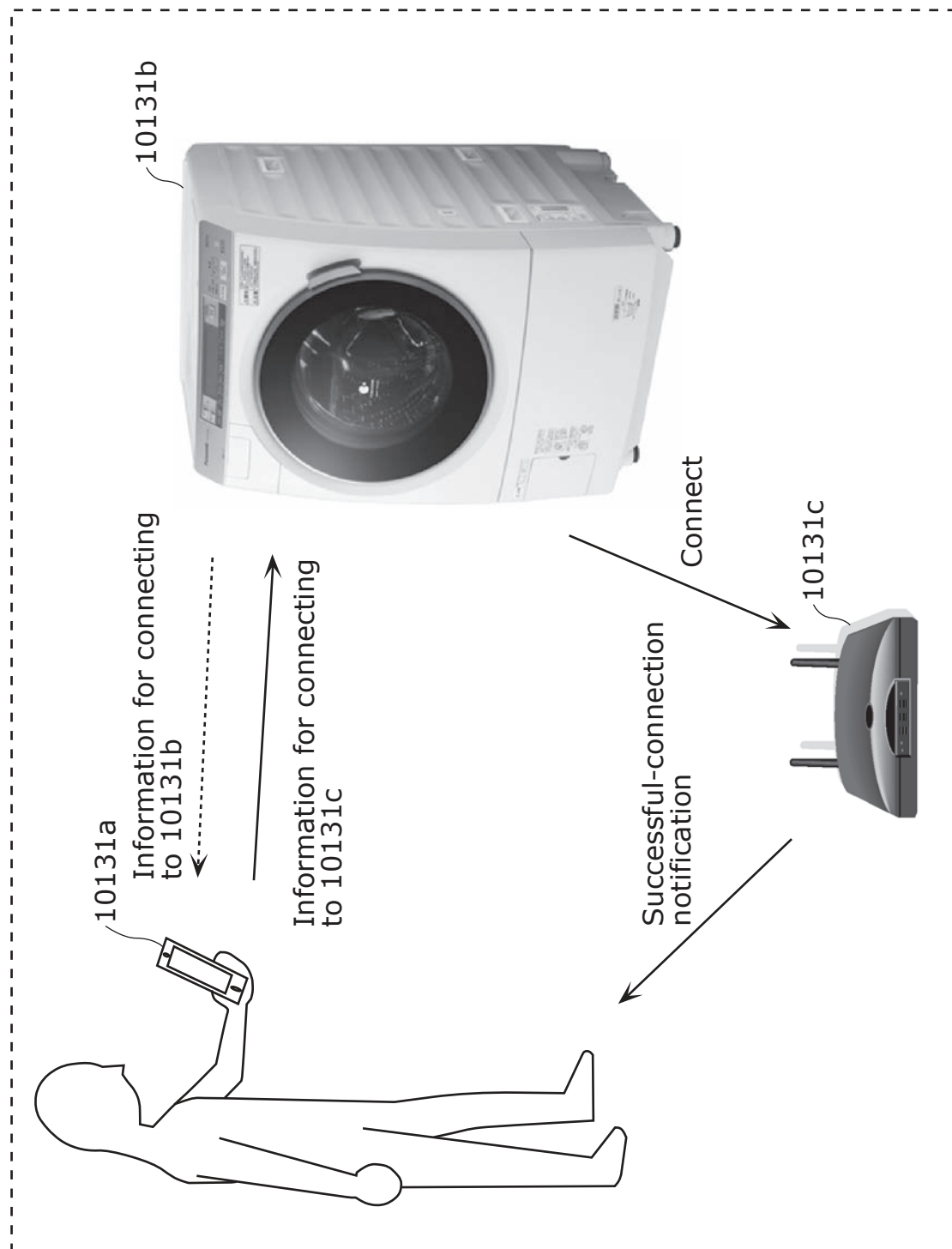


FIG. 282

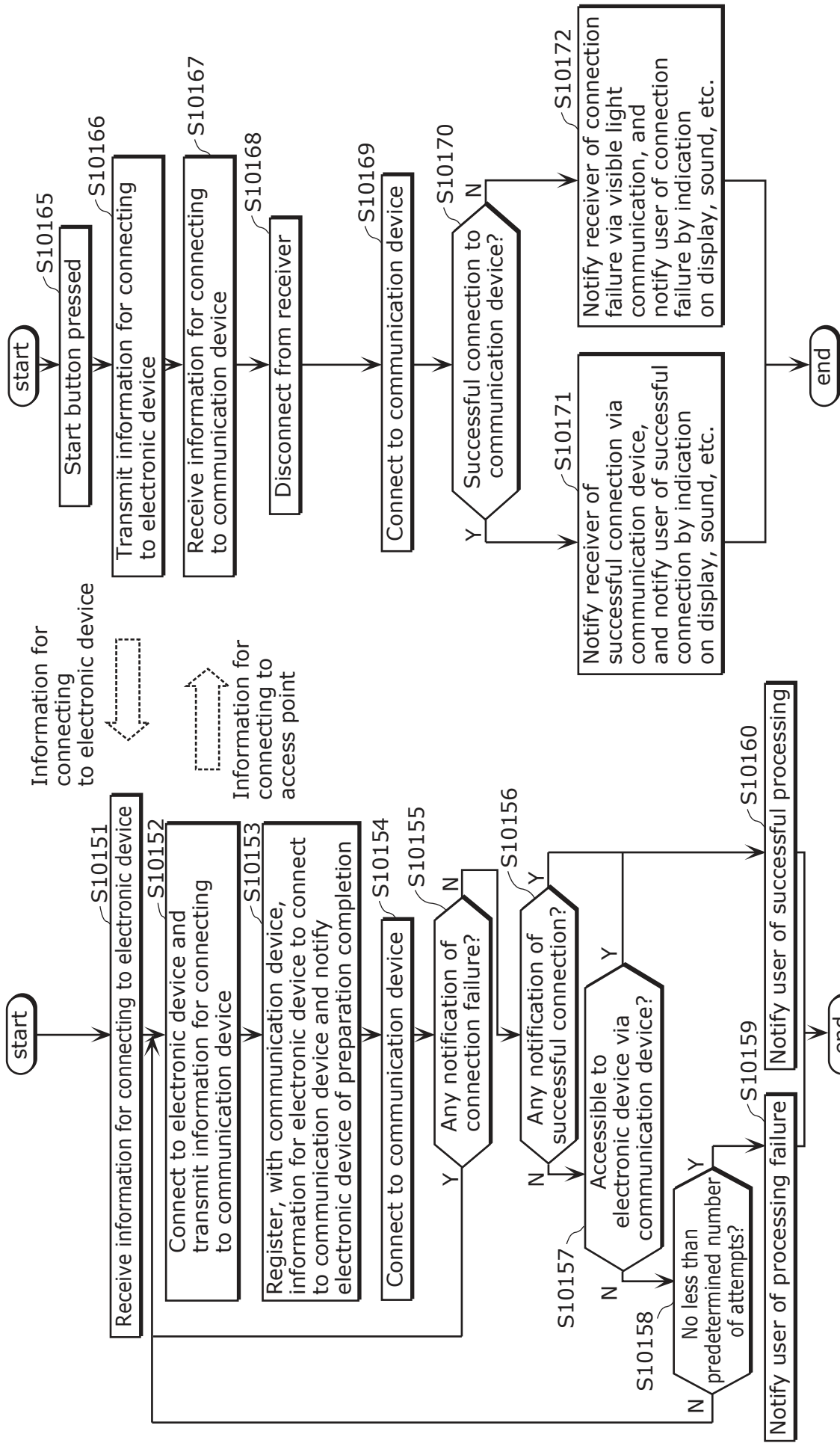


FIG. 283

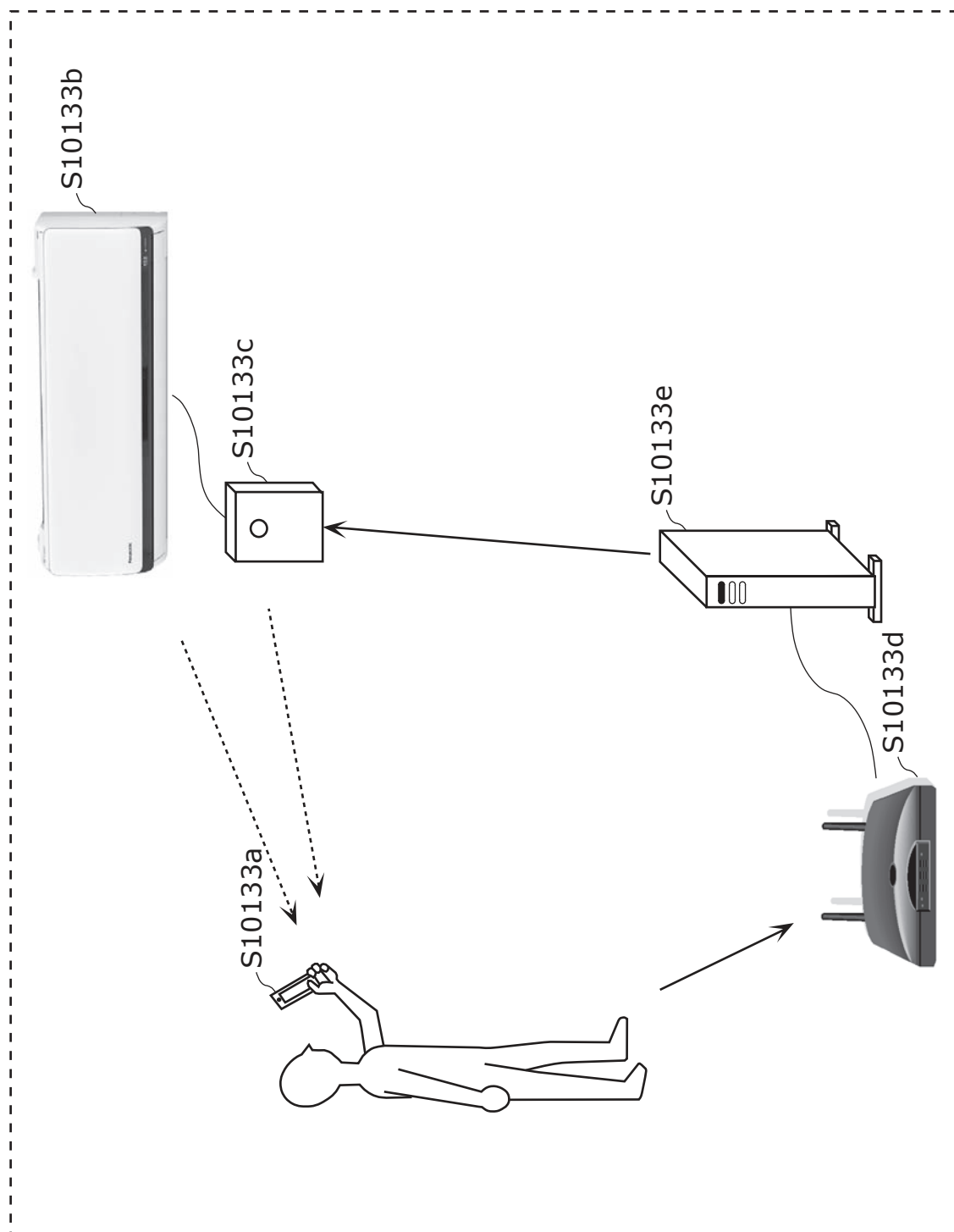


FIG. 284

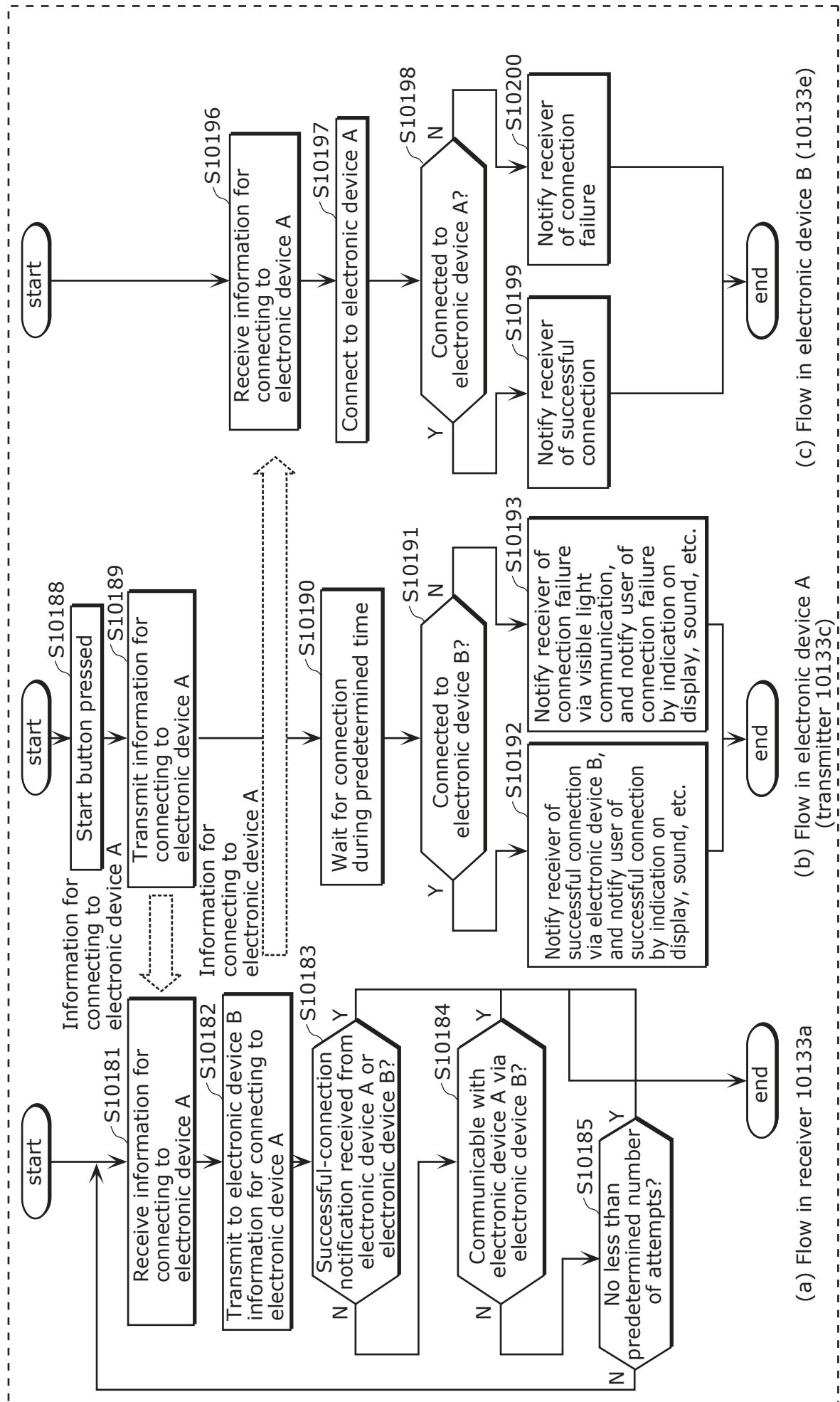


FIG. 285

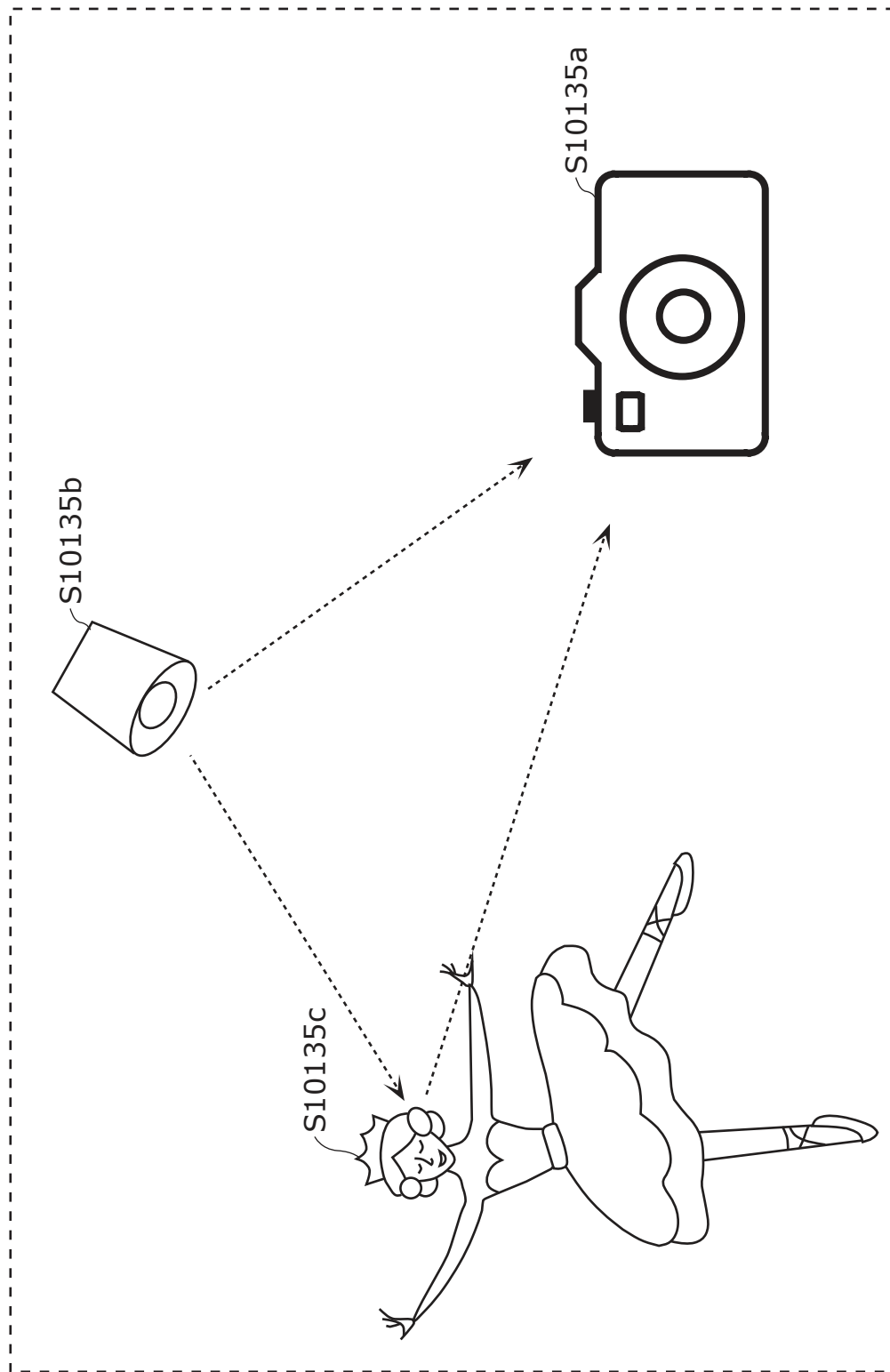


FIG. 286

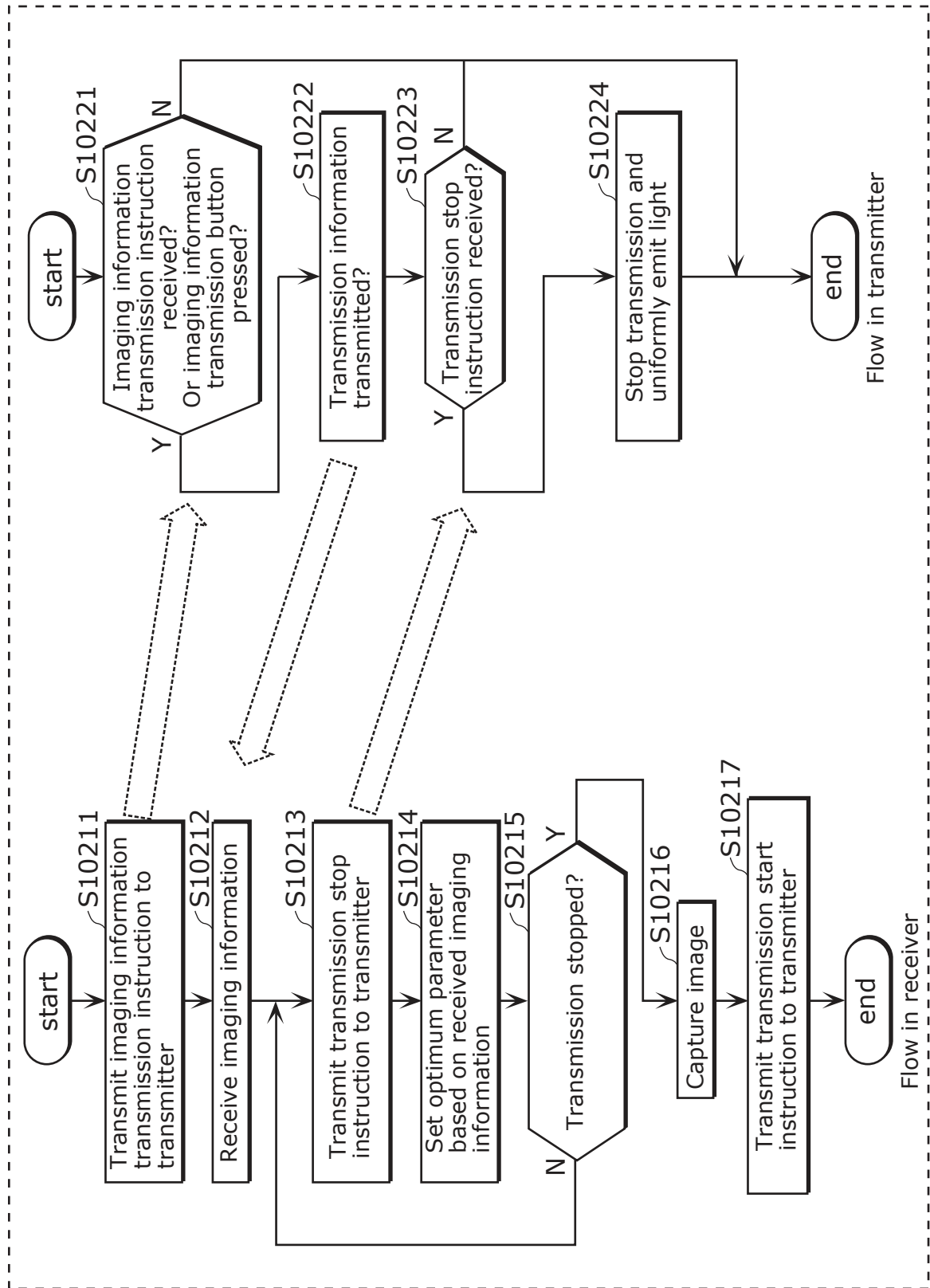


FIG. 287

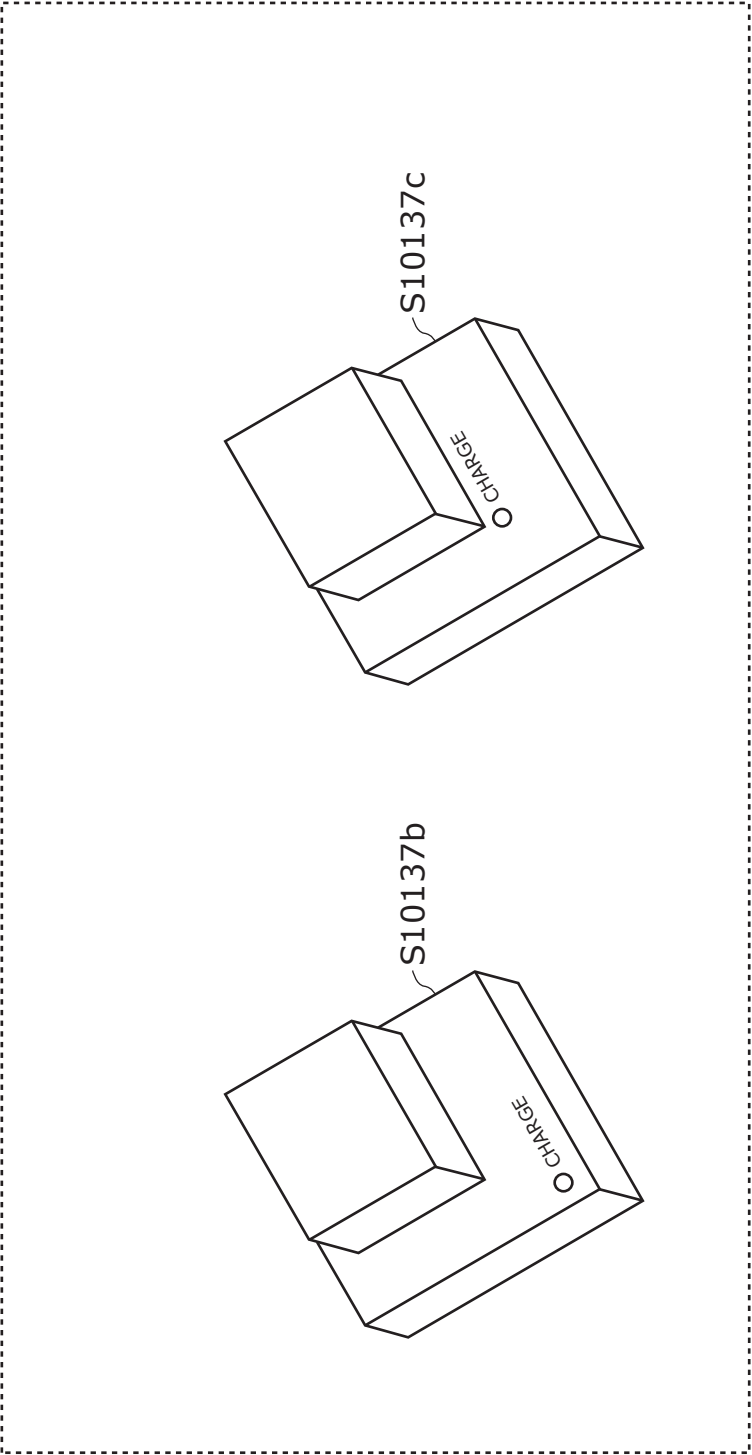


FIG. 288

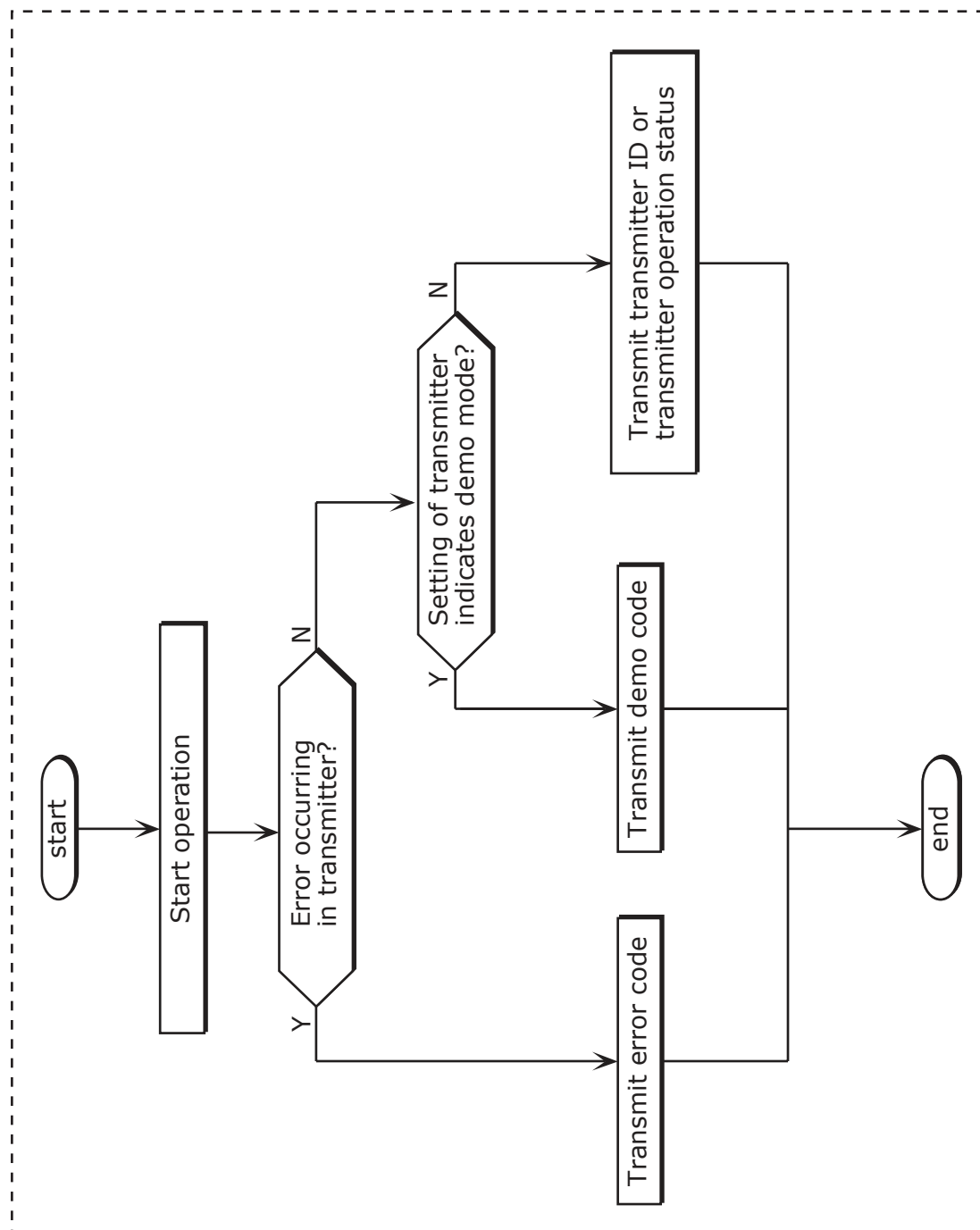


FIG. 289

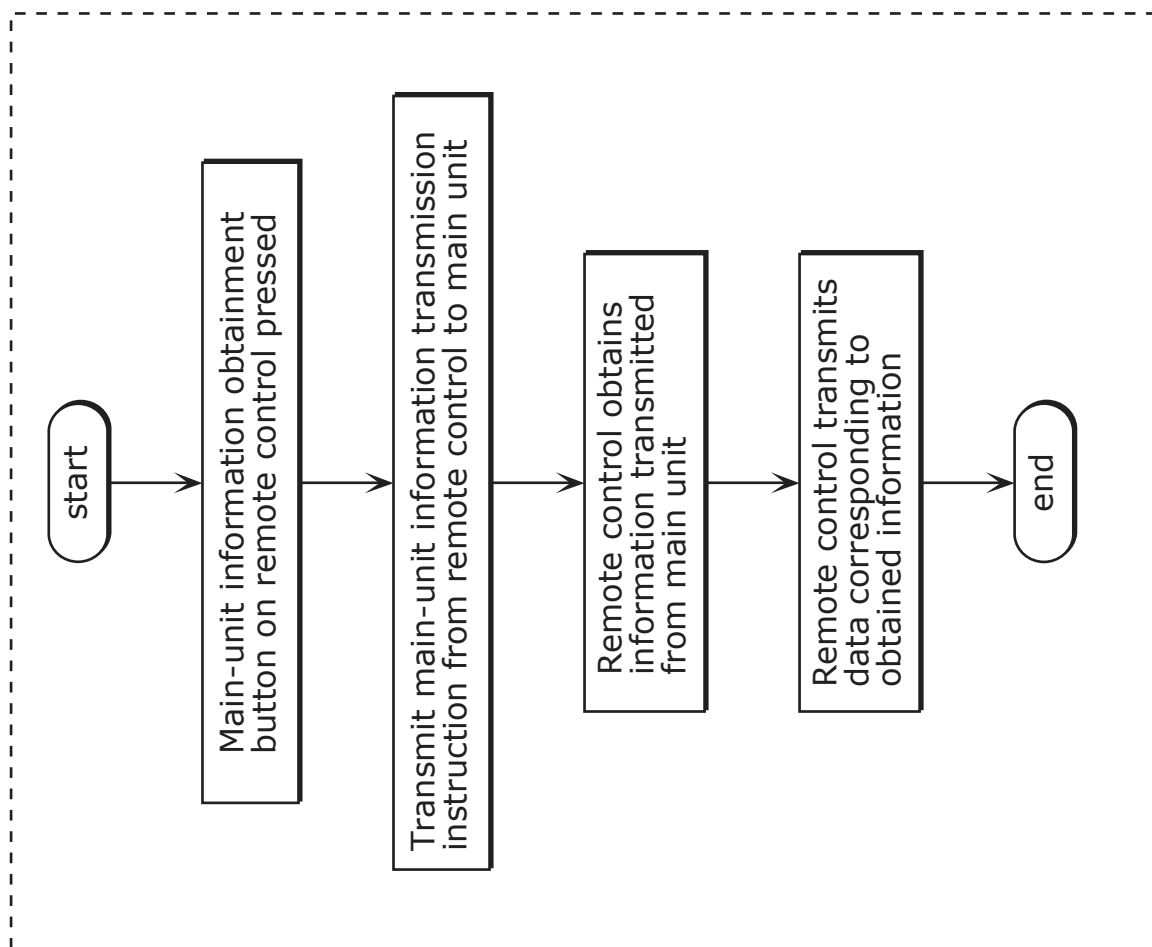


FIG. 290

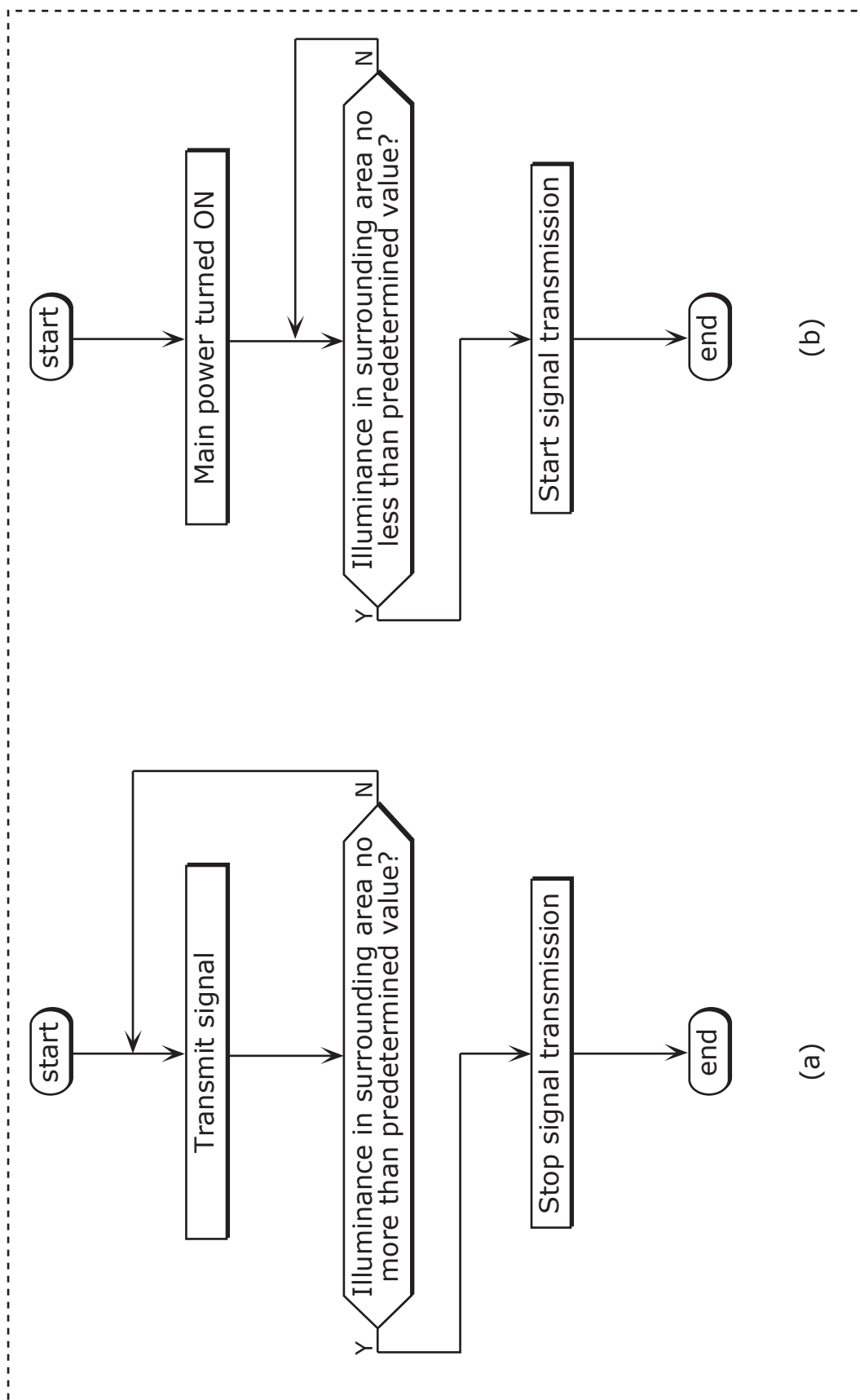


FIG. 291

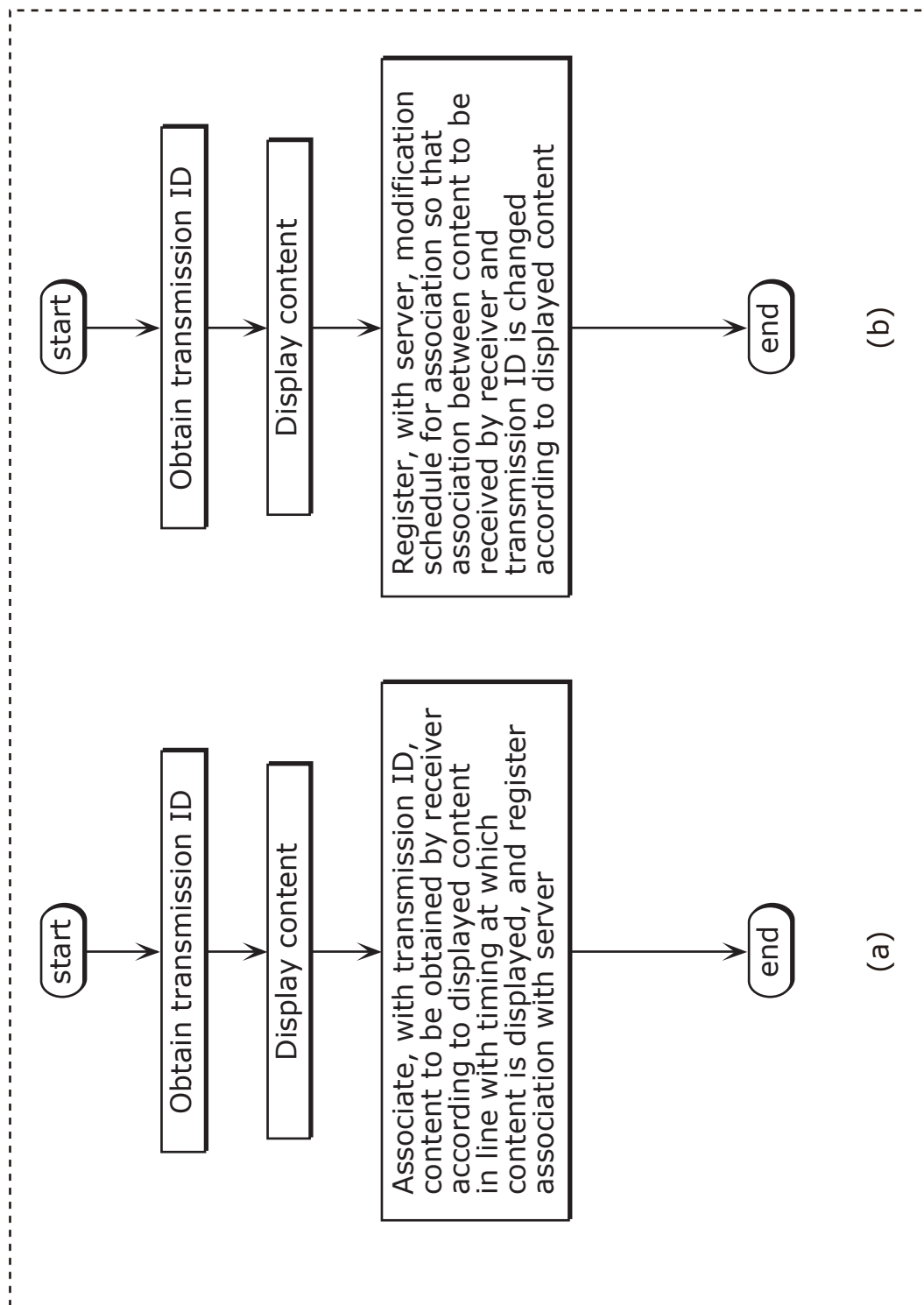


FIG. 292

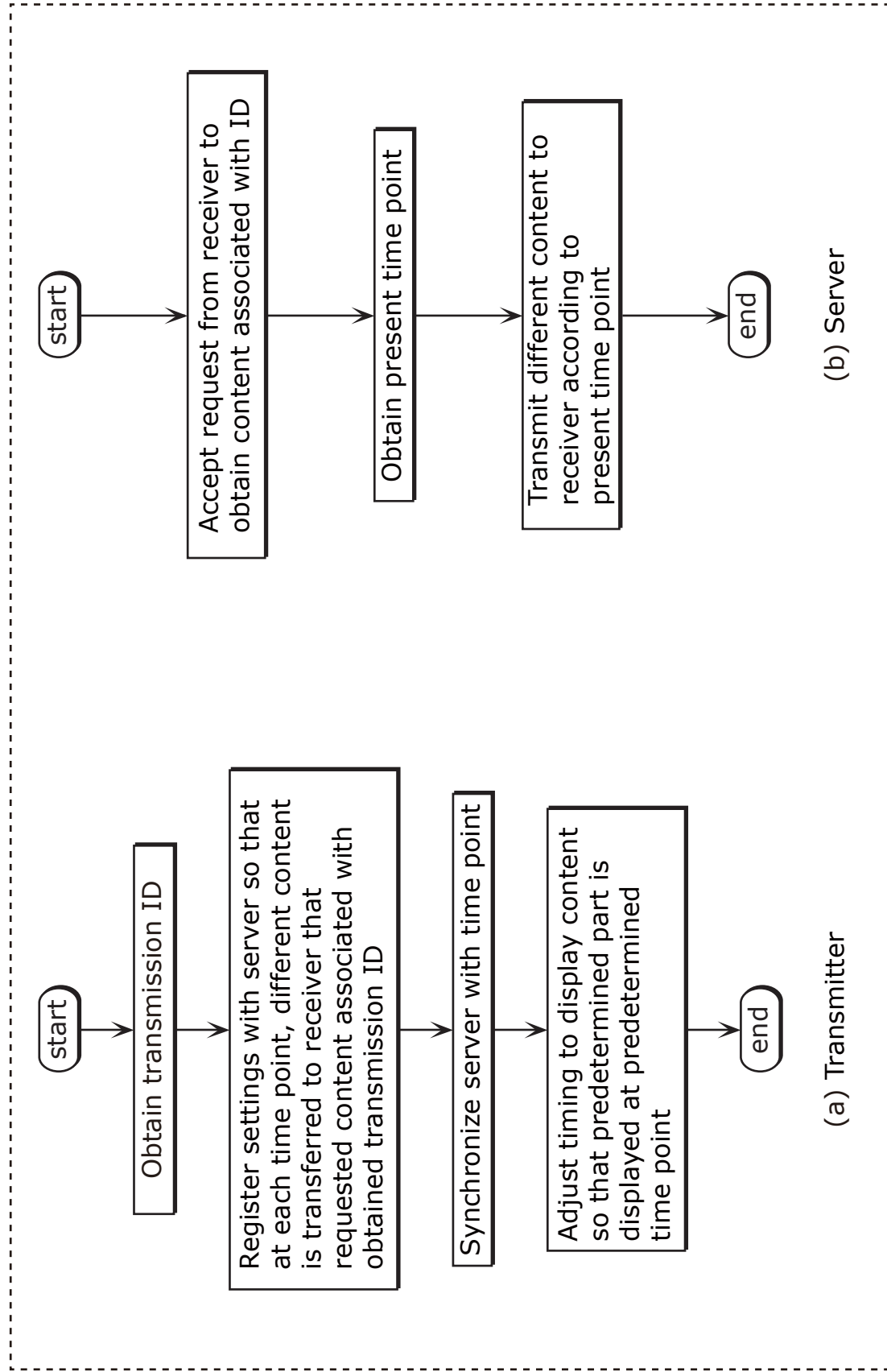


FIG. 293

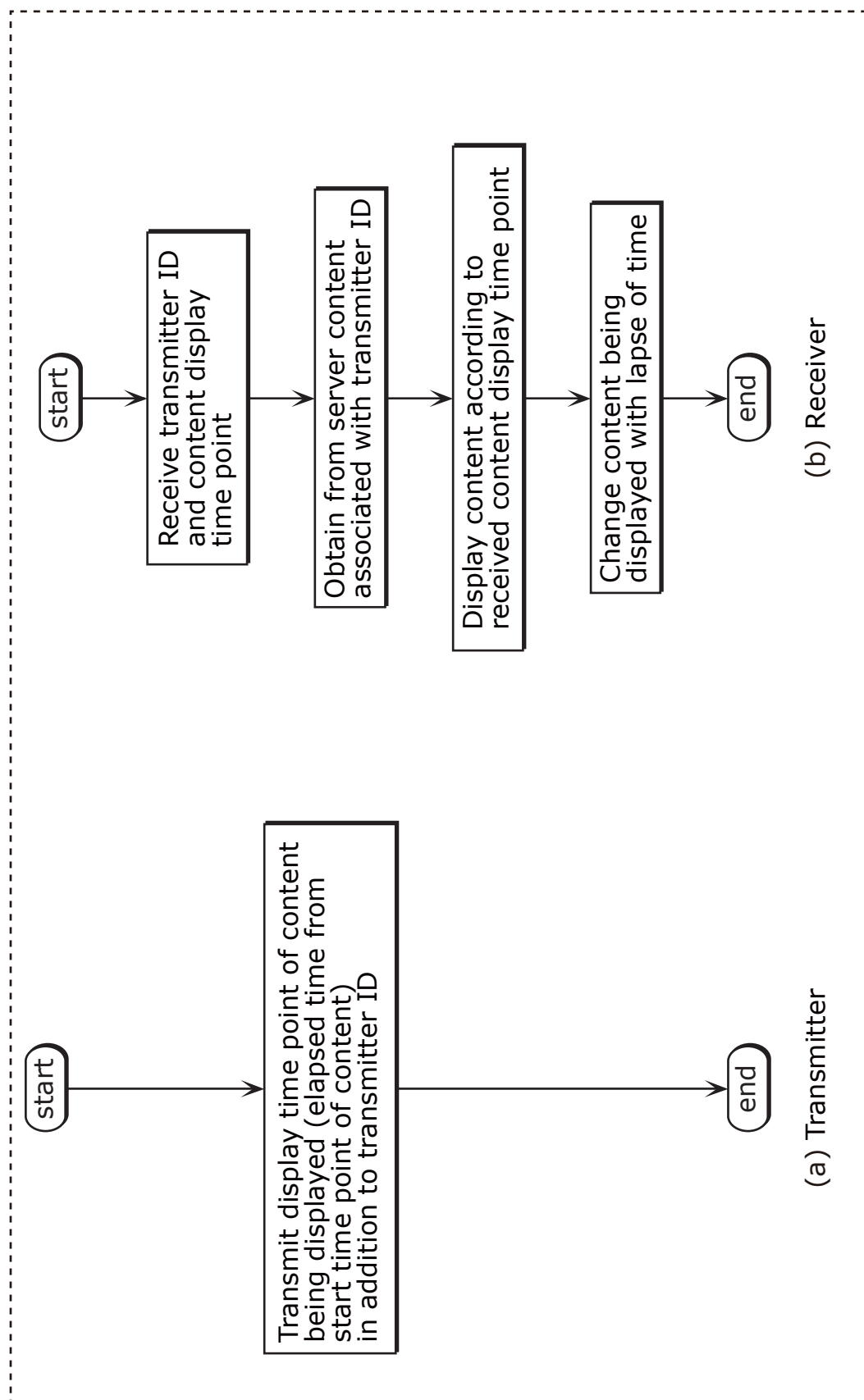


FIG. 294

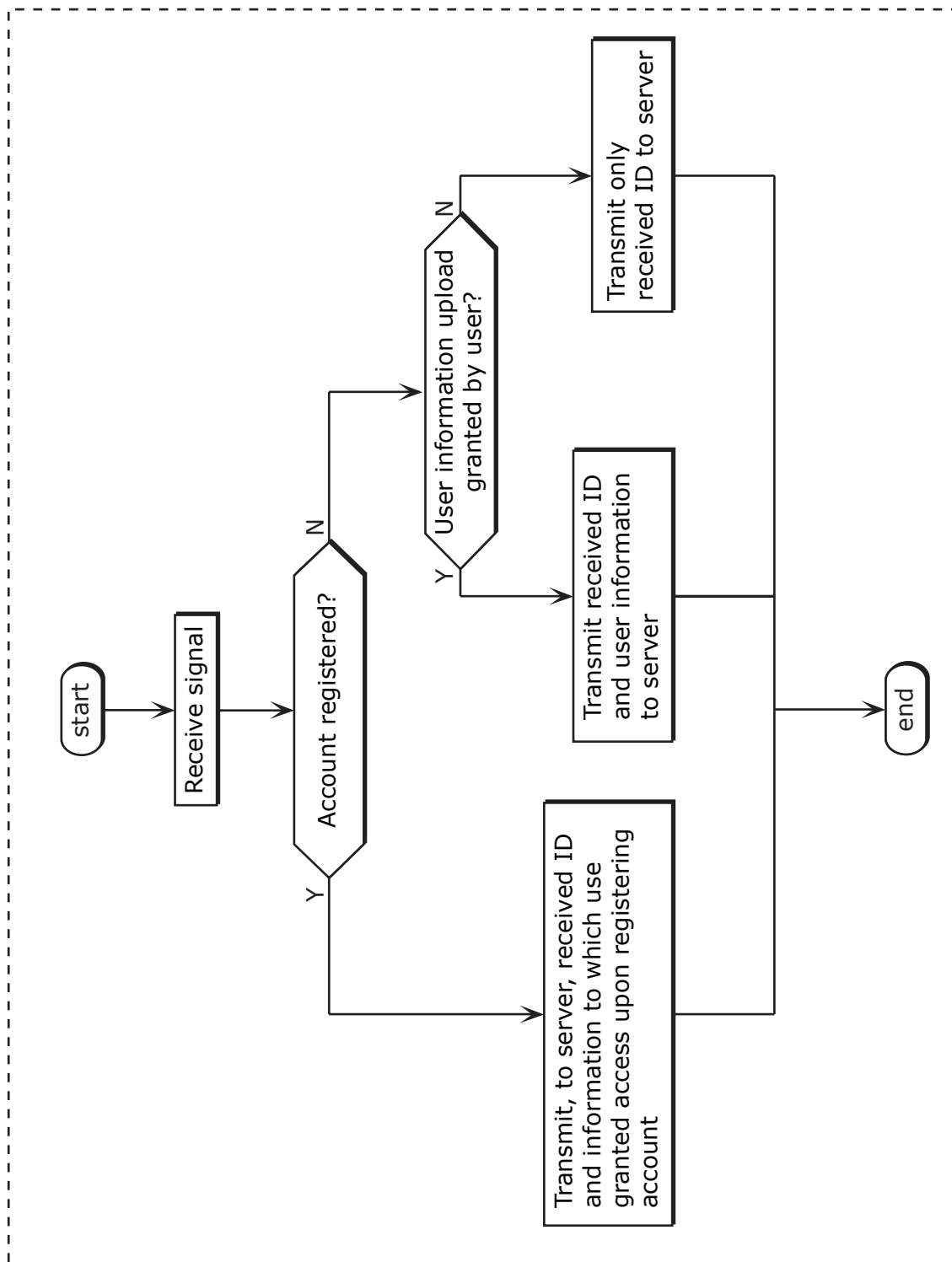


FIG. 295

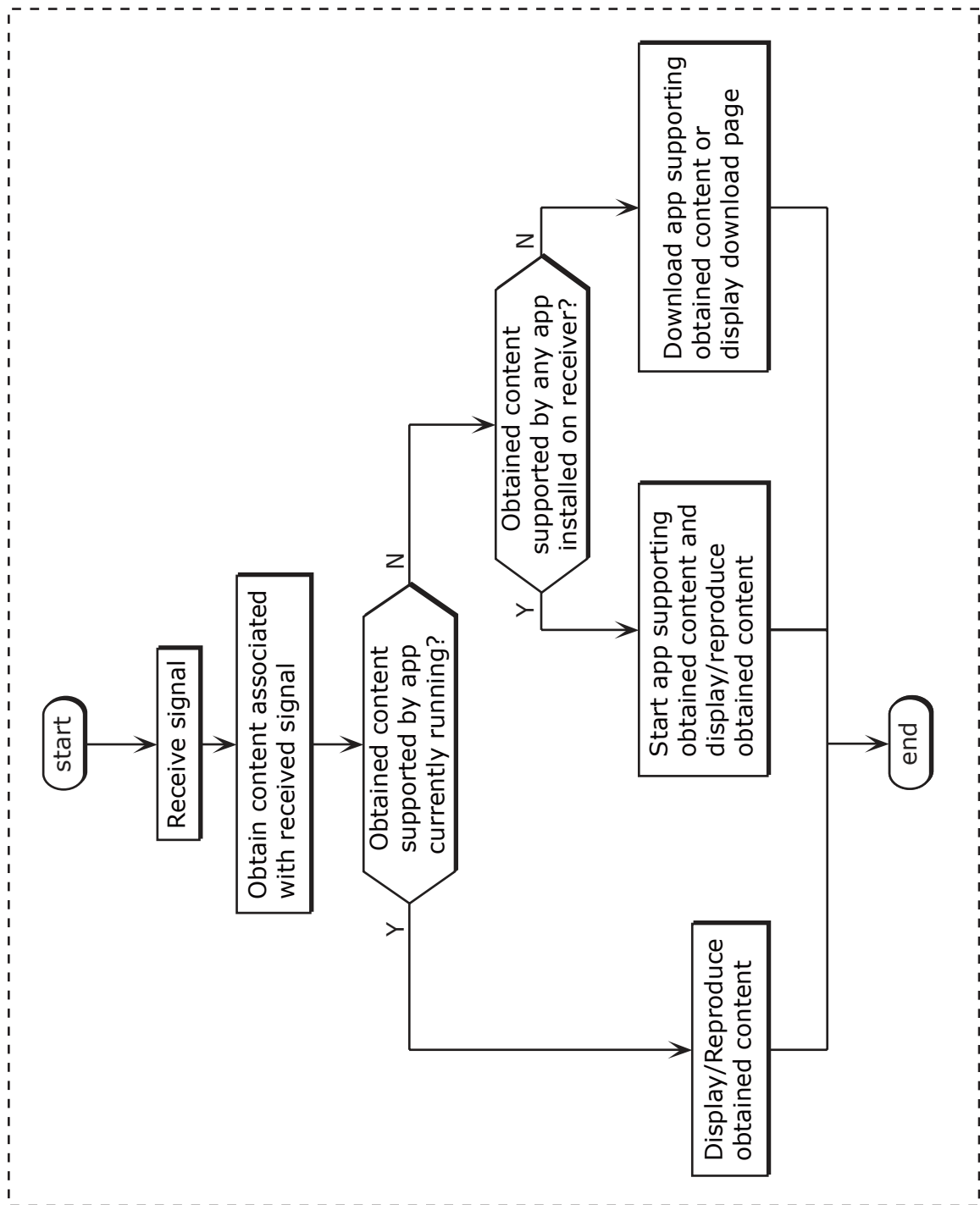


FIG. 296

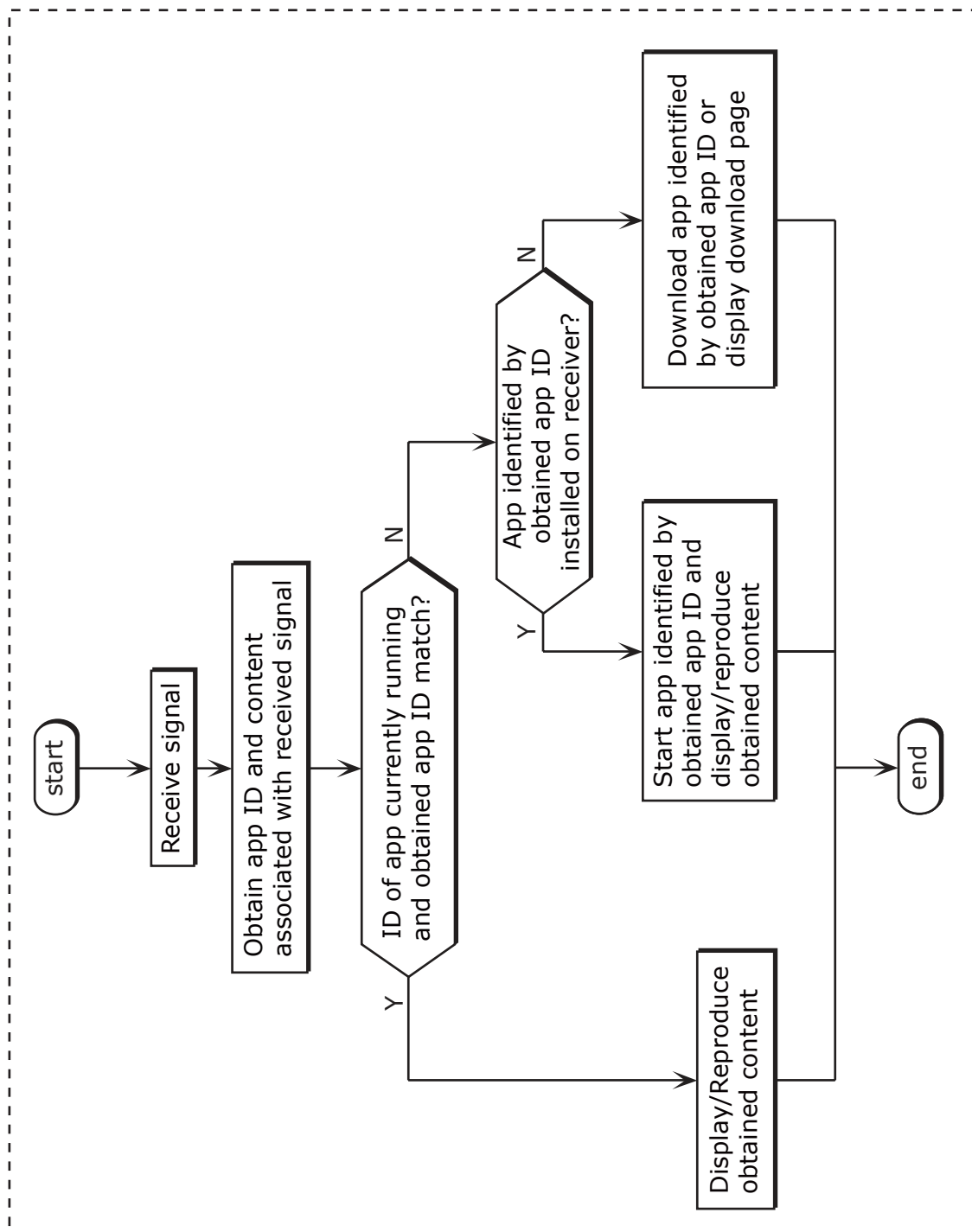


FIG. 297

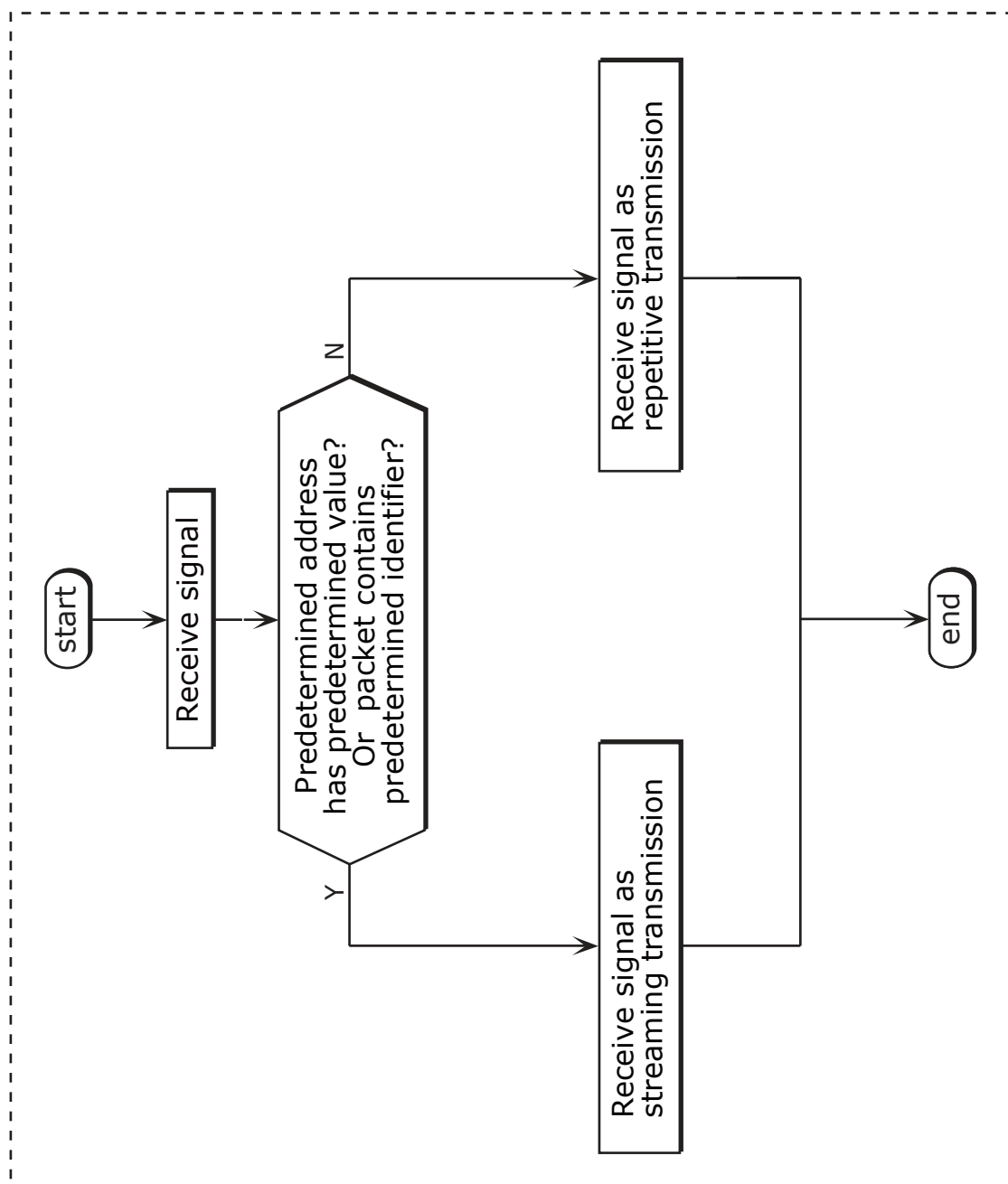


FIG. 298

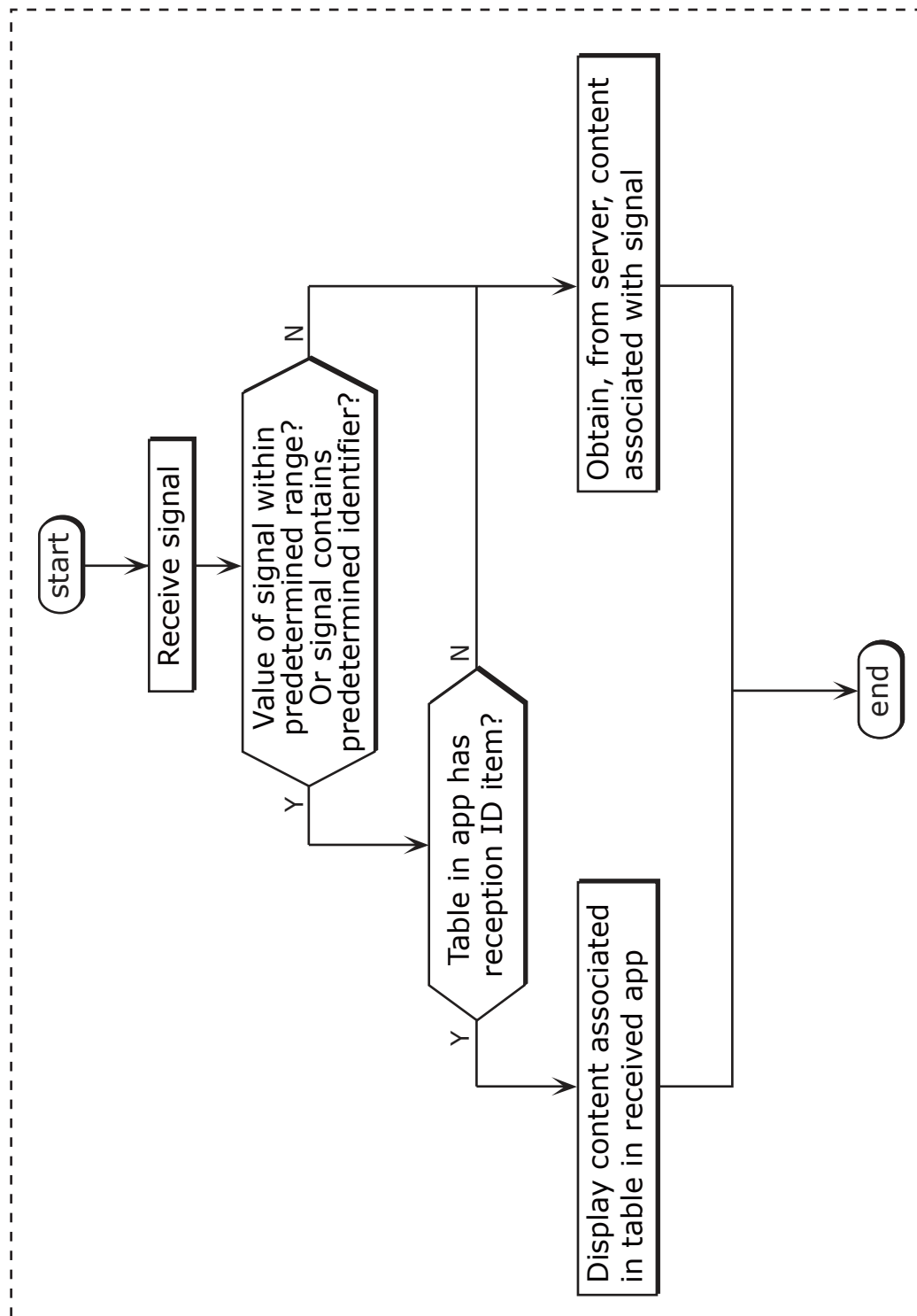


FIG. 299

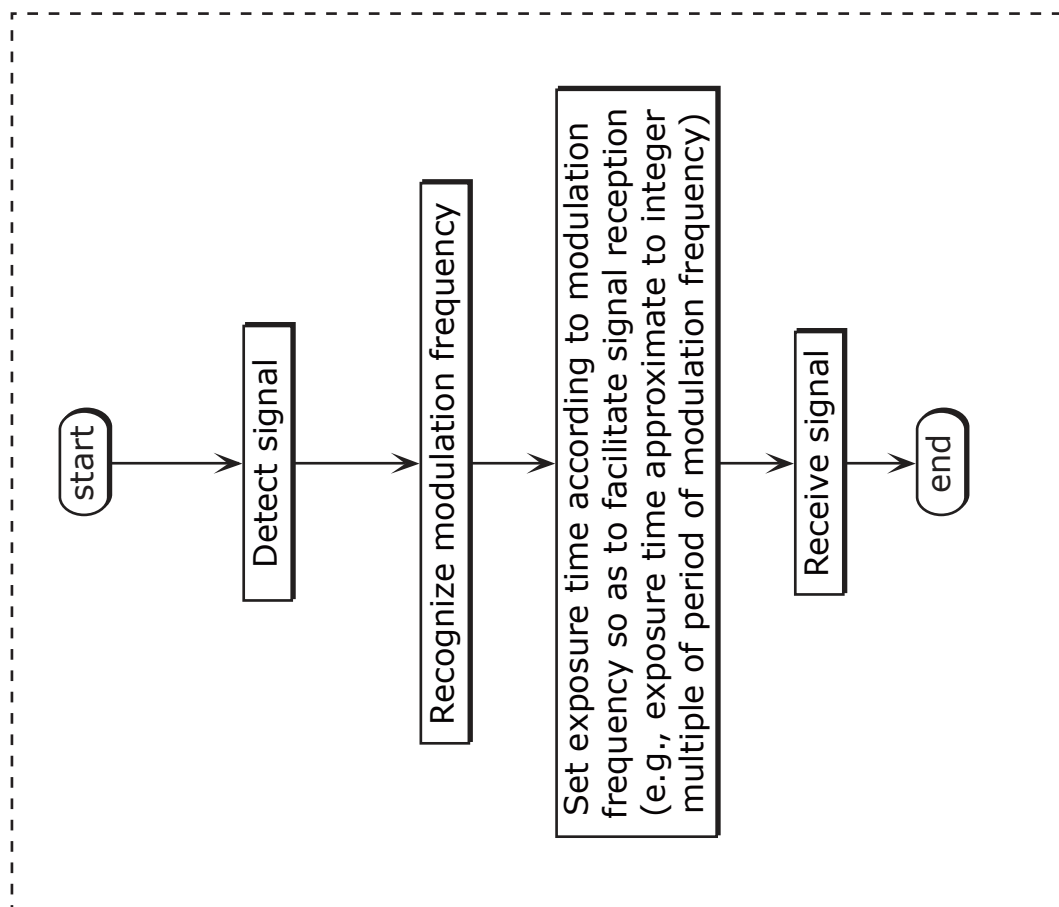


FIG. 300

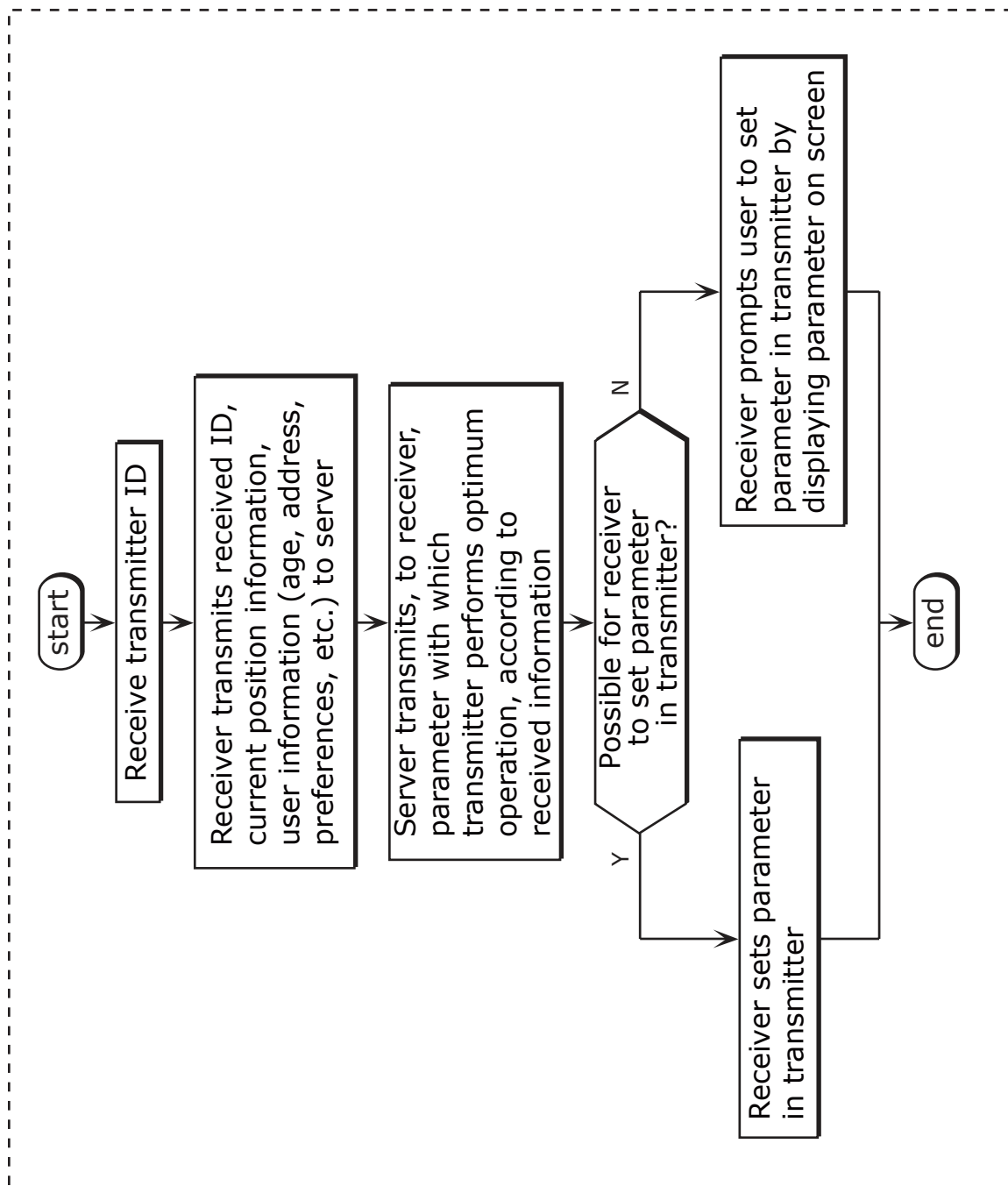


FIG. 301

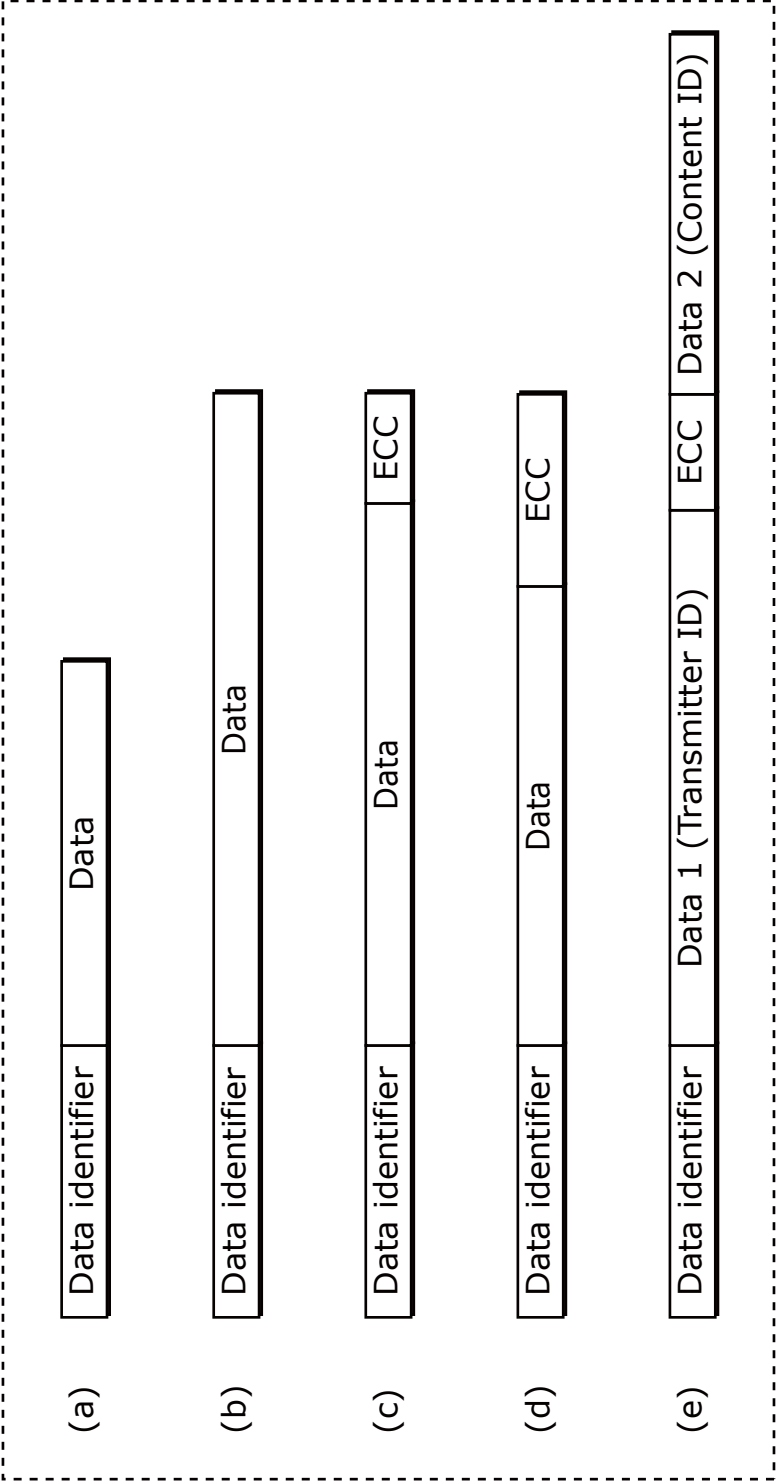


FIG. 302

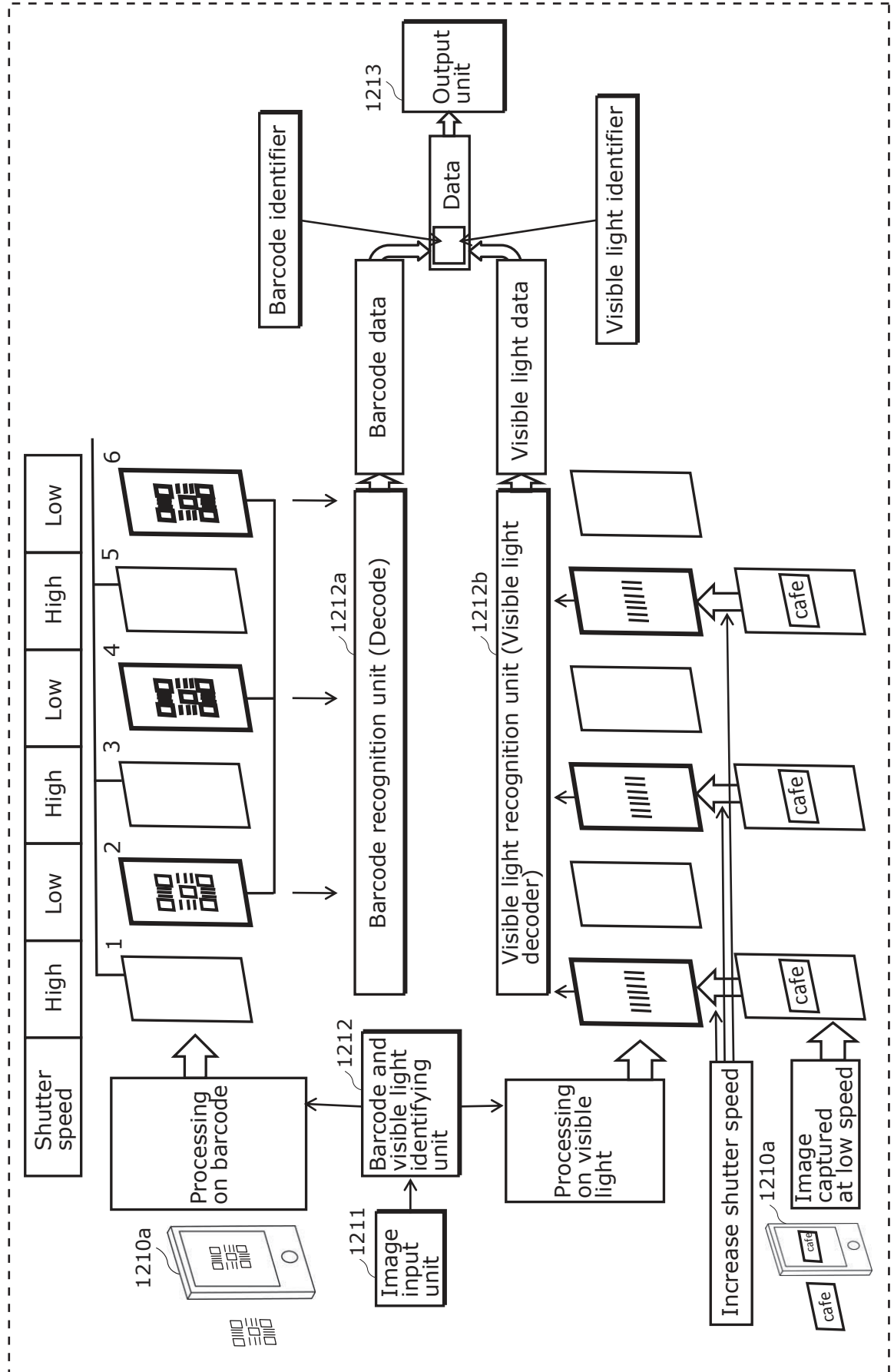


FIG. 303A

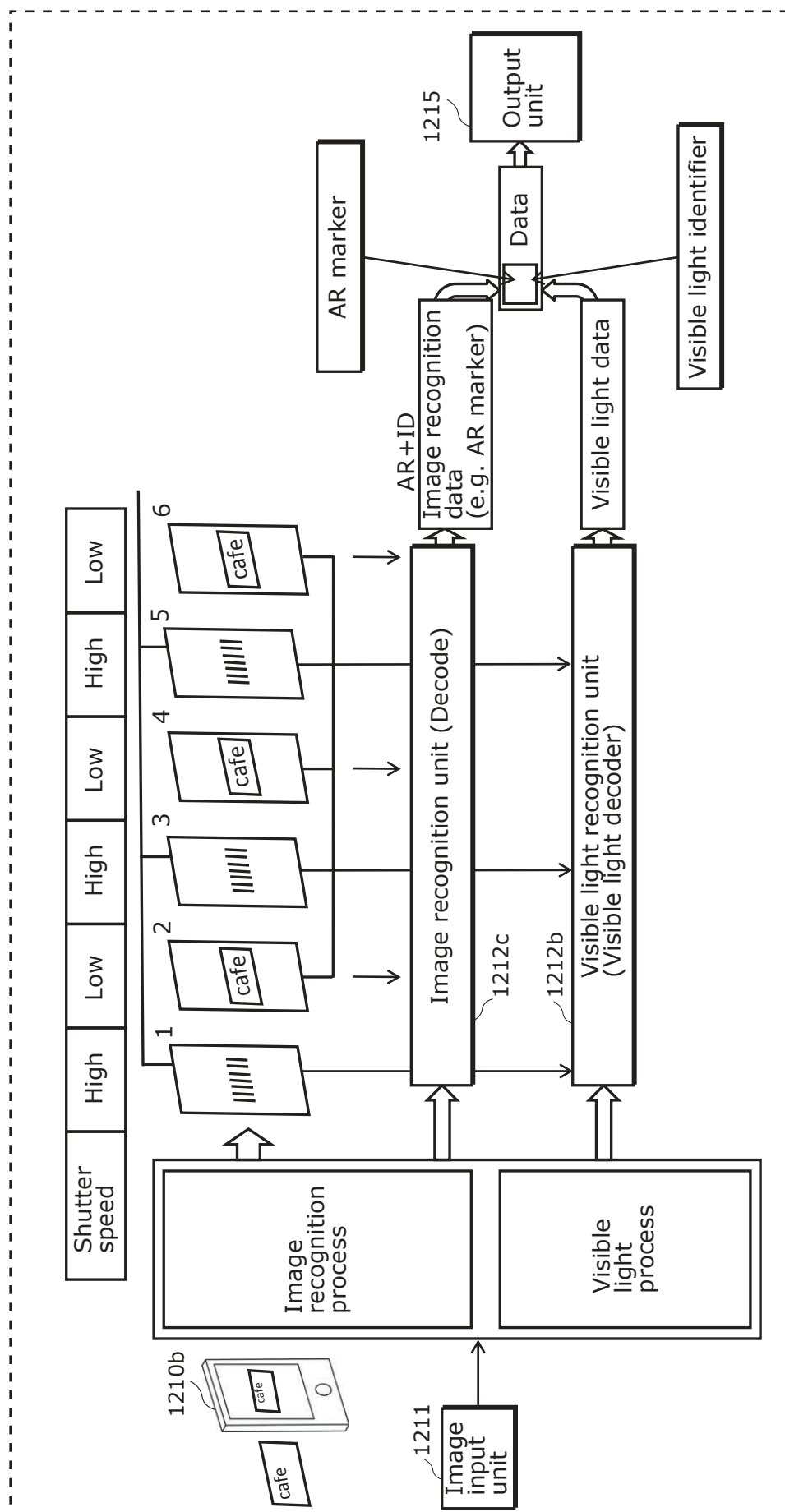


FIG. 303B

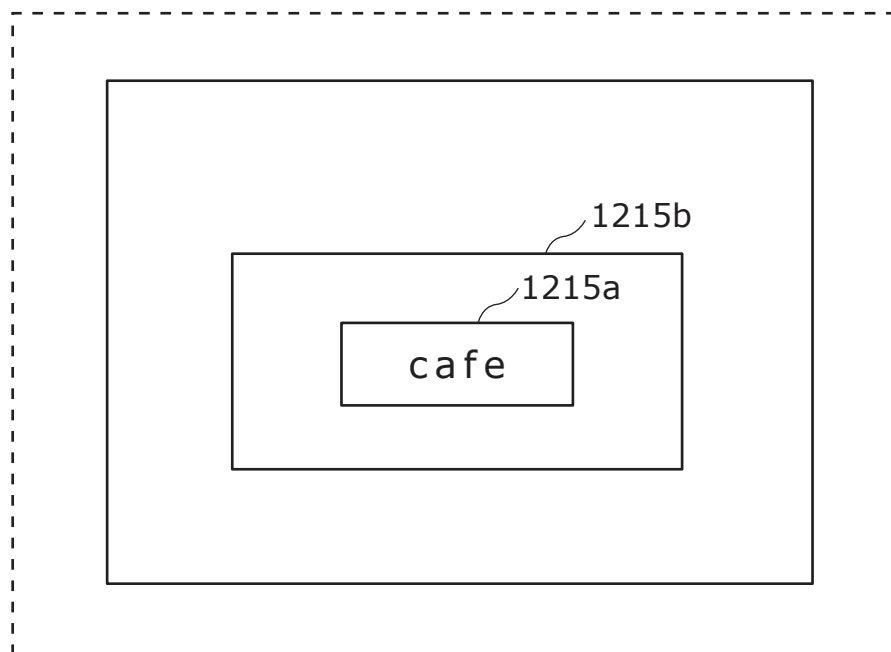


FIG. 303C

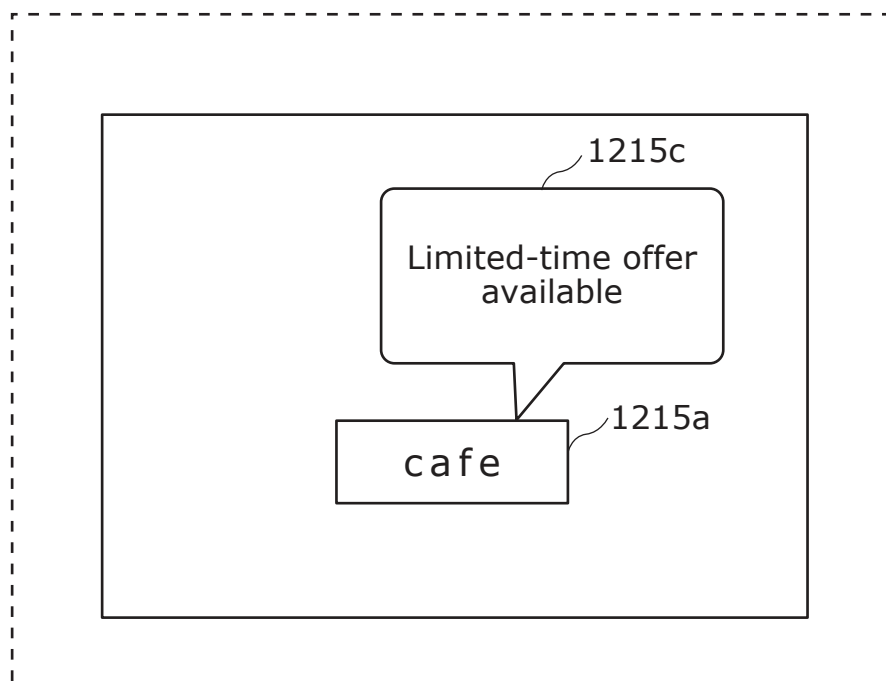


FIG. 304A

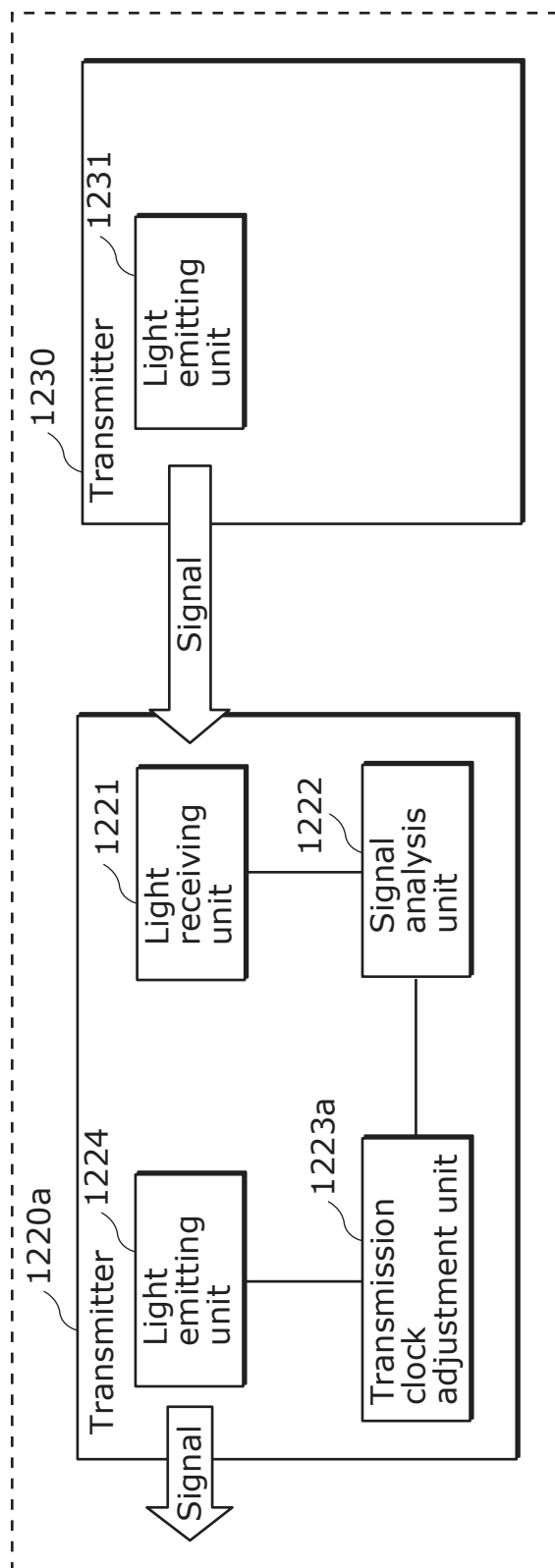


FIG. 304B

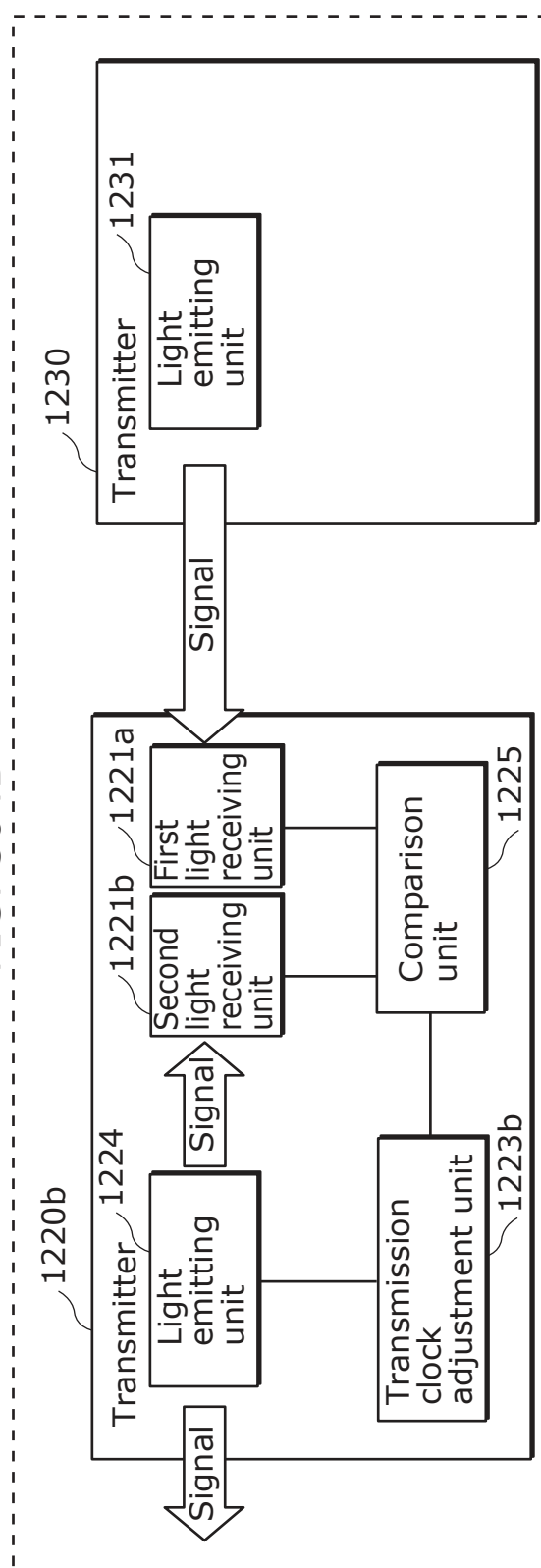


FIG. 305A

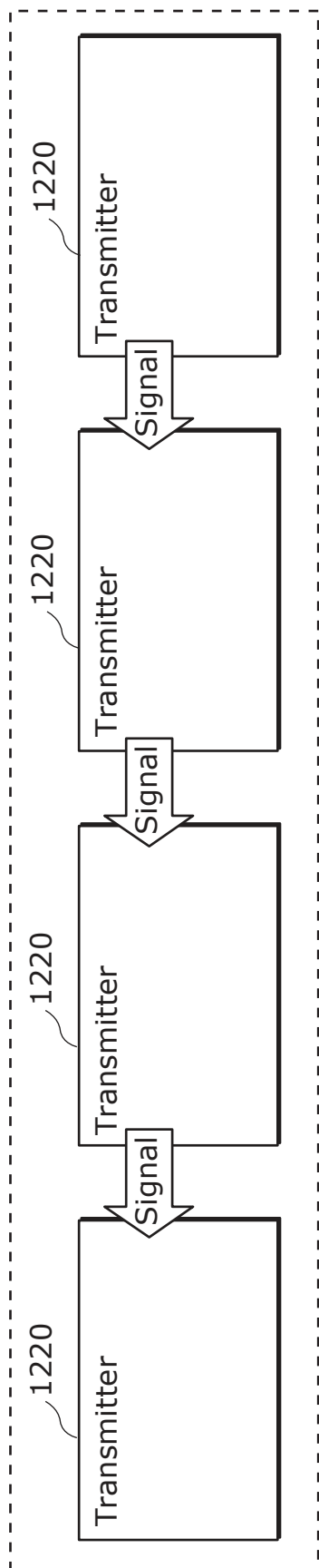


FIG. 305B

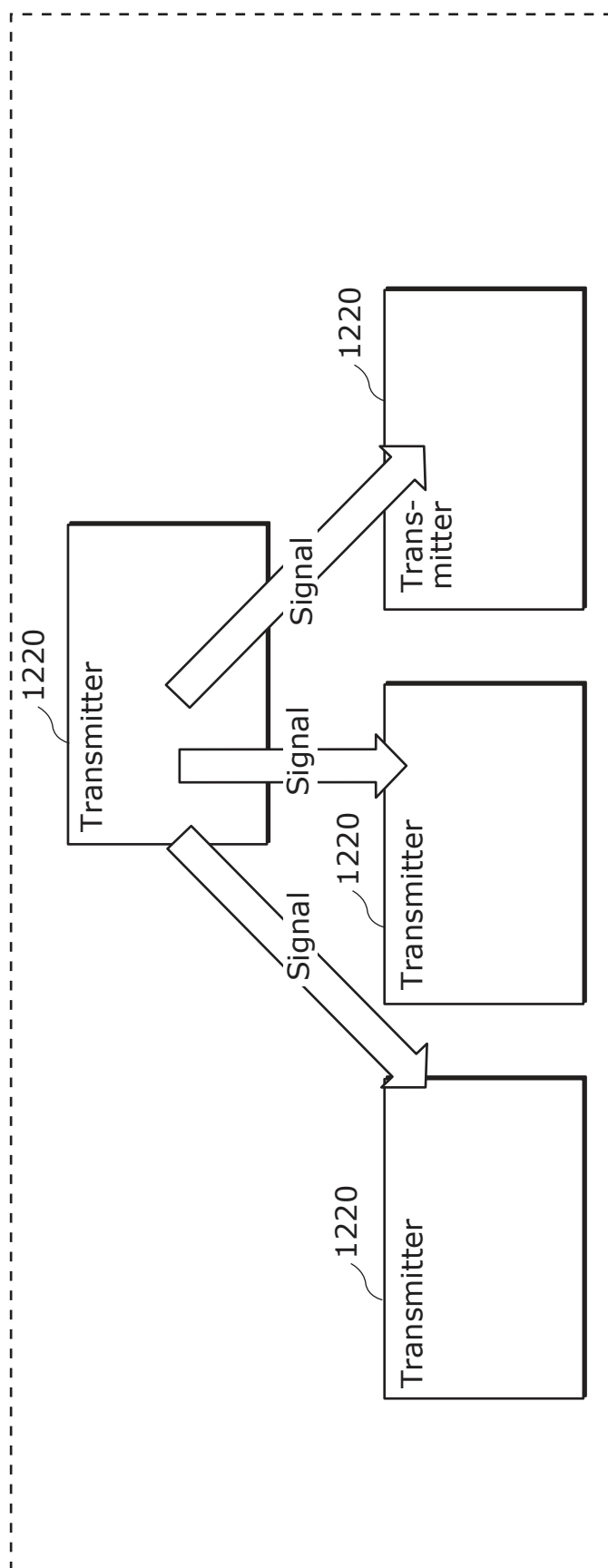


FIG. 306

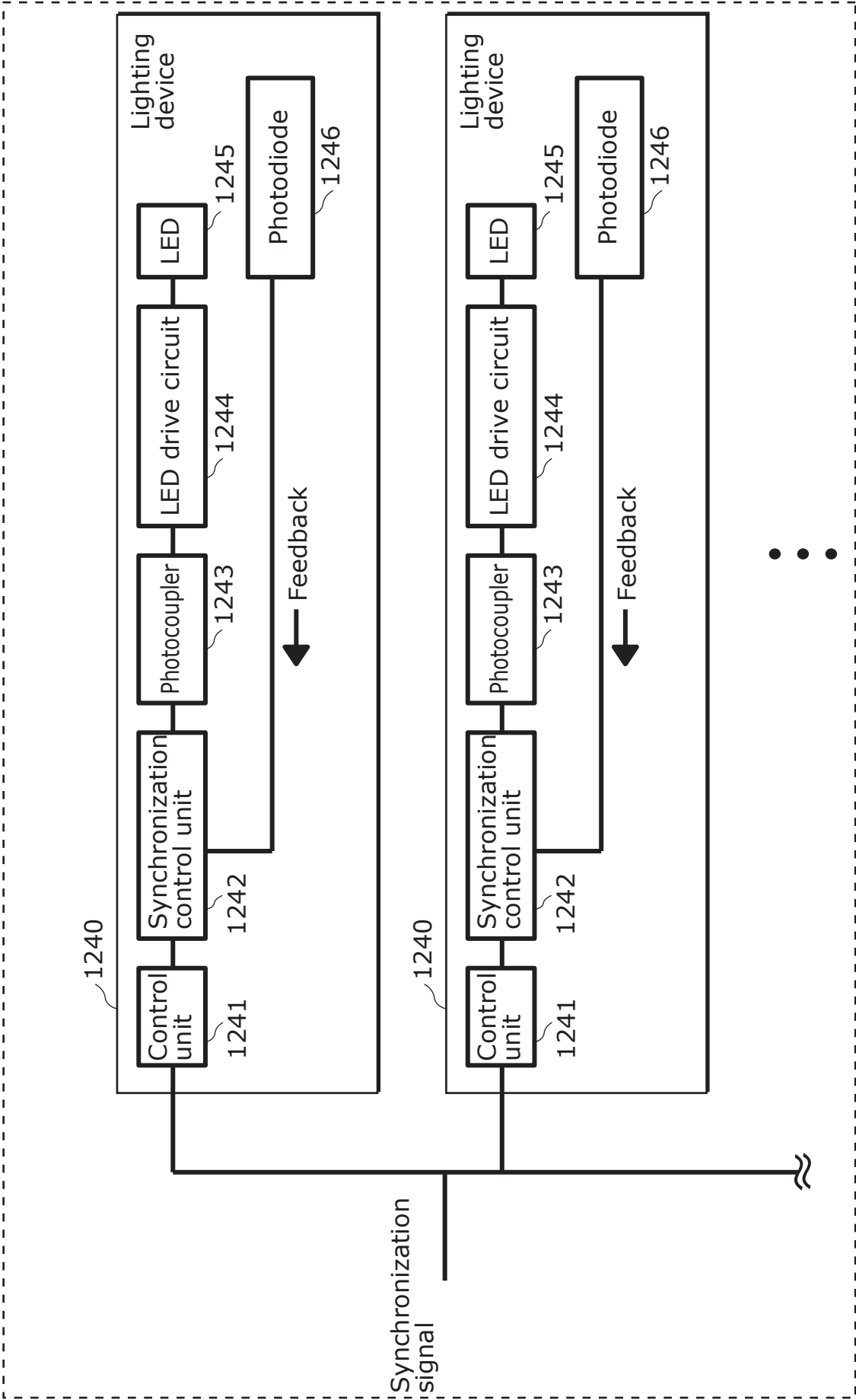


FIG. 307

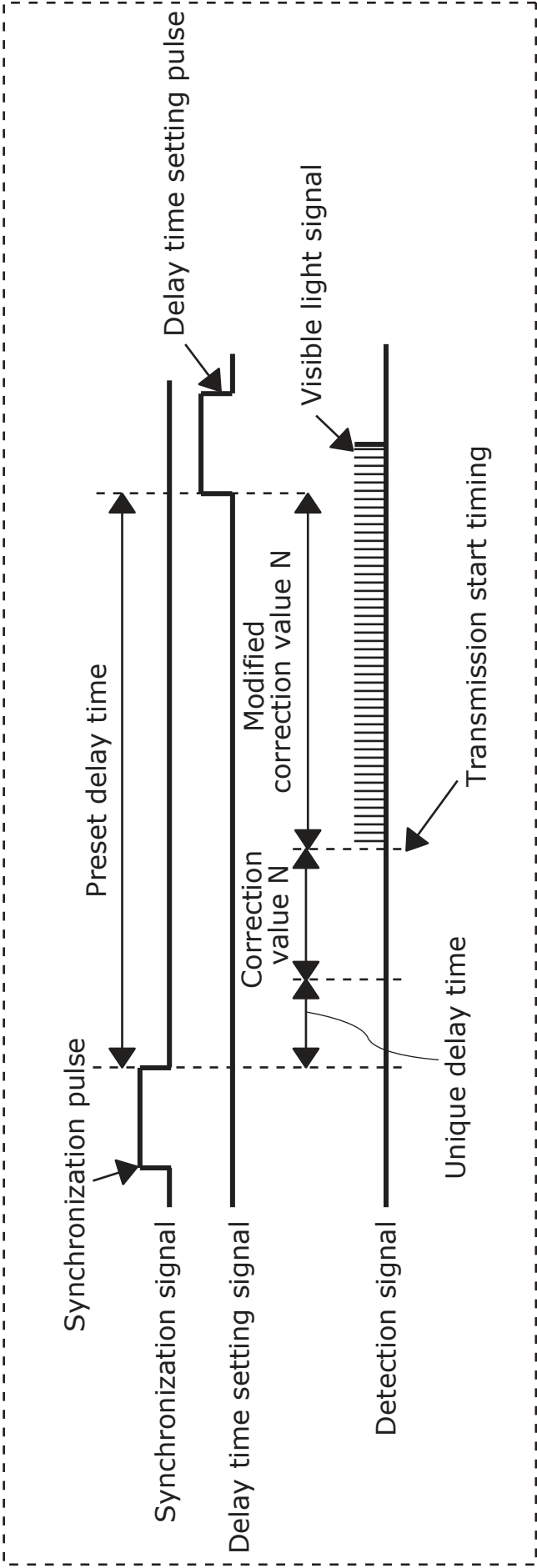


FIG. 308

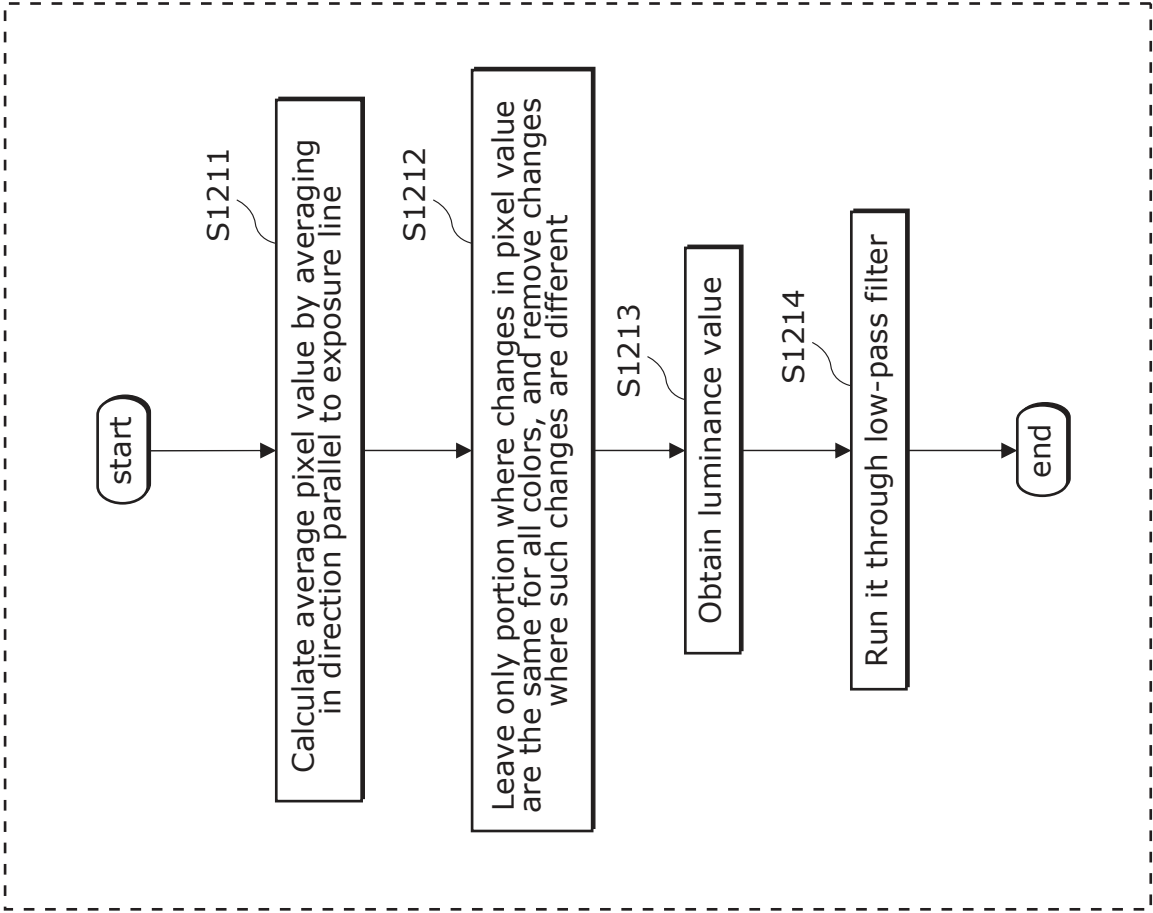


FIG. 309

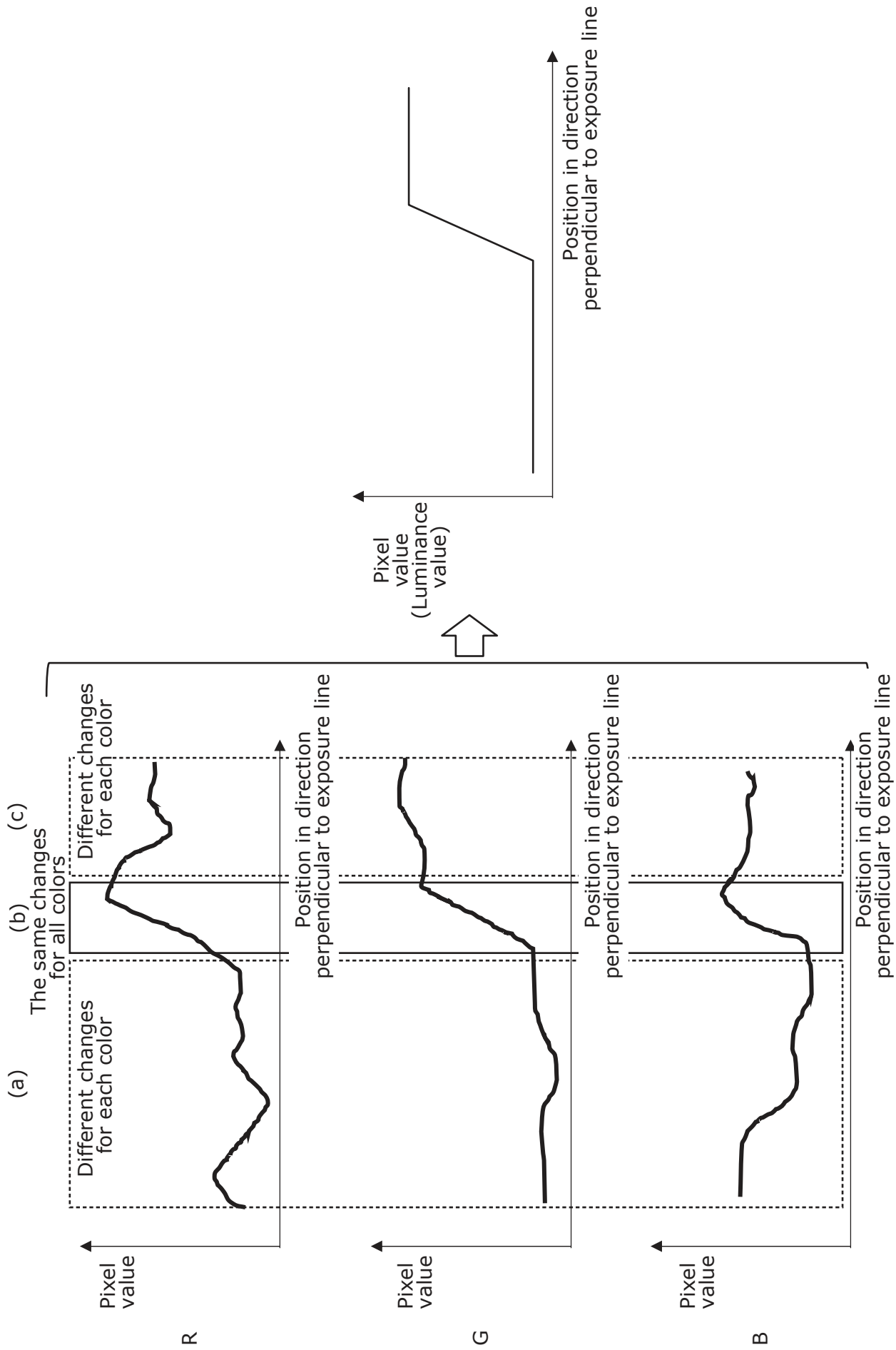


FIG. 310

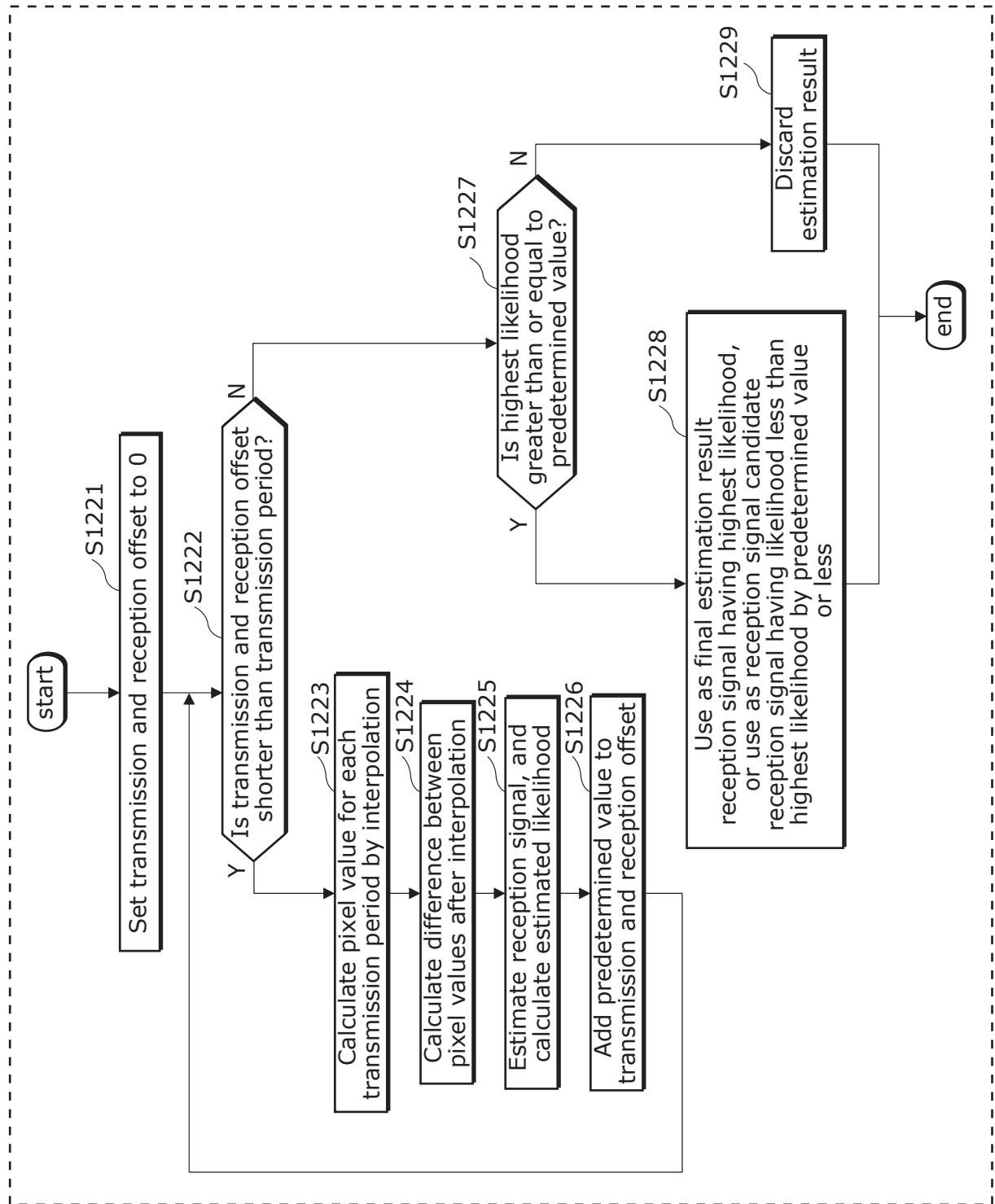


FIG. 311

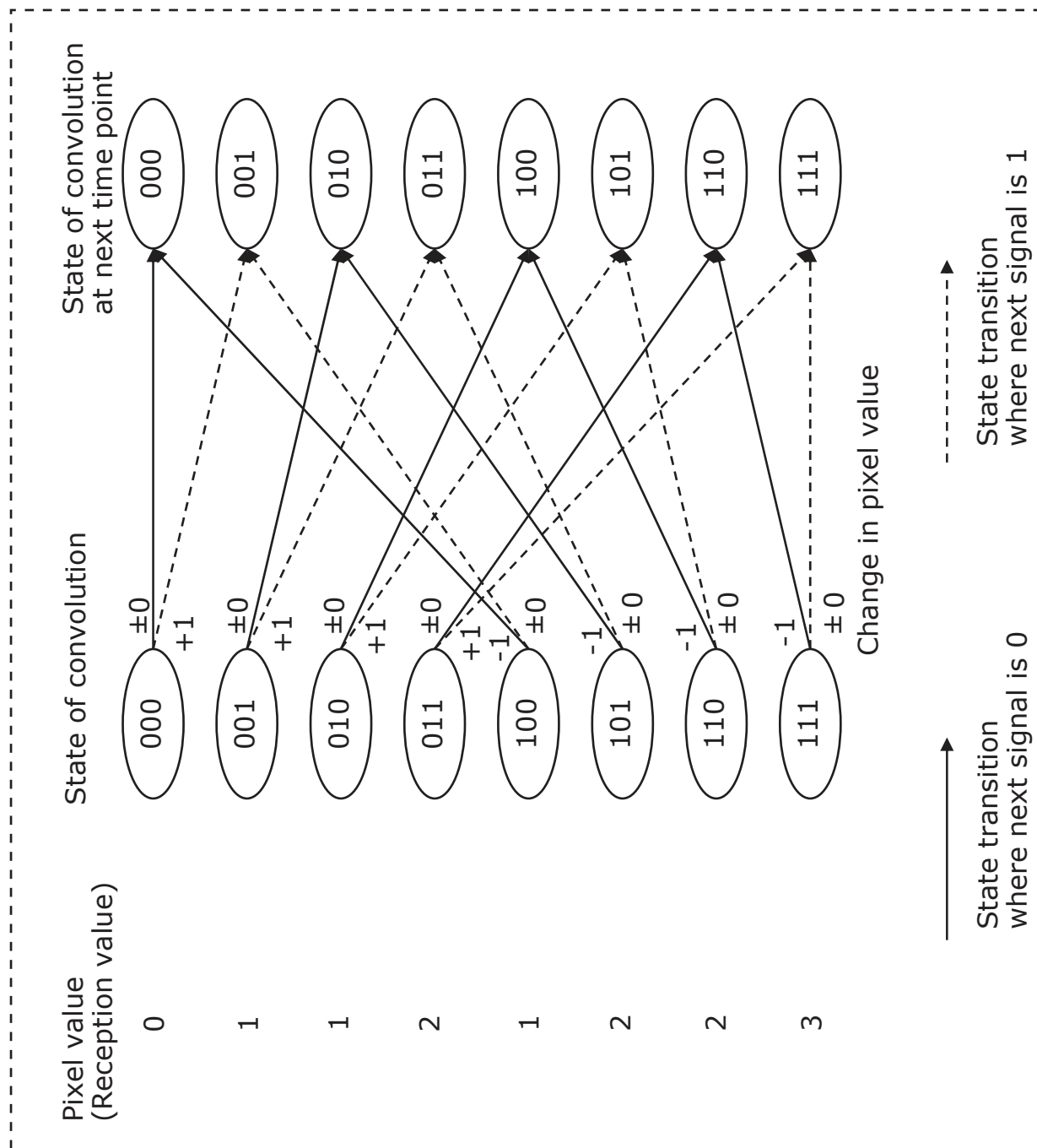


FIG. 312

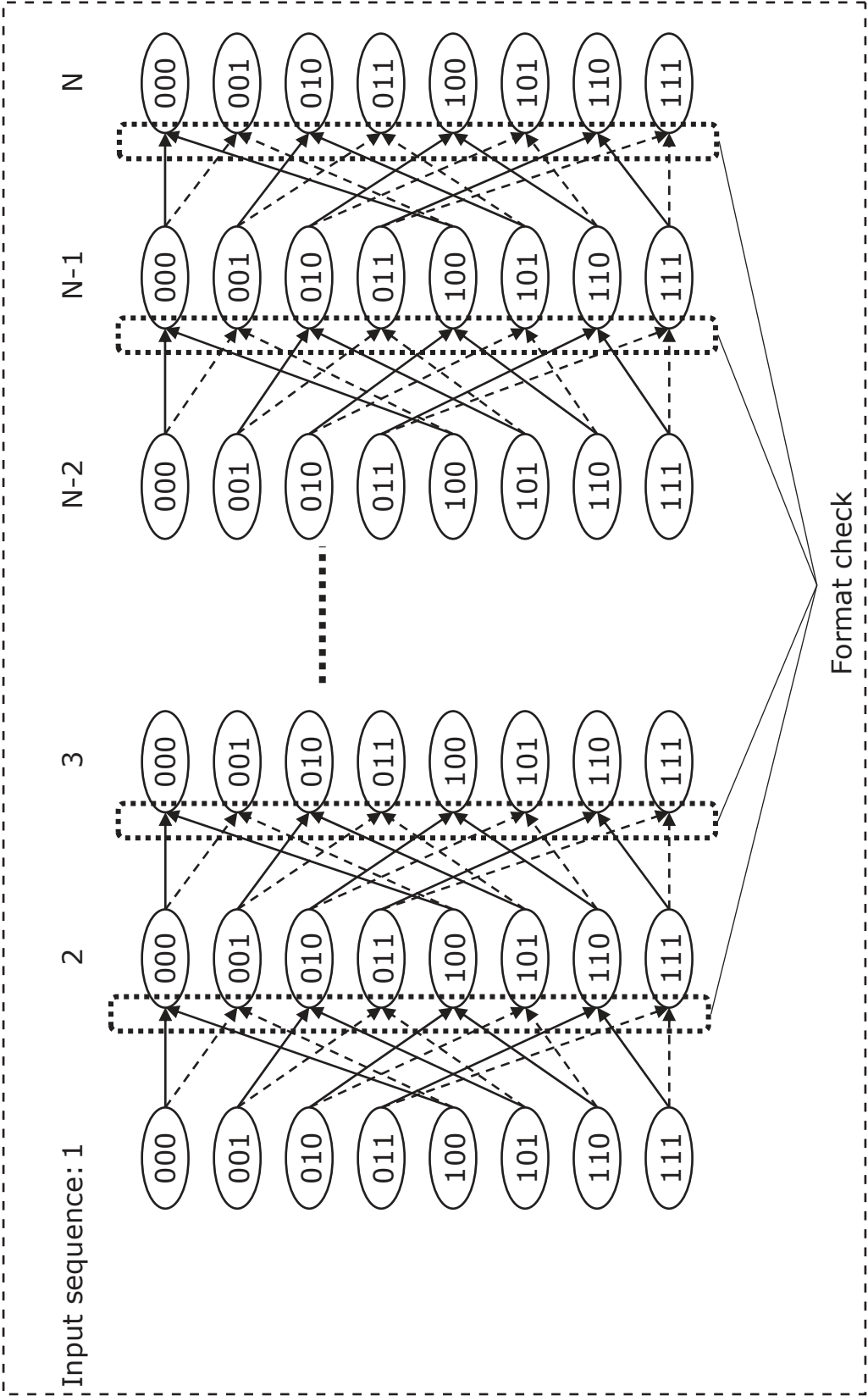


FIG. 313

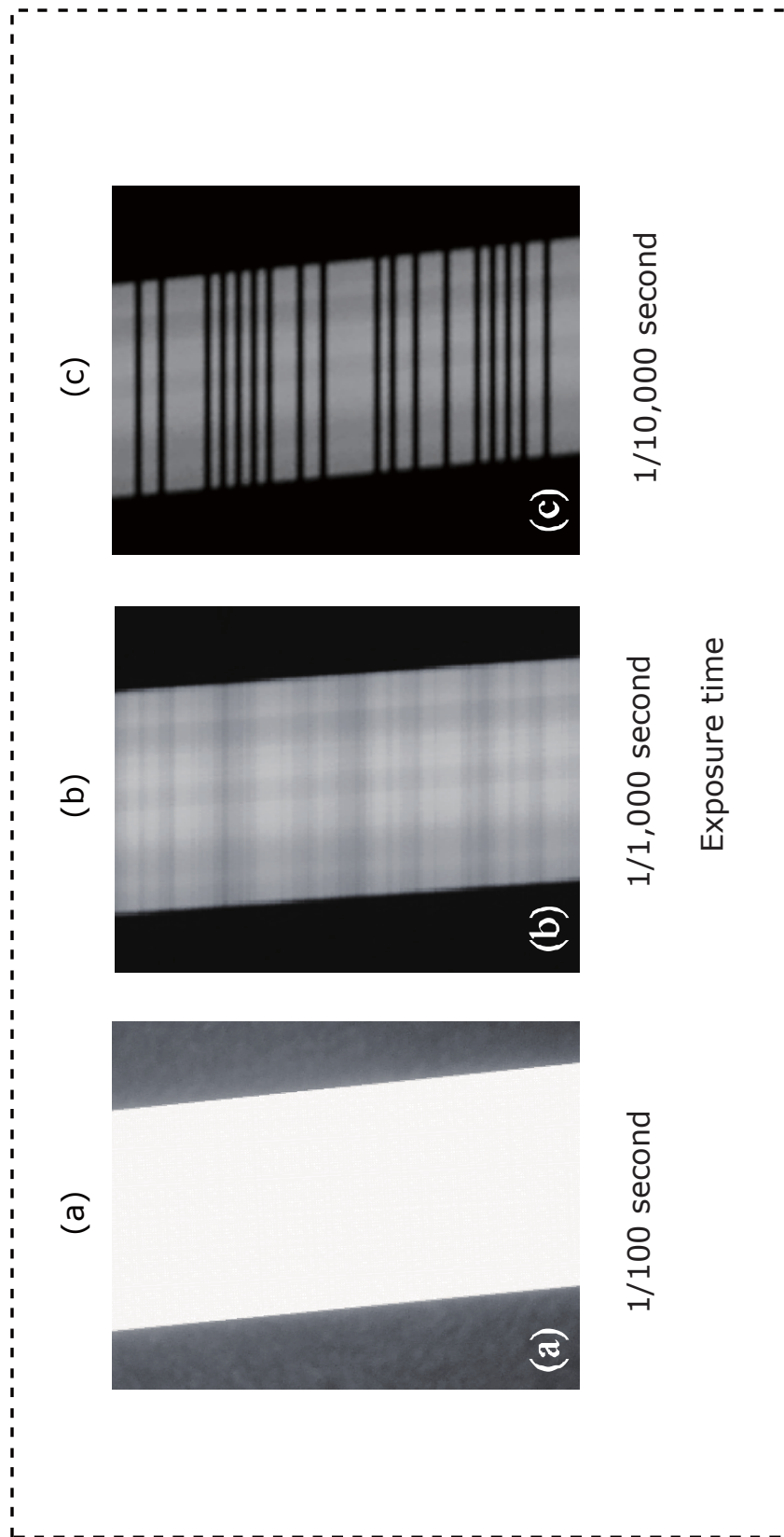


FIG. 314

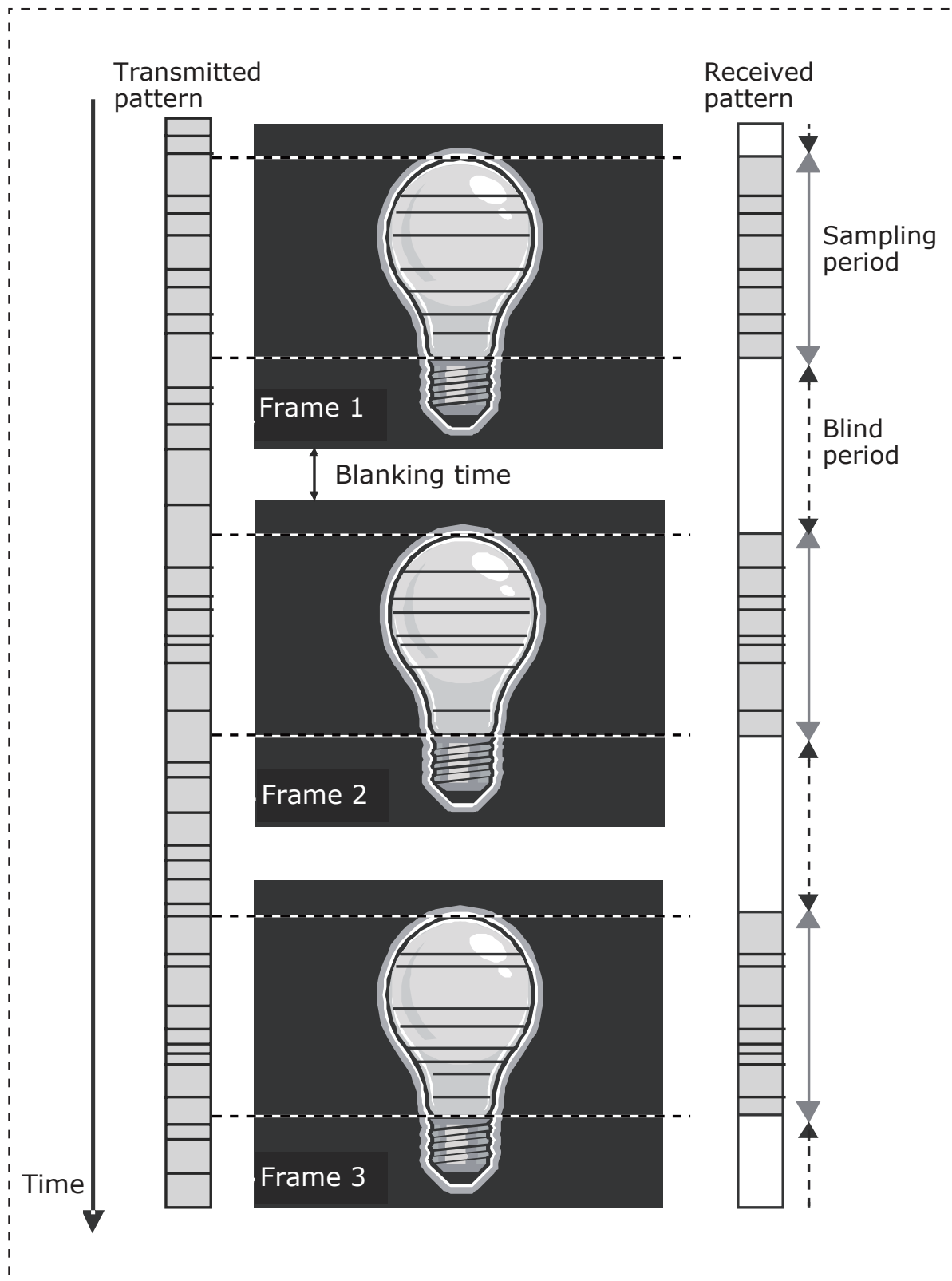


FIG. 315

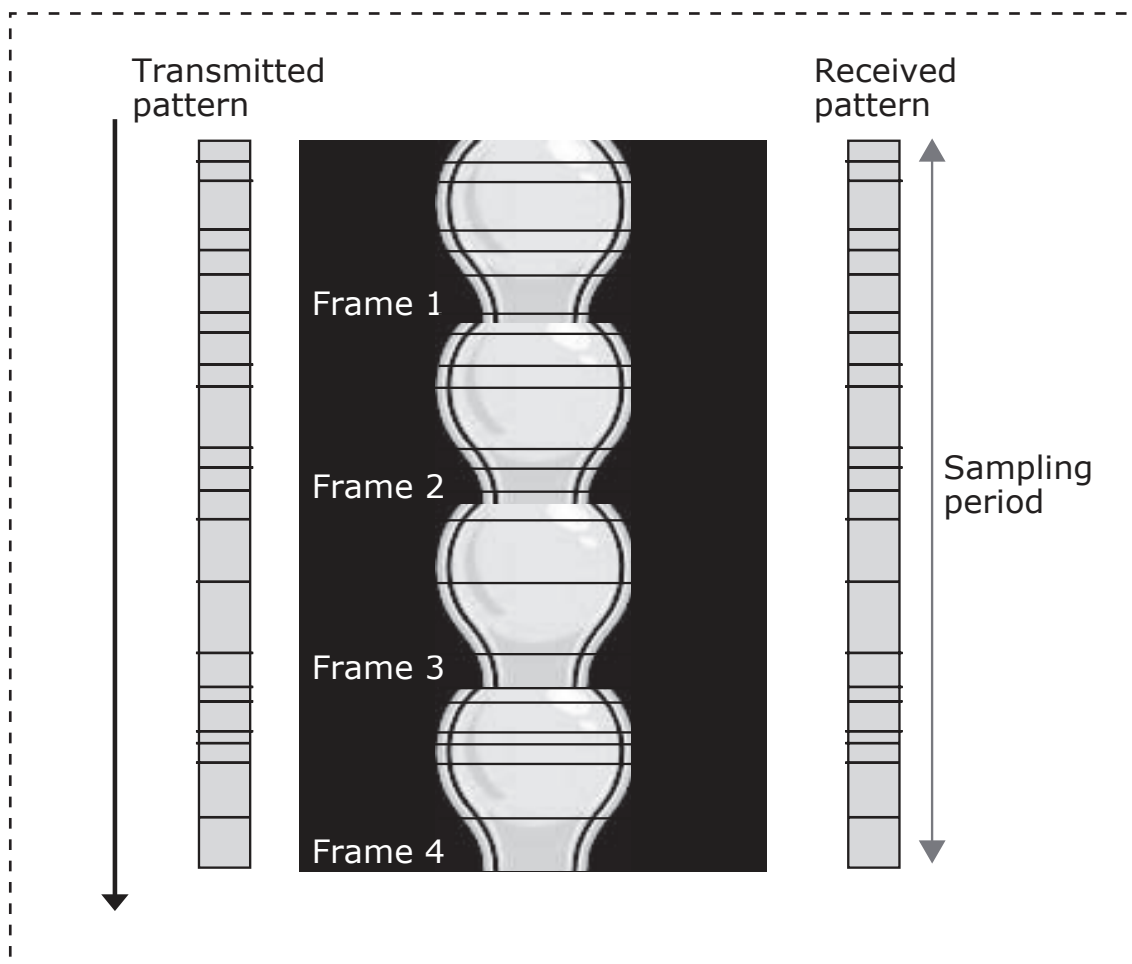


FIG. 316

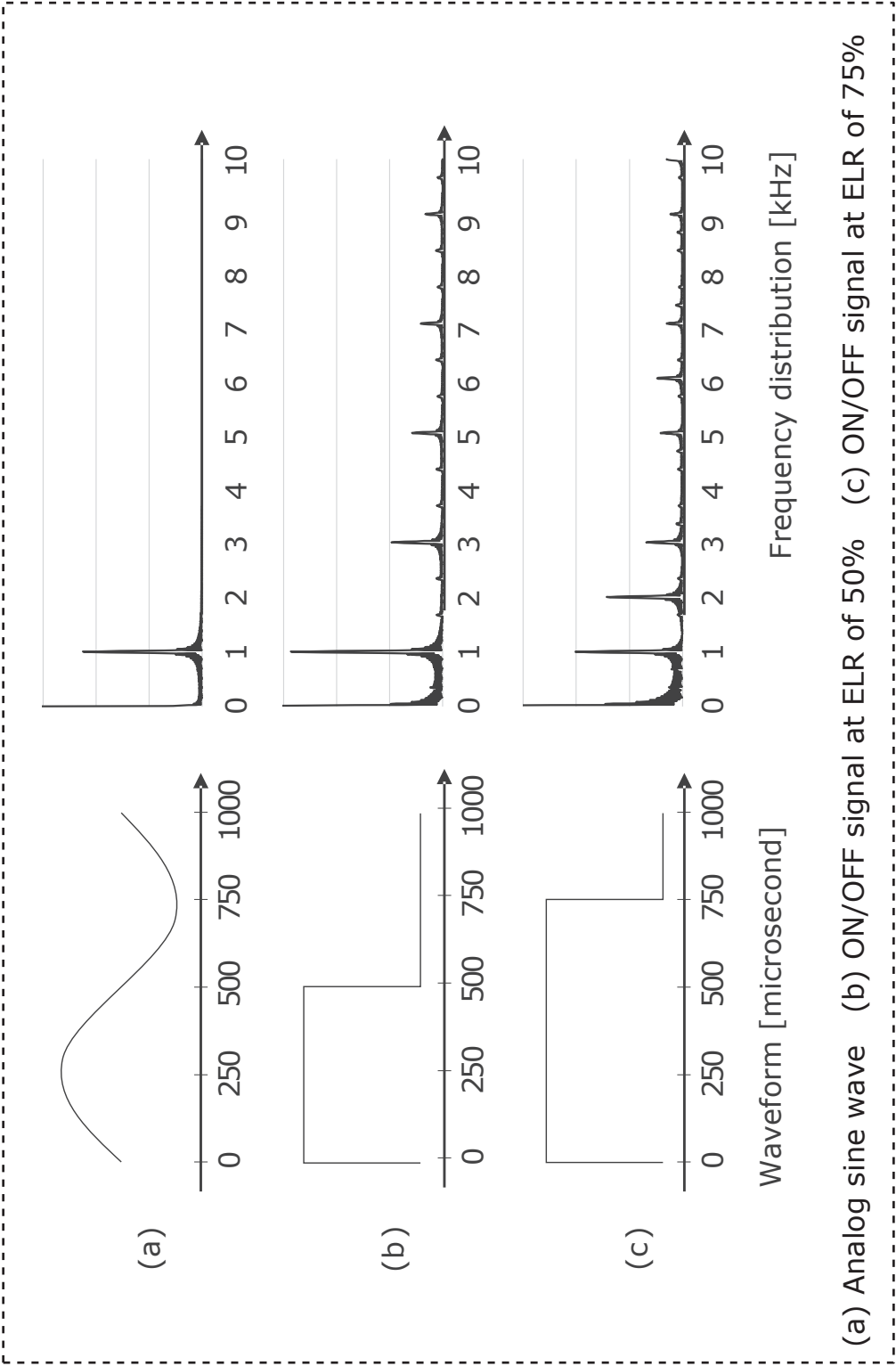


FIG. 317

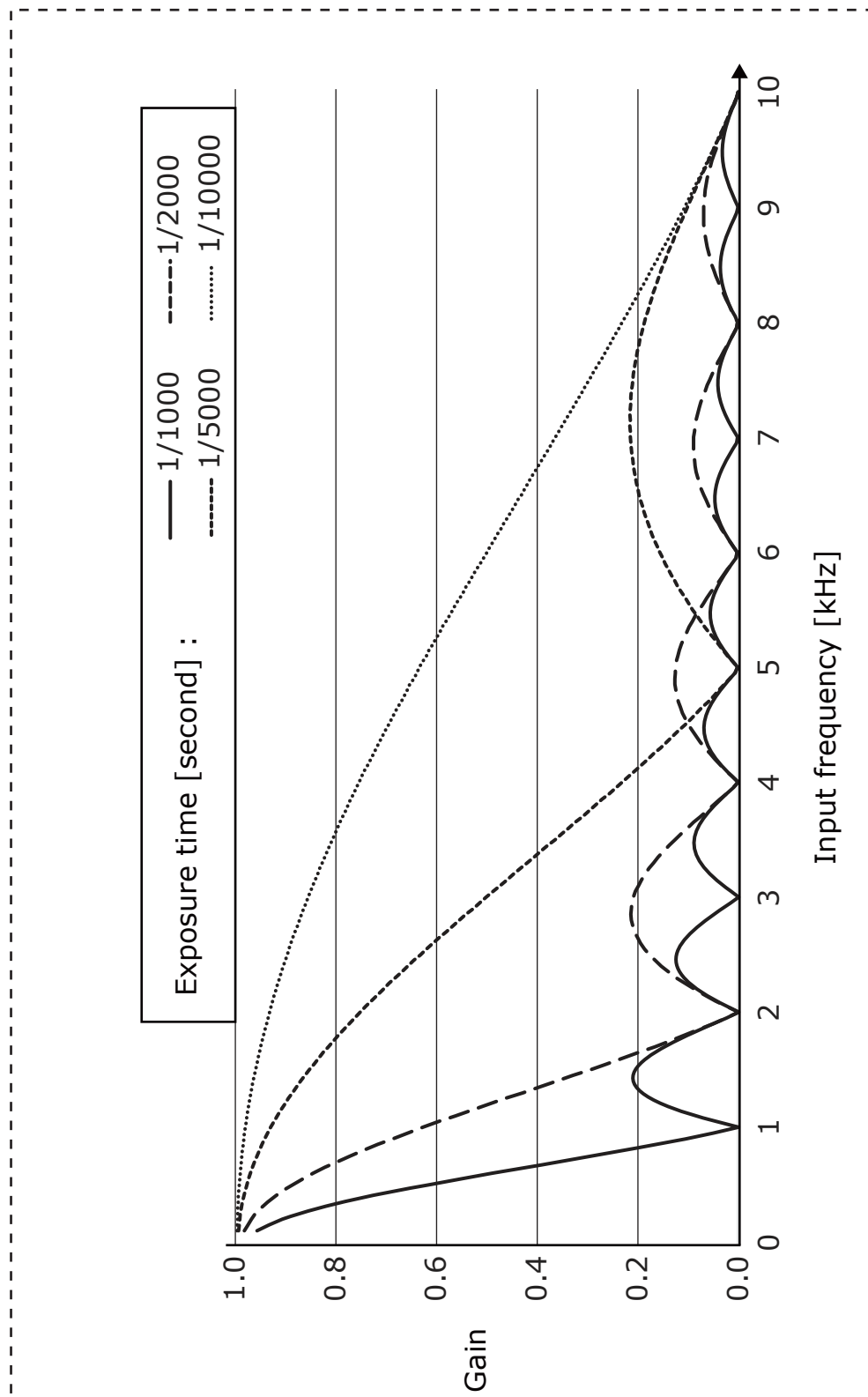


FIG. 318

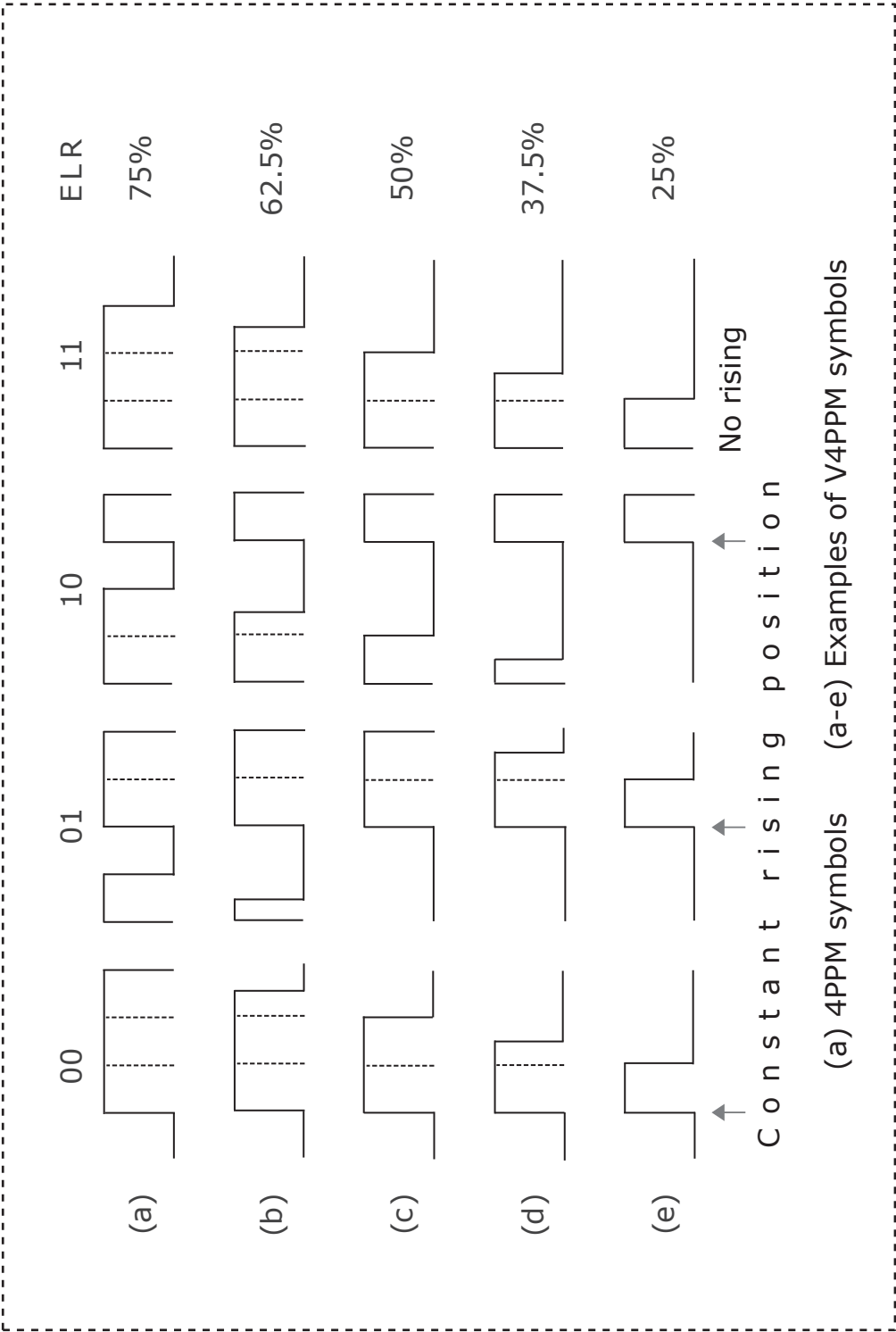


FIG. 319

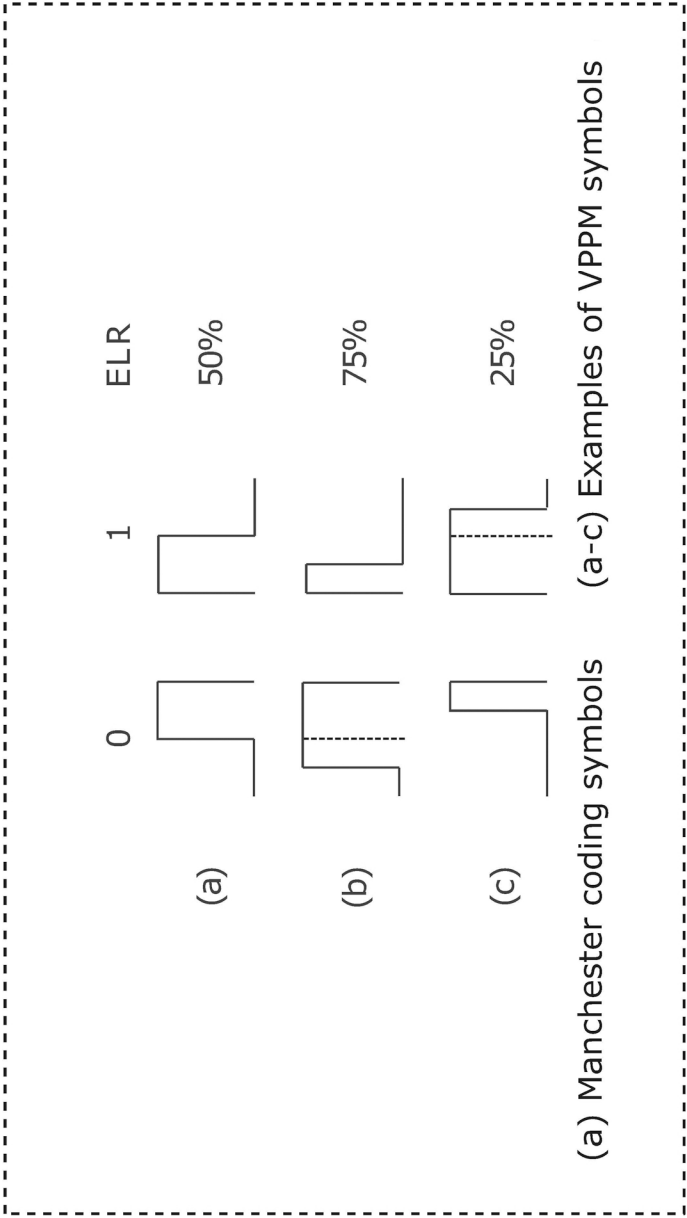


FIG. 320

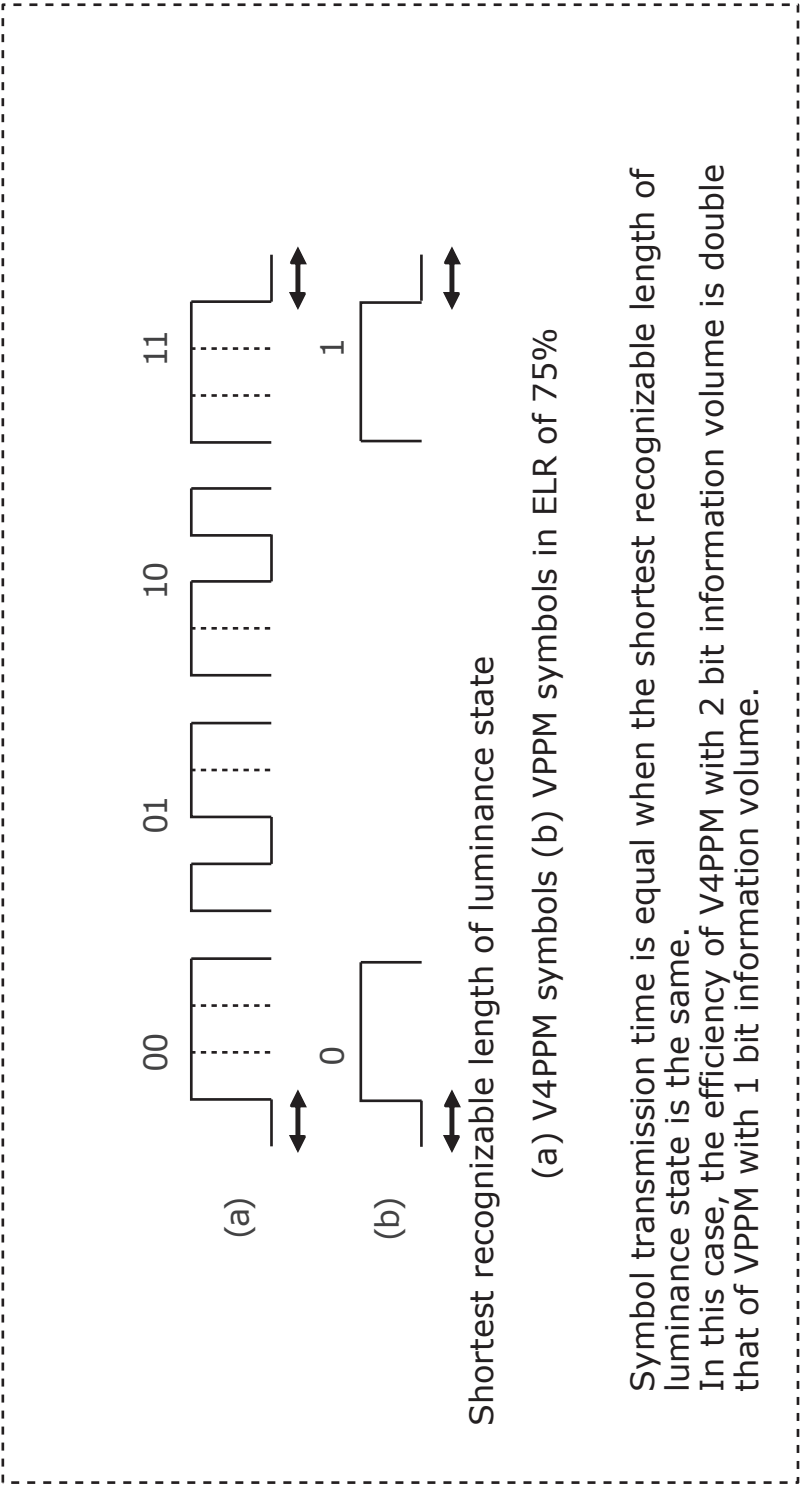


FIG. 321

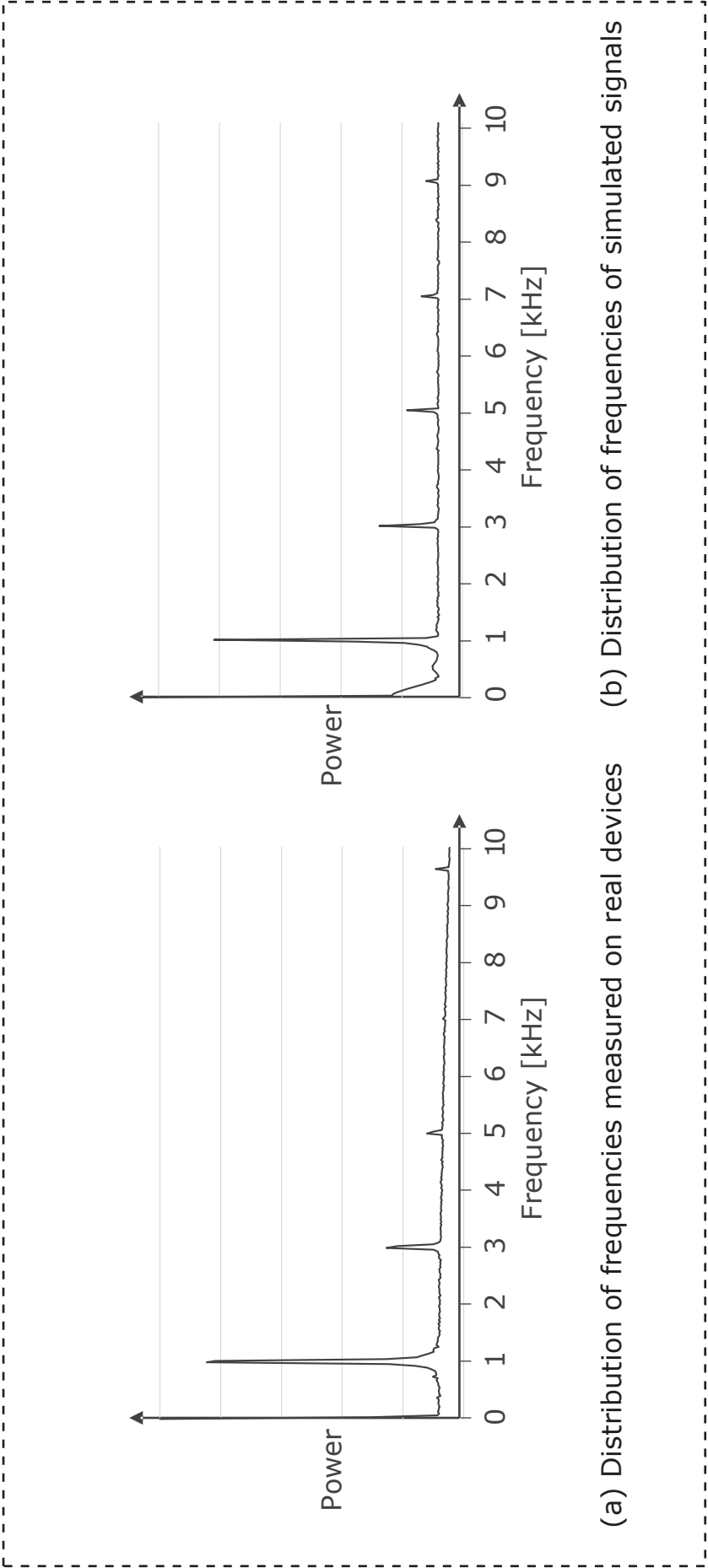


FIG. 322A

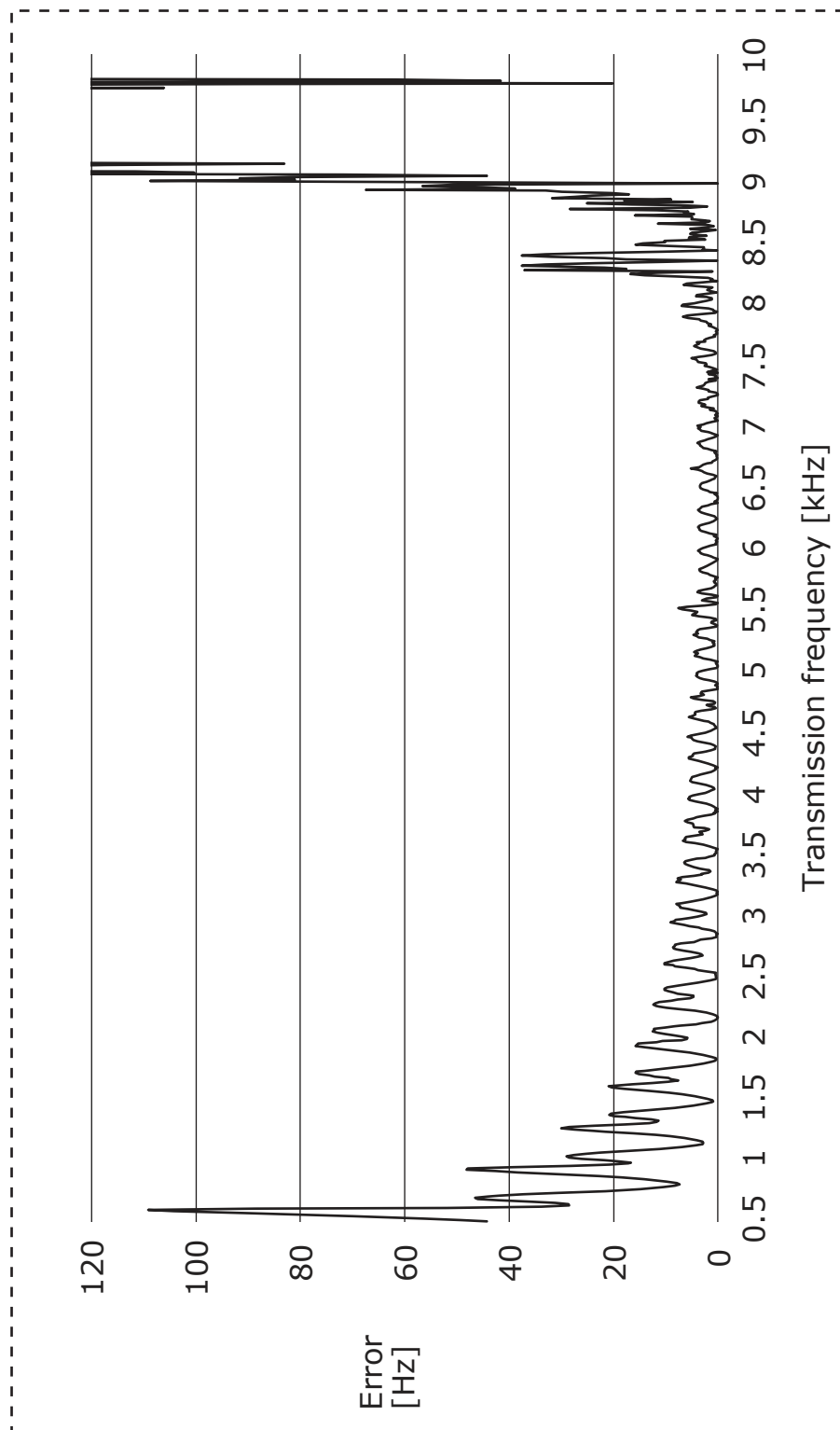


FIG. 322B

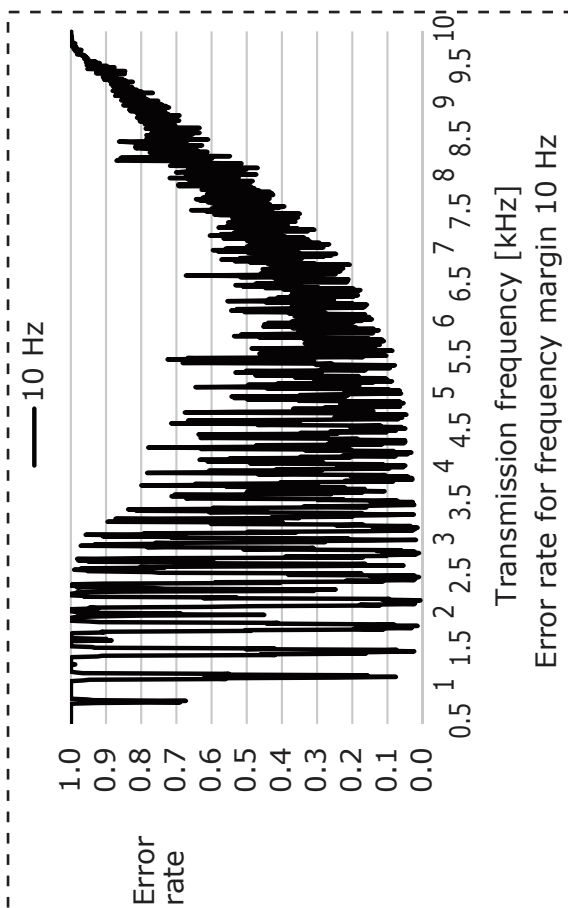


FIG. 322D

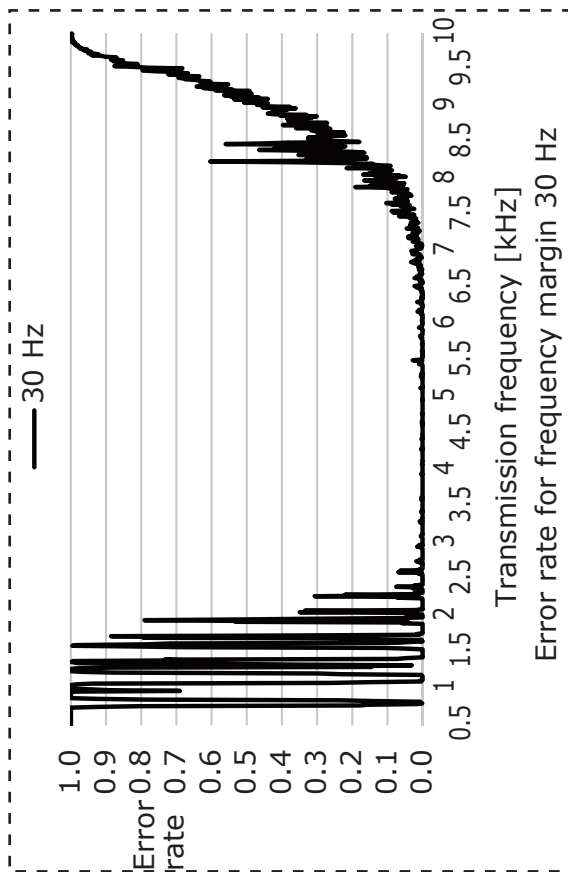


FIG. 322C

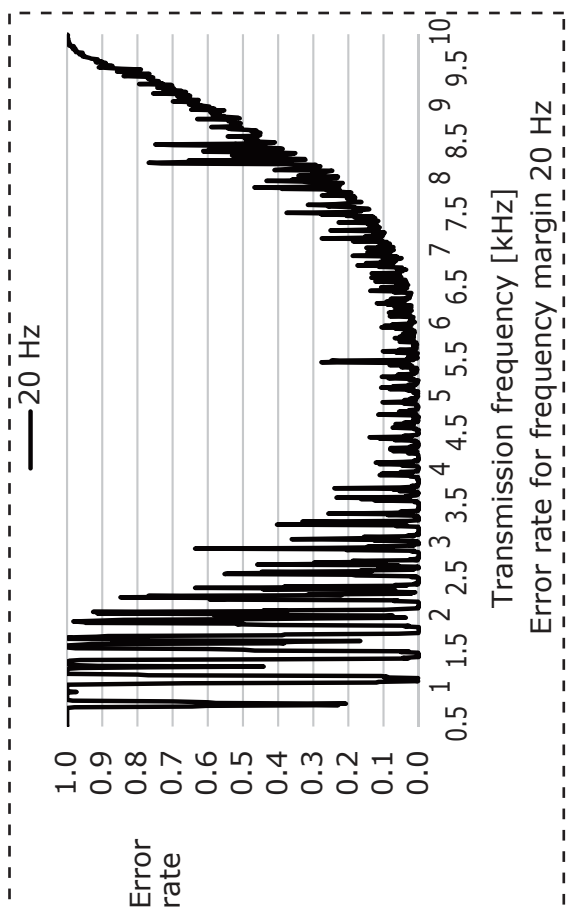


FIG. 322E

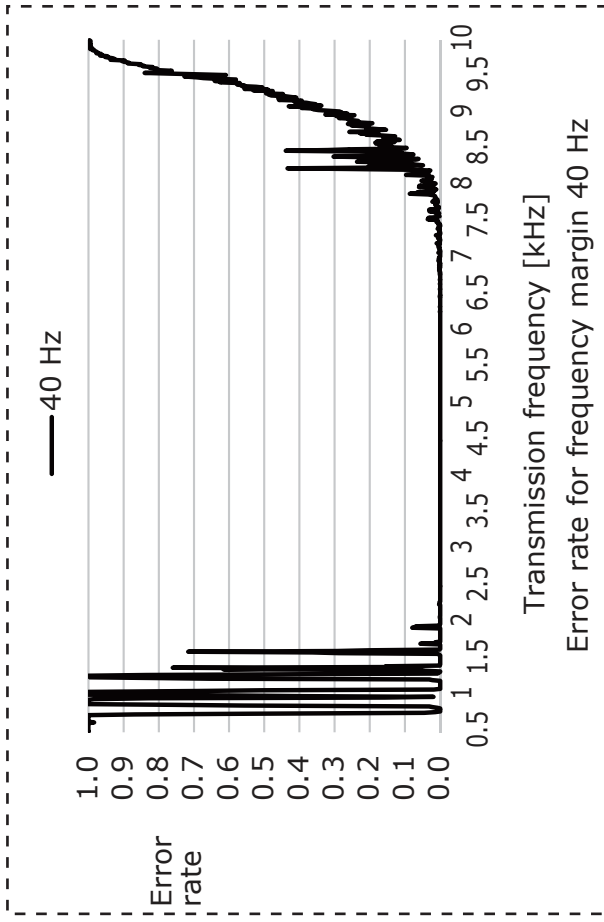


FIG. 322F

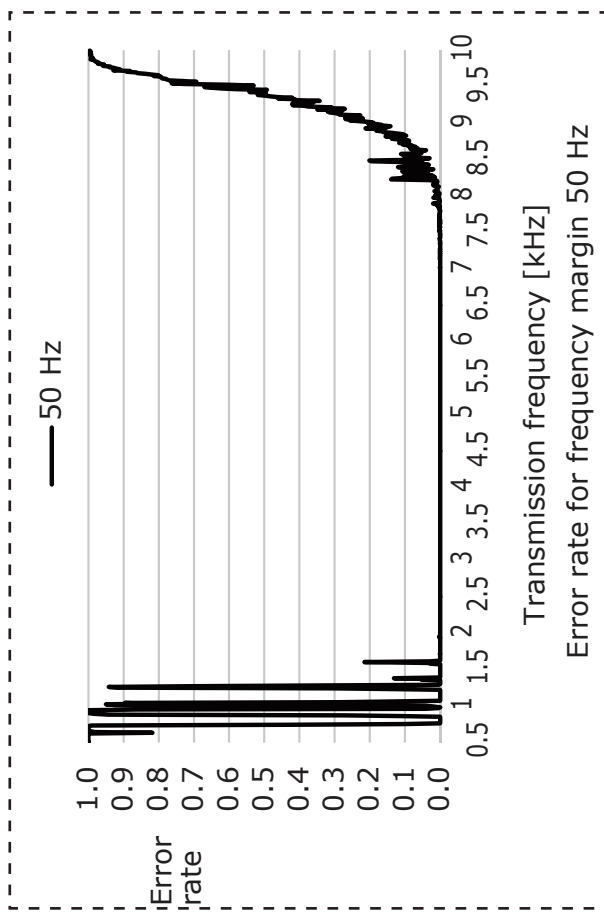


FIG. 323

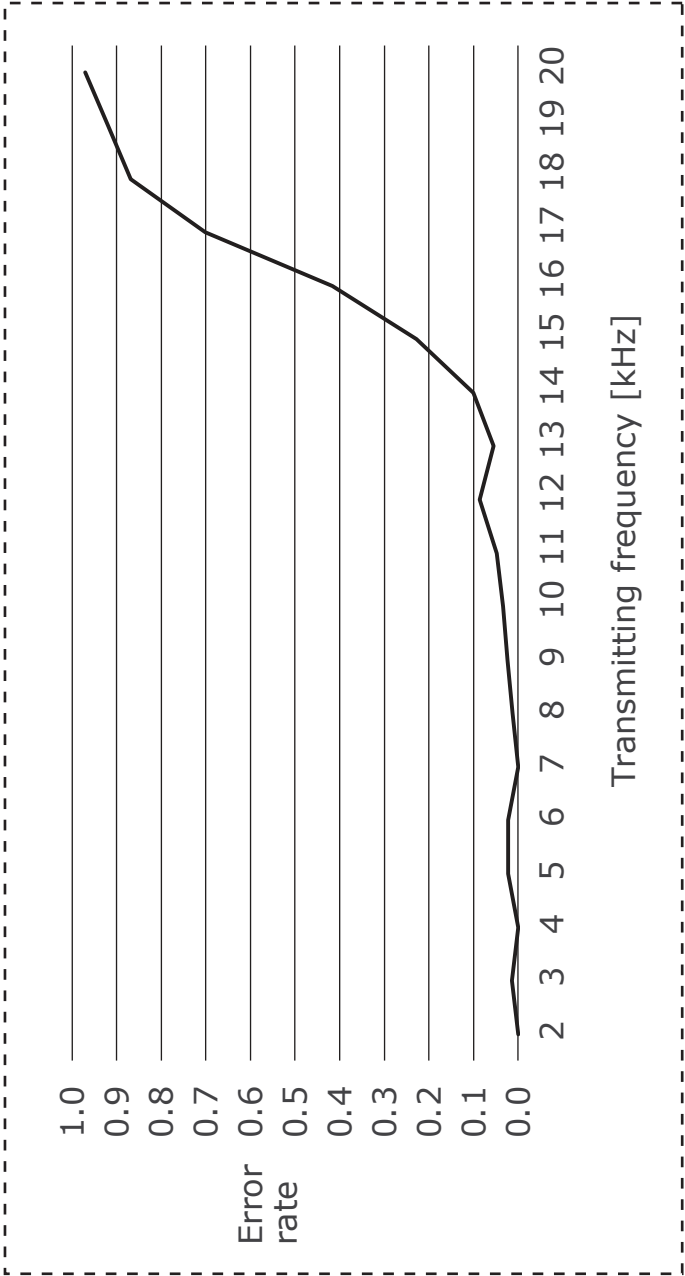


FIG. 324

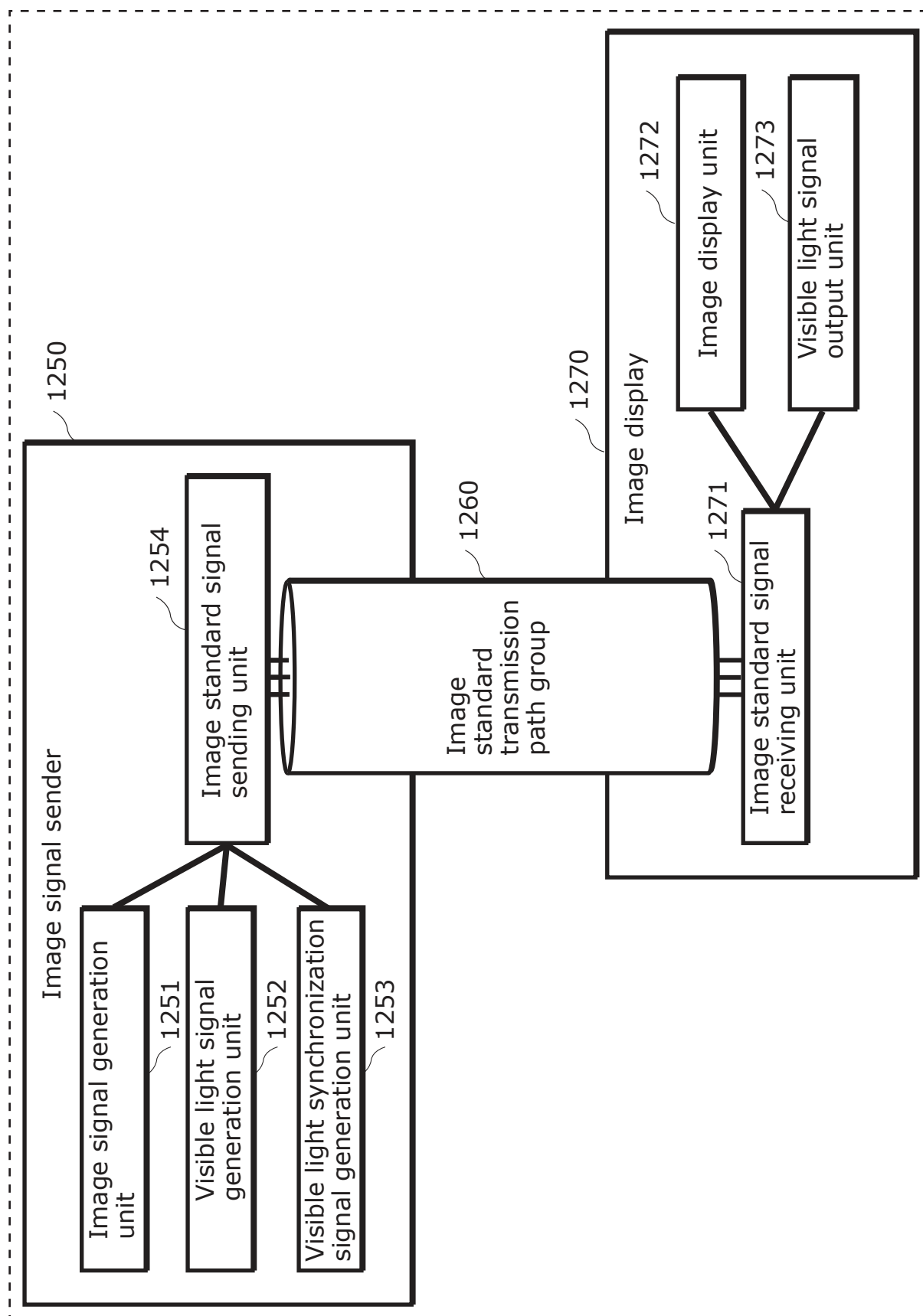


FIG. 325

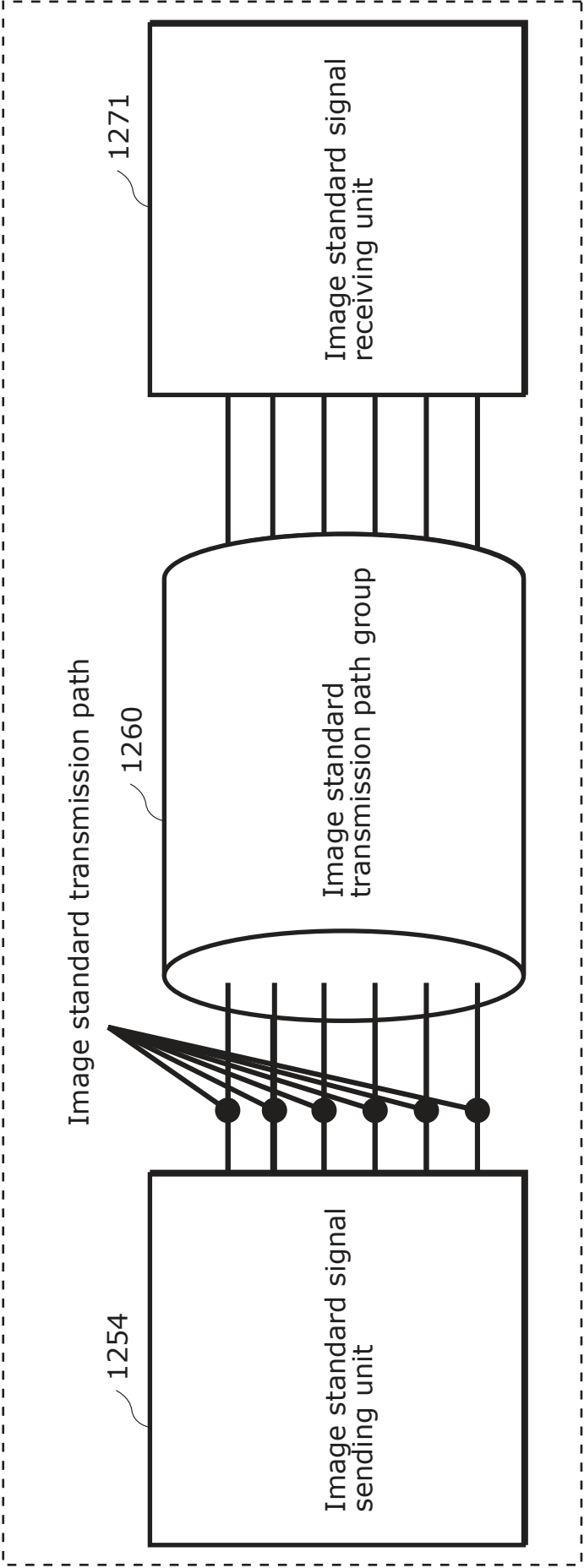


FIG. 326

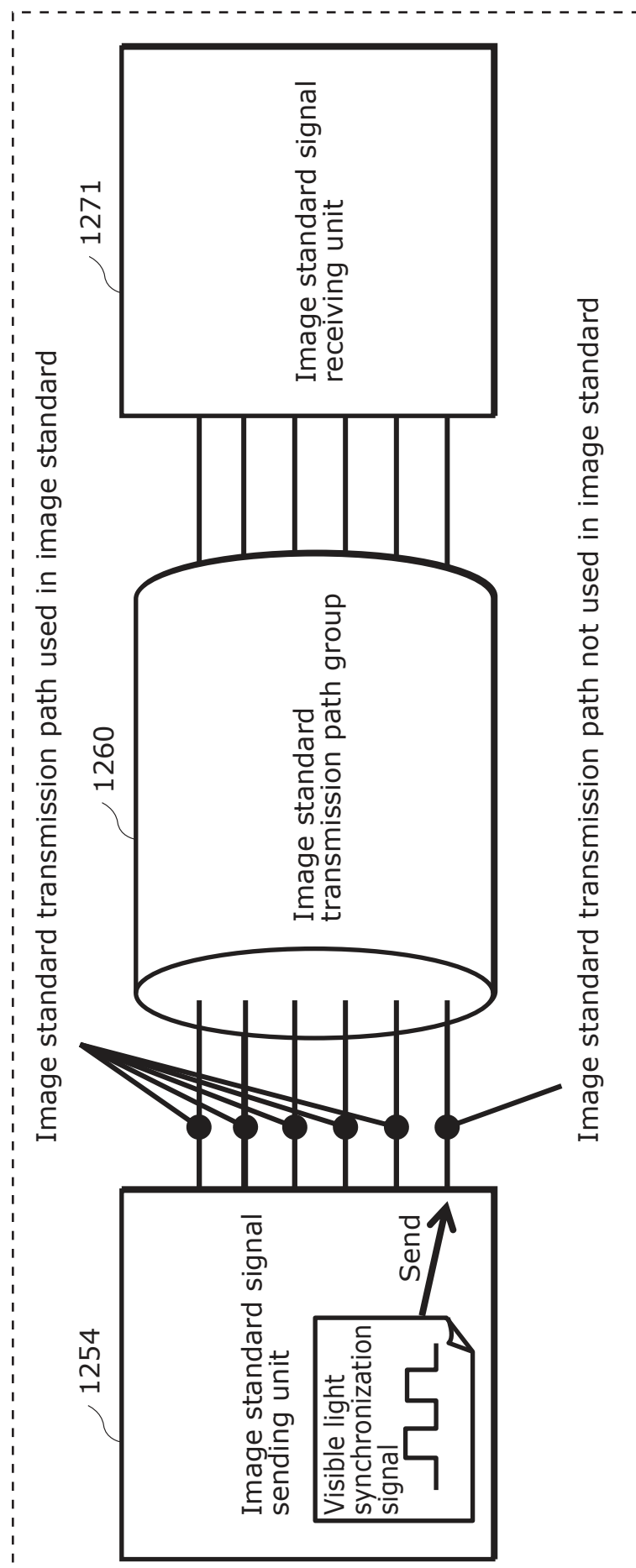


FIG. 327

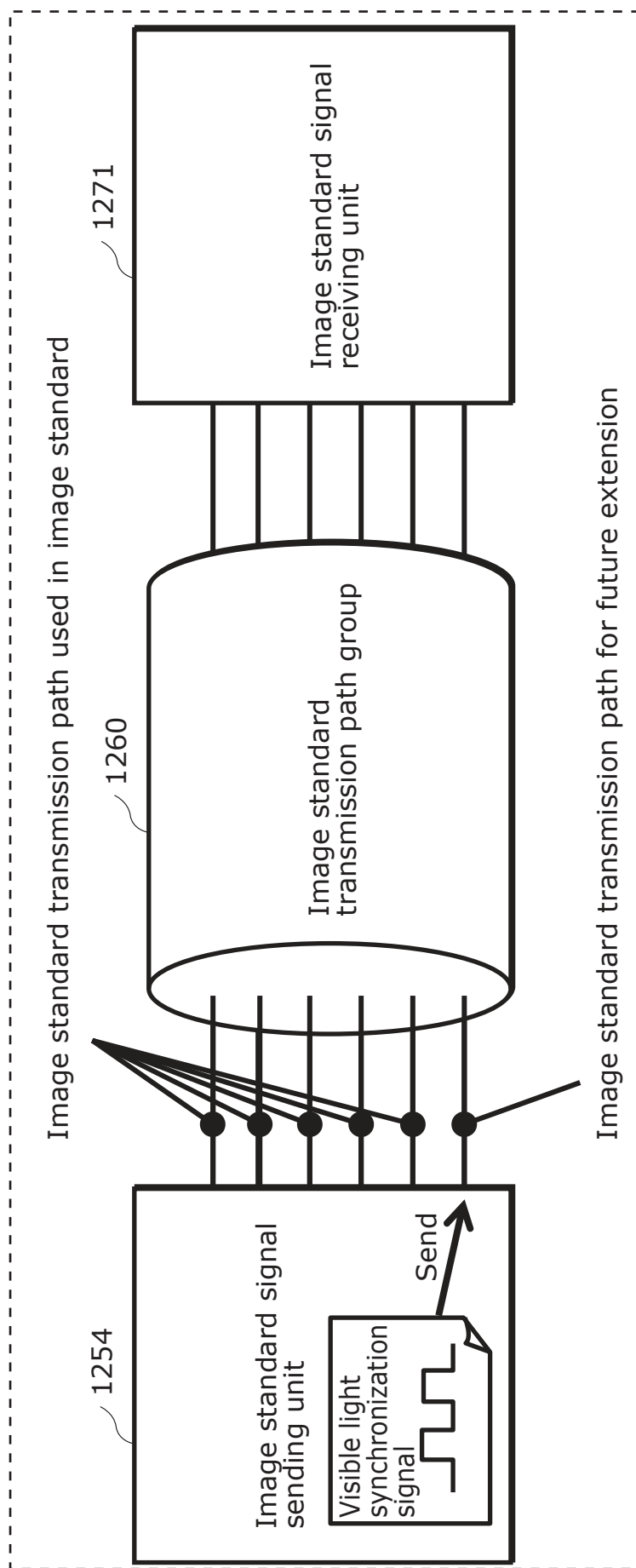


FIG. 328

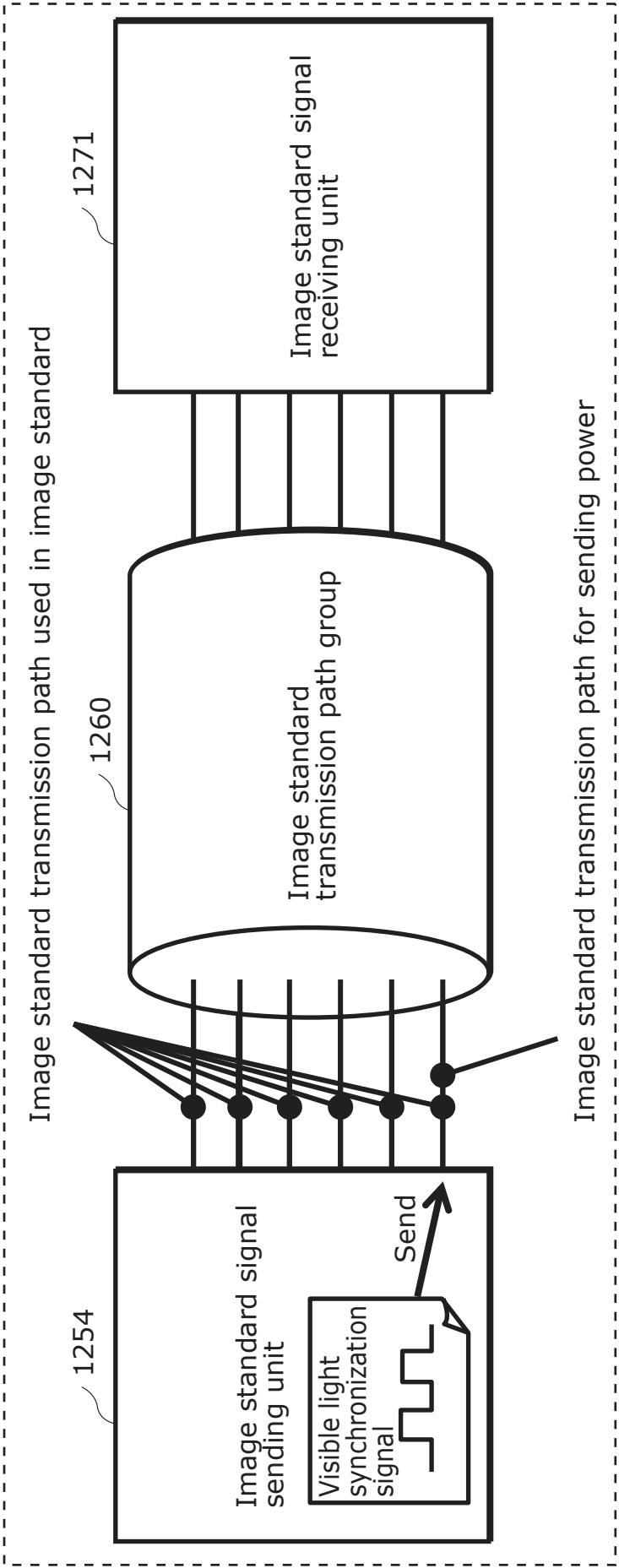


FIG. 329A

FIG. 329B

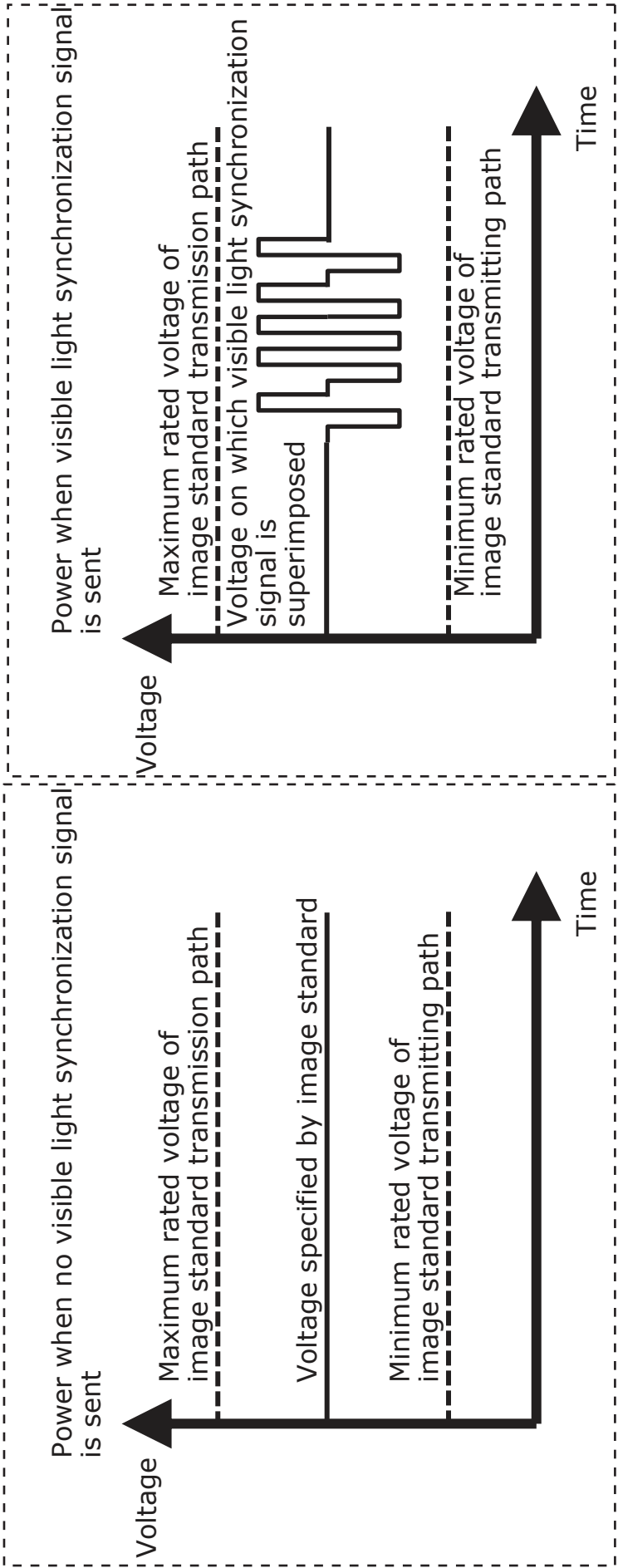


FIG. 330

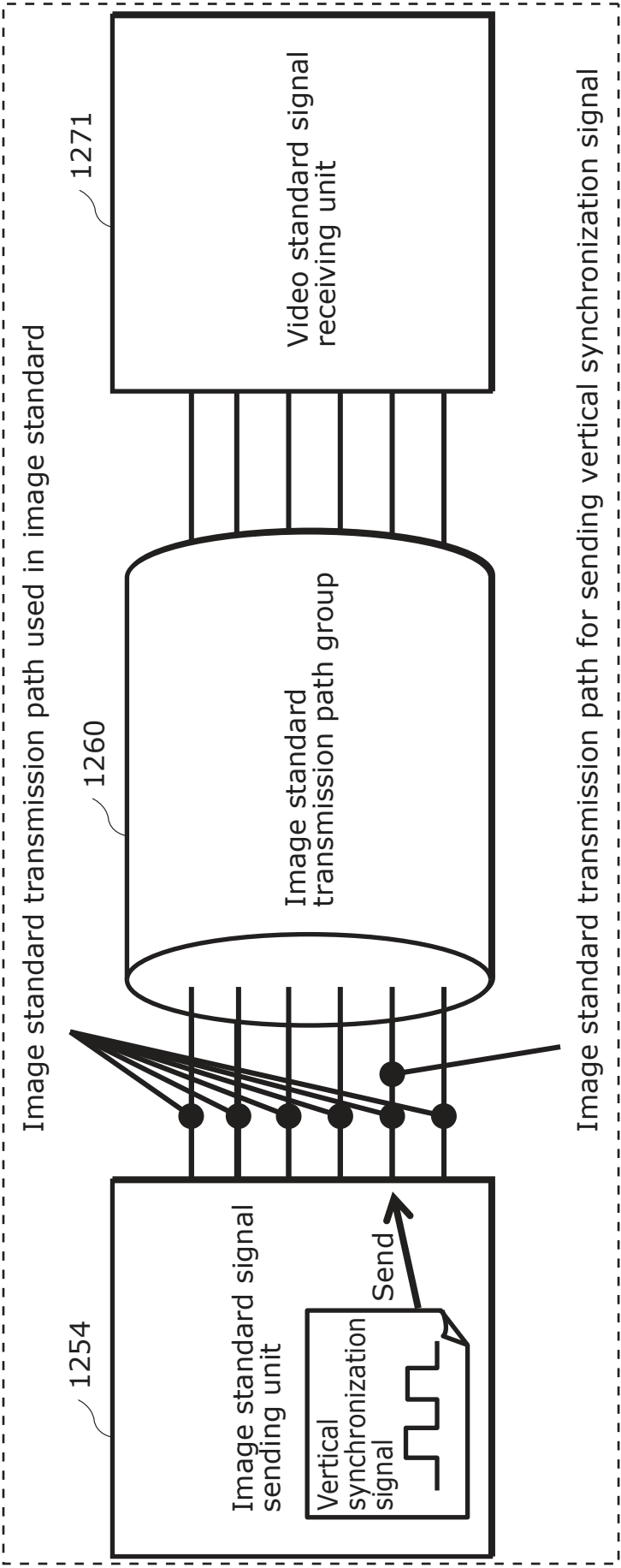


FIG. 331

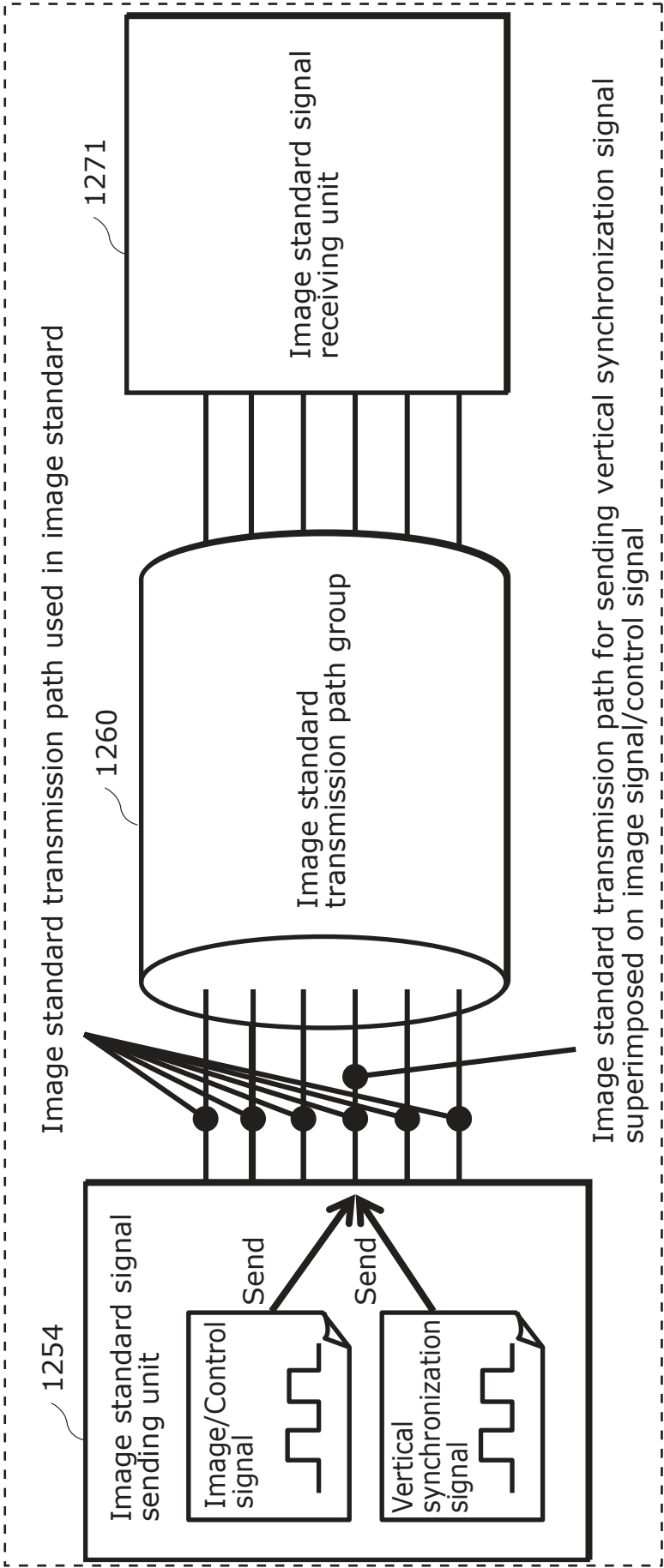


FIG. 332

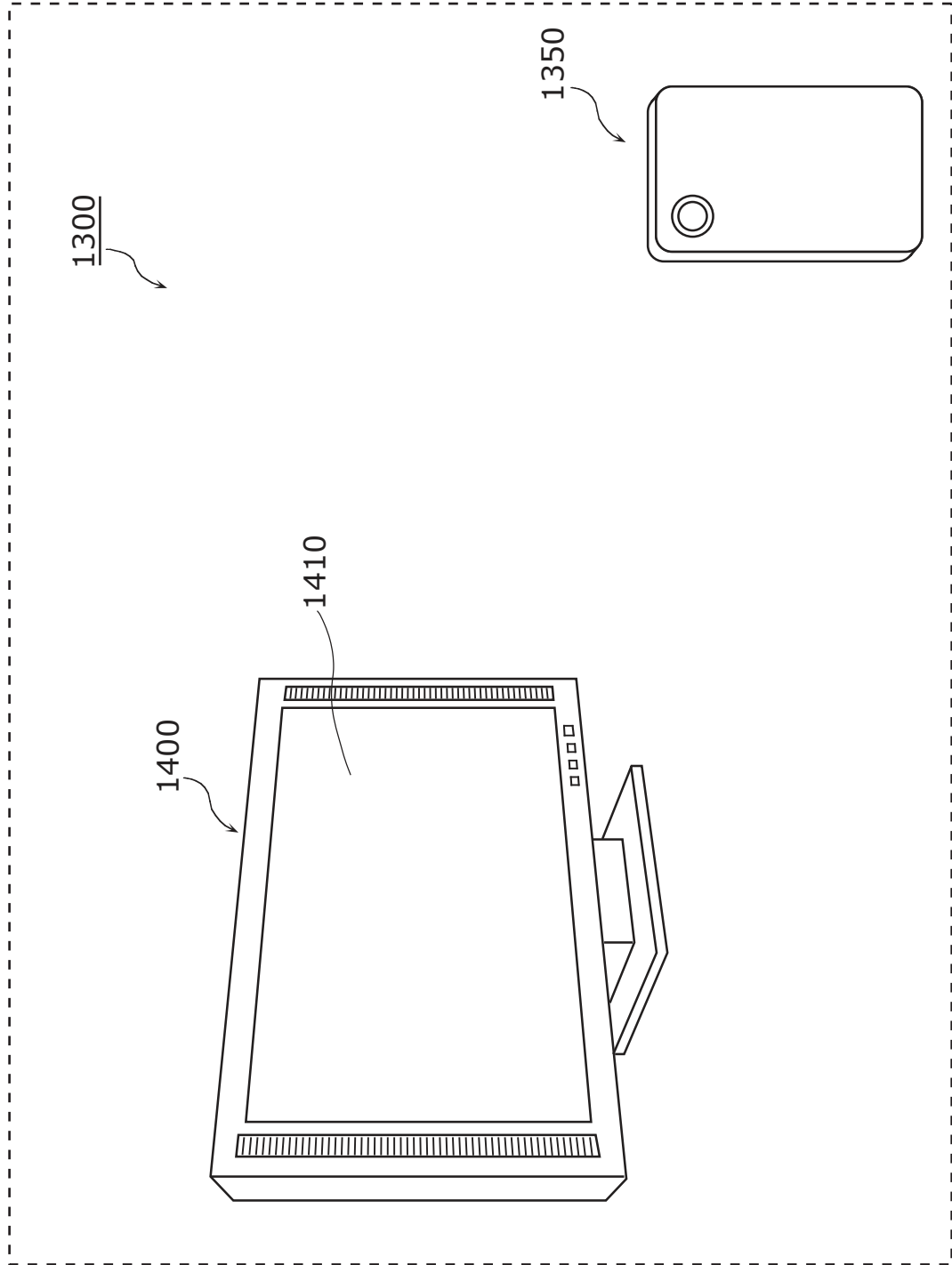


FIG. 333

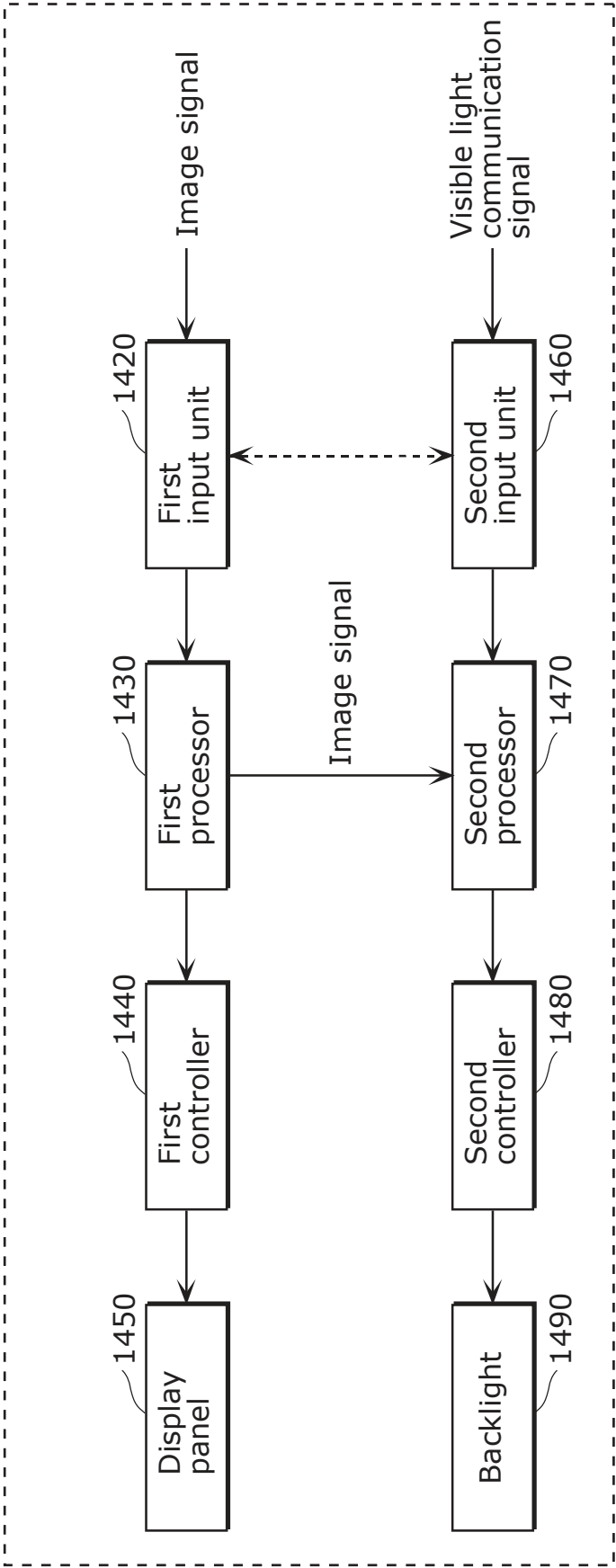


FIG. 334A

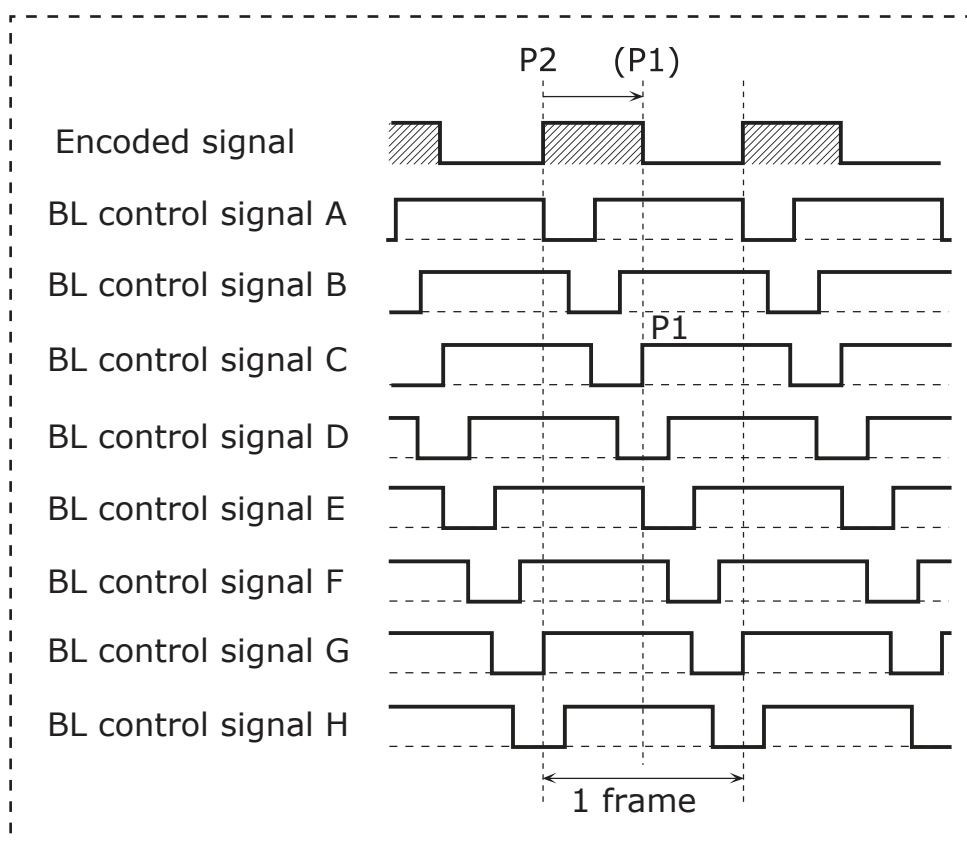


FIG. 334B

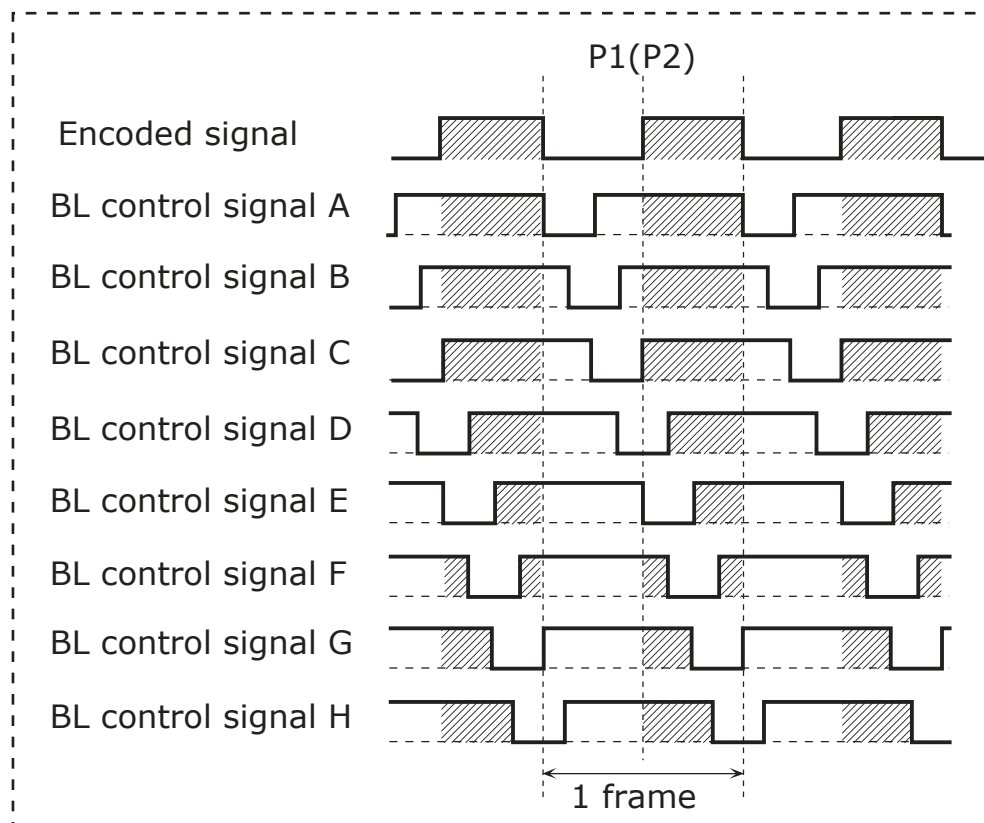


FIG. 335

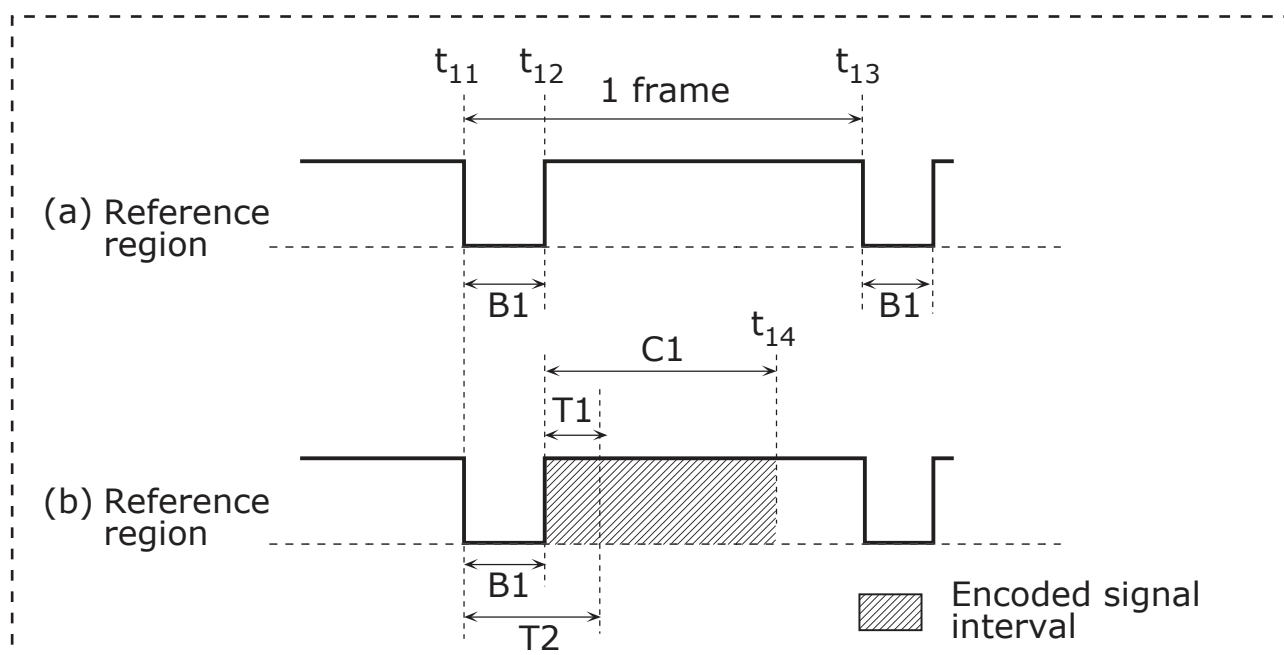


FIG. 336

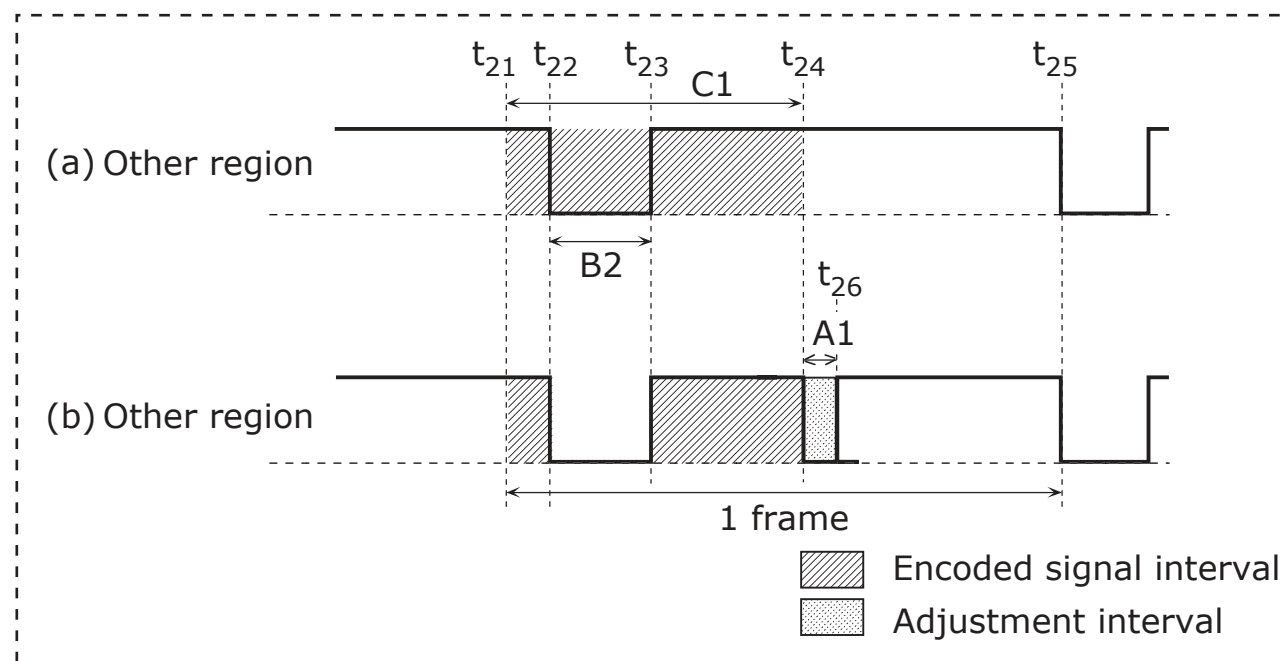


FIG. 337A

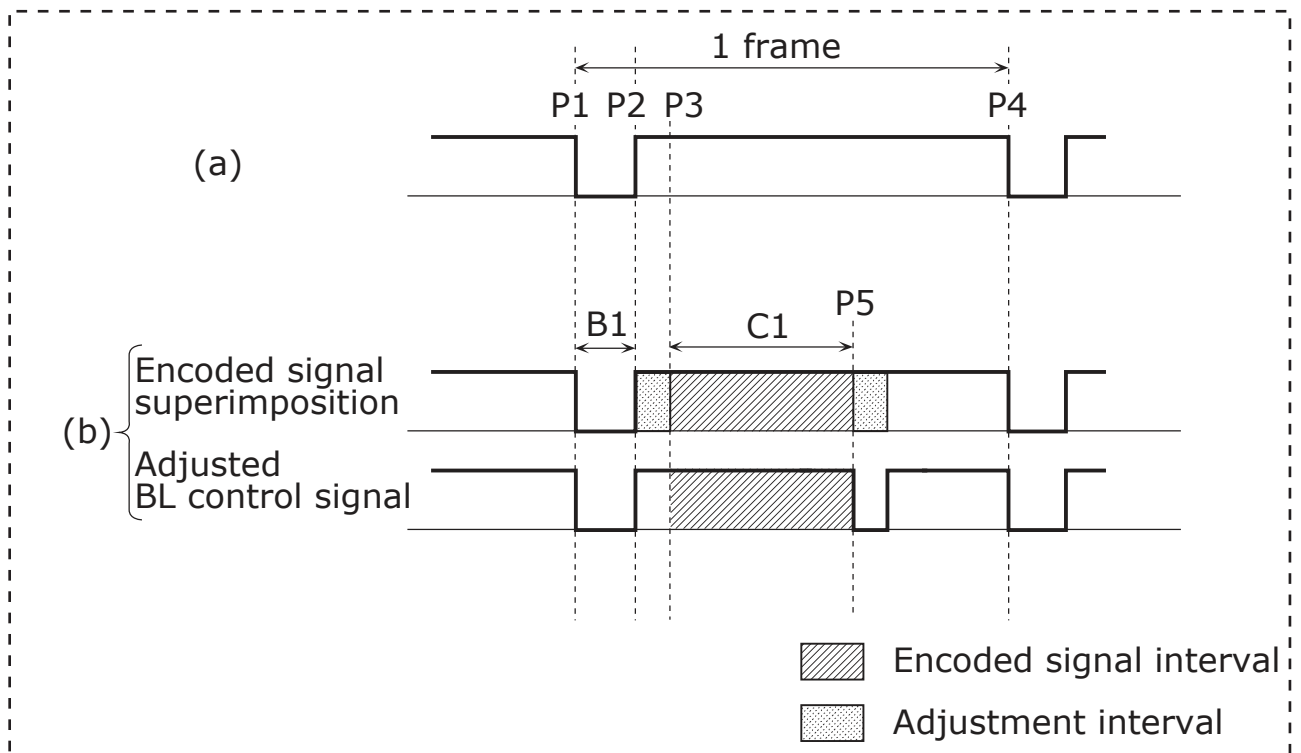


FIG. 337B

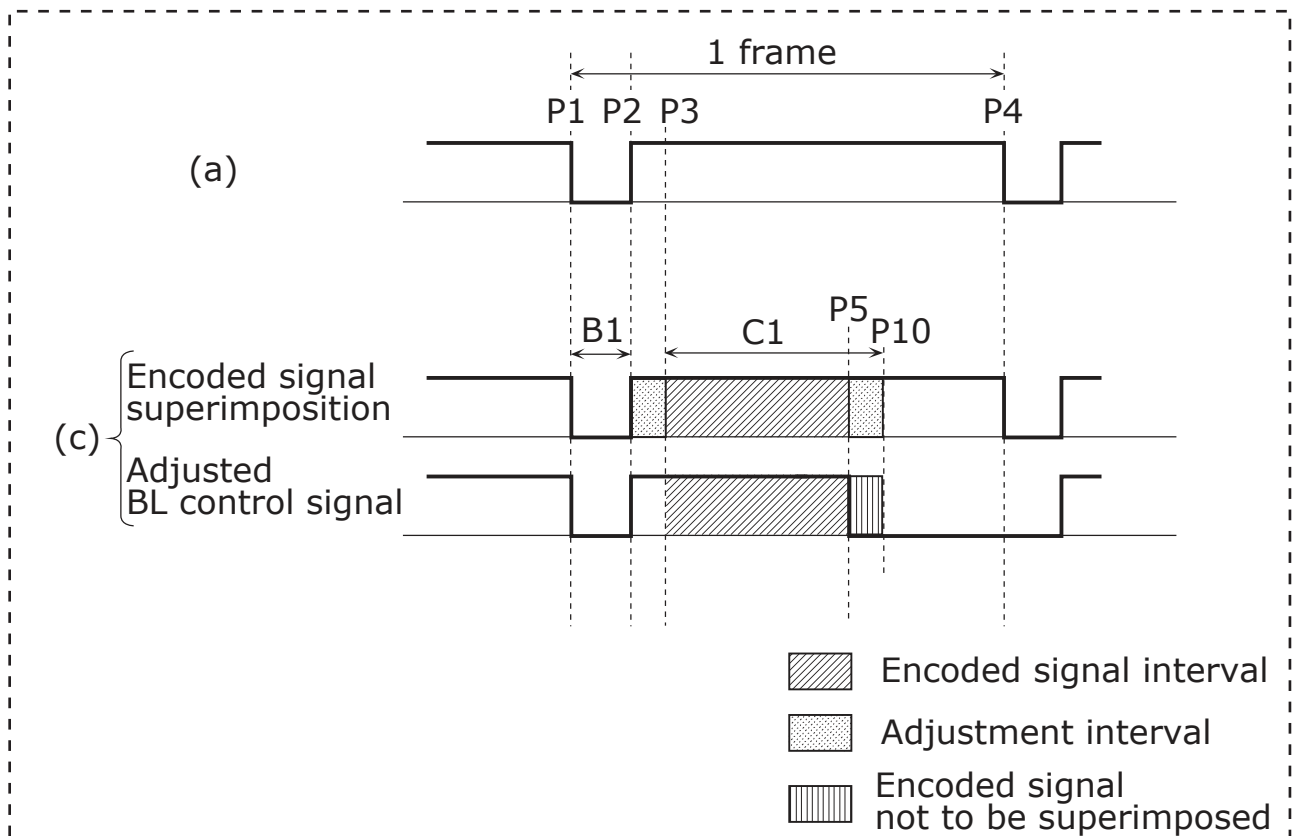


FIG. 337C

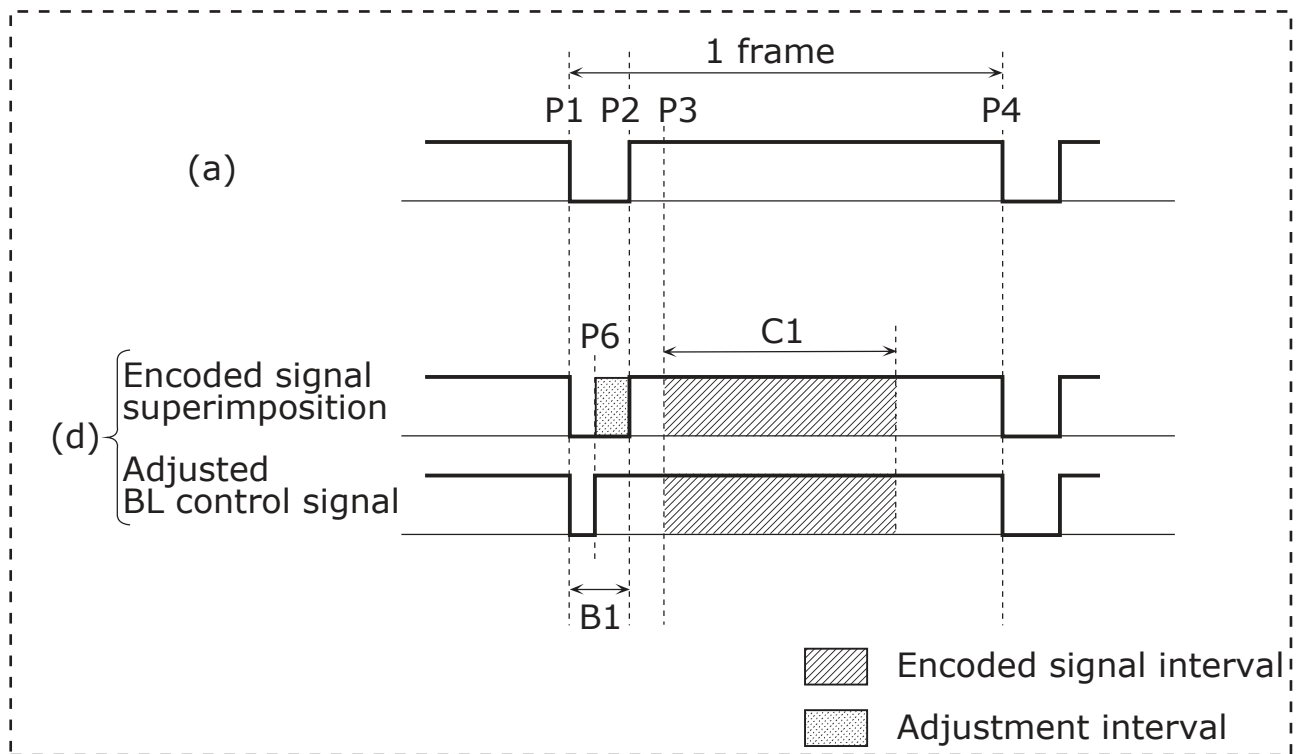


FIG. 337D

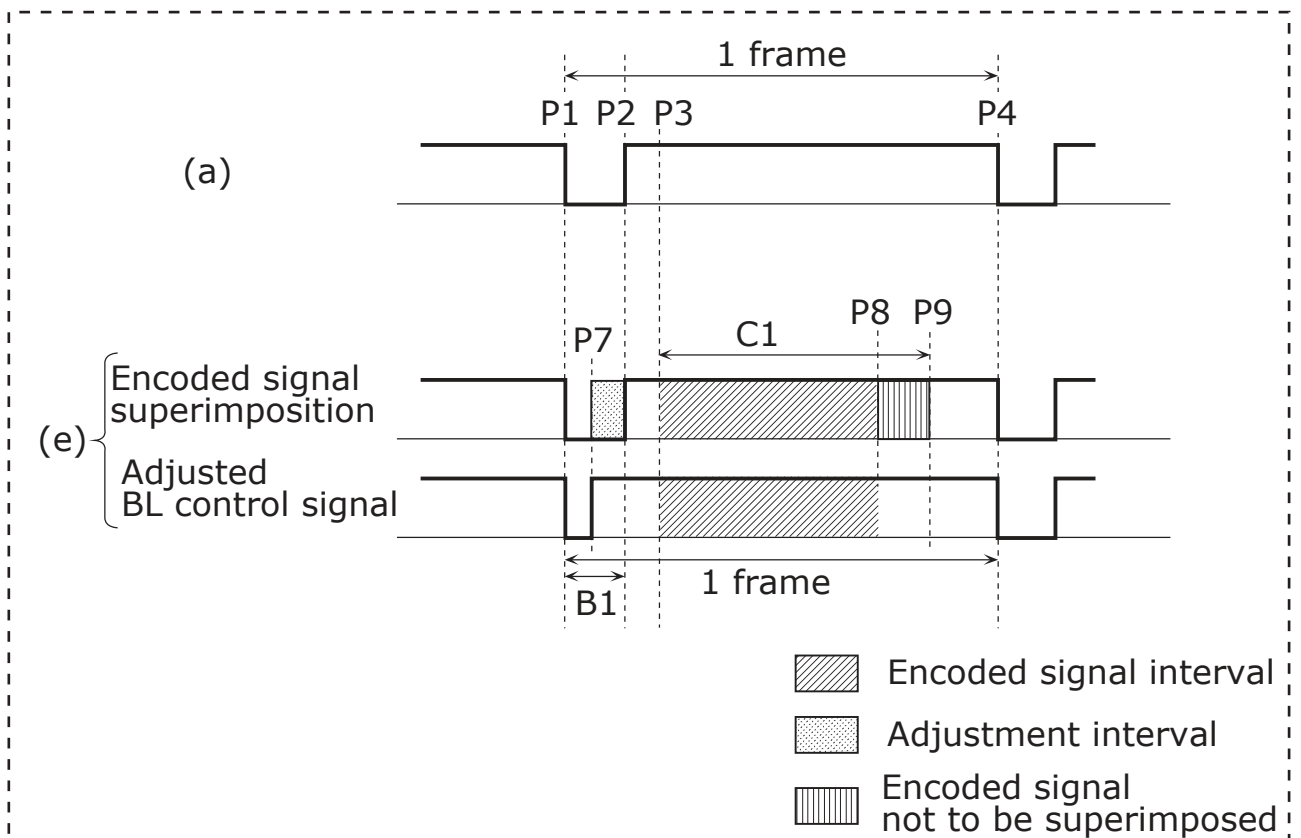


FIG. 338A

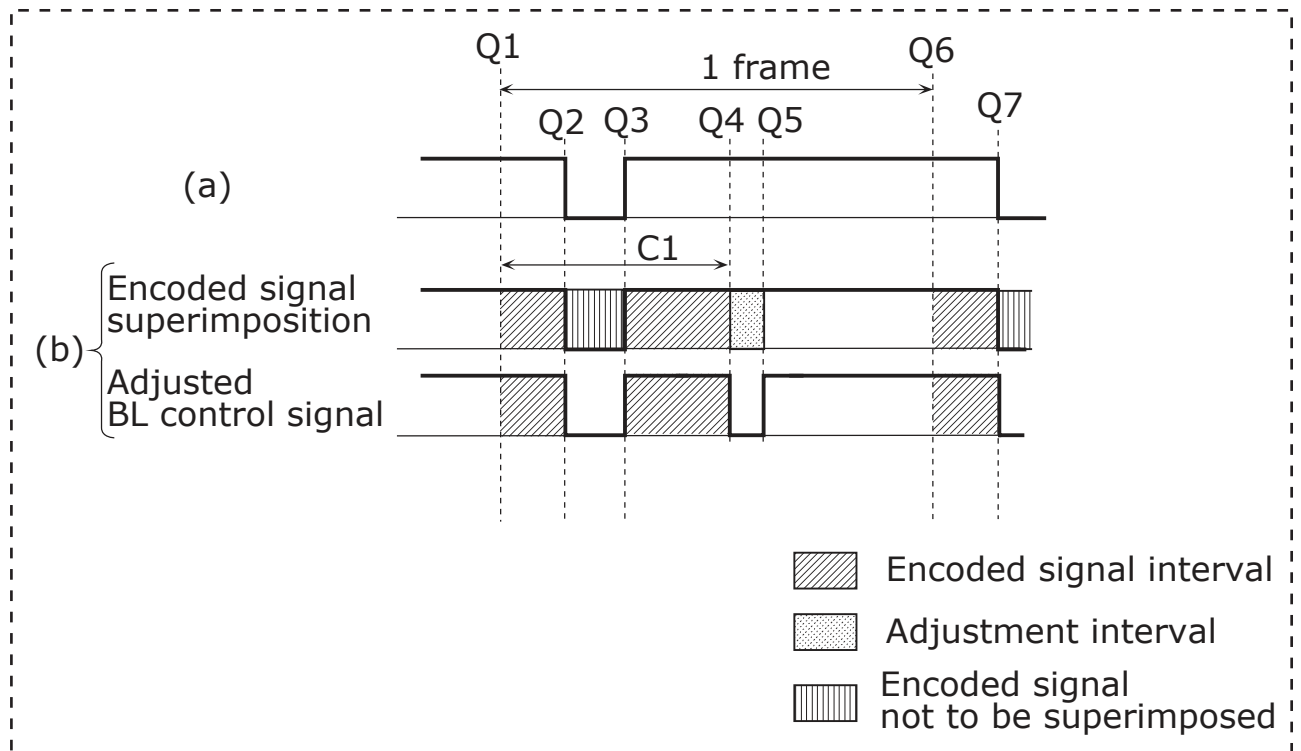


FIG. 338B

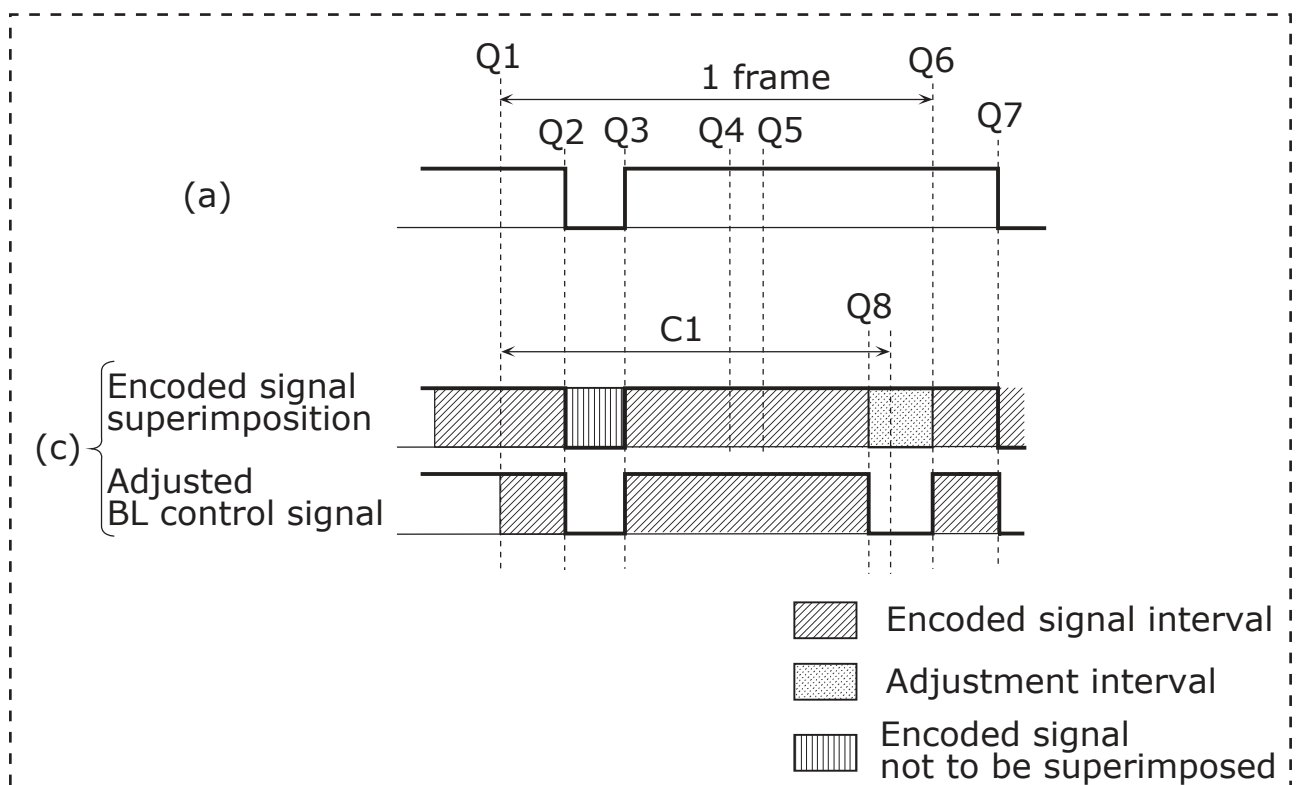


FIG. 338C

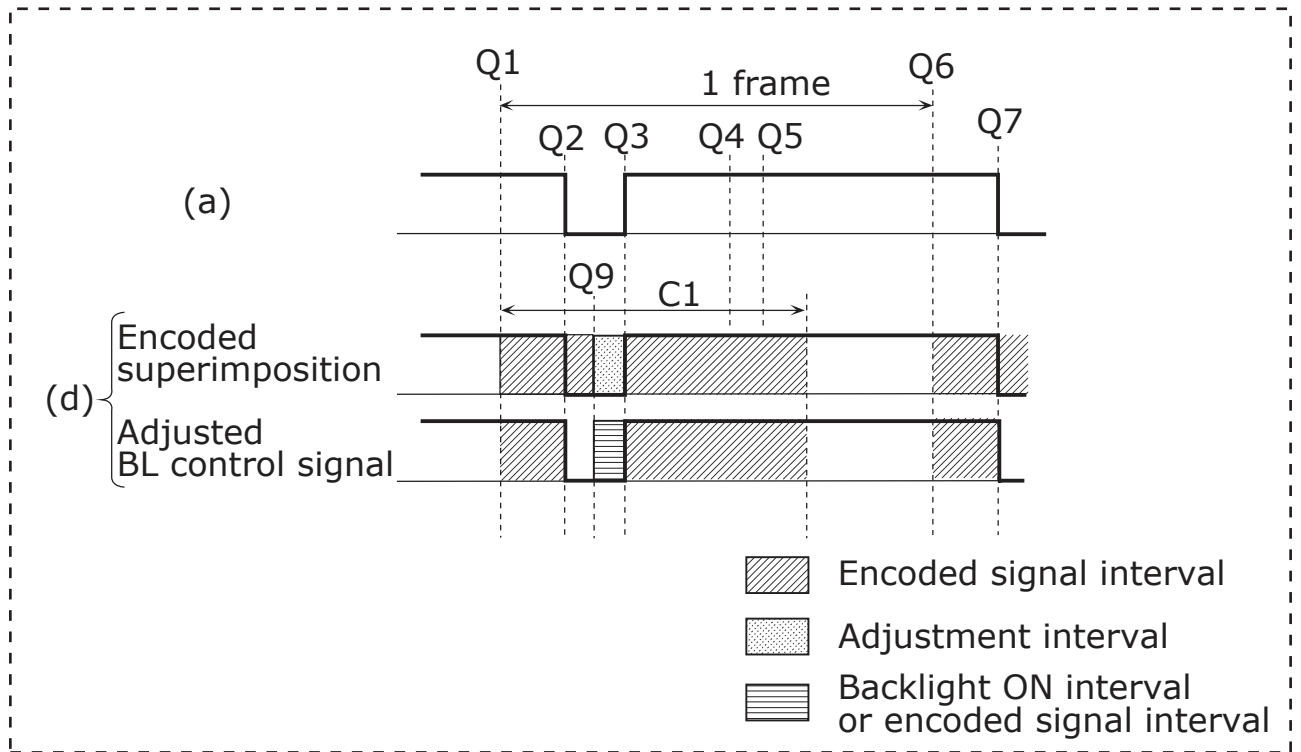


FIG. 338D

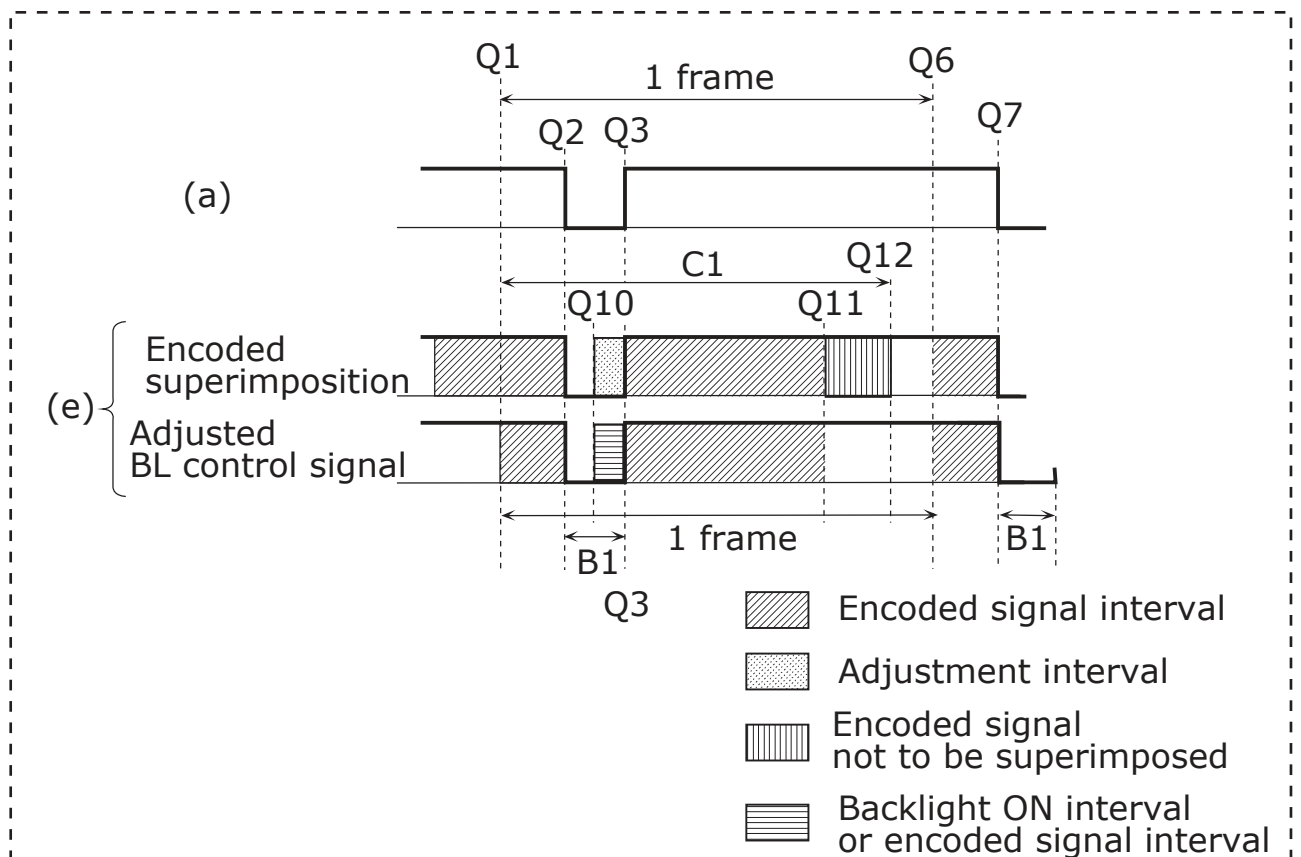


FIG. 339

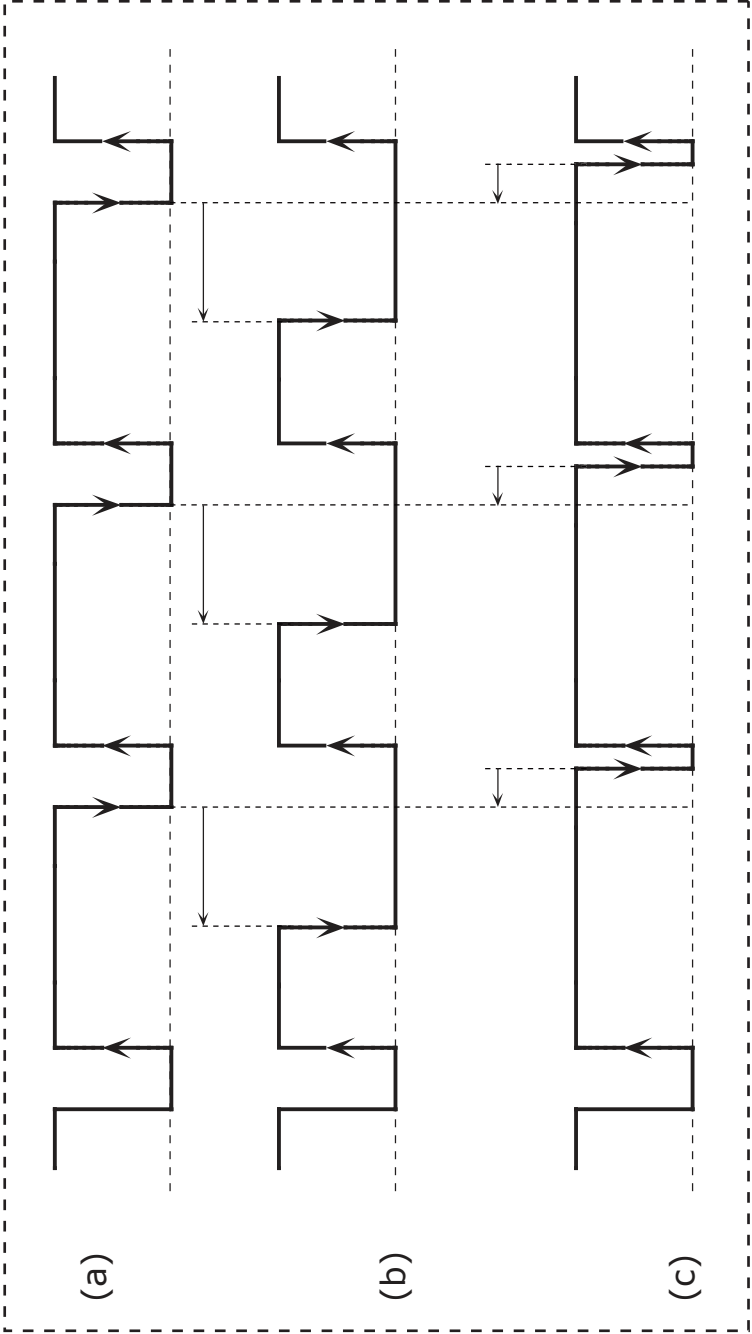


FIG. 340

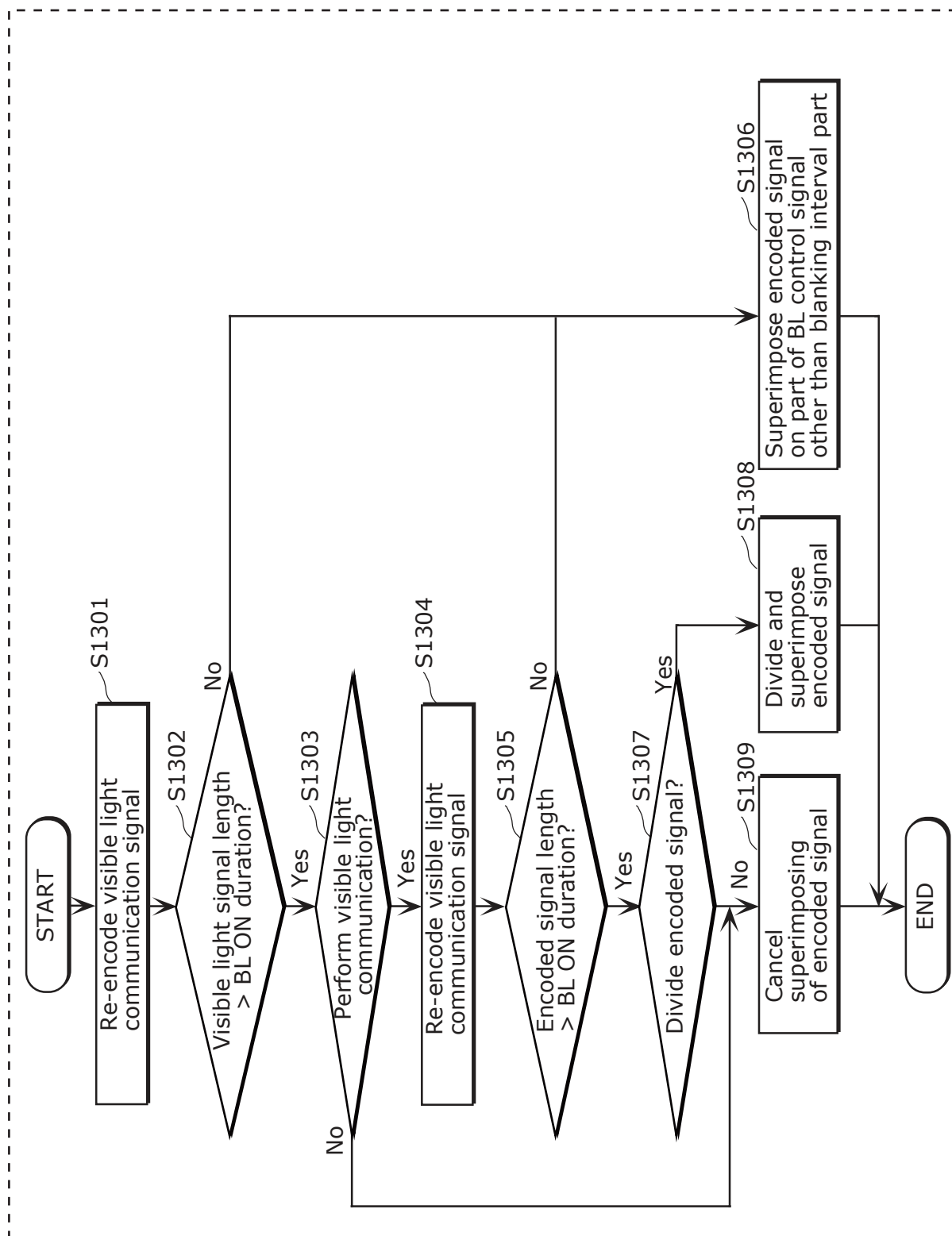


FIG. 341A

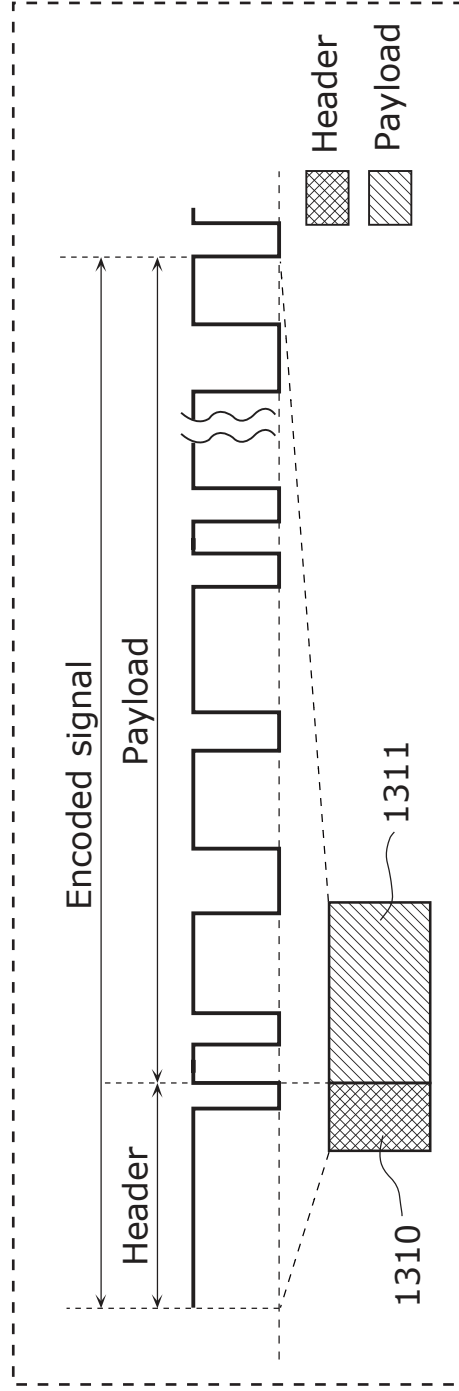


FIG. 341B

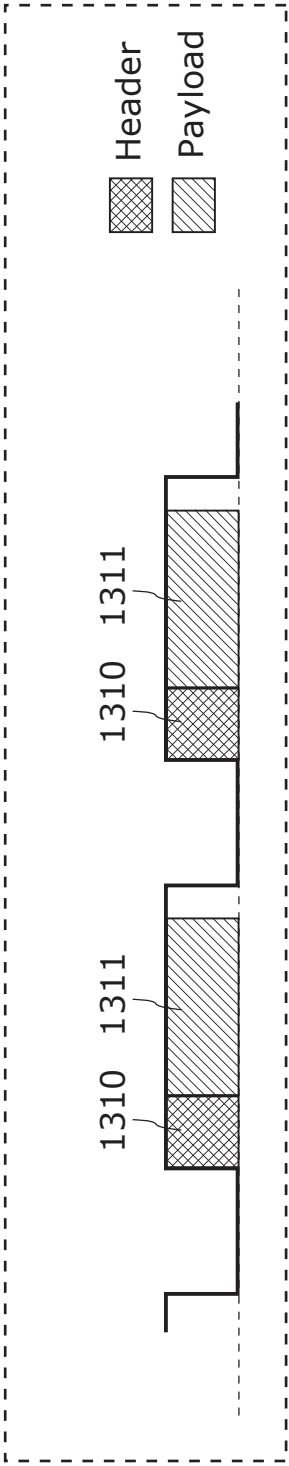


FIG. 341C

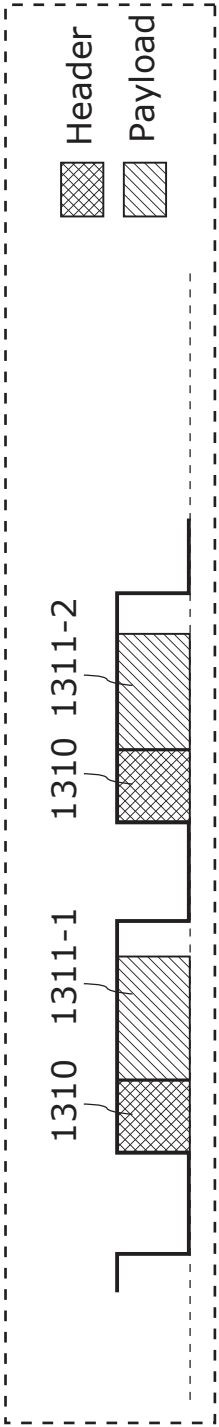


FIG. 341D

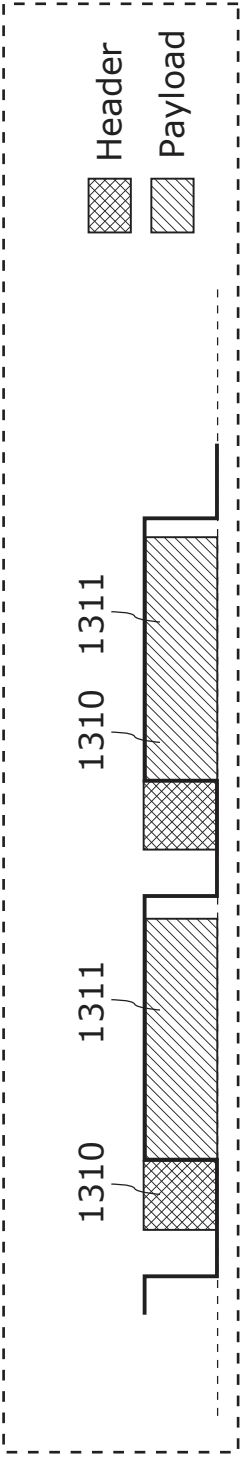


FIG. 342

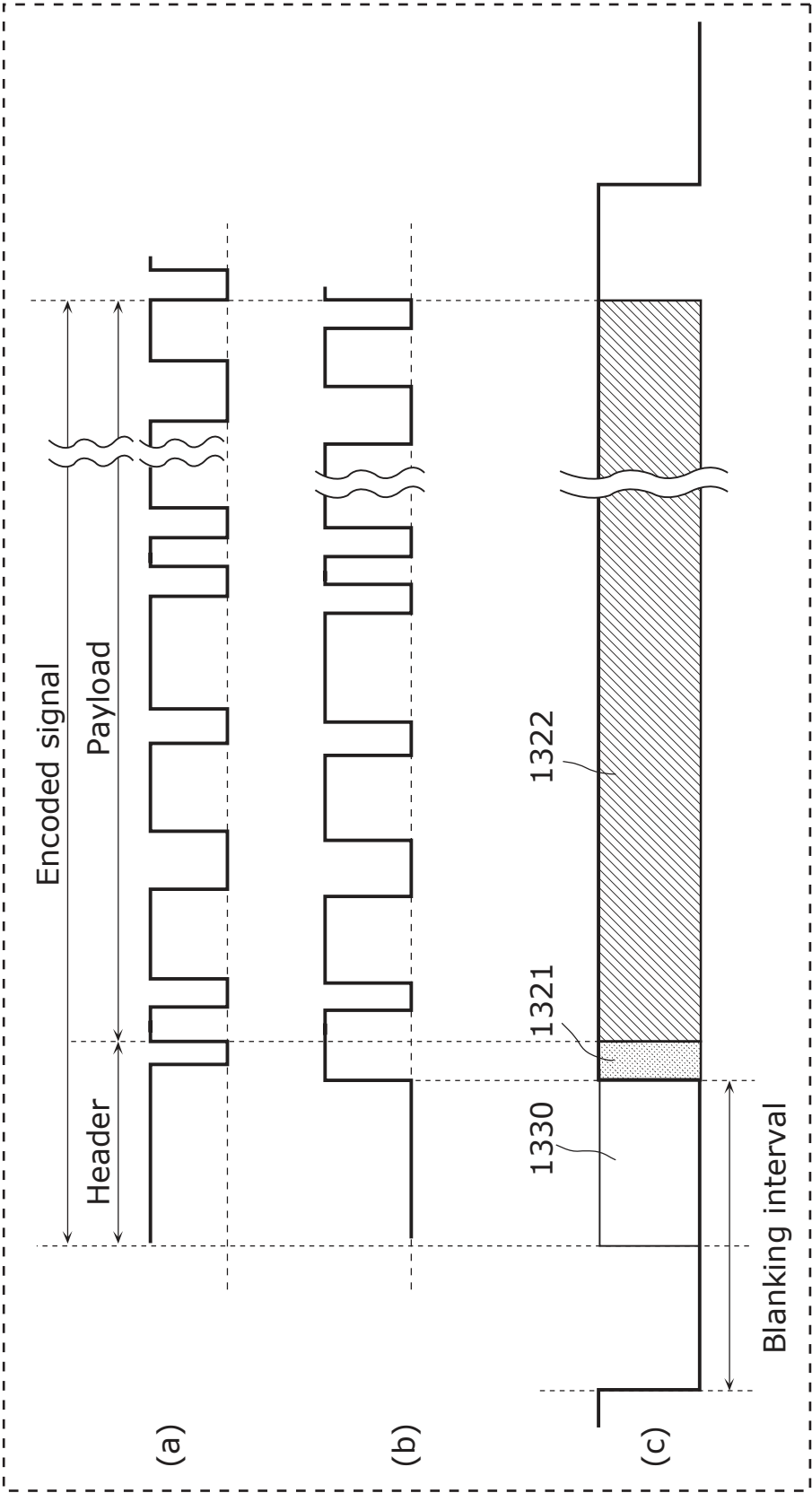


FIG. 343

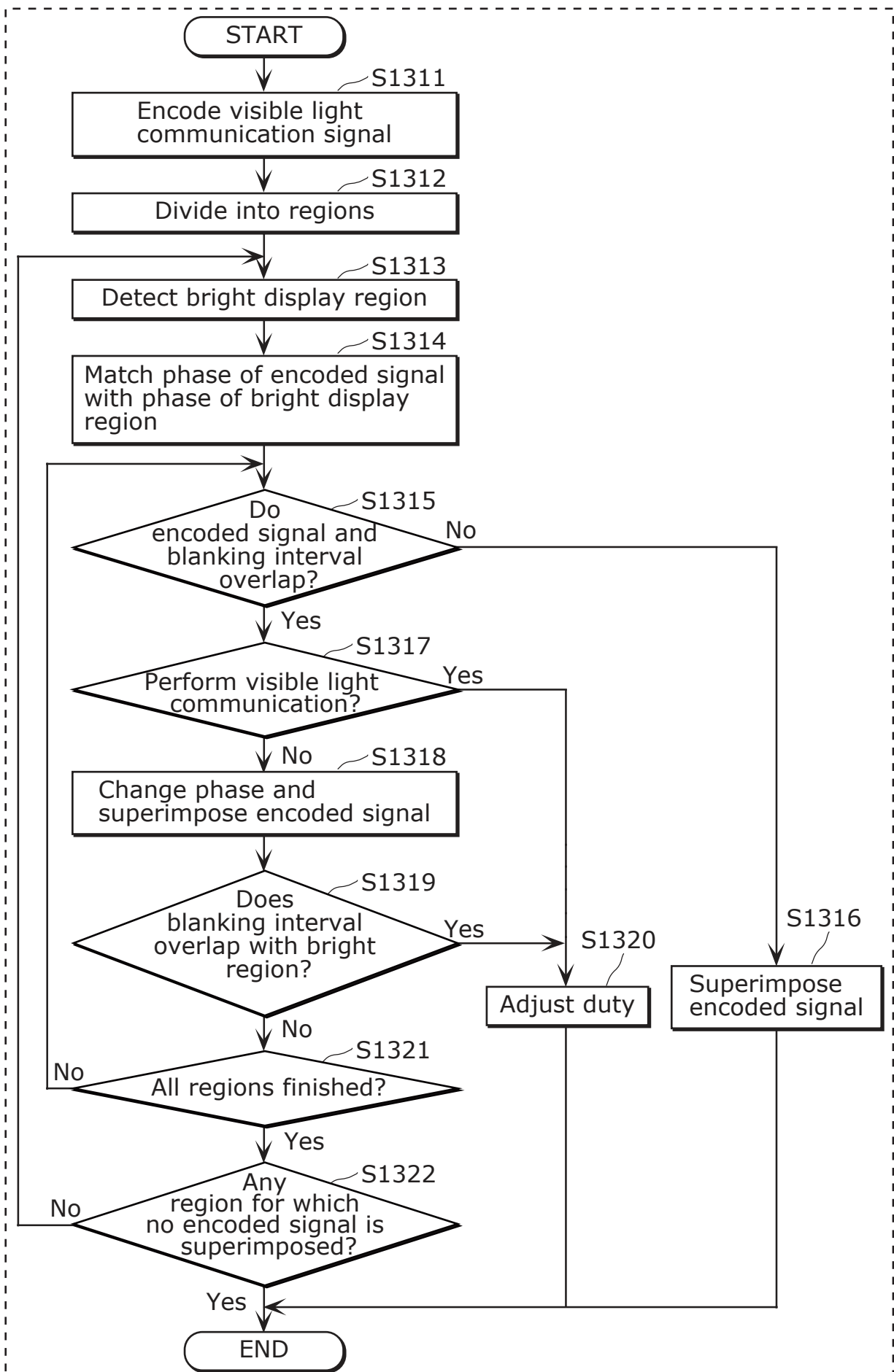


FIG. 344

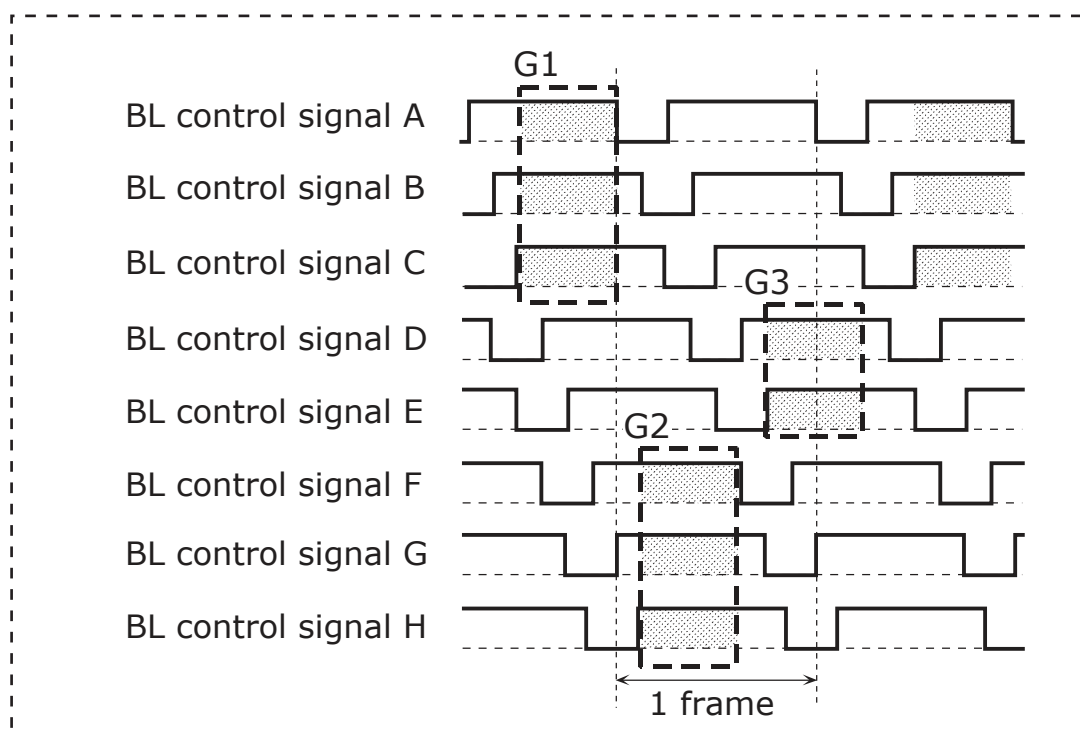


FIG. 345

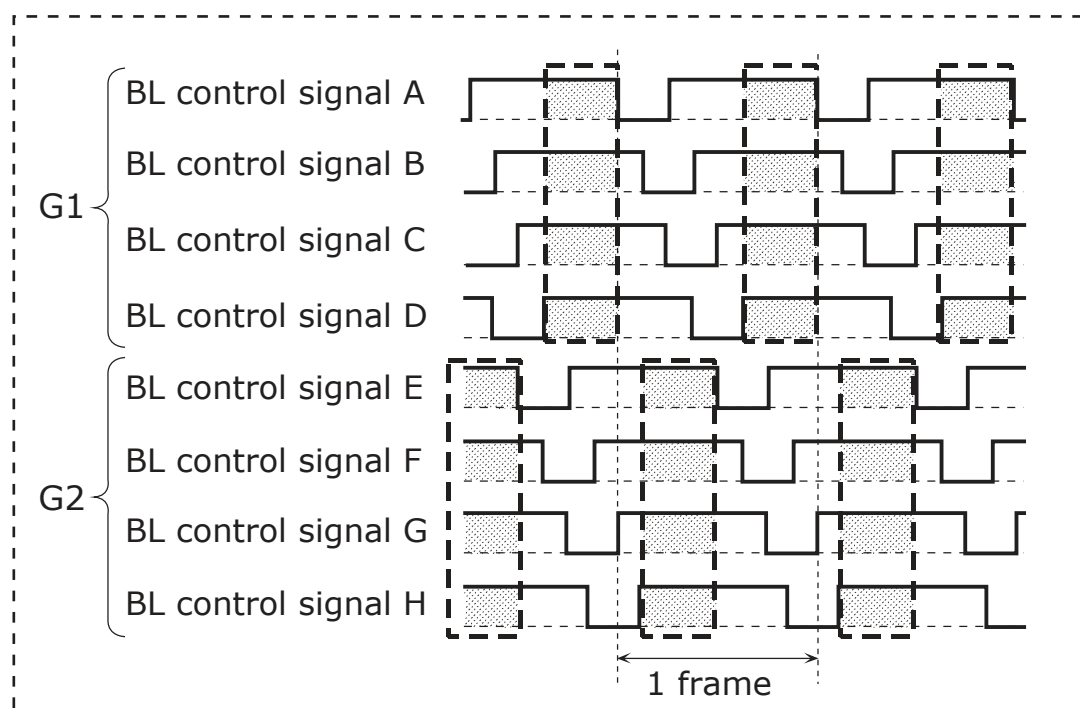


FIG. 346

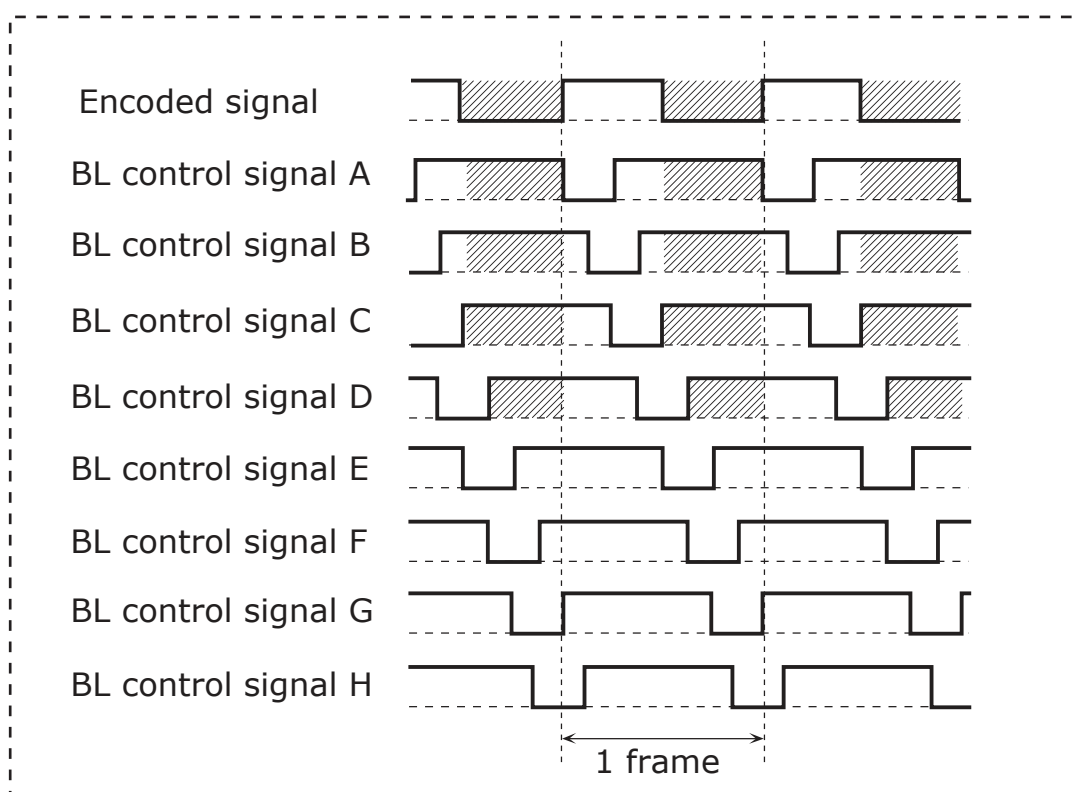


FIG. 347

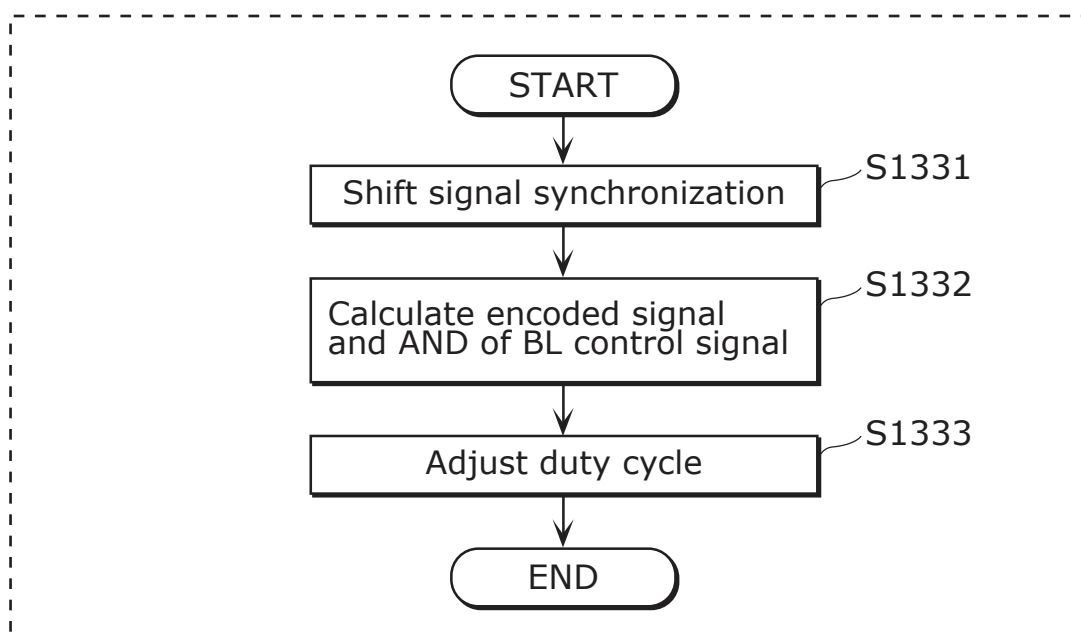


FIG. 348A

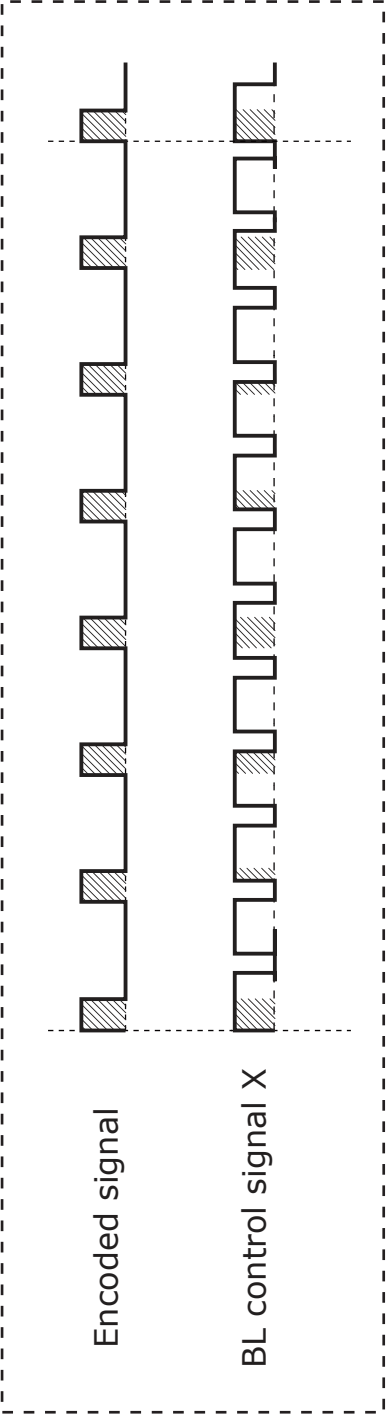


FIG. 348B

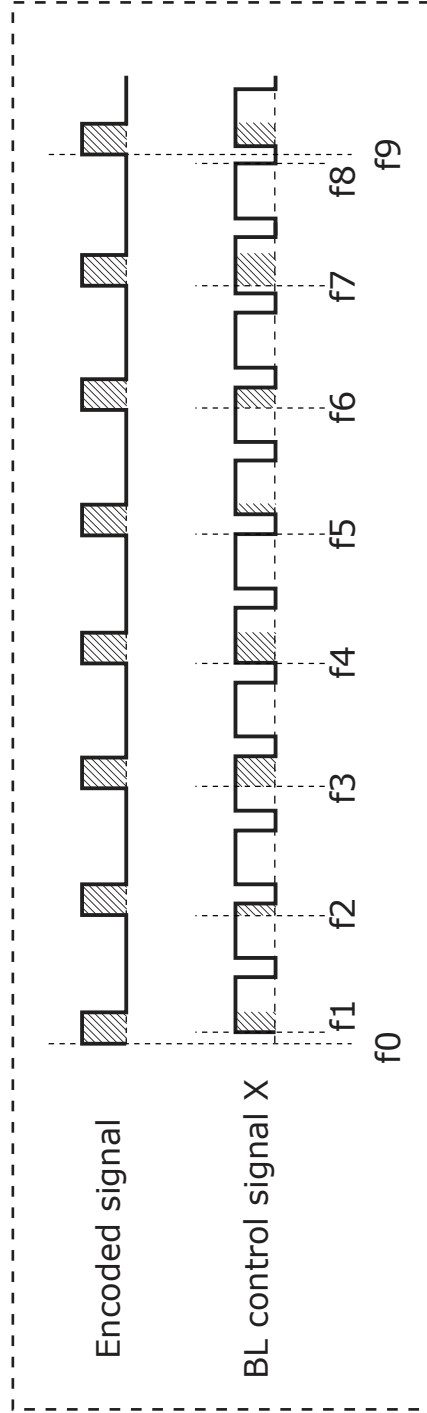


FIG. 349A

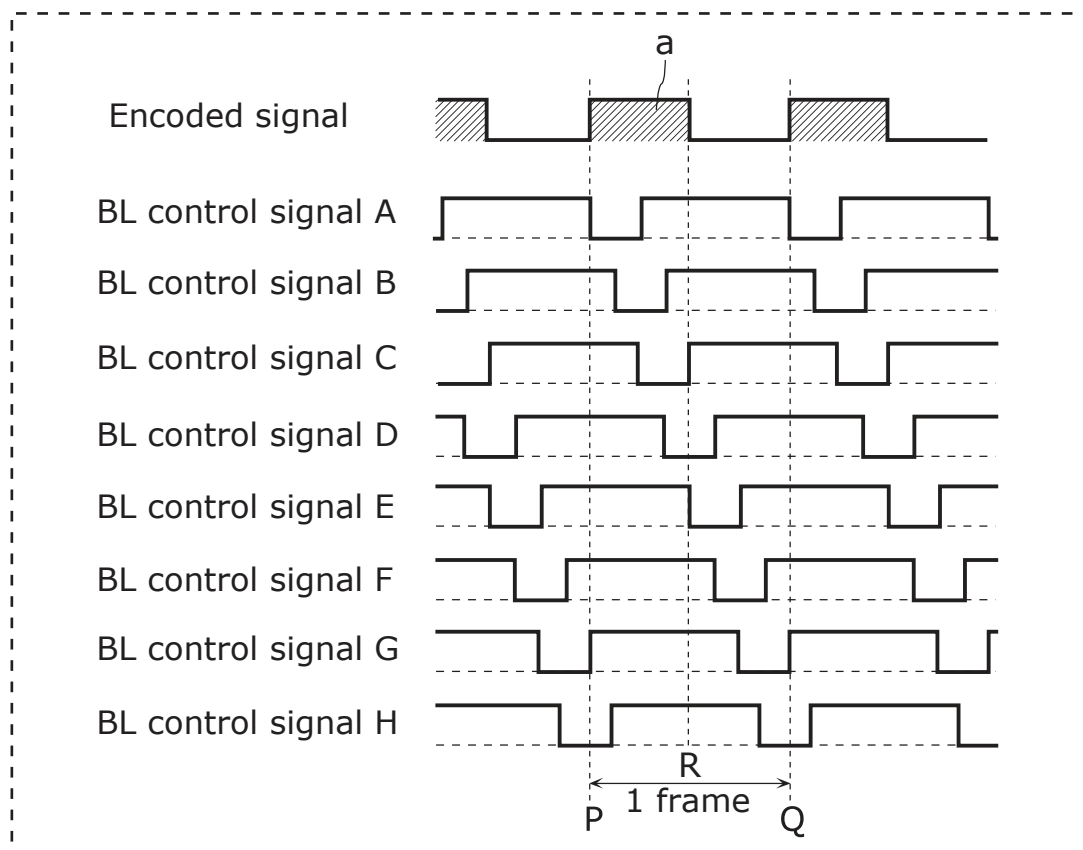


FIG. 349B

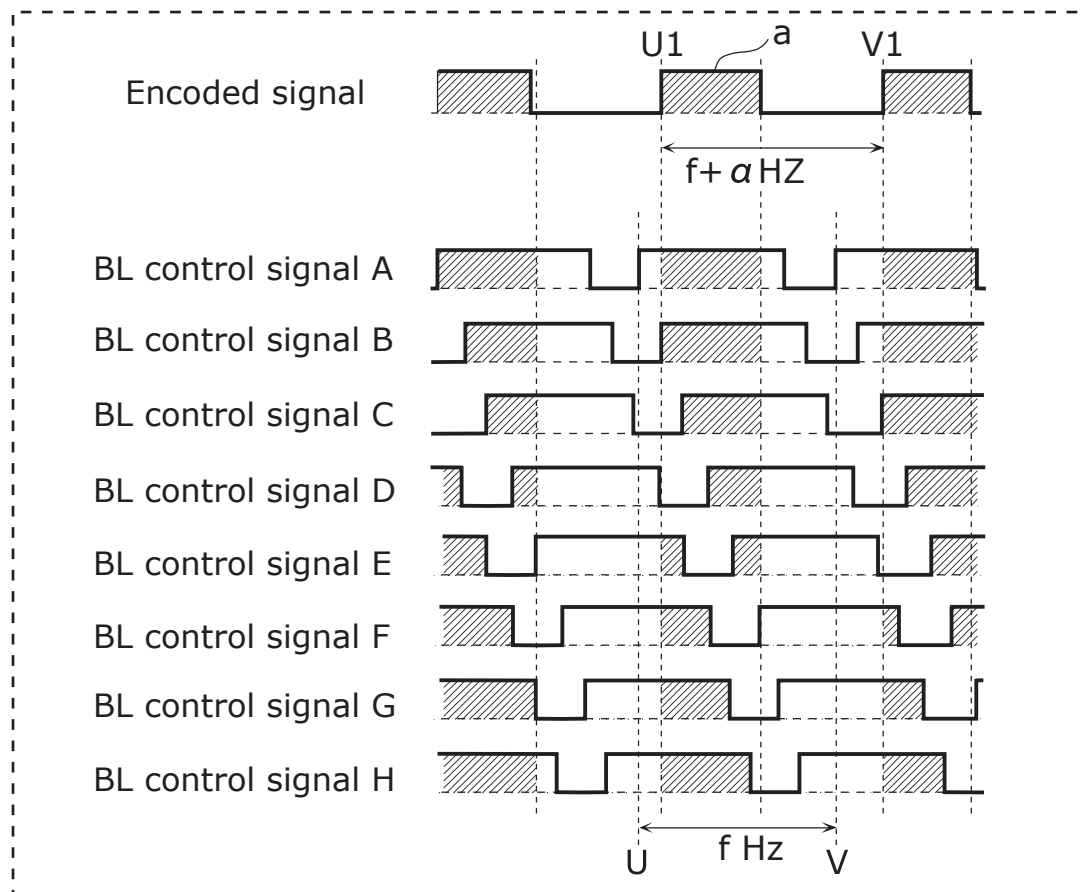


FIG. 349C

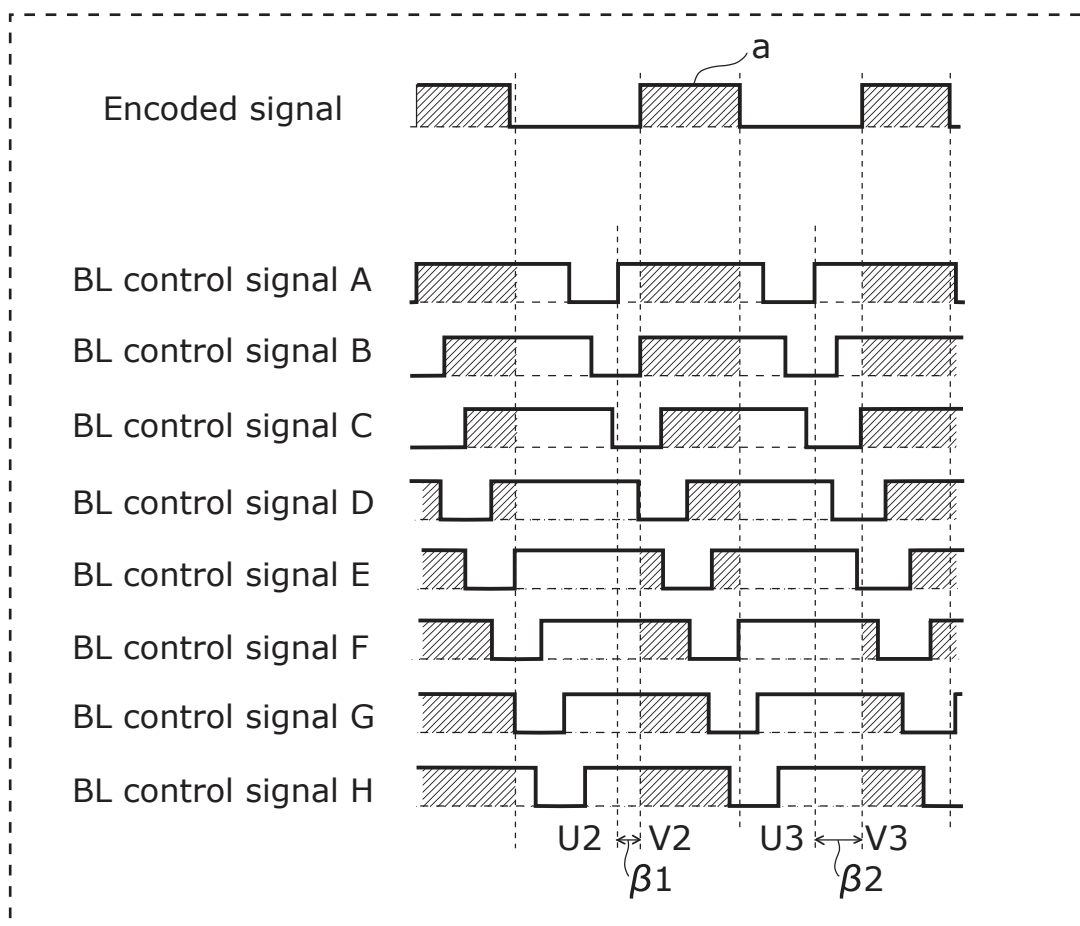


FIG. 350A

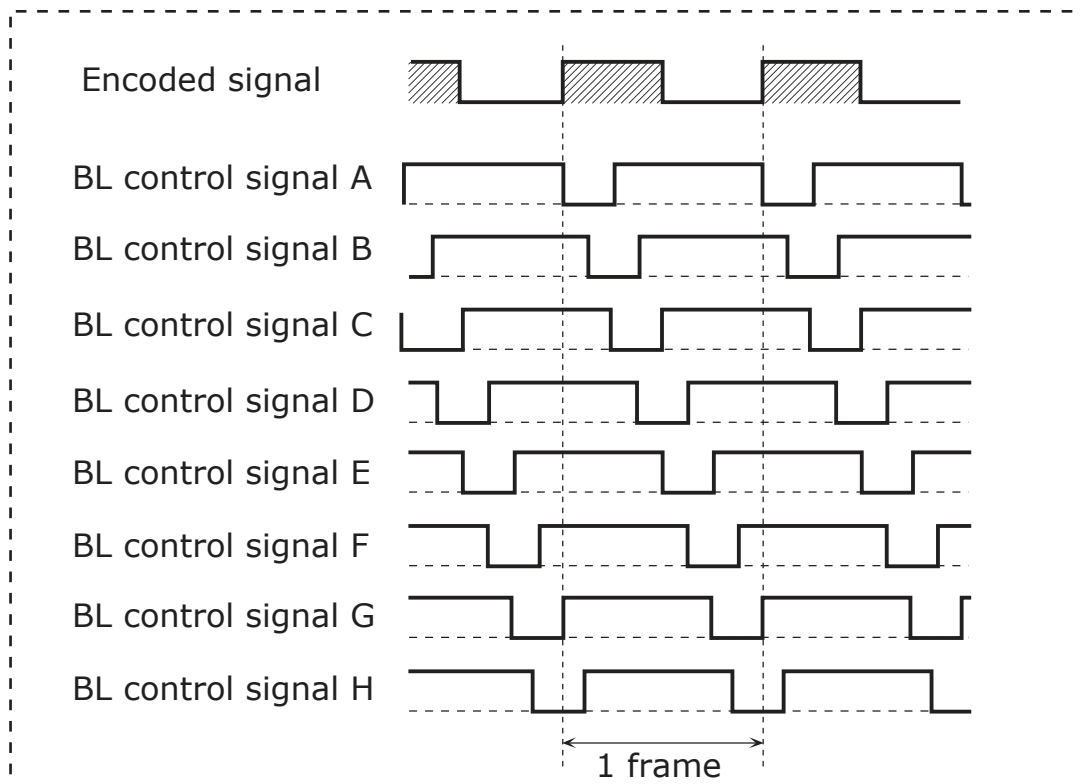


FIG. 350B

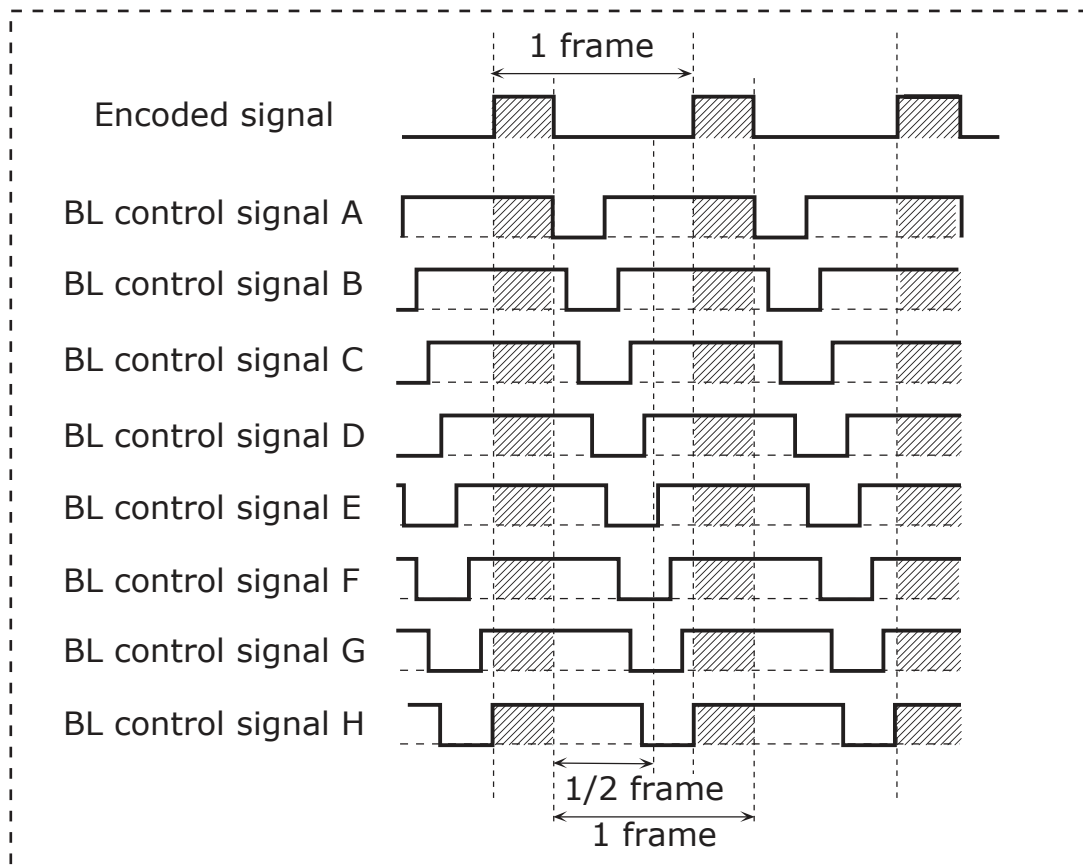


FIG. 351

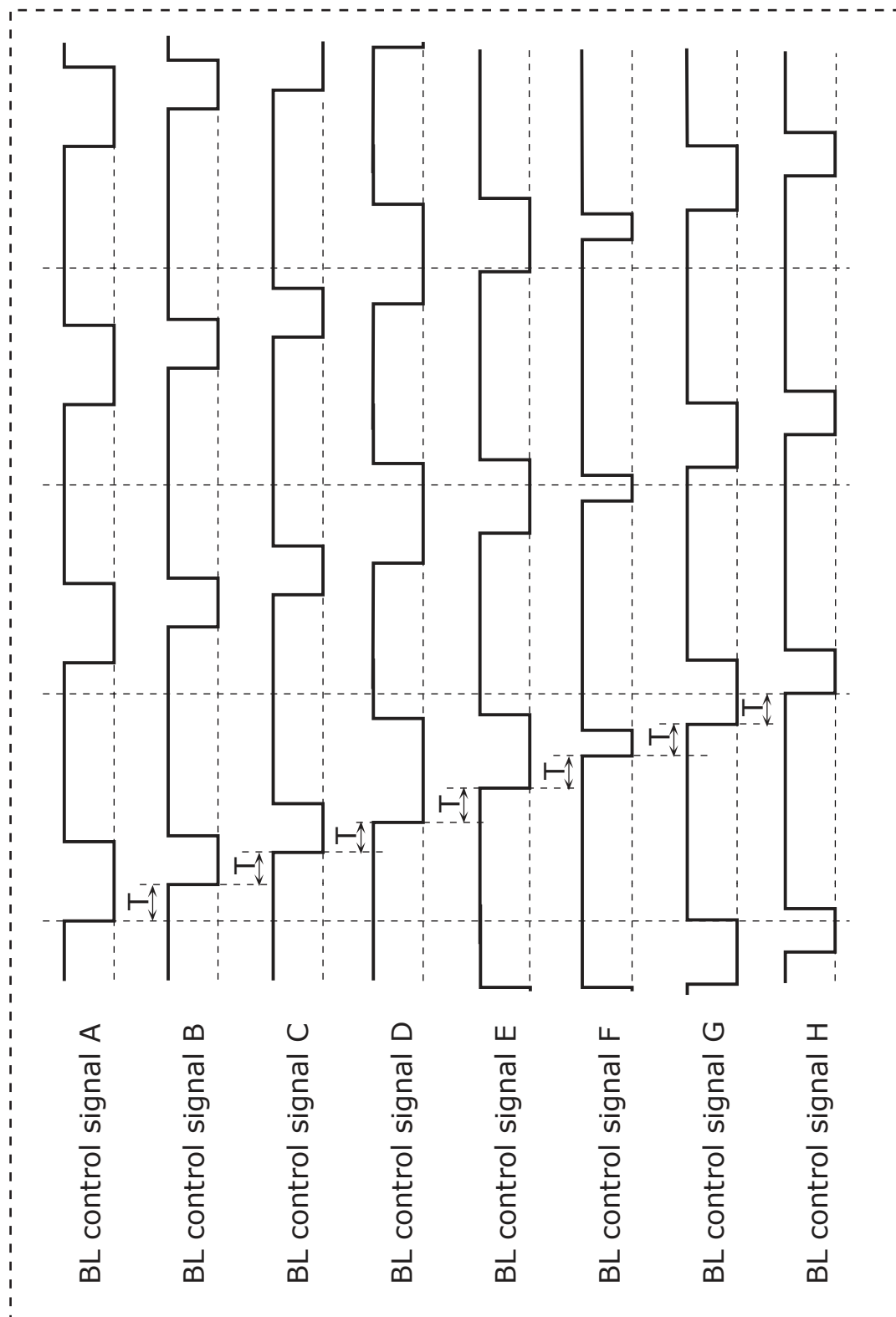


FIG. 352

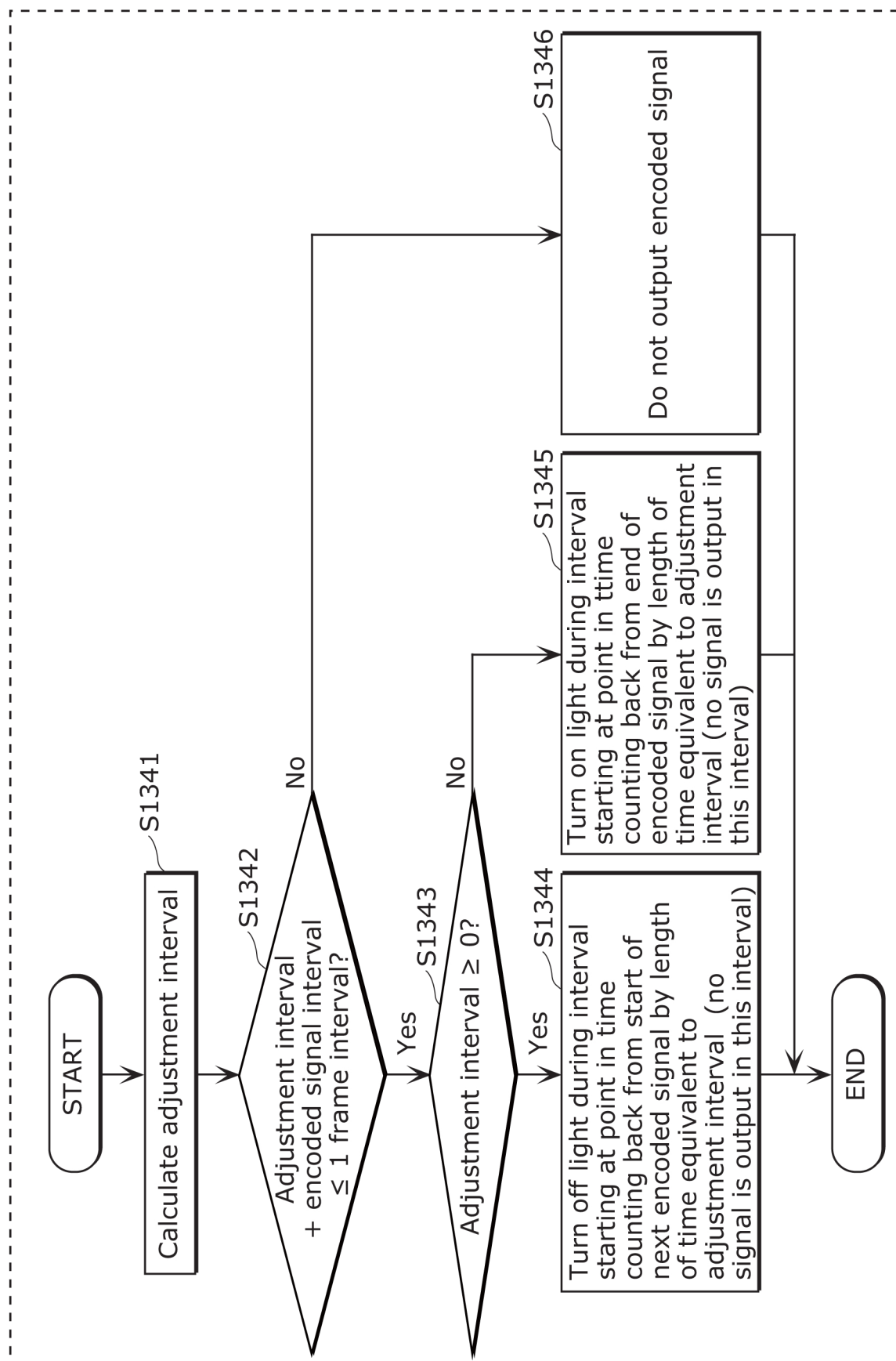


FIG. 353

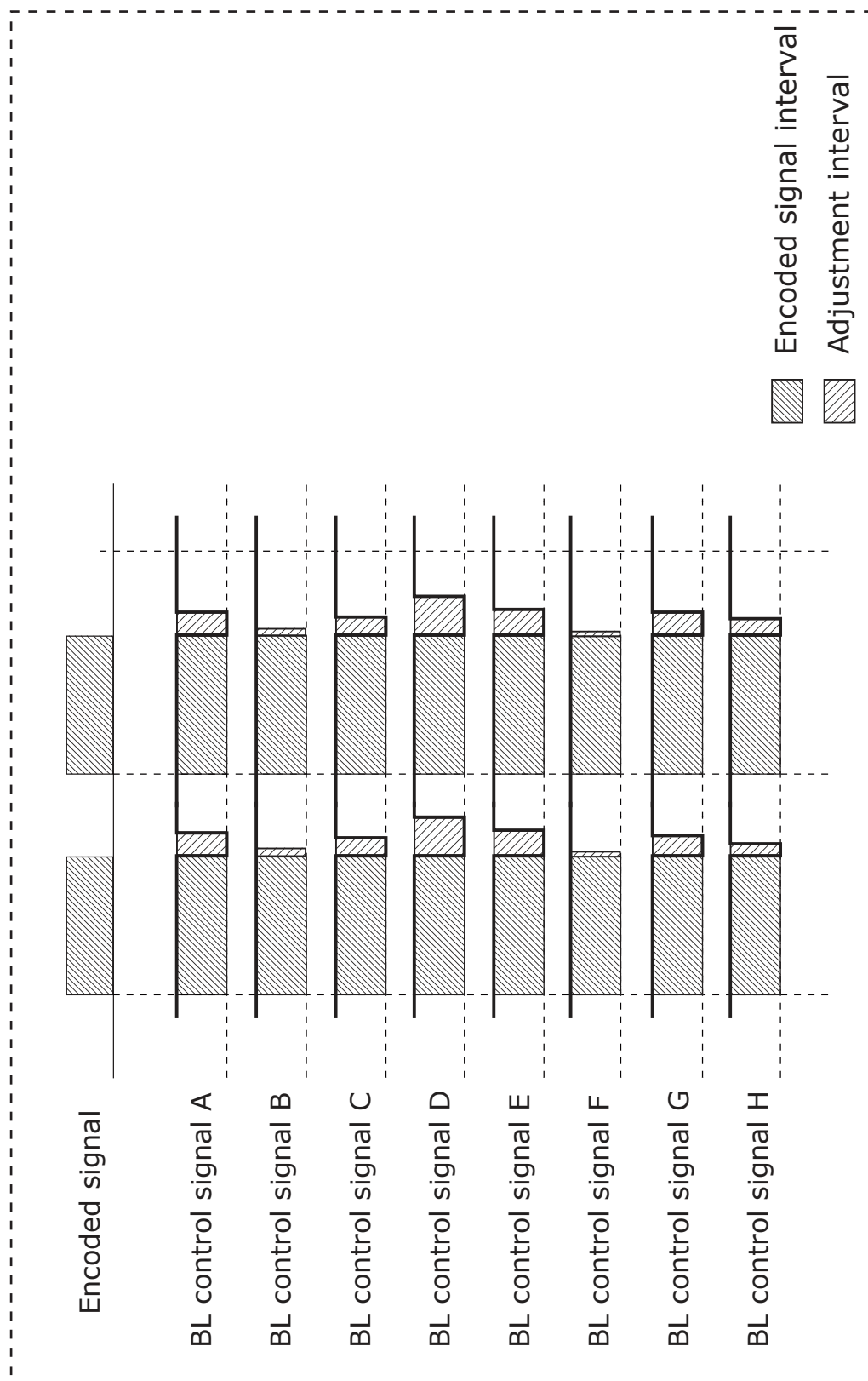


FIG. 354

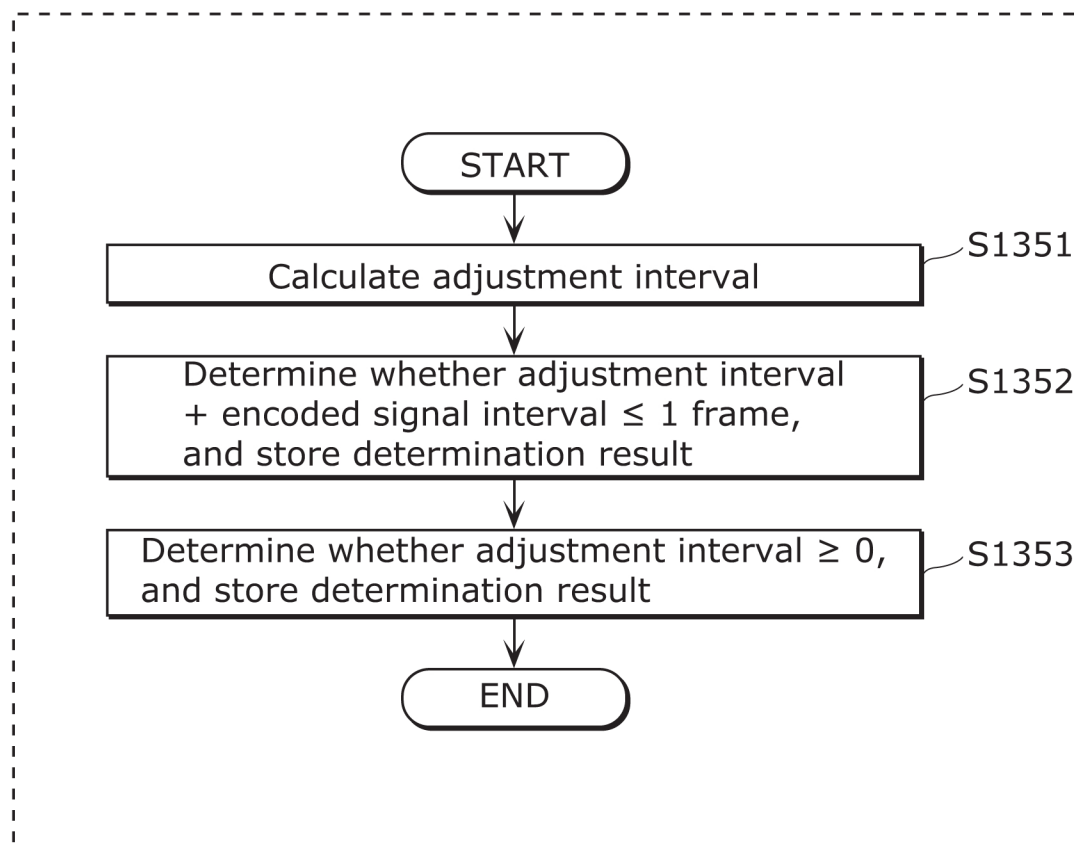


FIG. 355

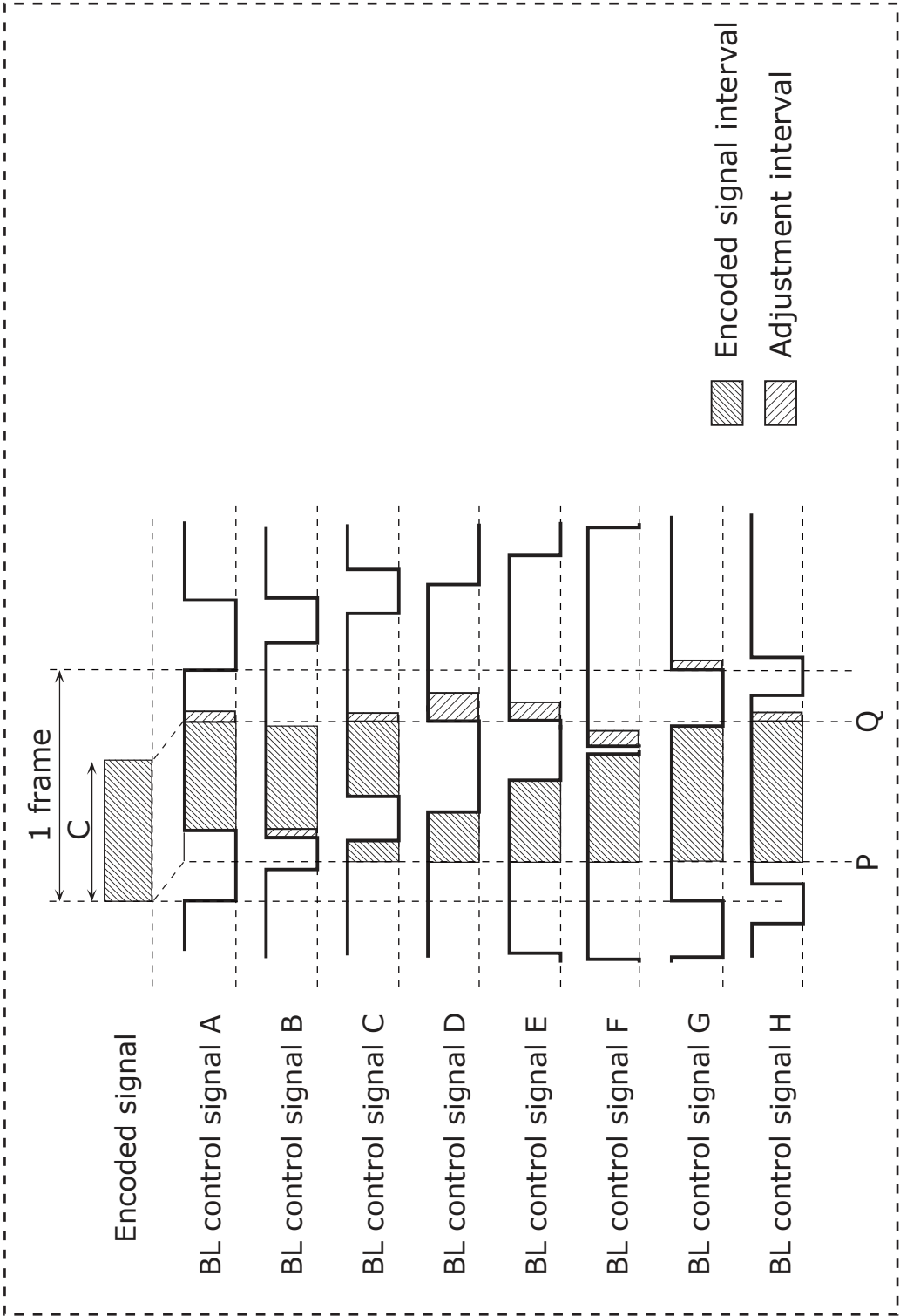


FIG. 356

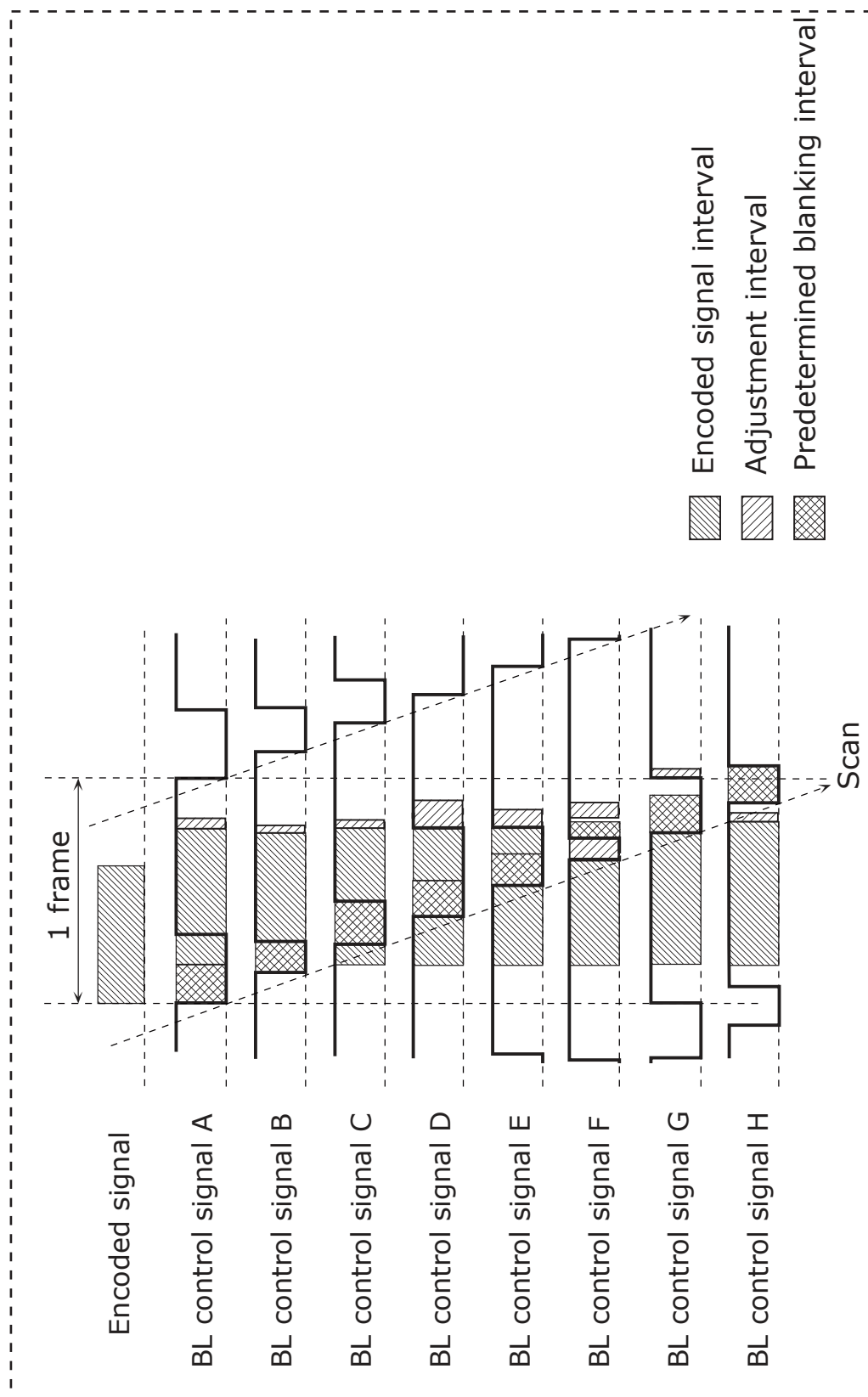


FIG. 357

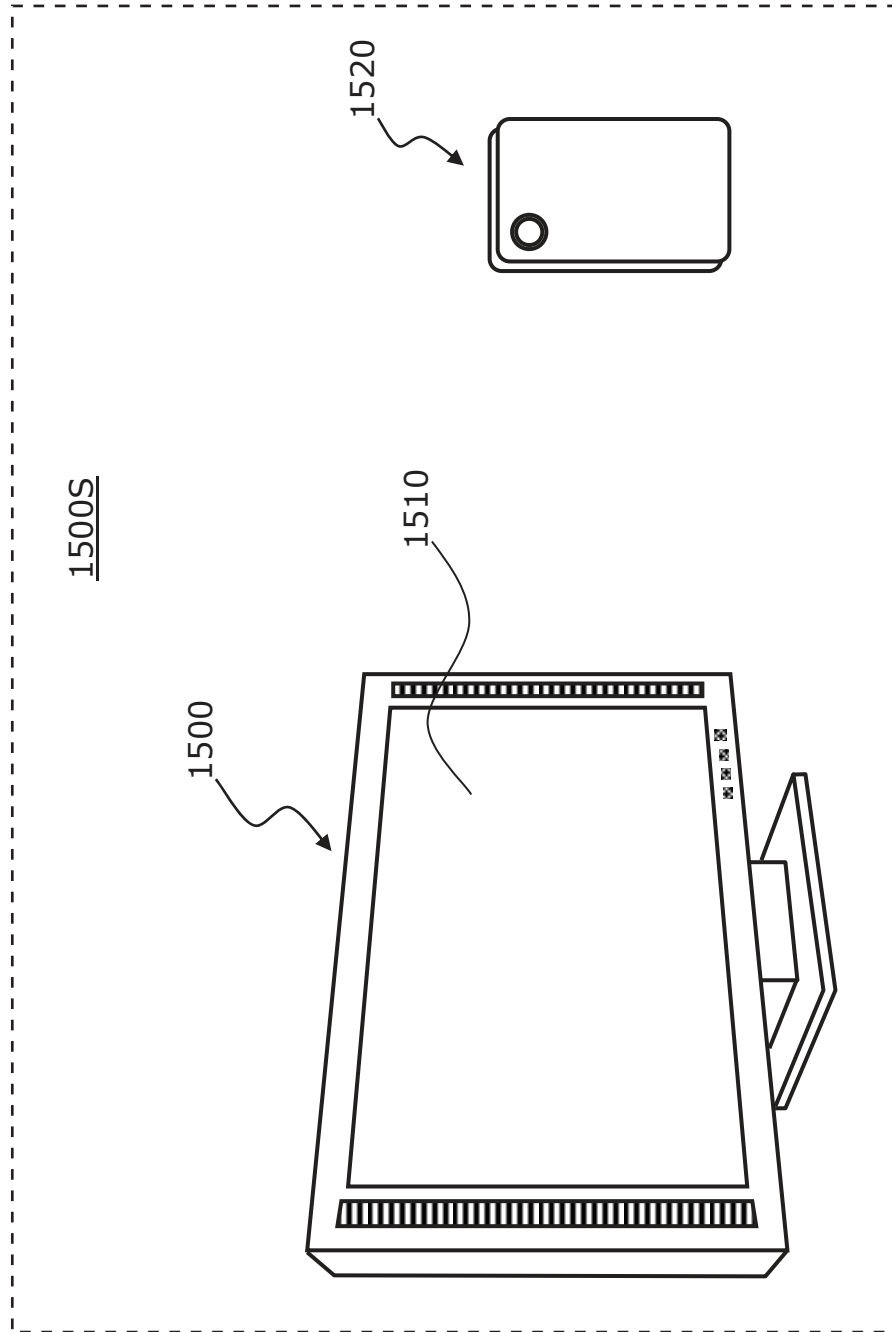


FIG. 358

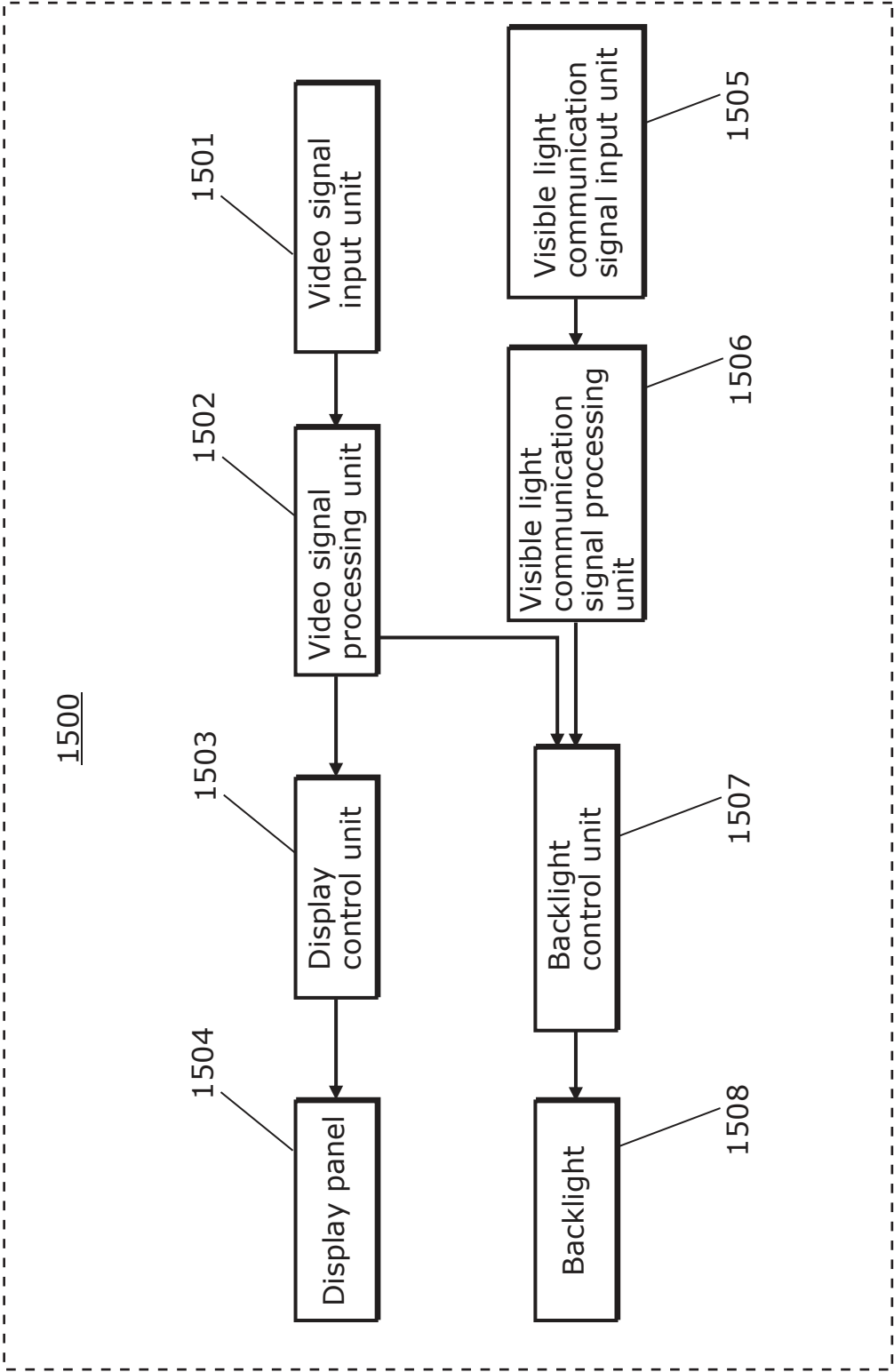


FIG. 359

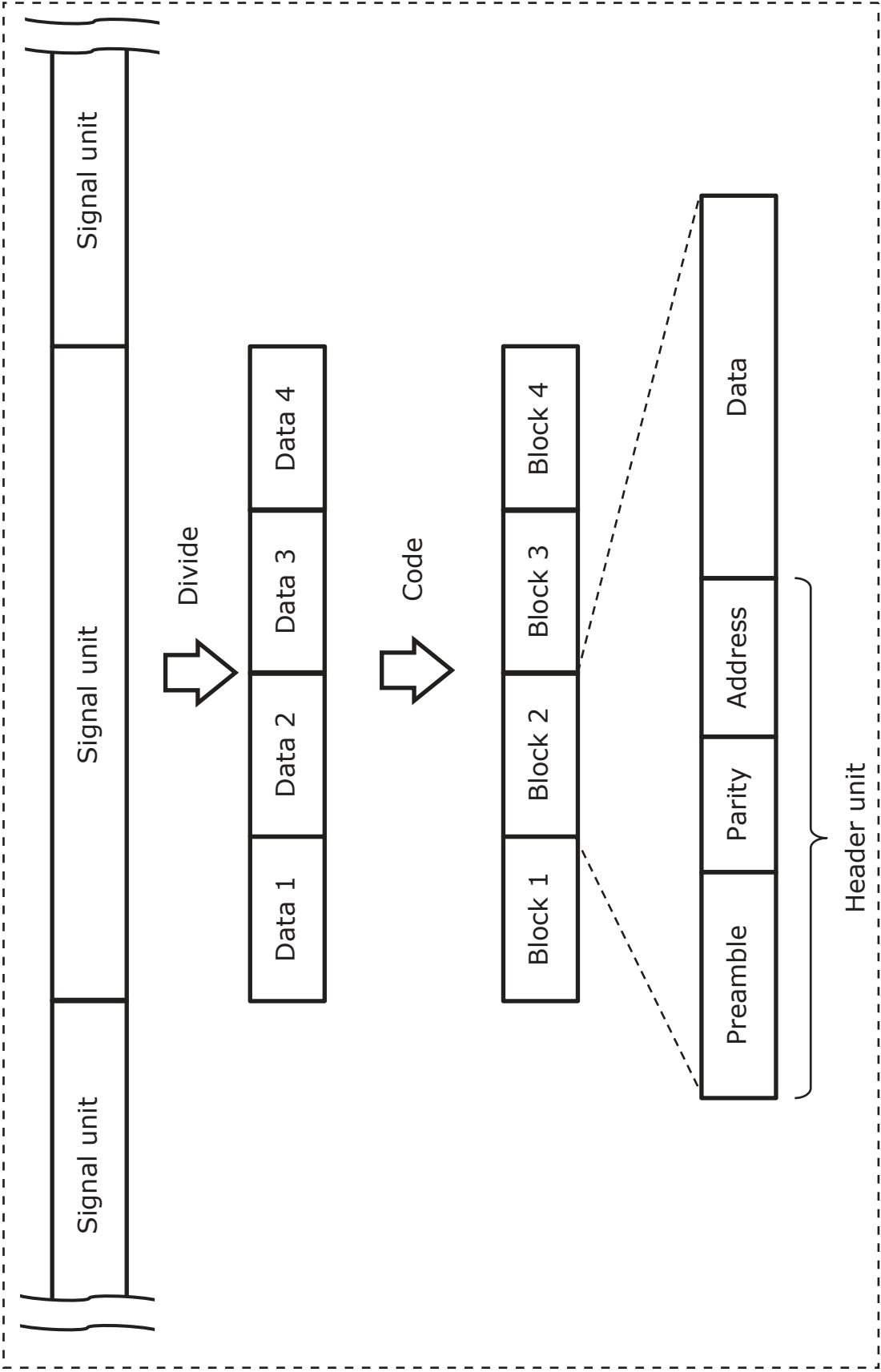


FIG. 360

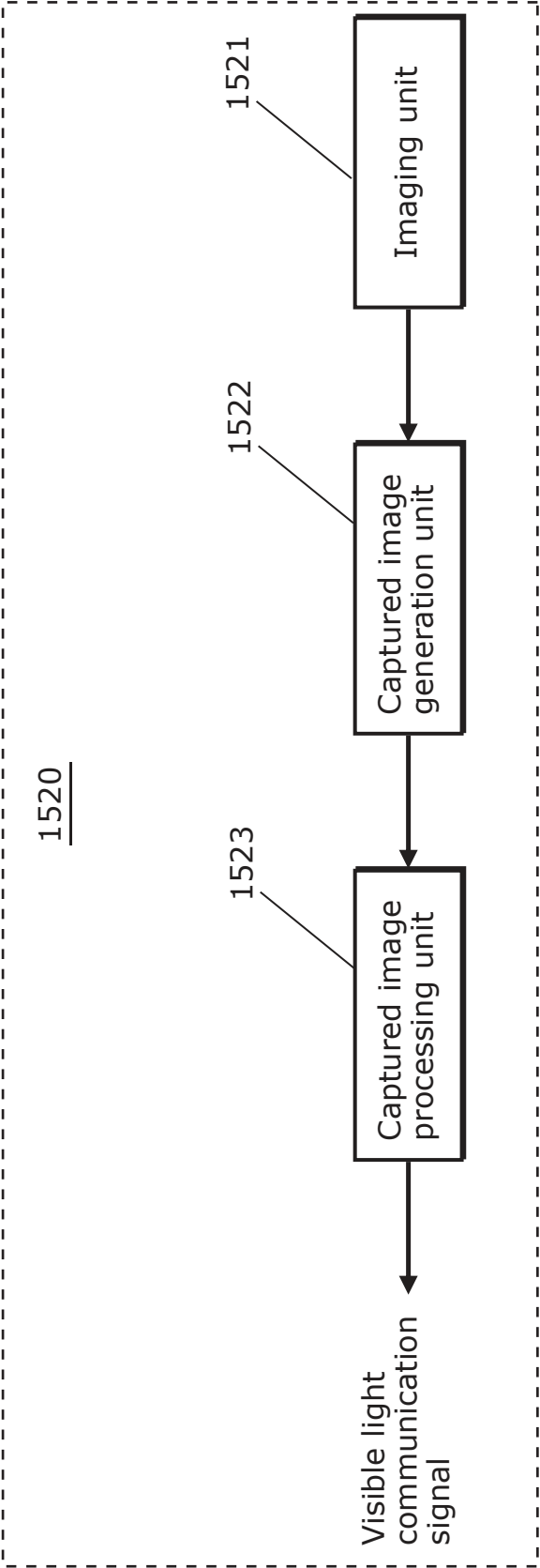


FIG. 361

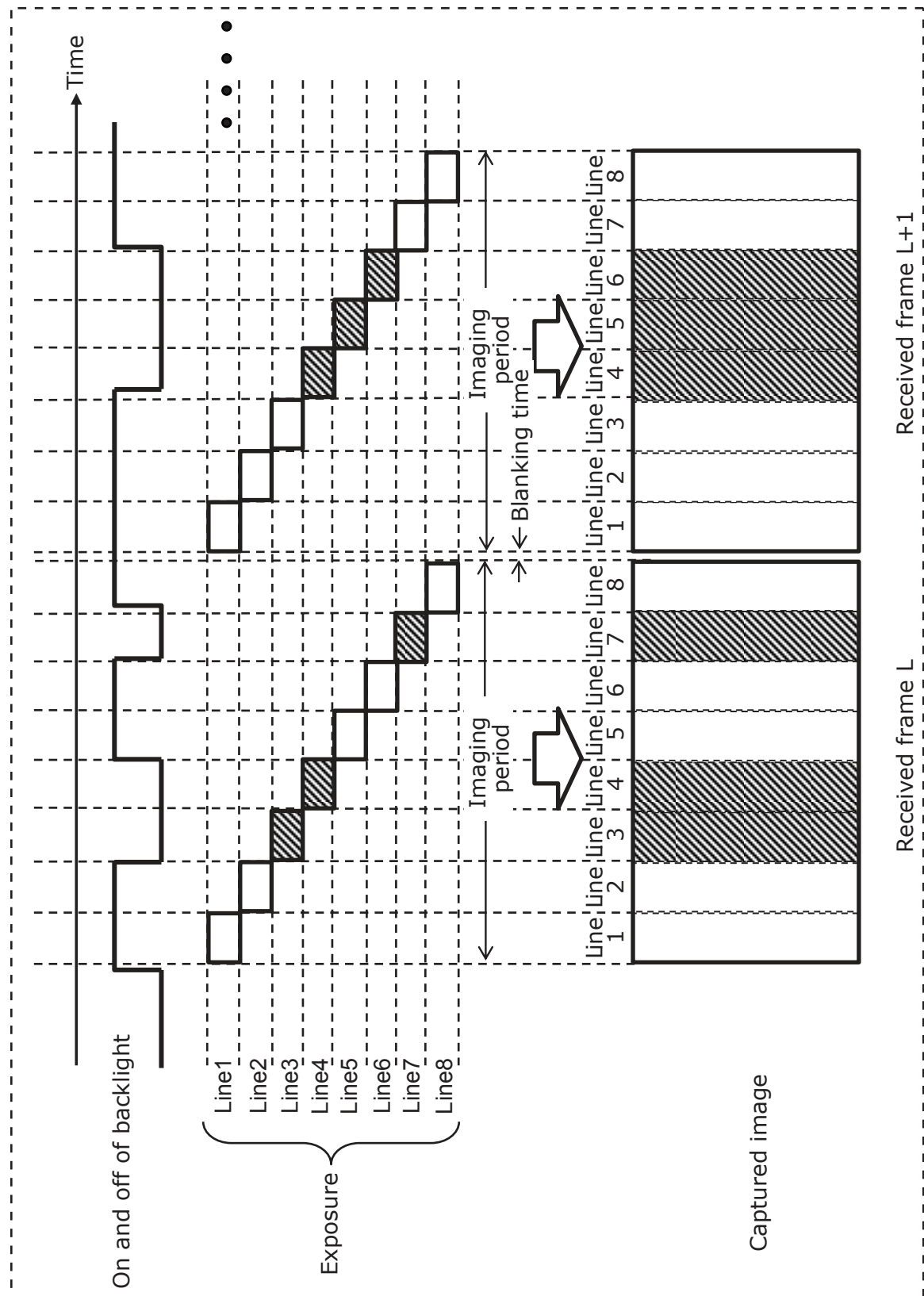


FIG. 362

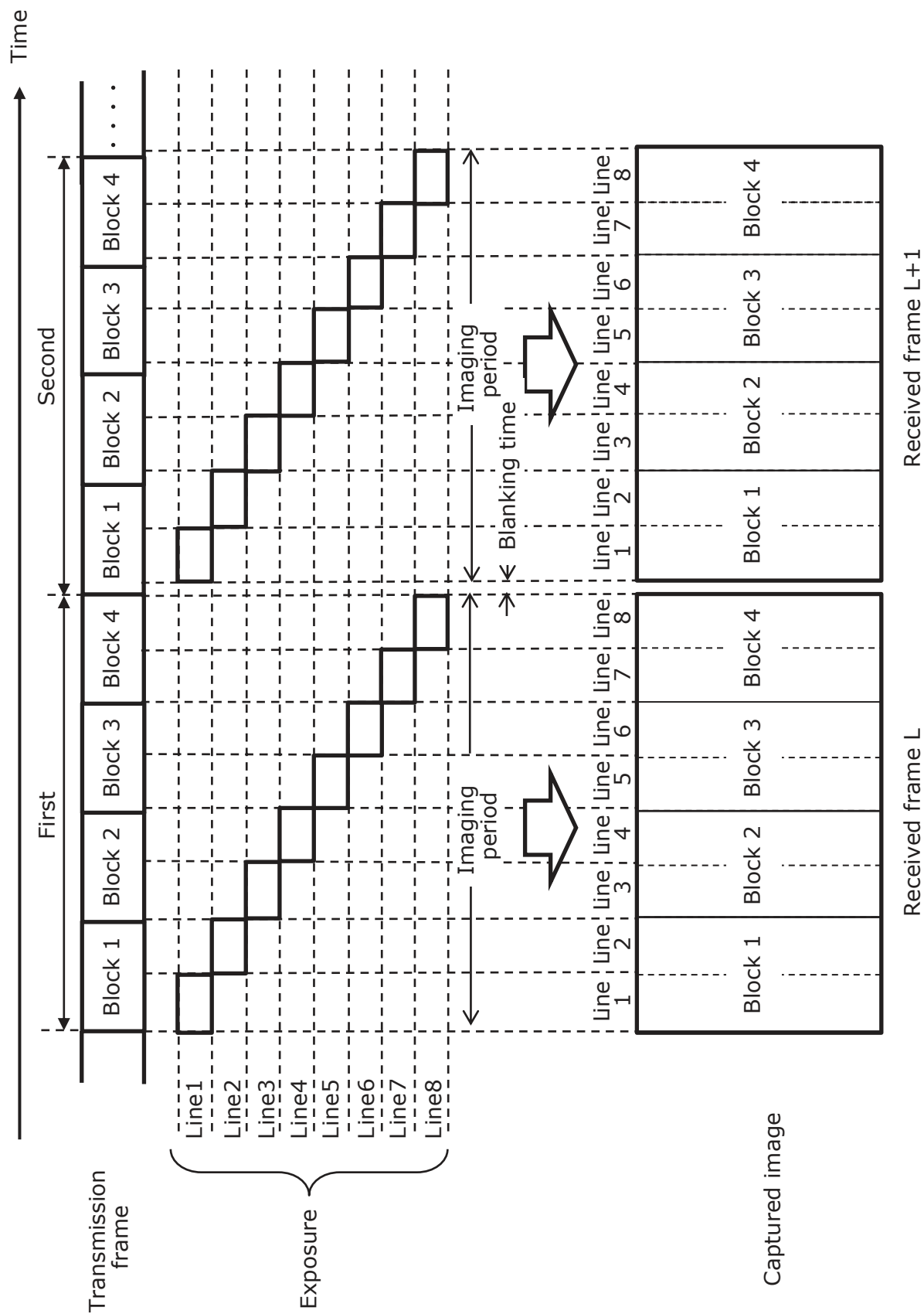


FIG. 363

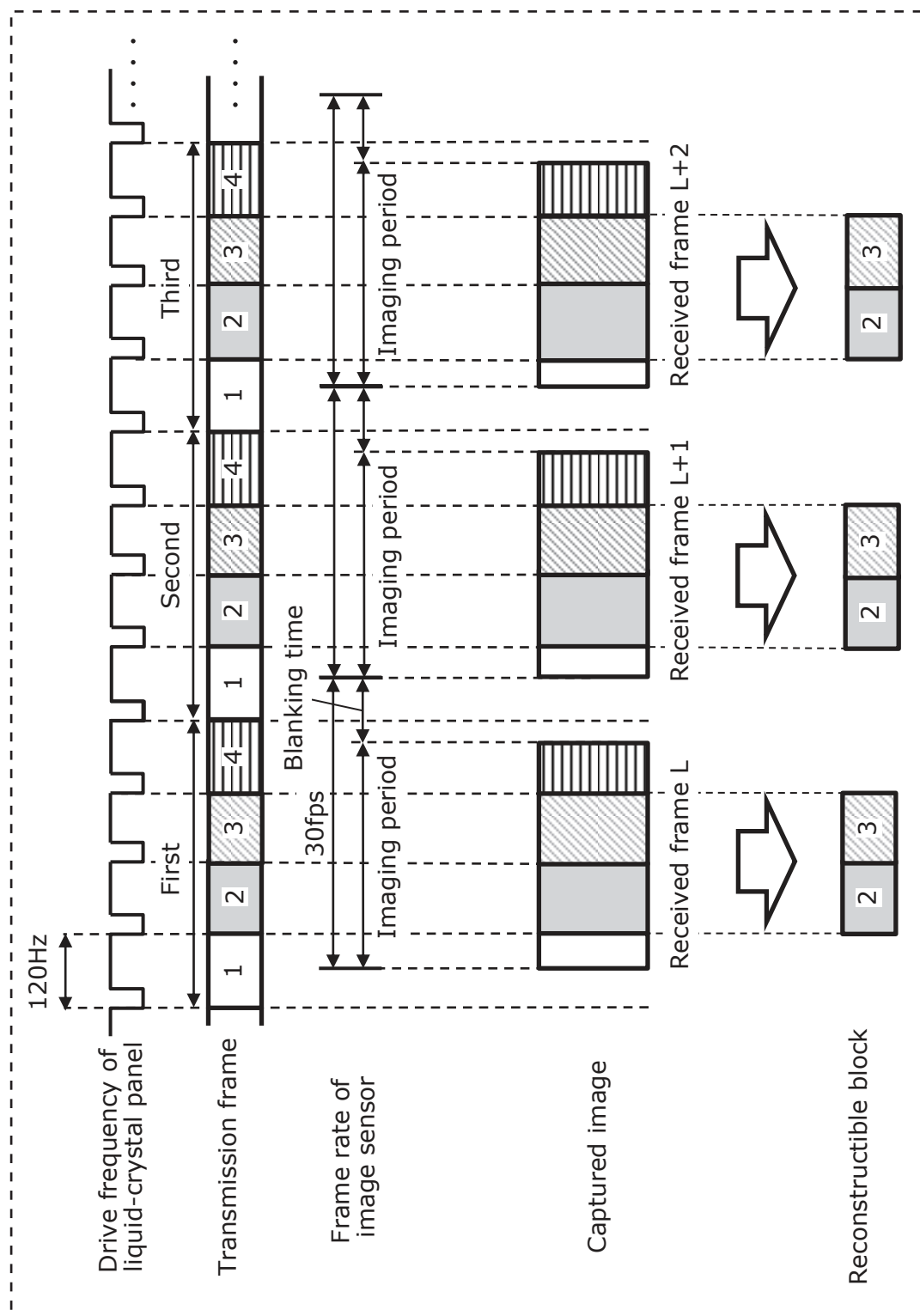


FIG. 364

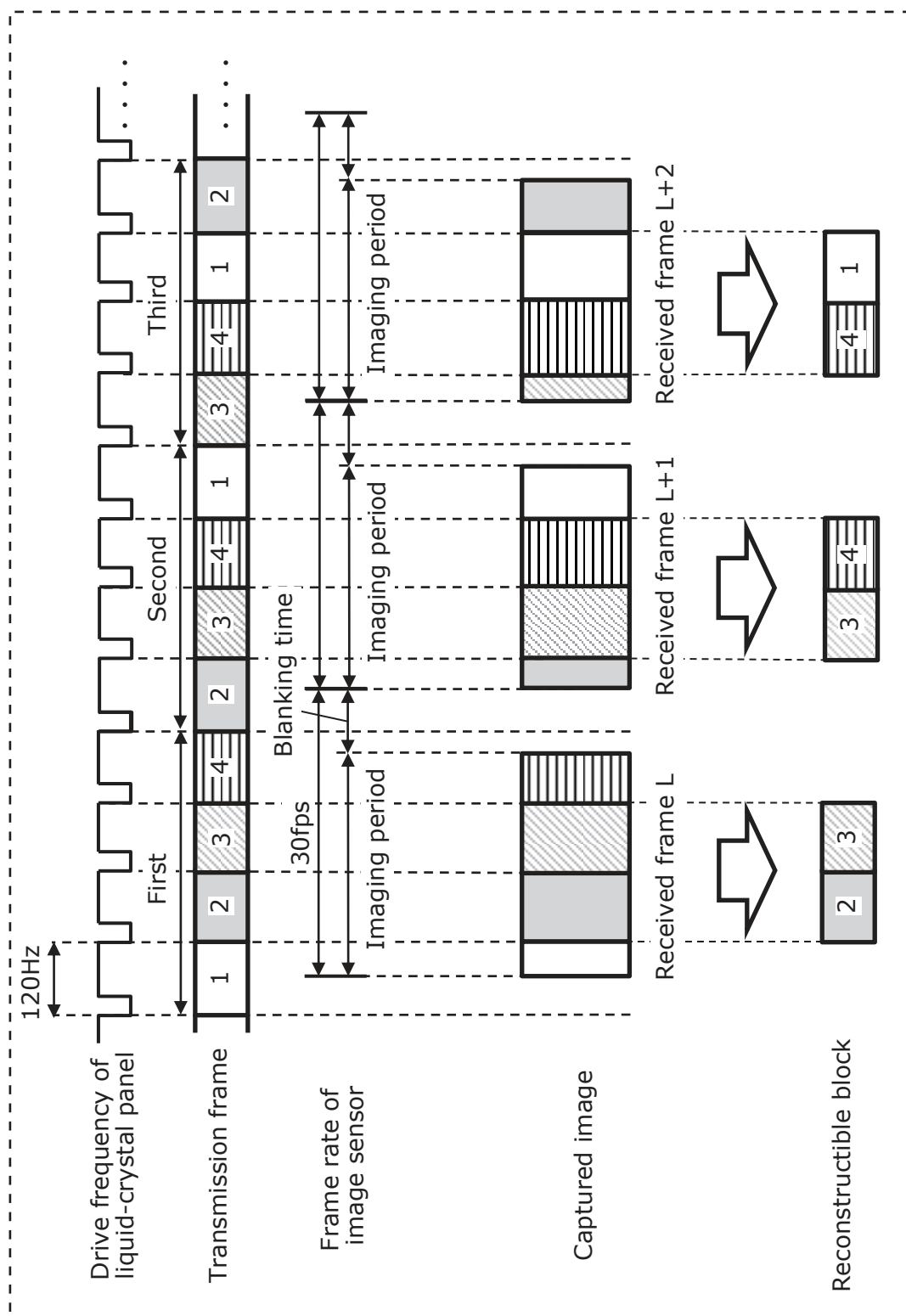


FIG. 365A

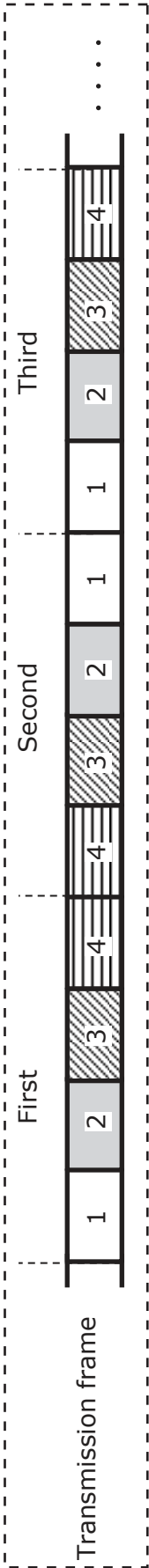


FIG. 365B

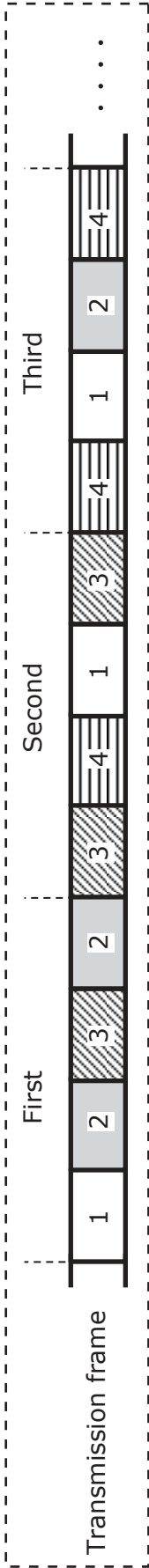


FIG. 365C

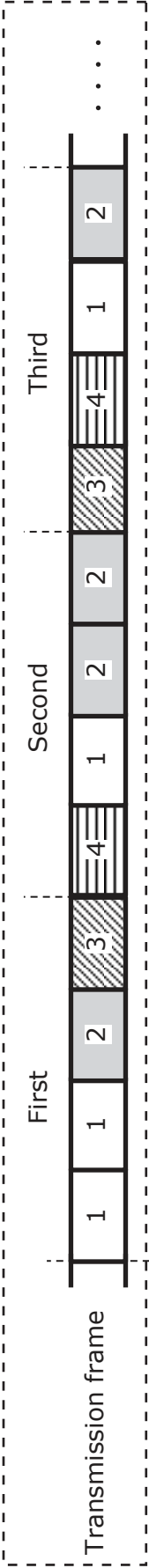


FIG. 365D

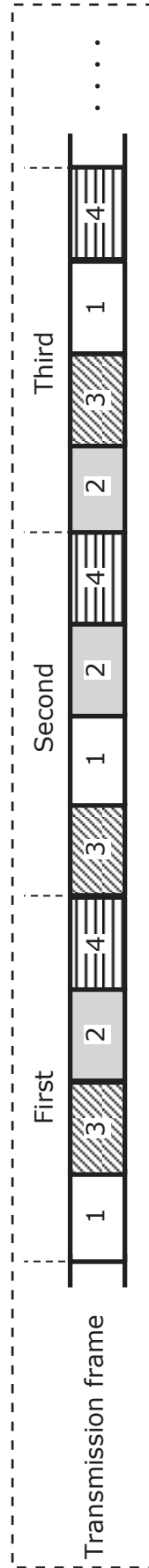


FIG. 365E

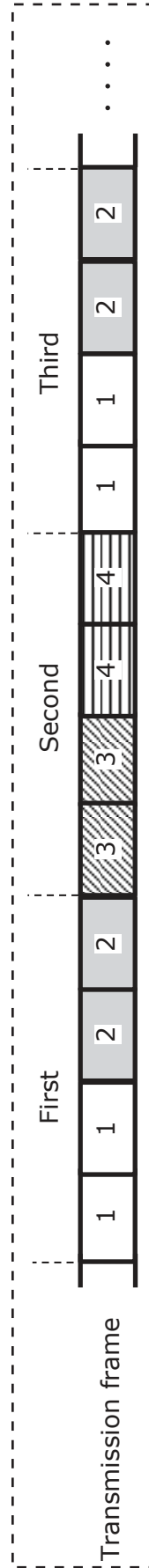


FIG. 366

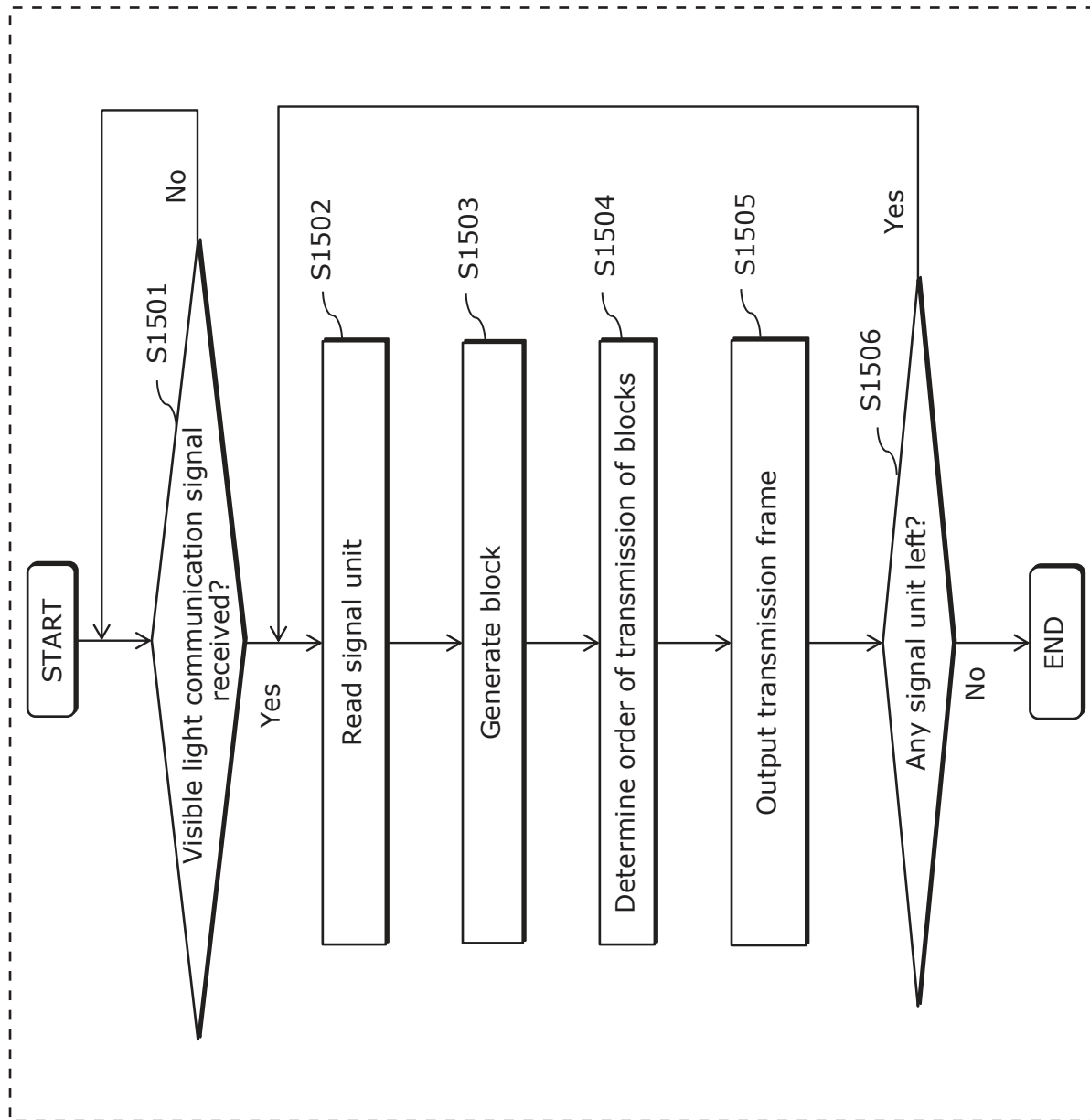


FIG. 367

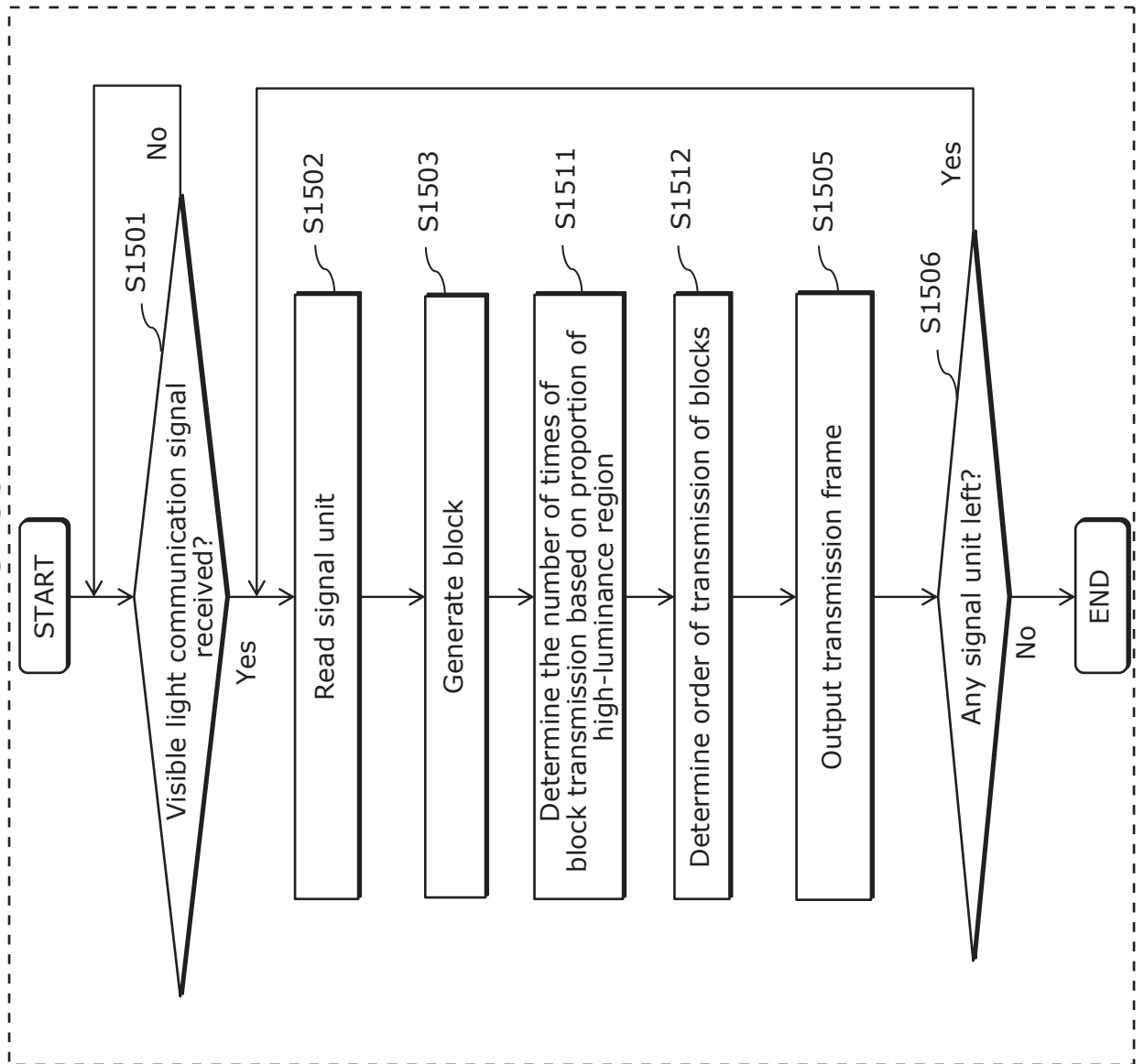


FIG. 368

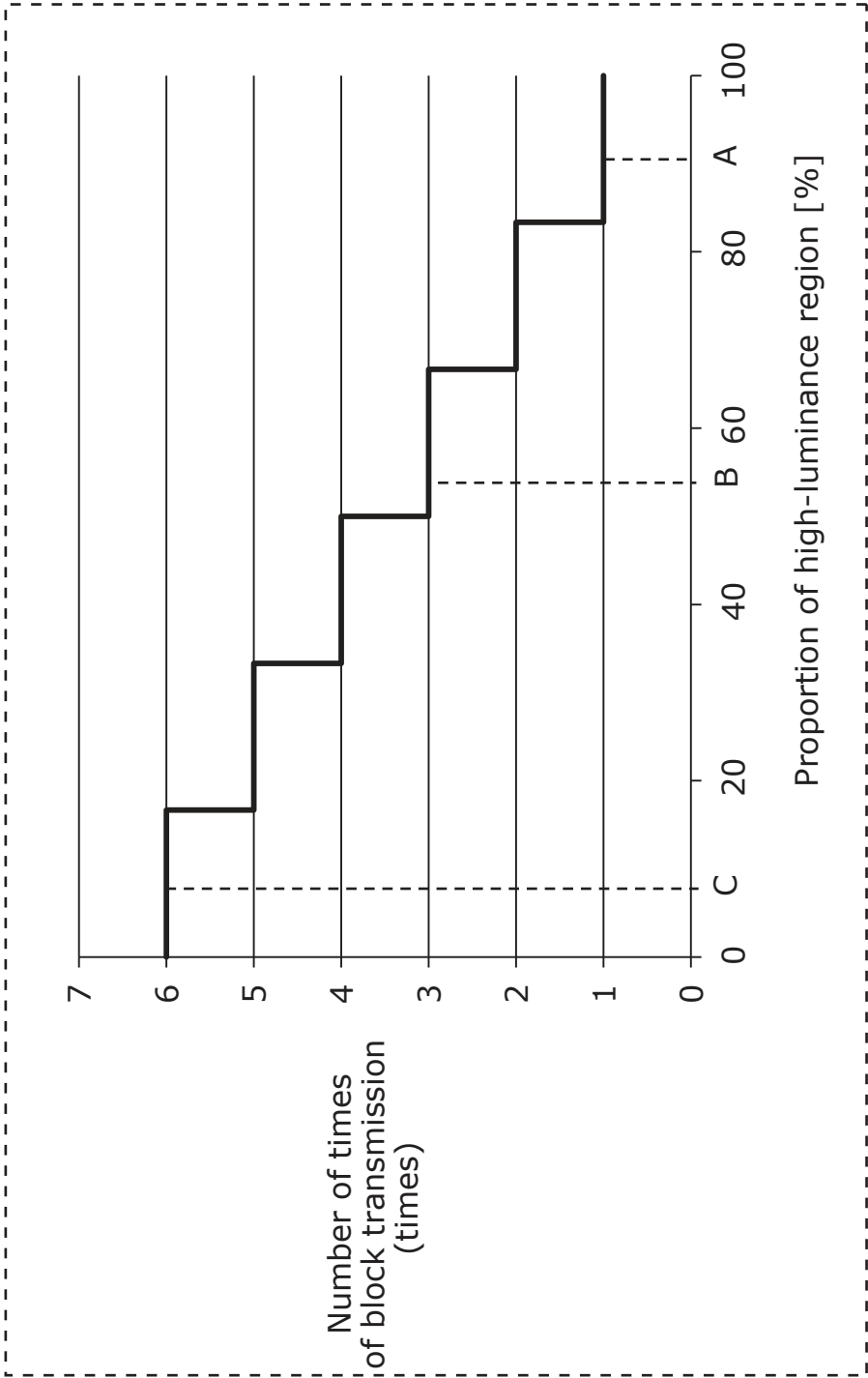


FIG. 369

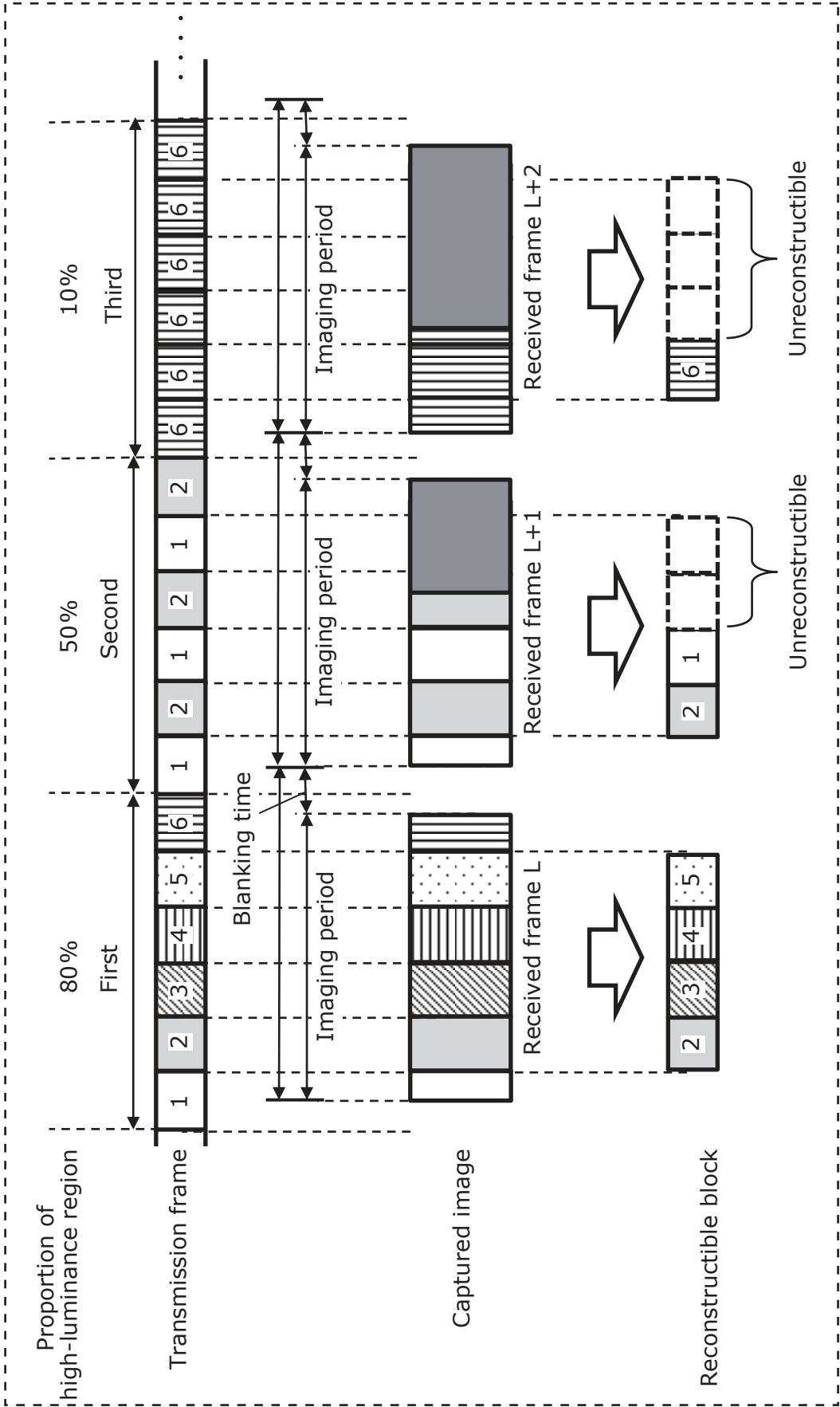


FIG. 370

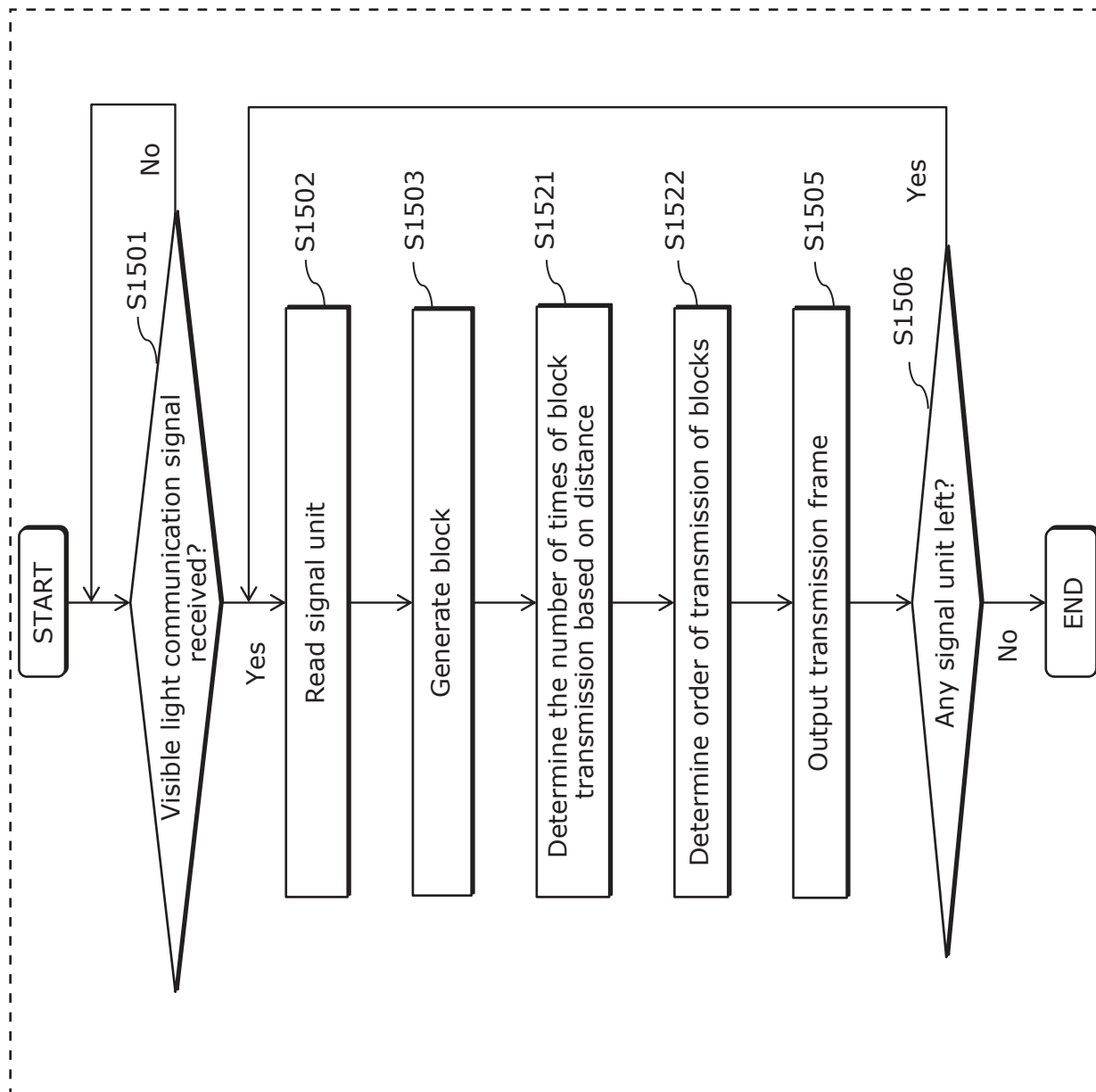


FIG. 371

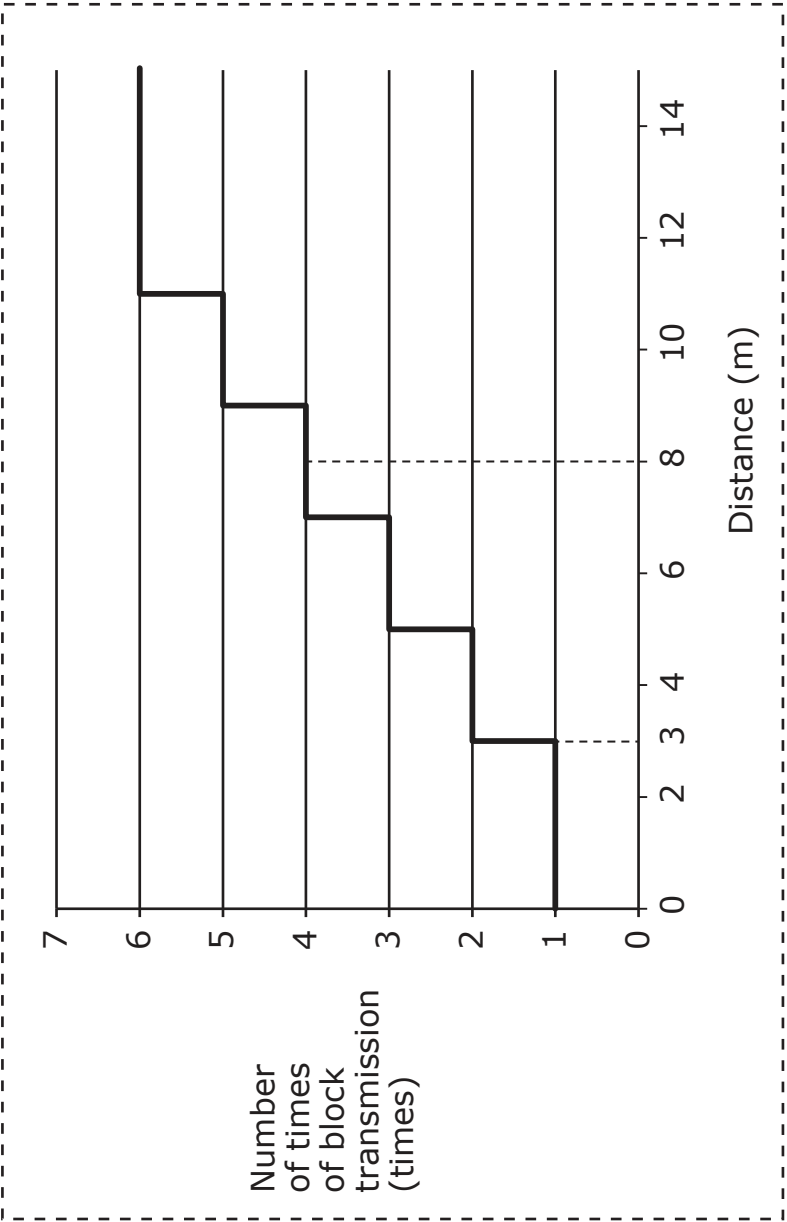


FIG. 373

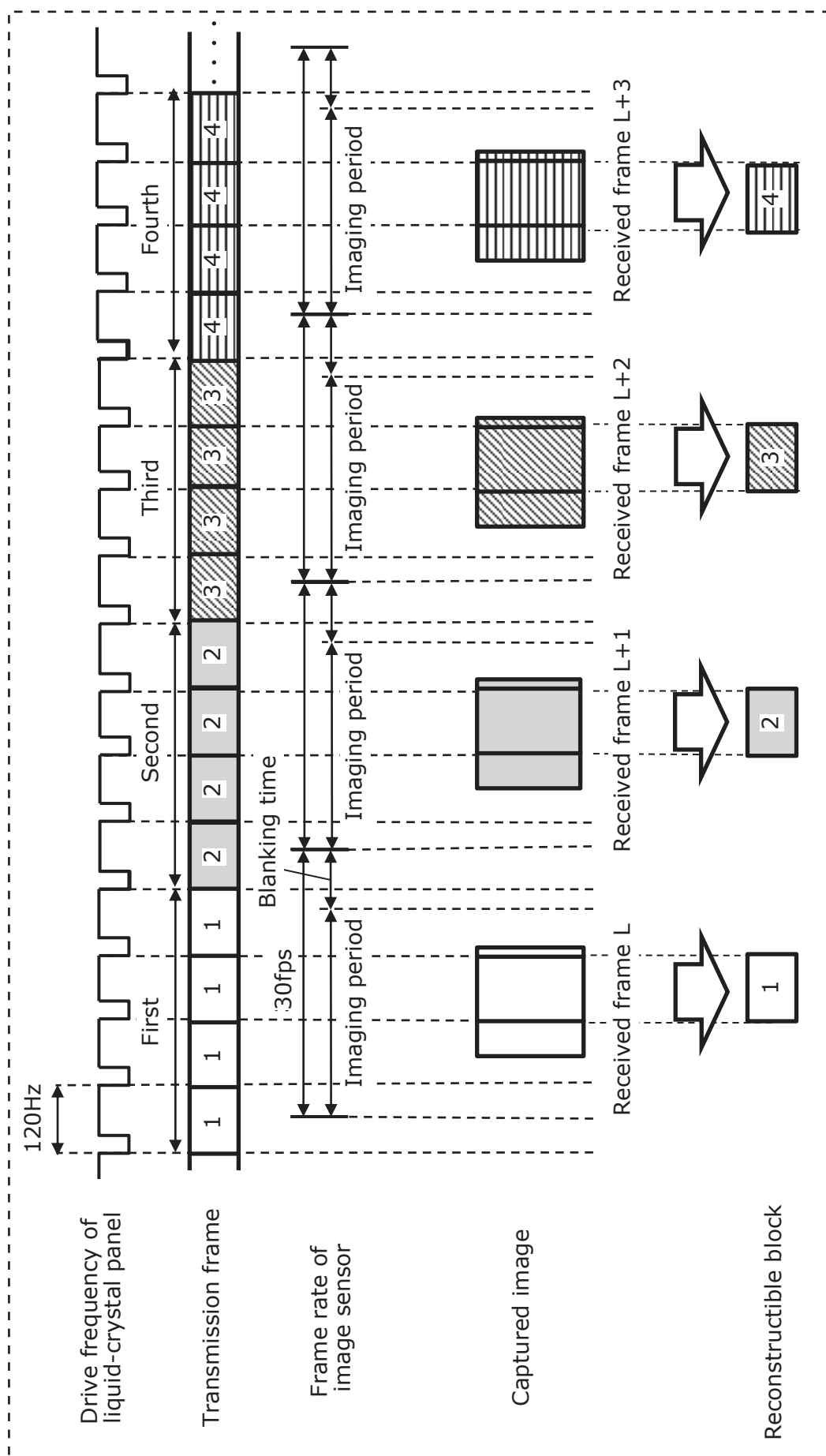


FIG. 374

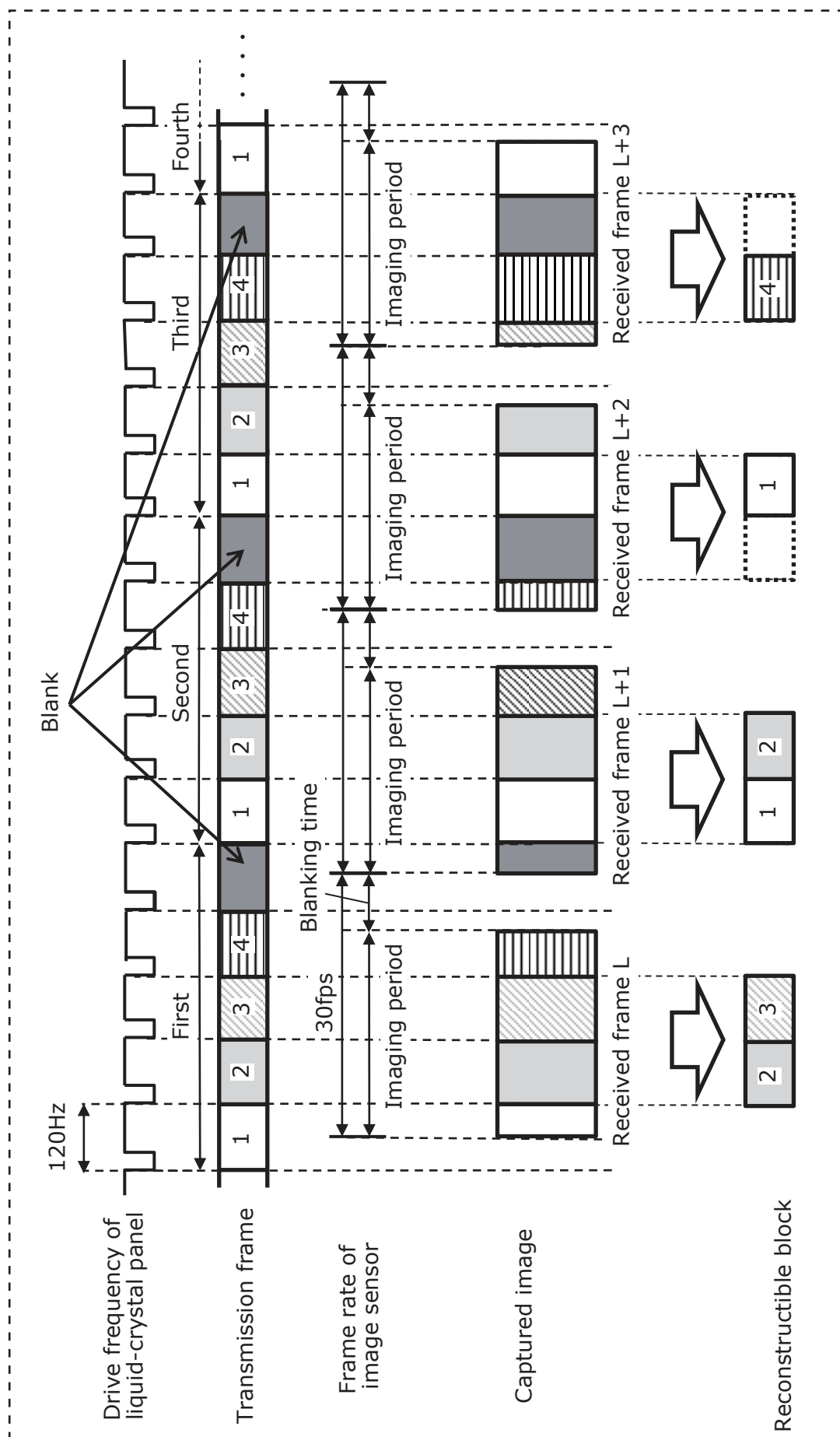


FIG. 375A

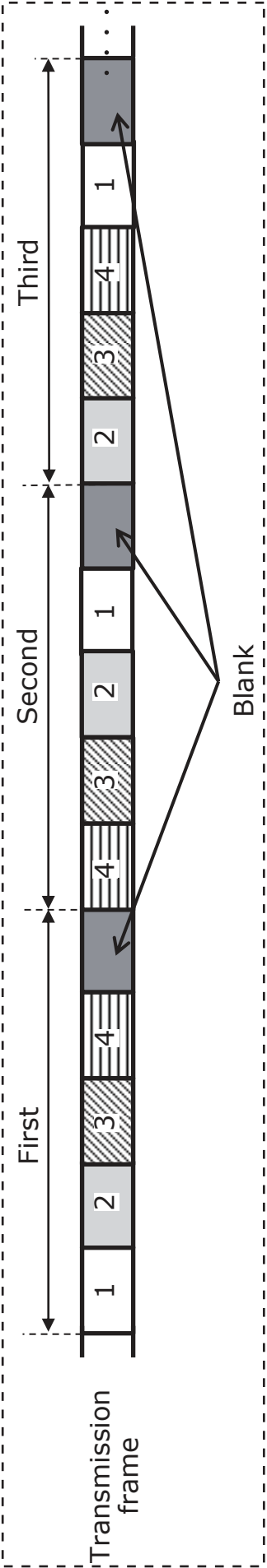


FIG. 375B

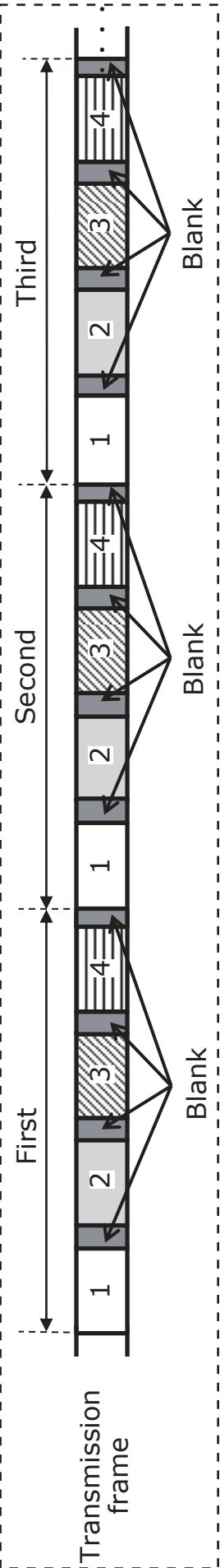


FIG. 375C

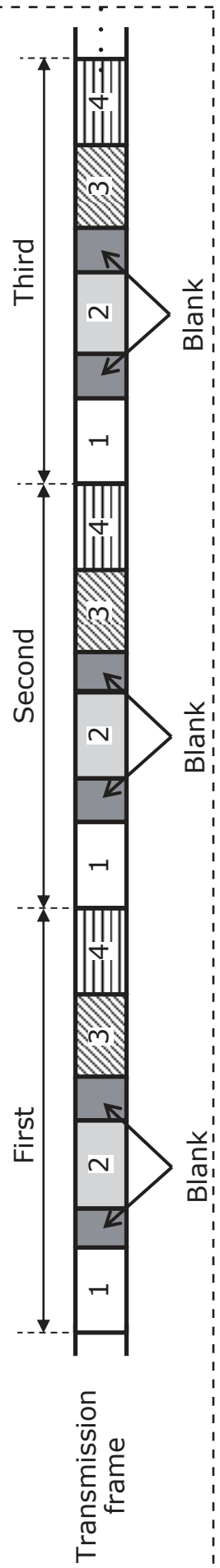


FIG. 376

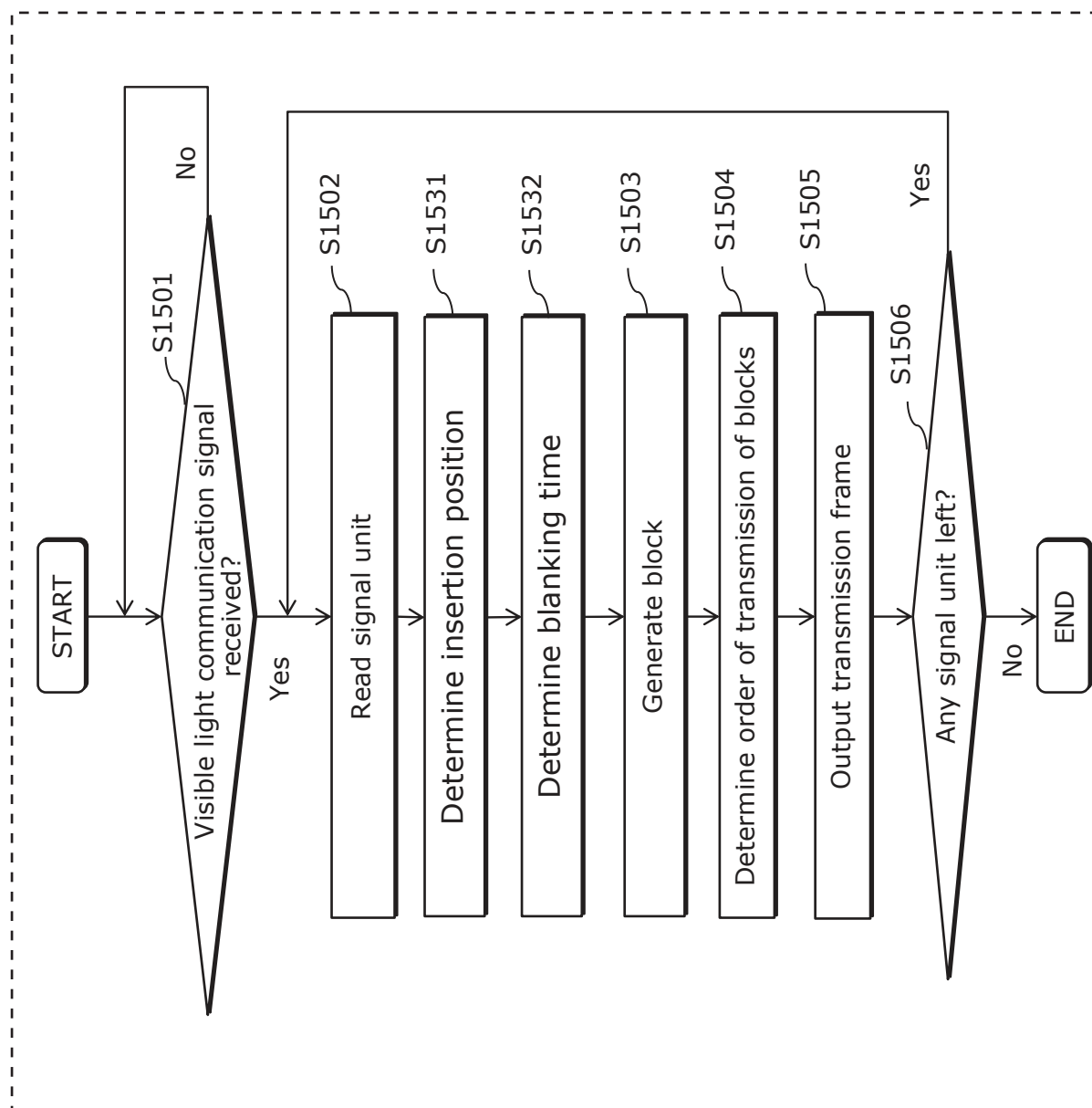


FIG. 377

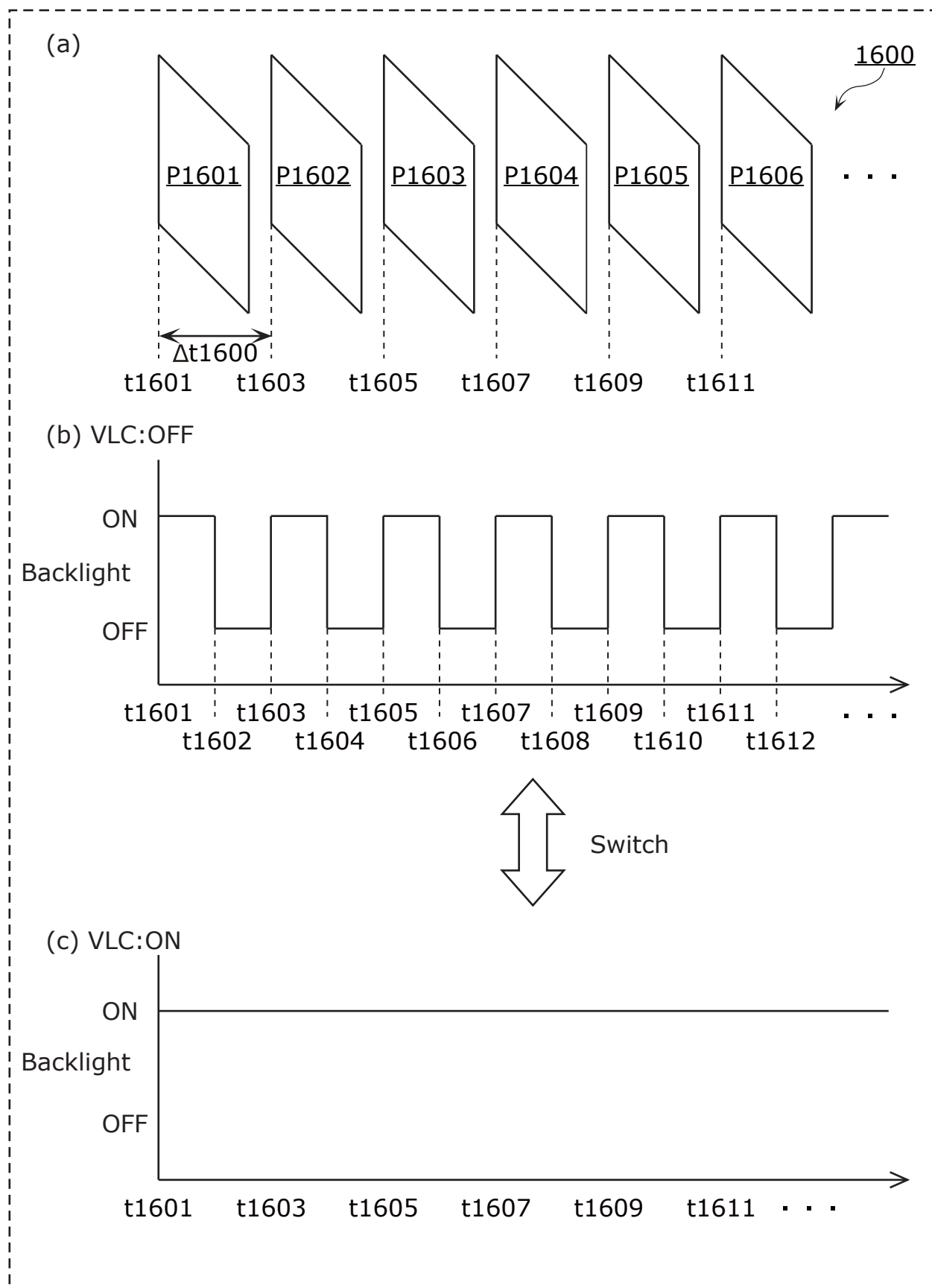


FIG. 378

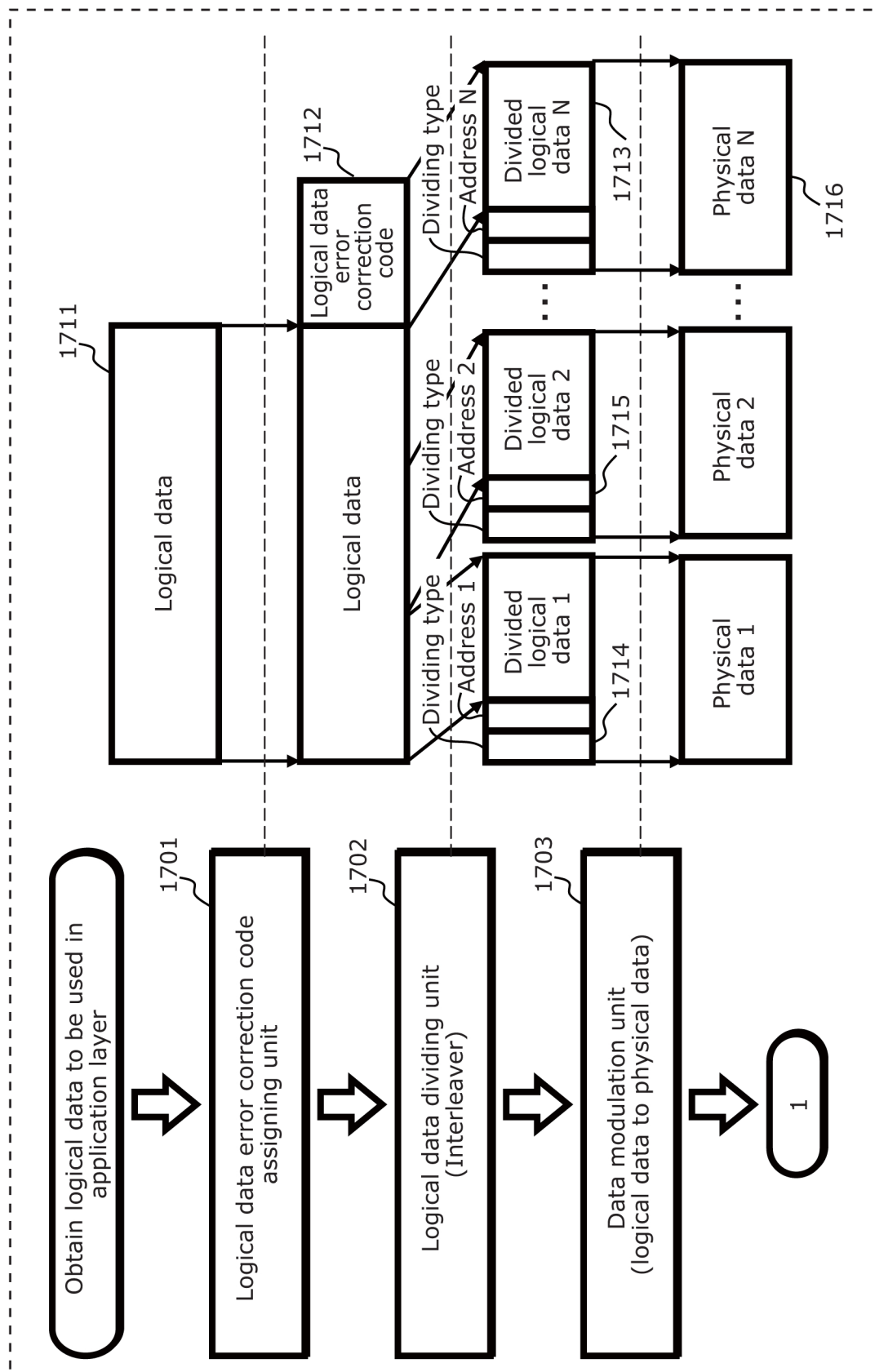


FIG. 379

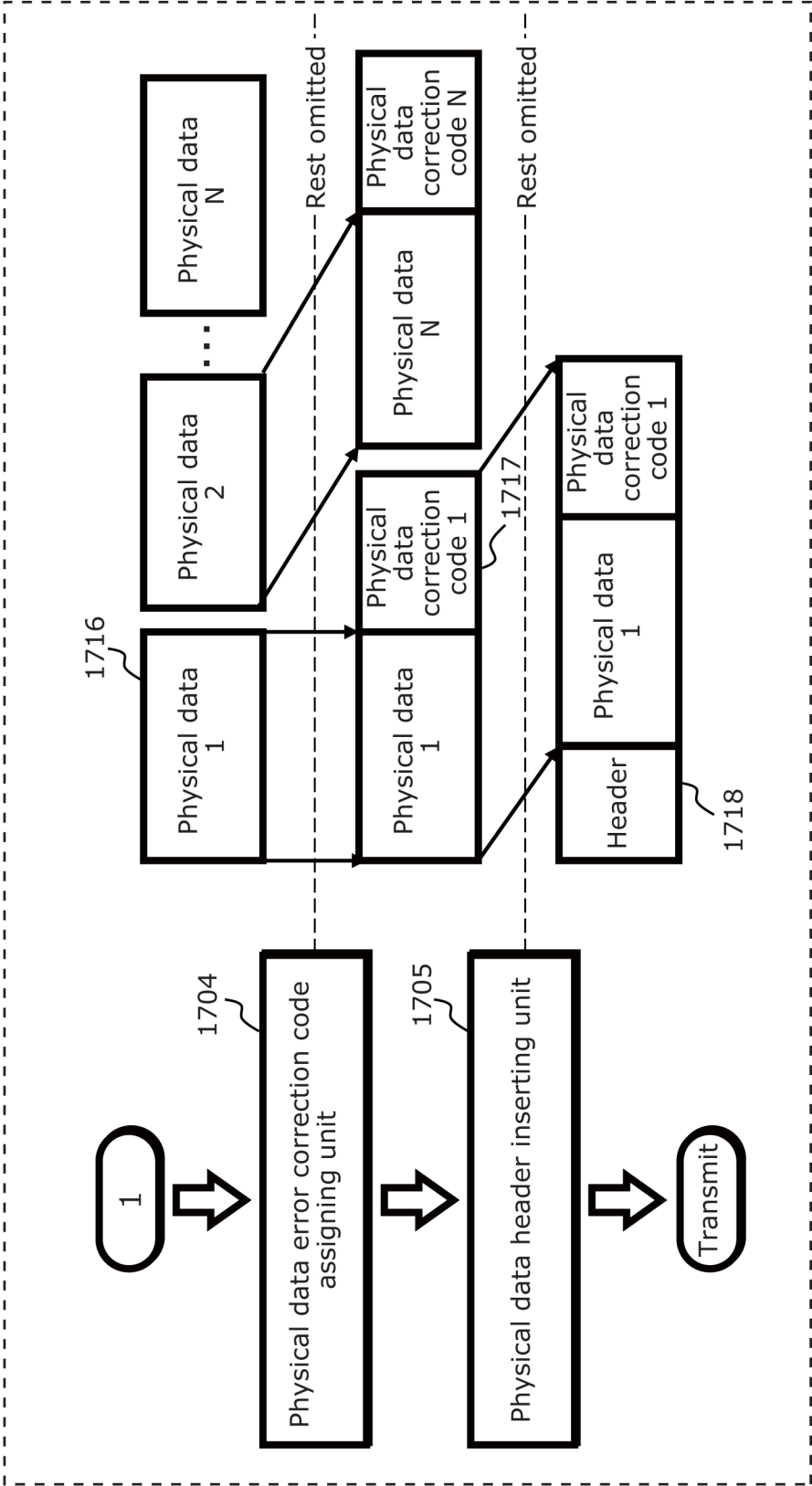
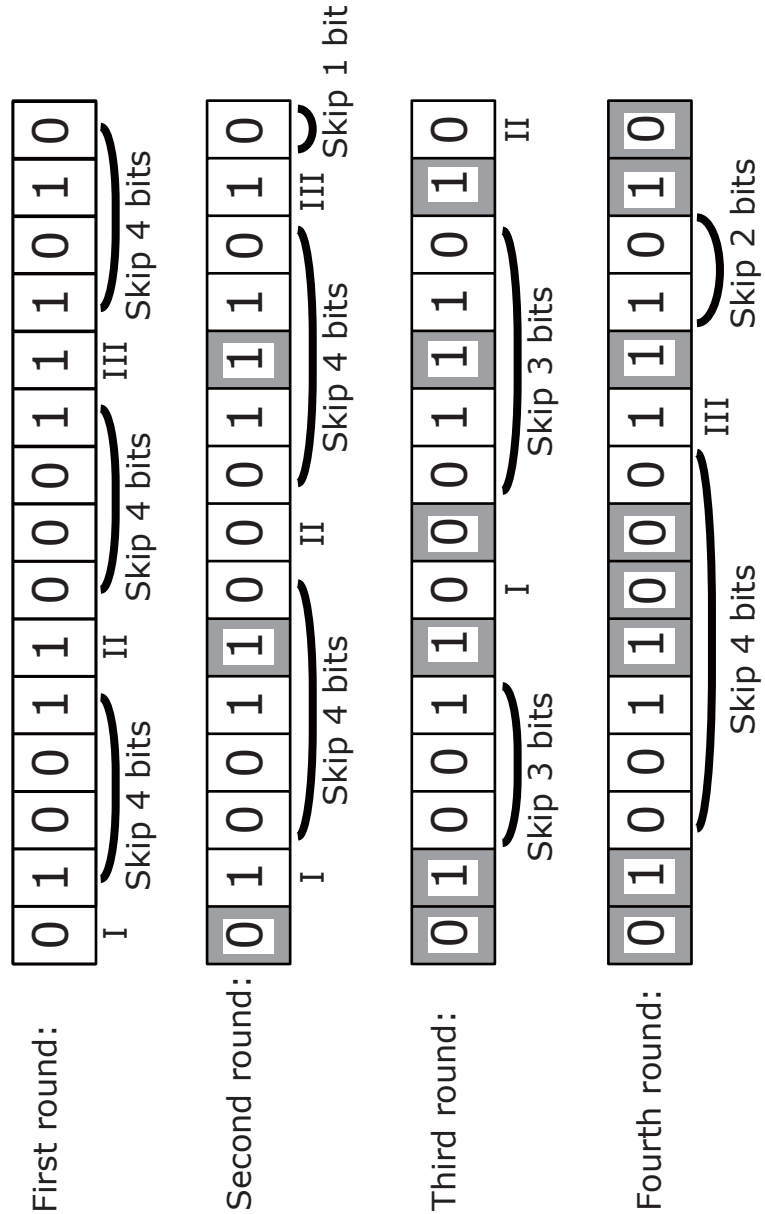


FIG. 381

Where the logical data is 010011000111010, the number of divisions is 3, and the number of skips is 4



Divided logical data I: 01010
Divided logical data II: 10000
Divided logical data III: 11101

FIG. 382

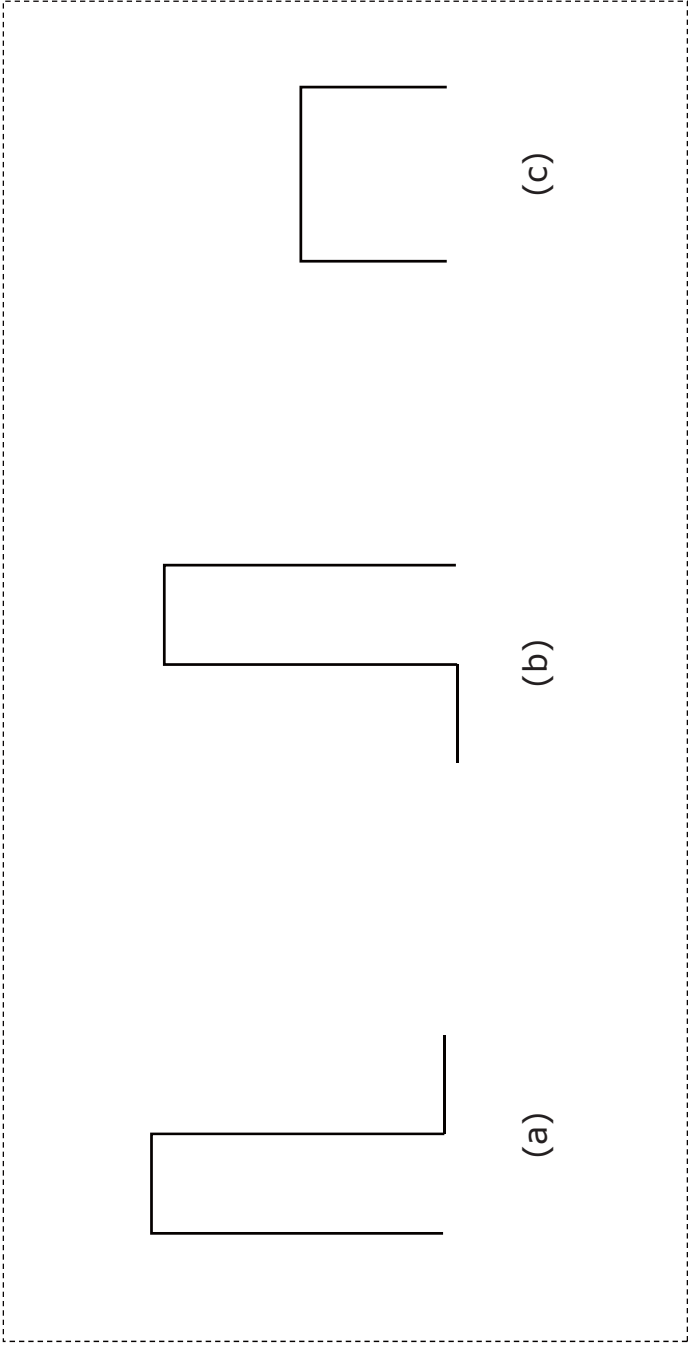


FIG. 383

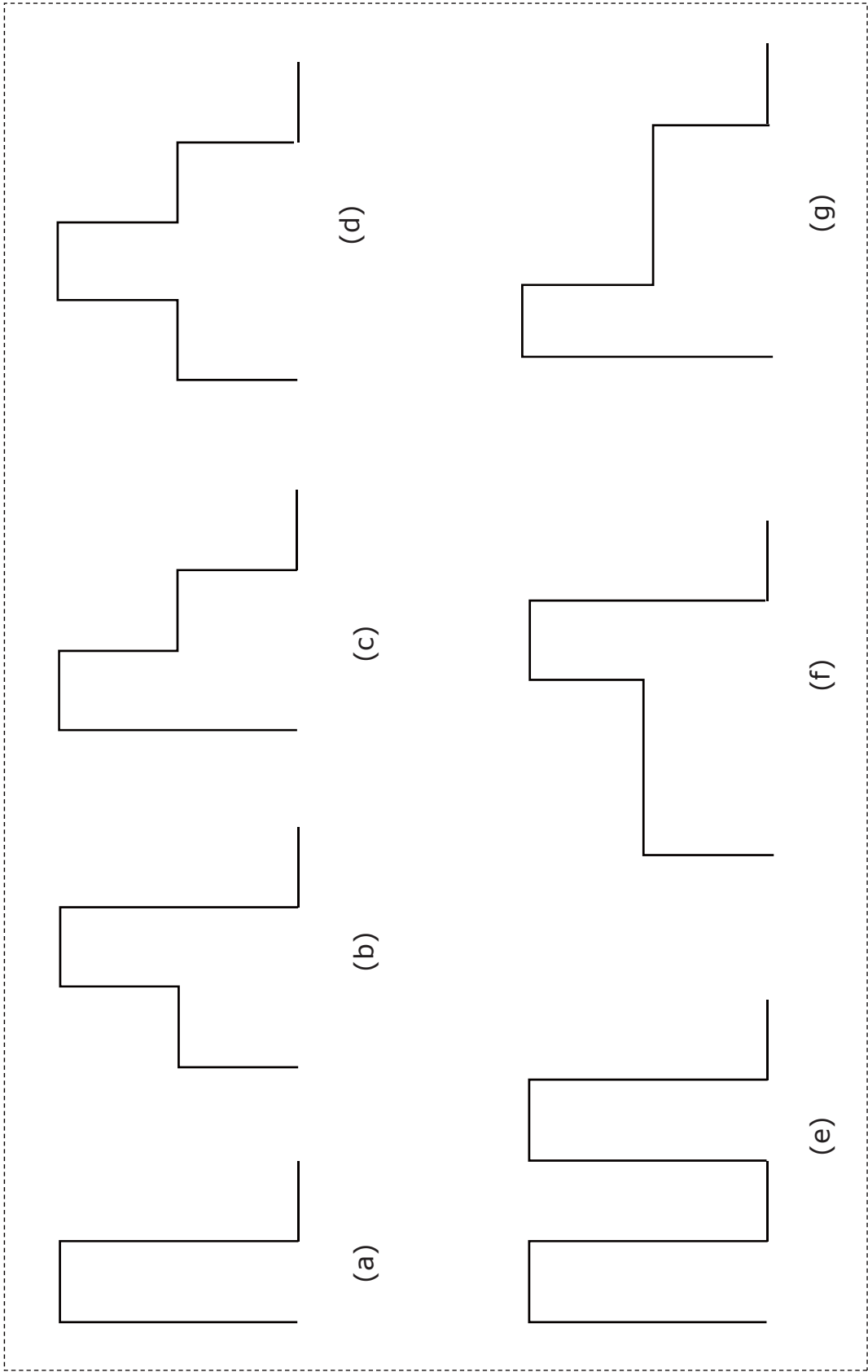


FIG. 384

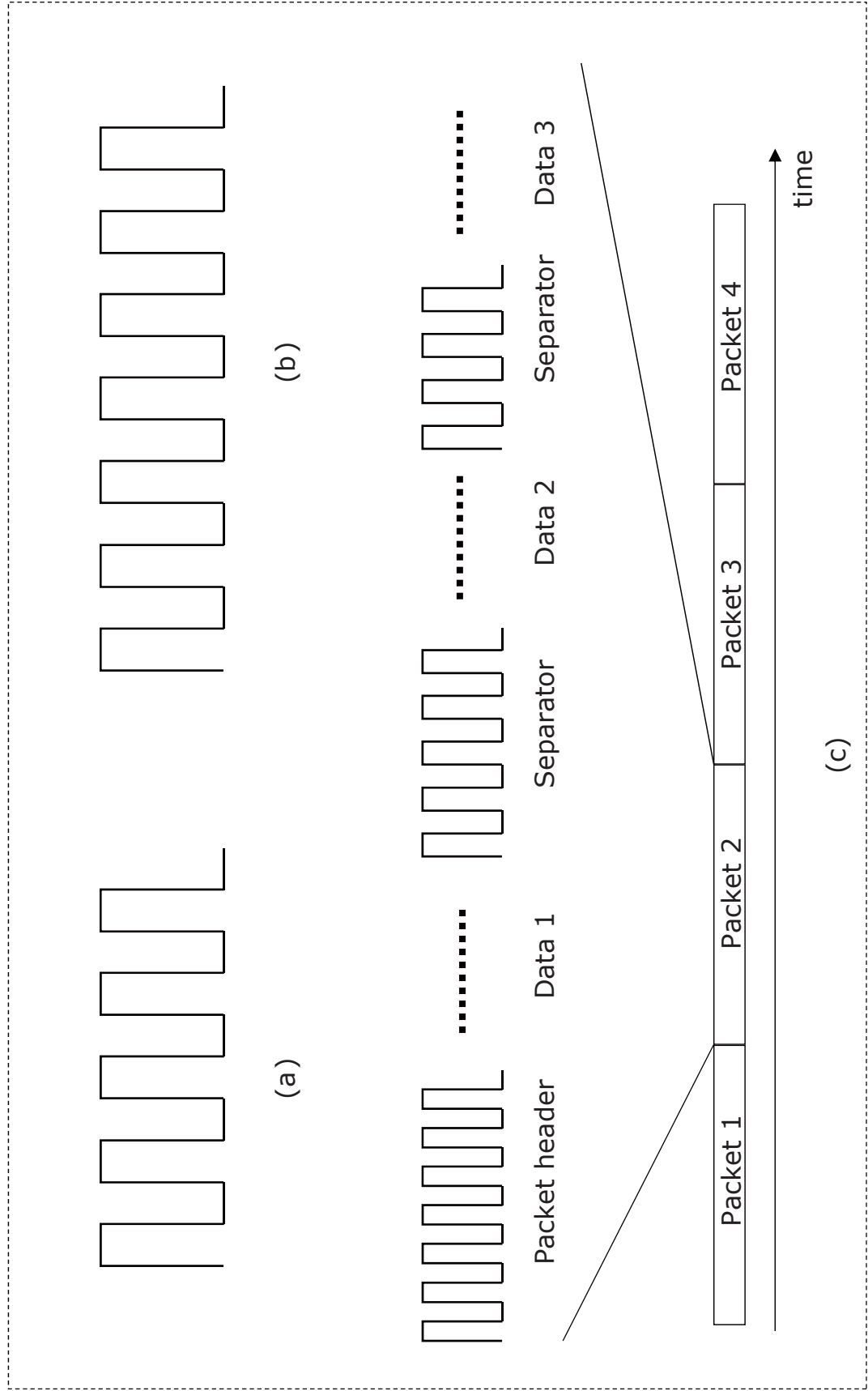


FIG. 385A

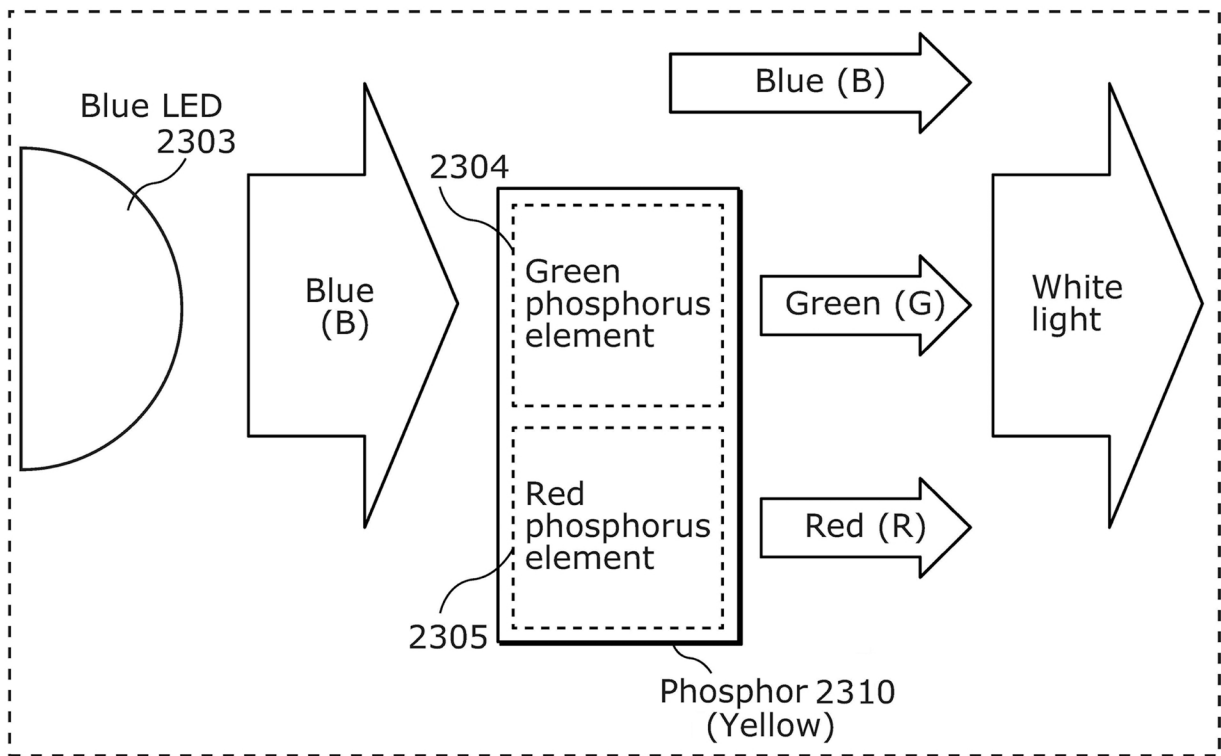


FIG. 385B

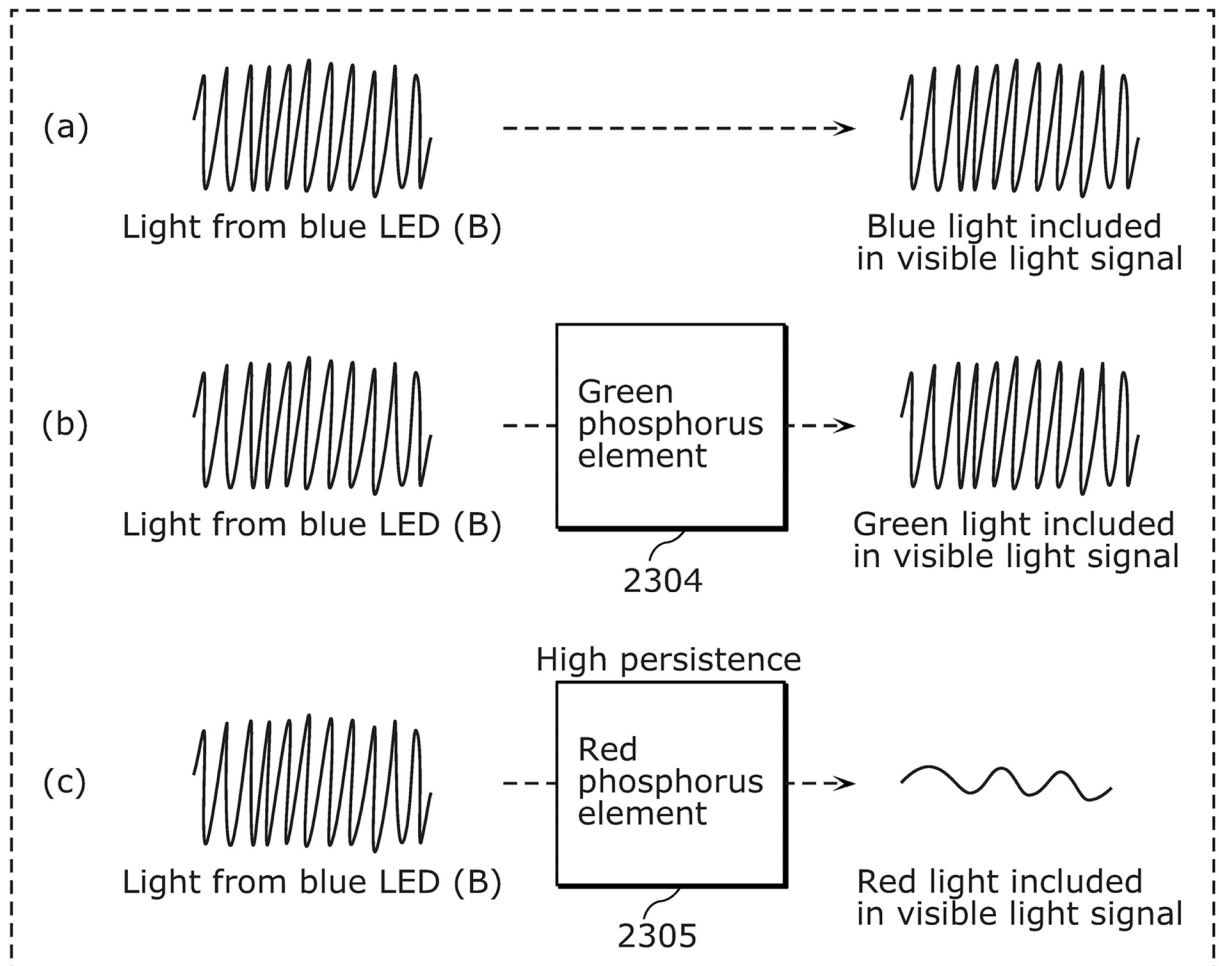


FIG. 386

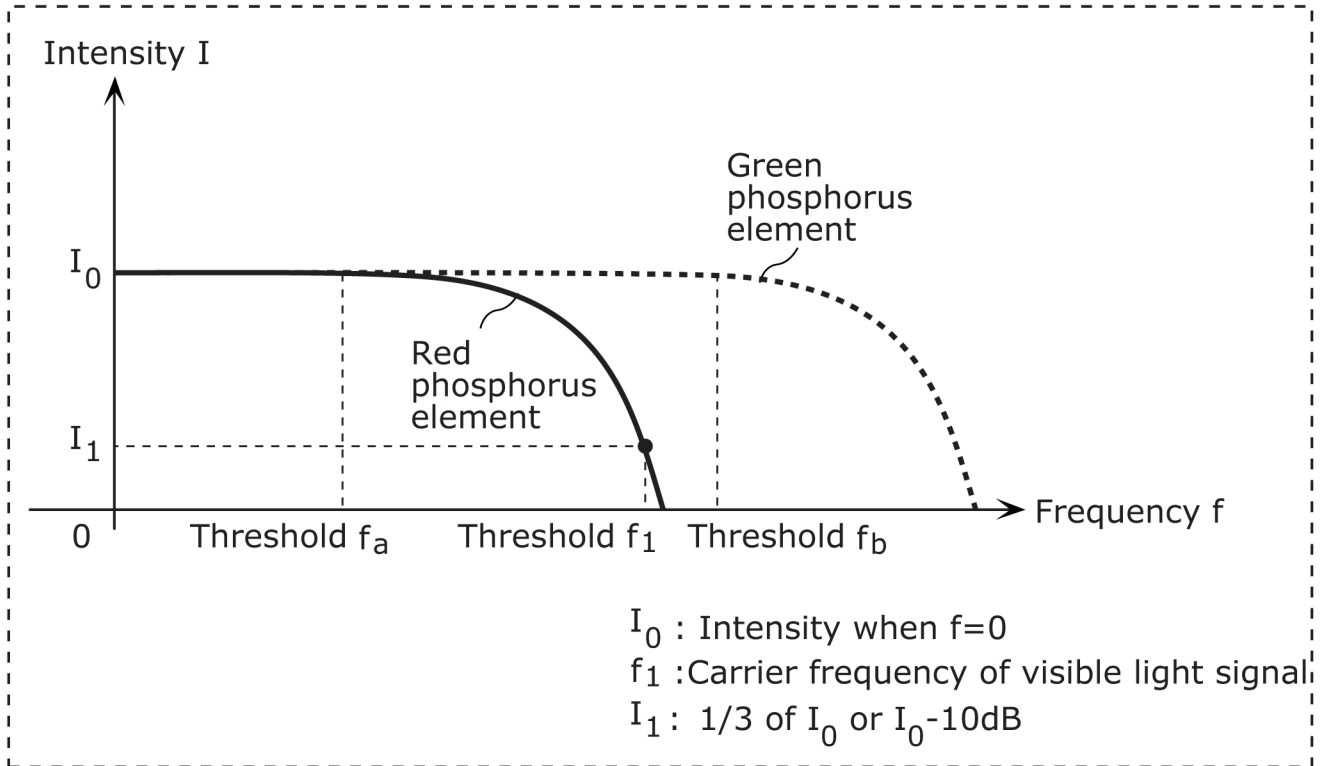


FIG. 387

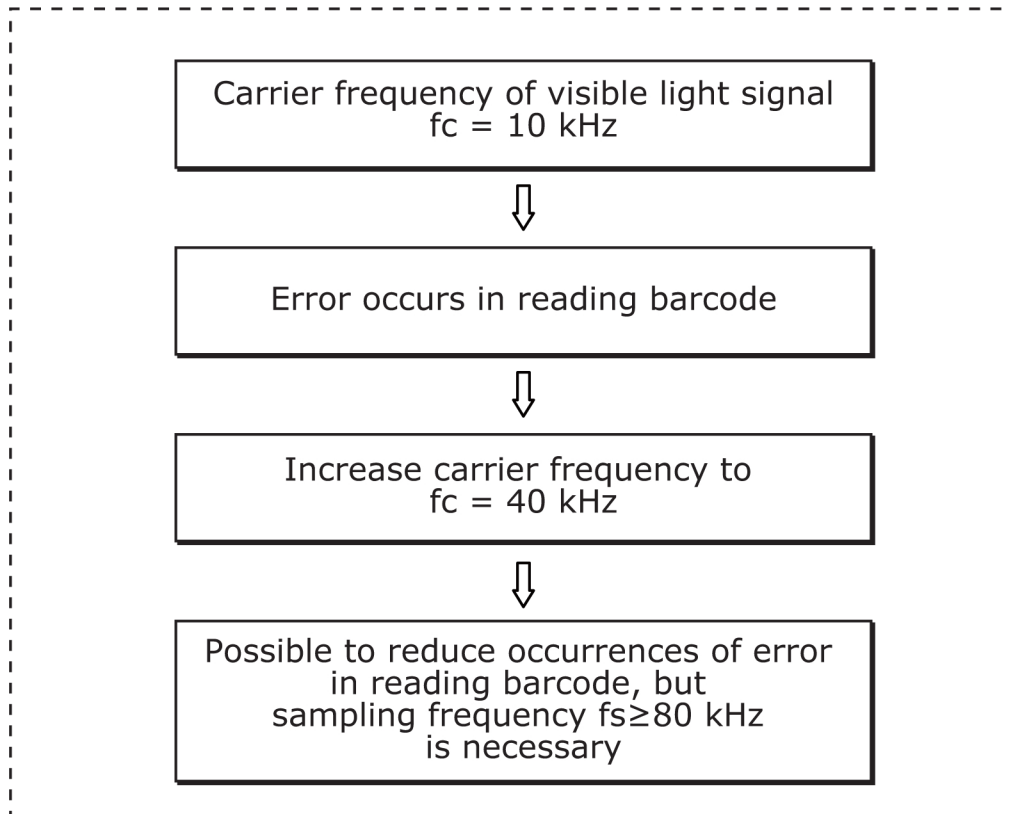


FIG. 388

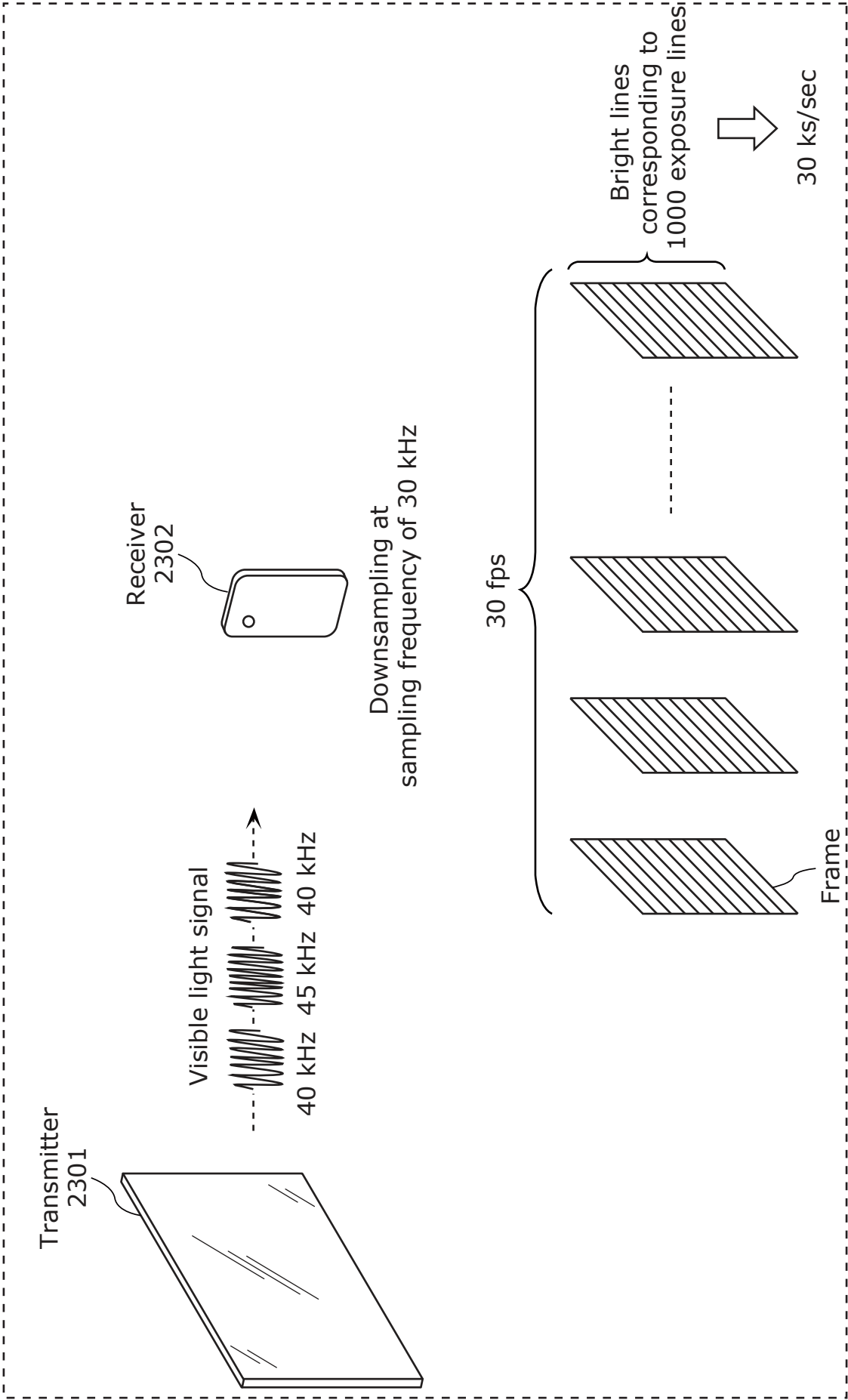


FIG. 389

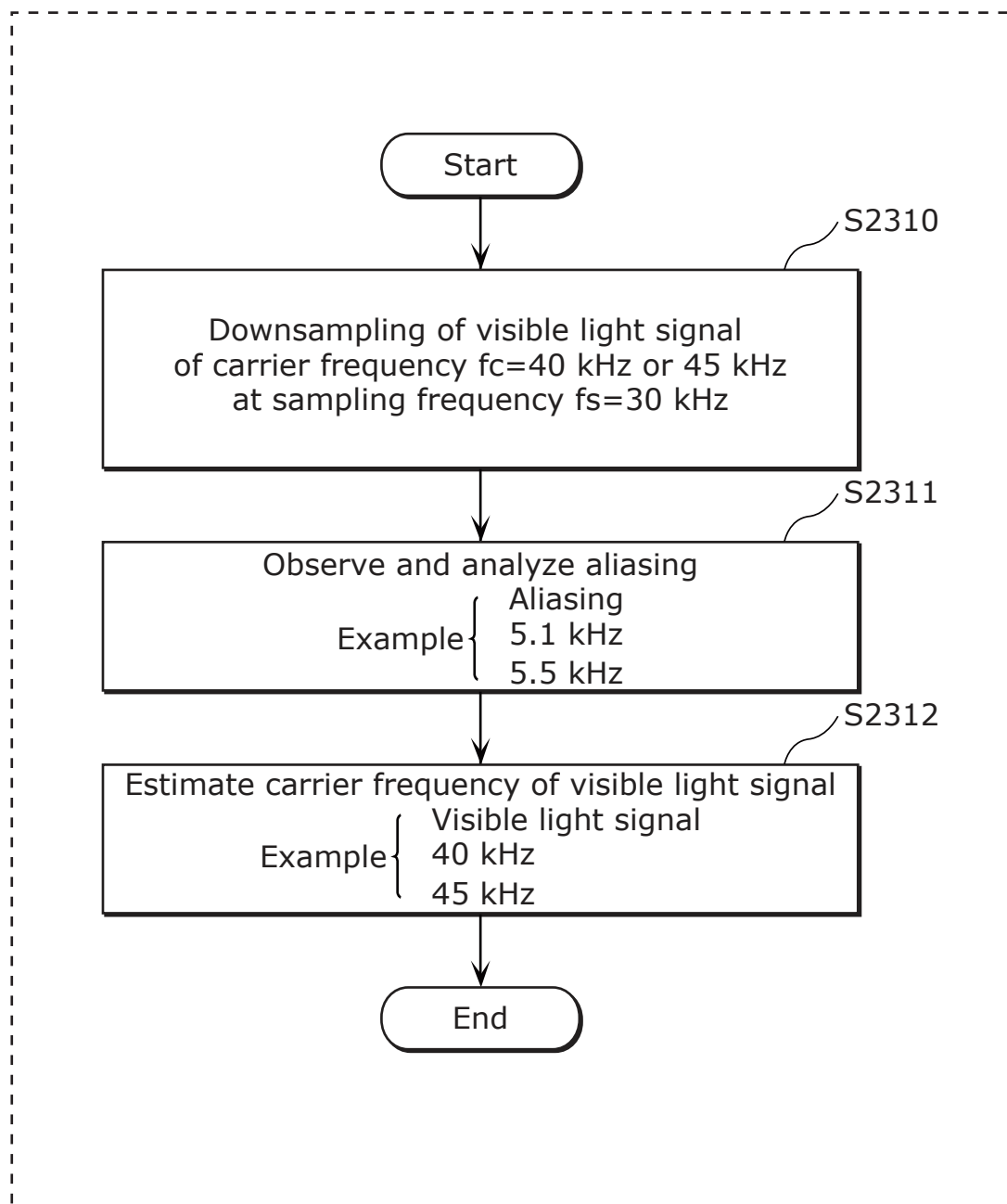


FIG. 390

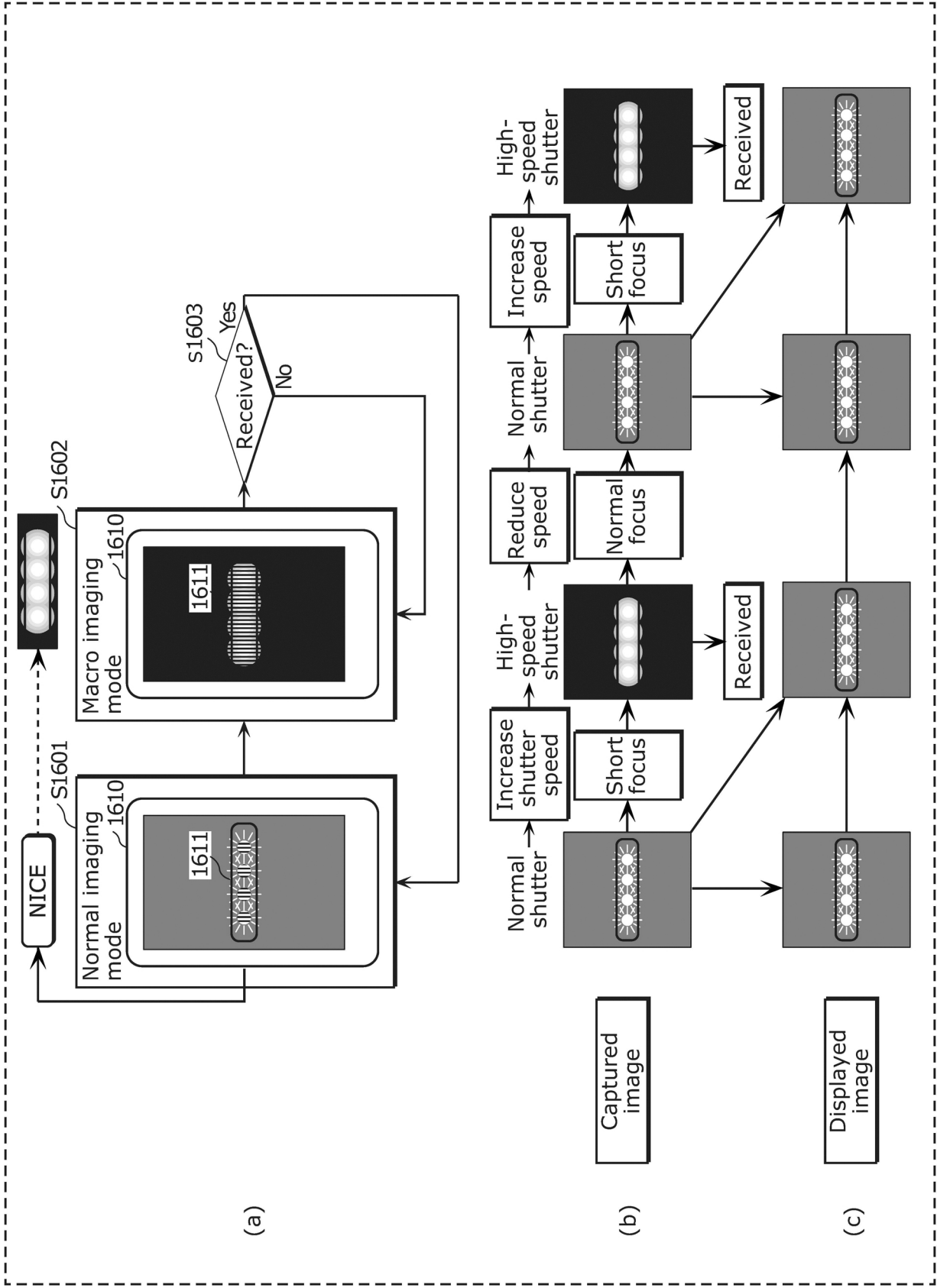


FIG. 391

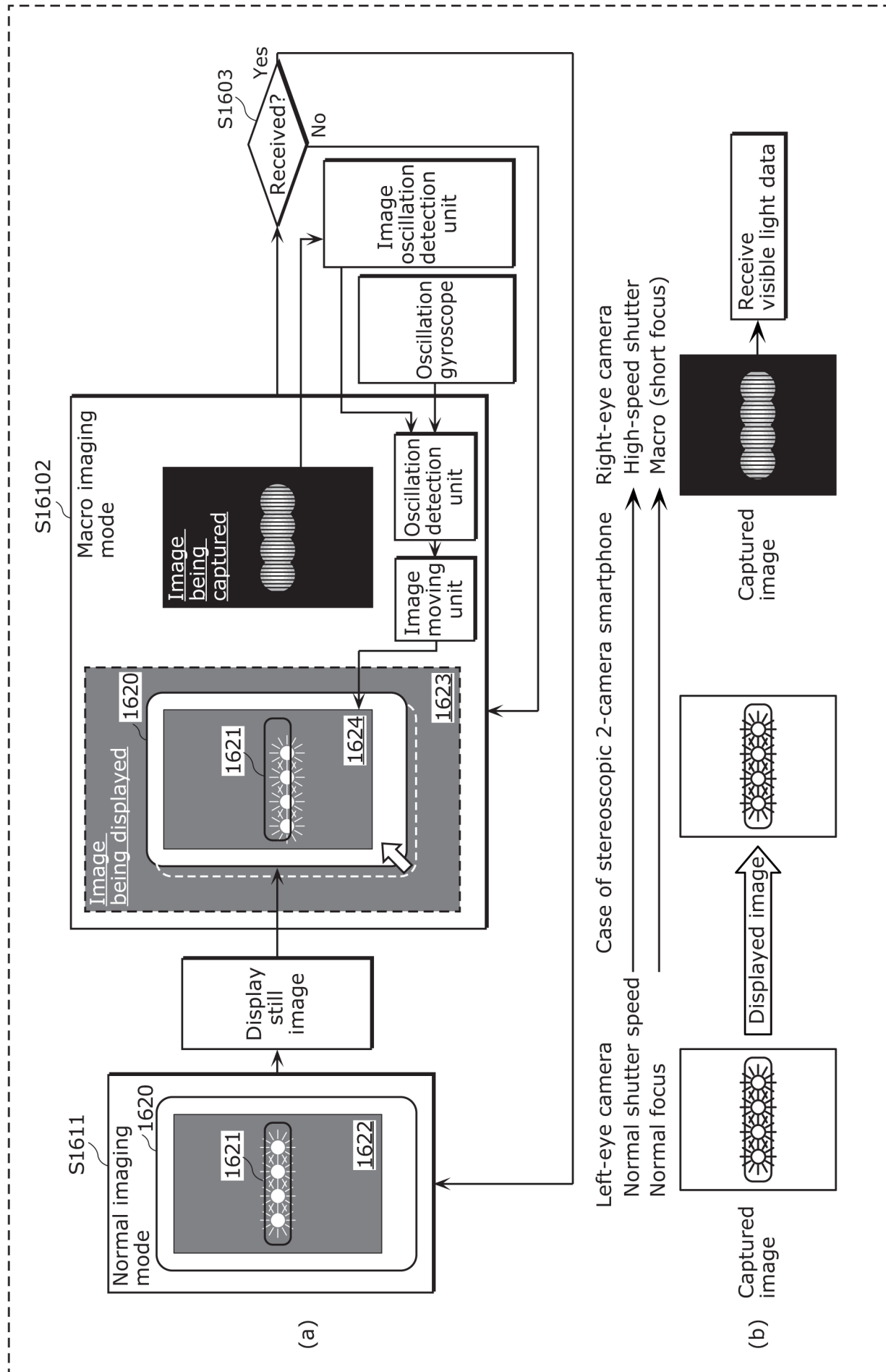


FIG. 392

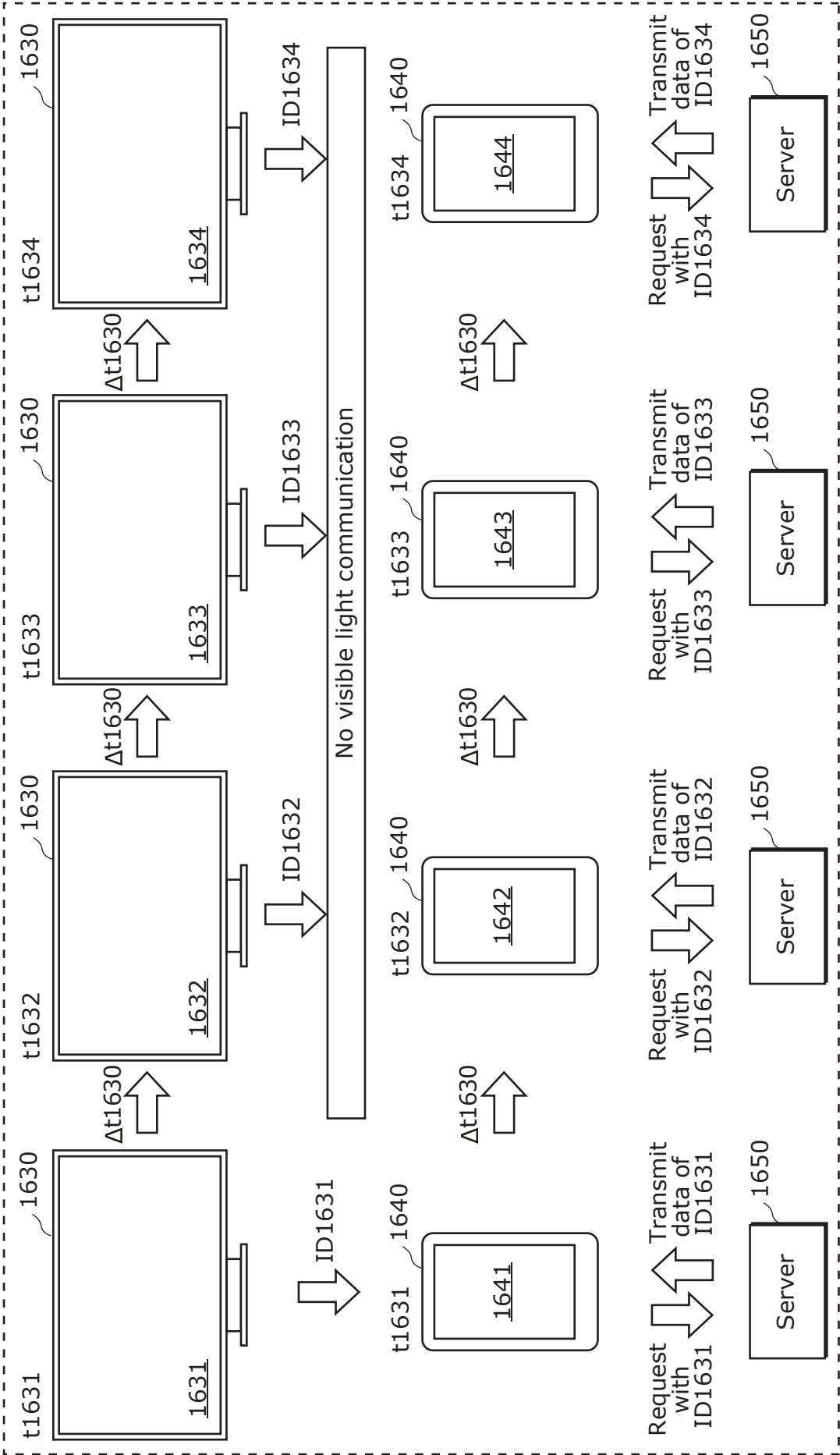


FIG. 393

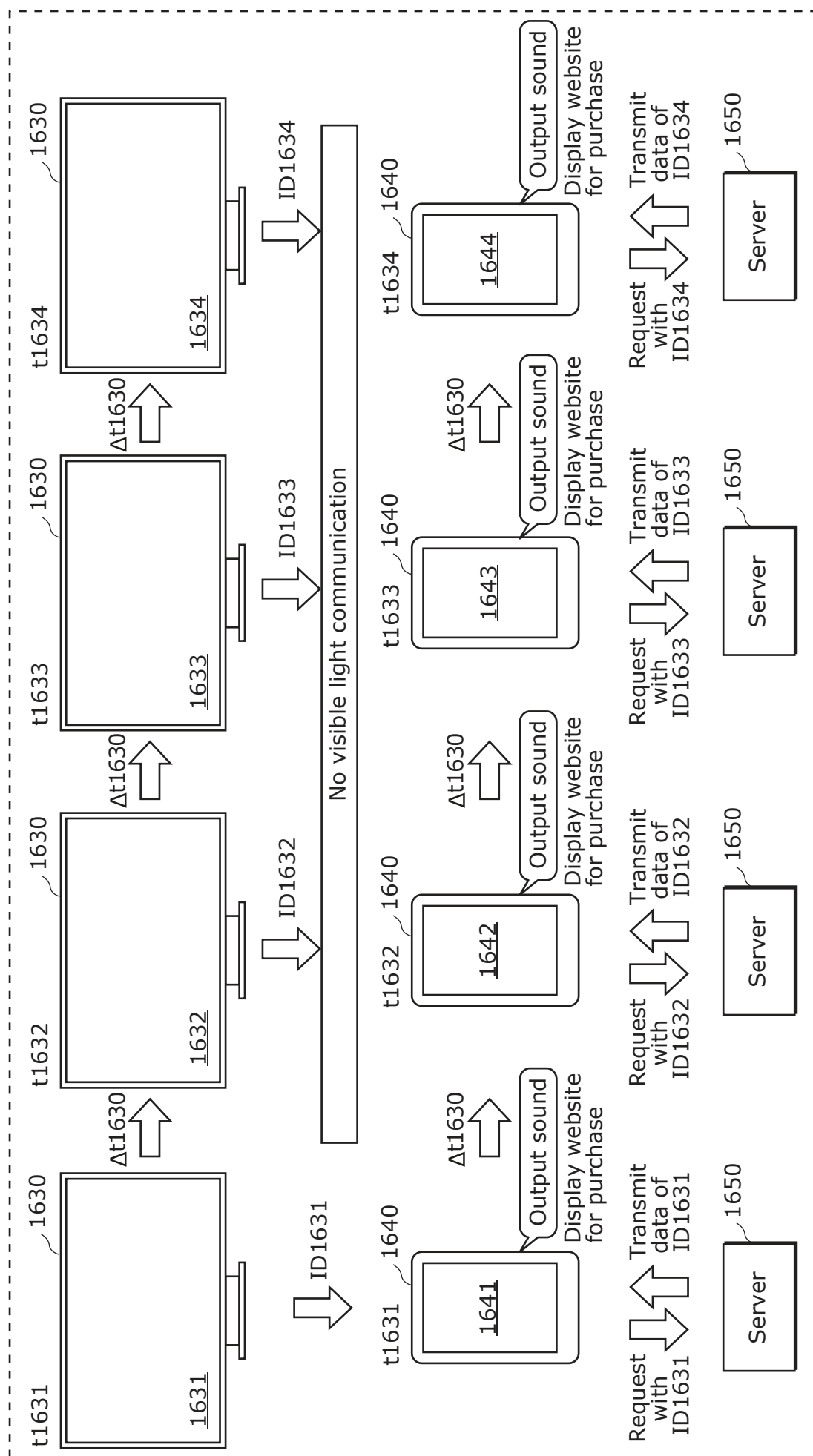


FIG. 394

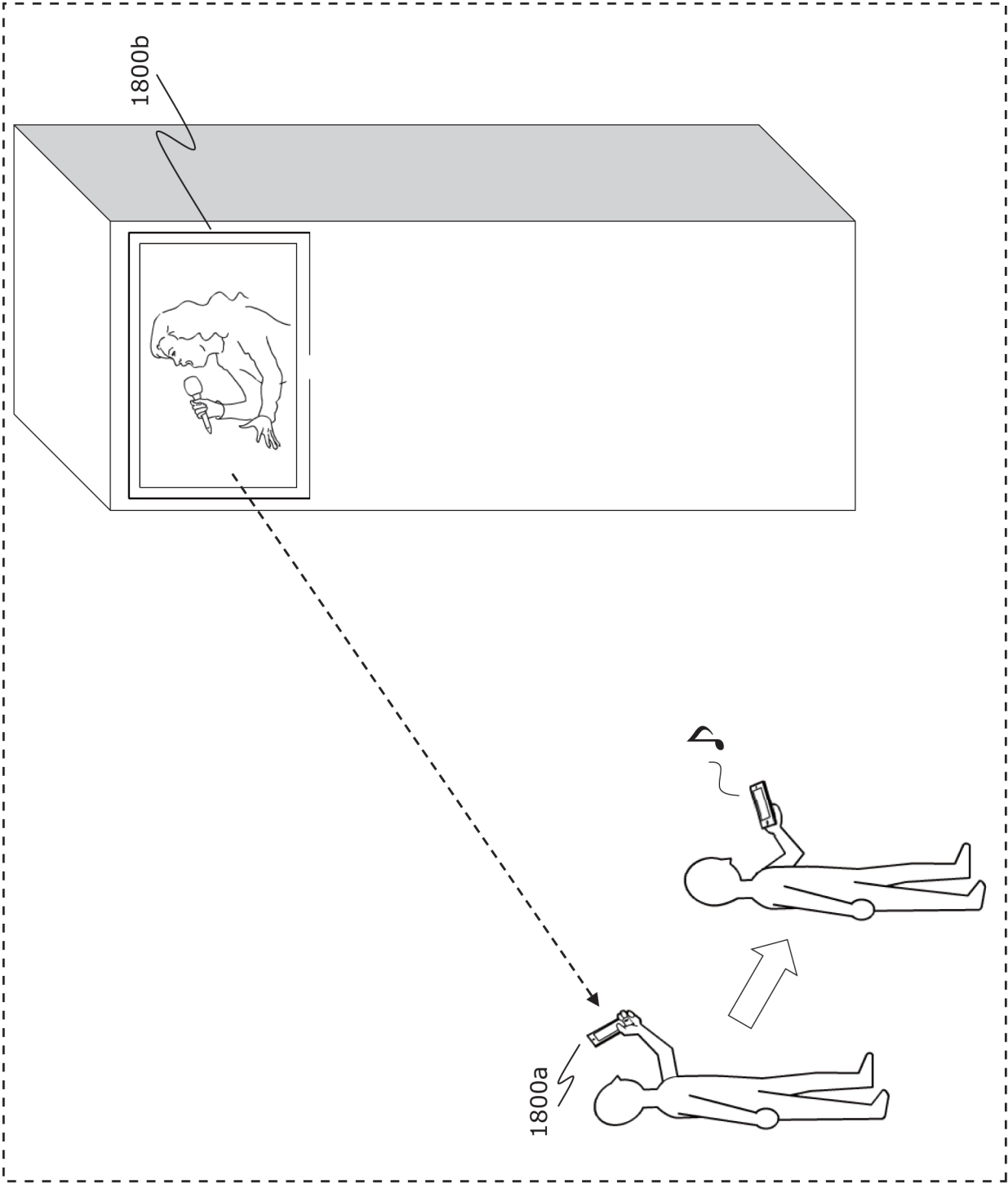


FIG. 395

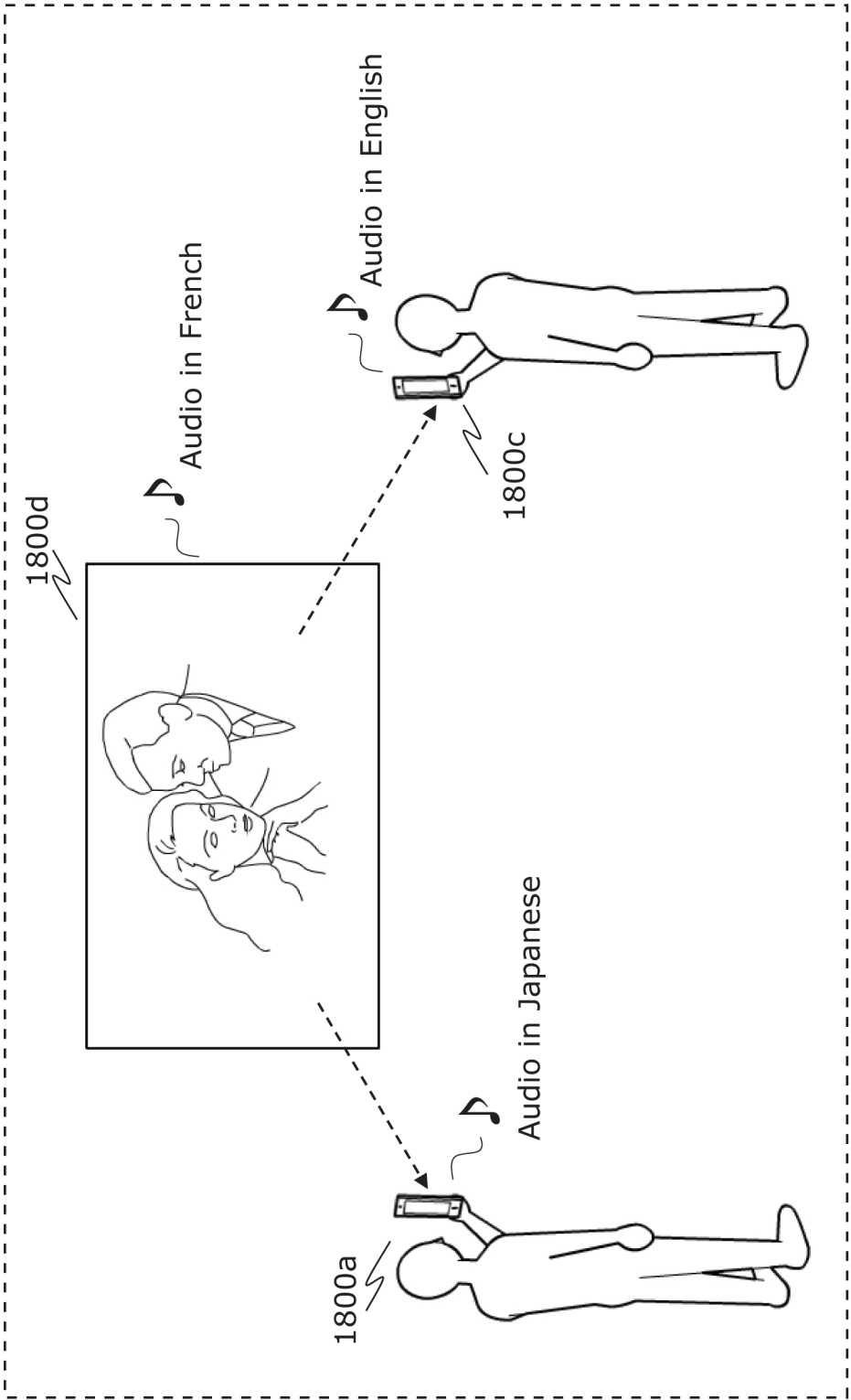


FIG. 396

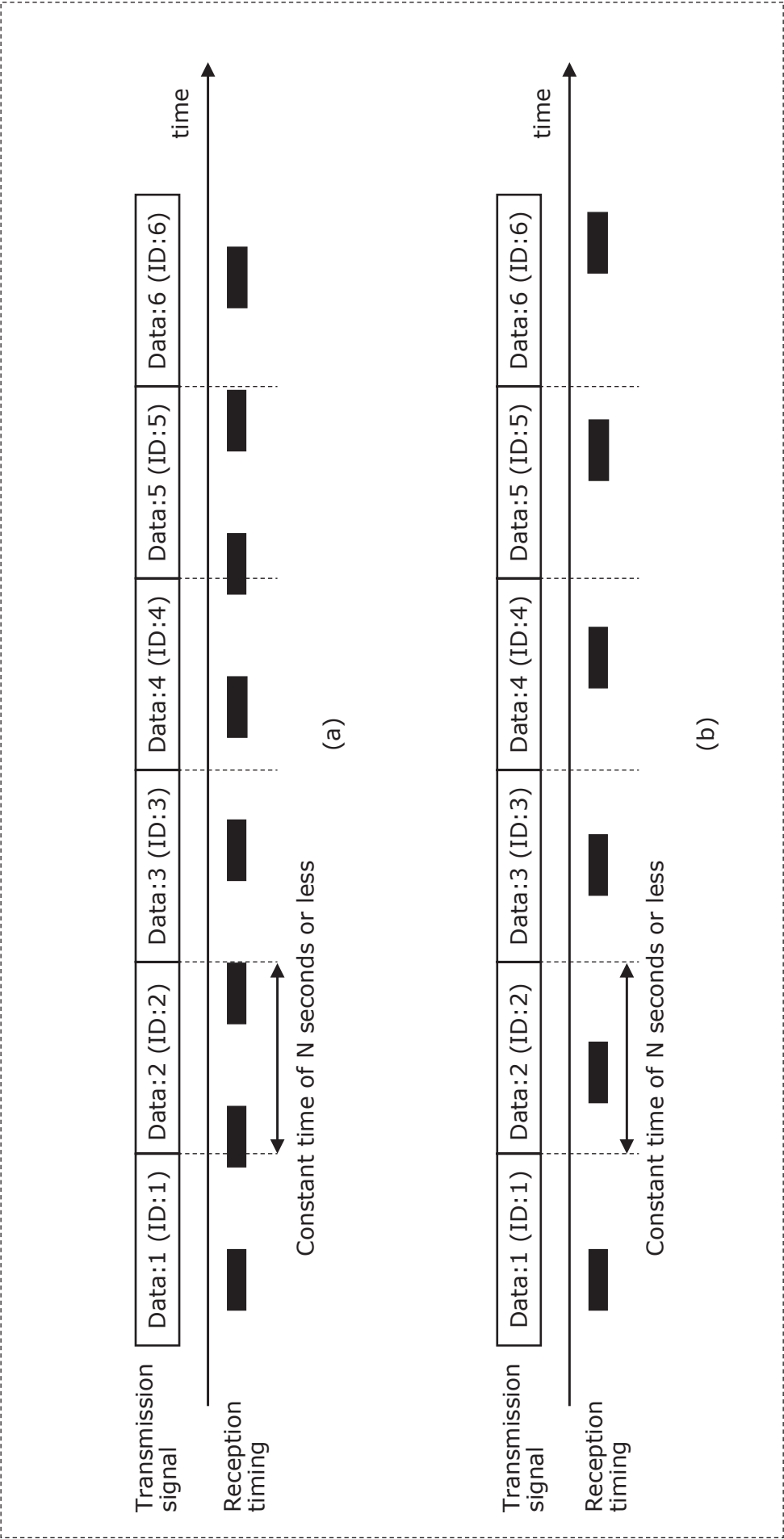


FIG.398

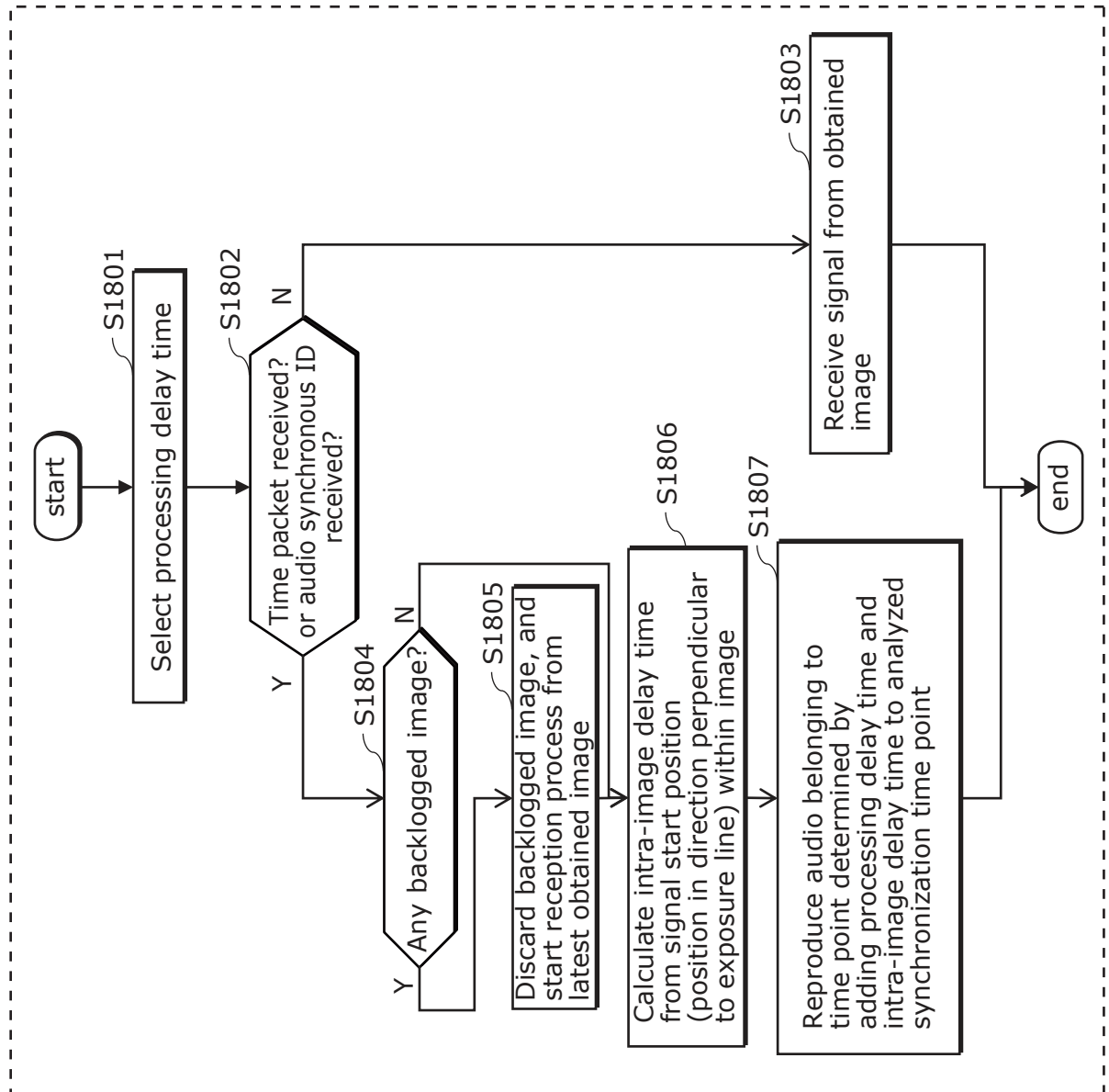


FIG. 399

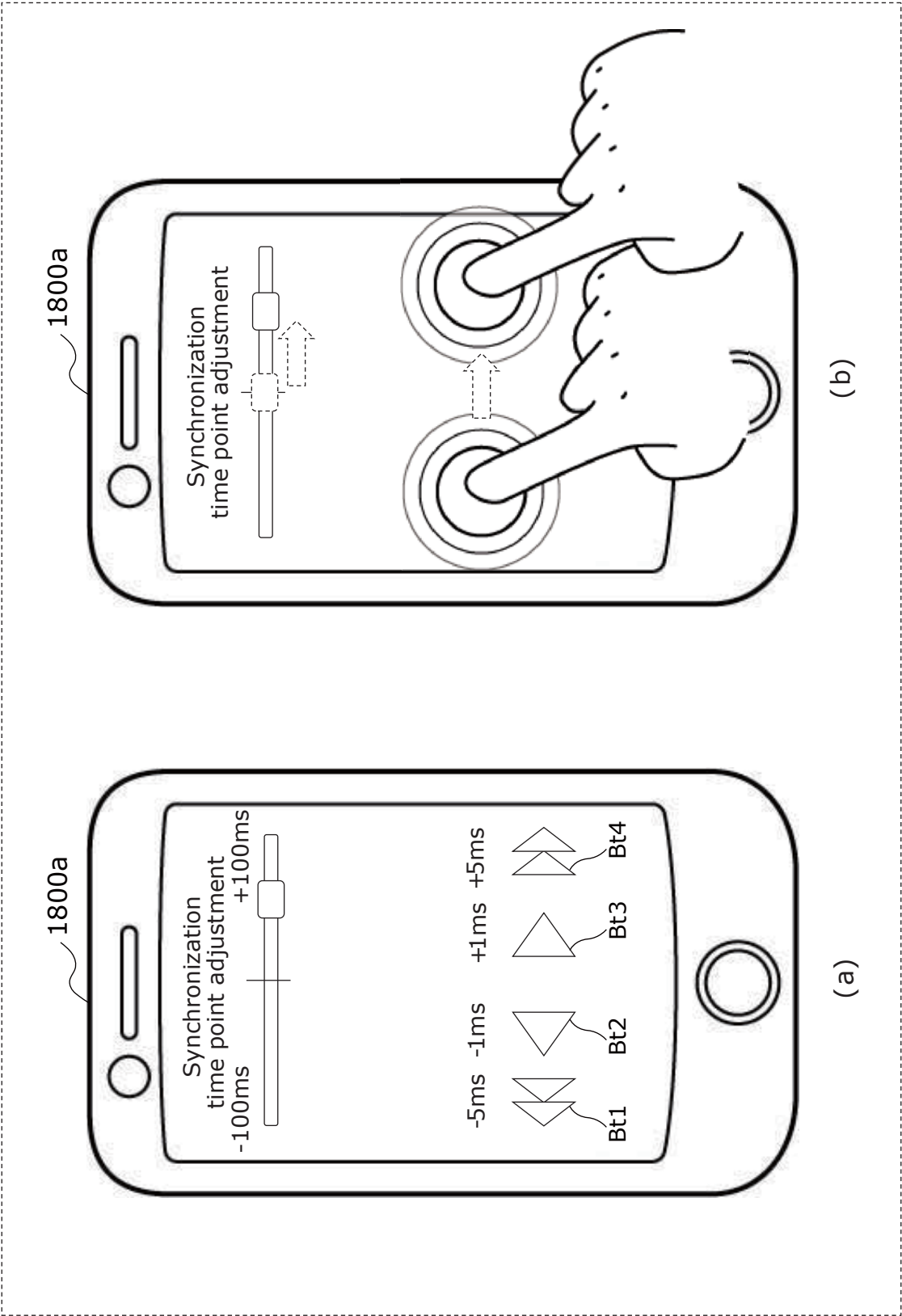


FIG. 400

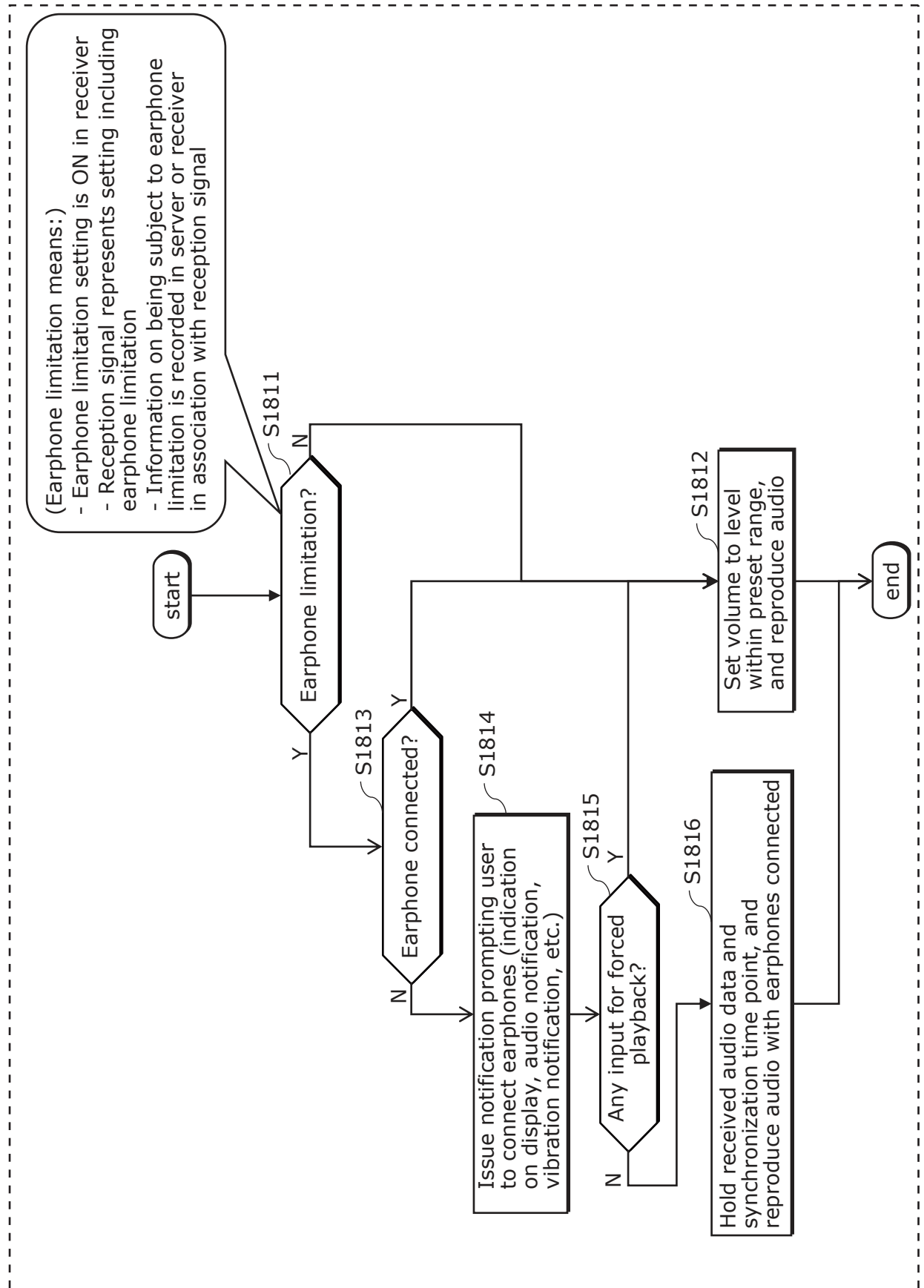


FIG. 401

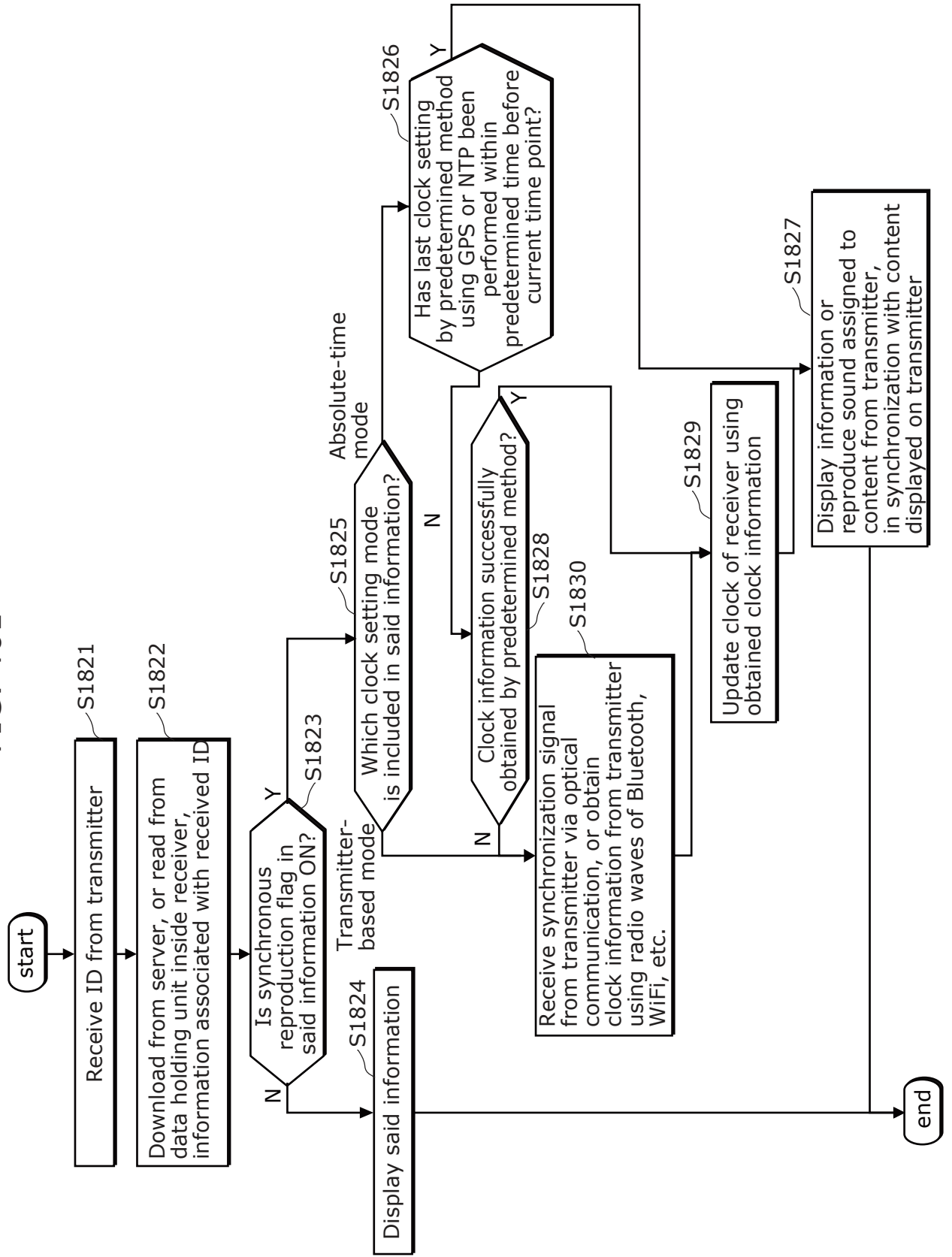
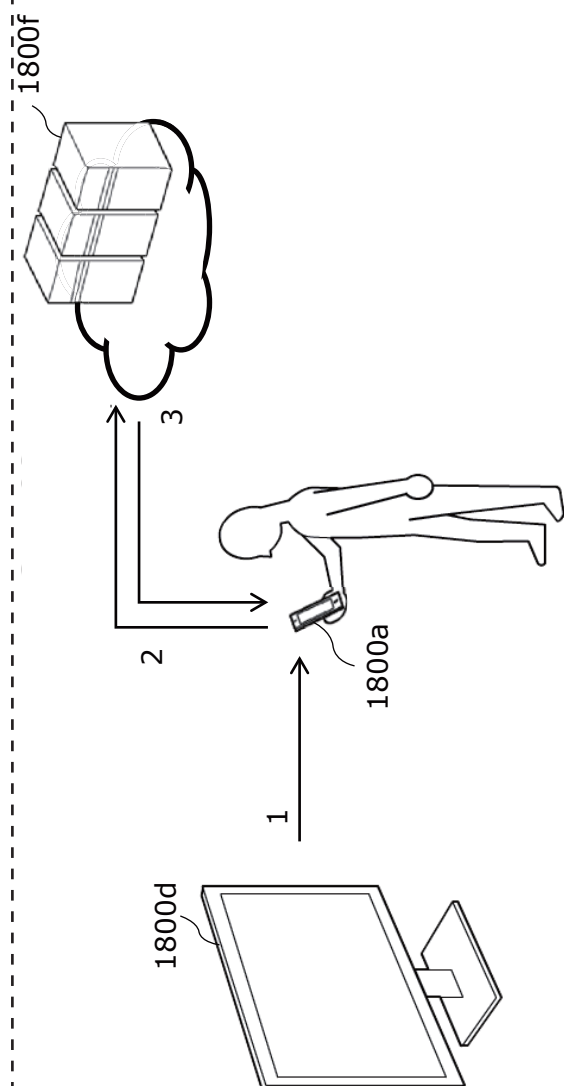


FIG. 402A



	1	2	Server	3	Receiver operation
a	Content ID, ongoing content reproduction time point	Content ID		Content	Reproduce content from point of time of (ongoing content reproduction time point + elapsed time since ID reception)
b	Content ID, ongoing content reproduction time point	Content ID, ongoing content reproduction time point		Partial content on and after ongoing content reproduction time point	Reproduce partial content after point of time of (elapsed time since ID reception)
c	Transmitter ID, ongoing content reproduction time point	Transmitter ID	Identify content that is being reproduced, from reproduction schedule of each transmitter and server time point	Content	Reproduce content from point of time of (ongoing content reproduction time point + elapsed time since ID reception)
d	Transmitter ID, transmitter time point	Transmitter ID, transmitter time point	Identify content that is being reproduced, from reproduction schedule of each transmitter and server time point	Content, ongoing content reproduction time point	Reproduce content from point of time of (ongoing content reproduction time point + elapsed time since ID reception)
e	Transmitter ID	Transmitter ID	Identify content that is being reproduced, from reproduction schedule of each transmitter and server time point	Content, content reproduction start time point	Clock setting of receiver Reproduce content from point of time of (receiver time point - content reproduction start time point)

FIG. 402B

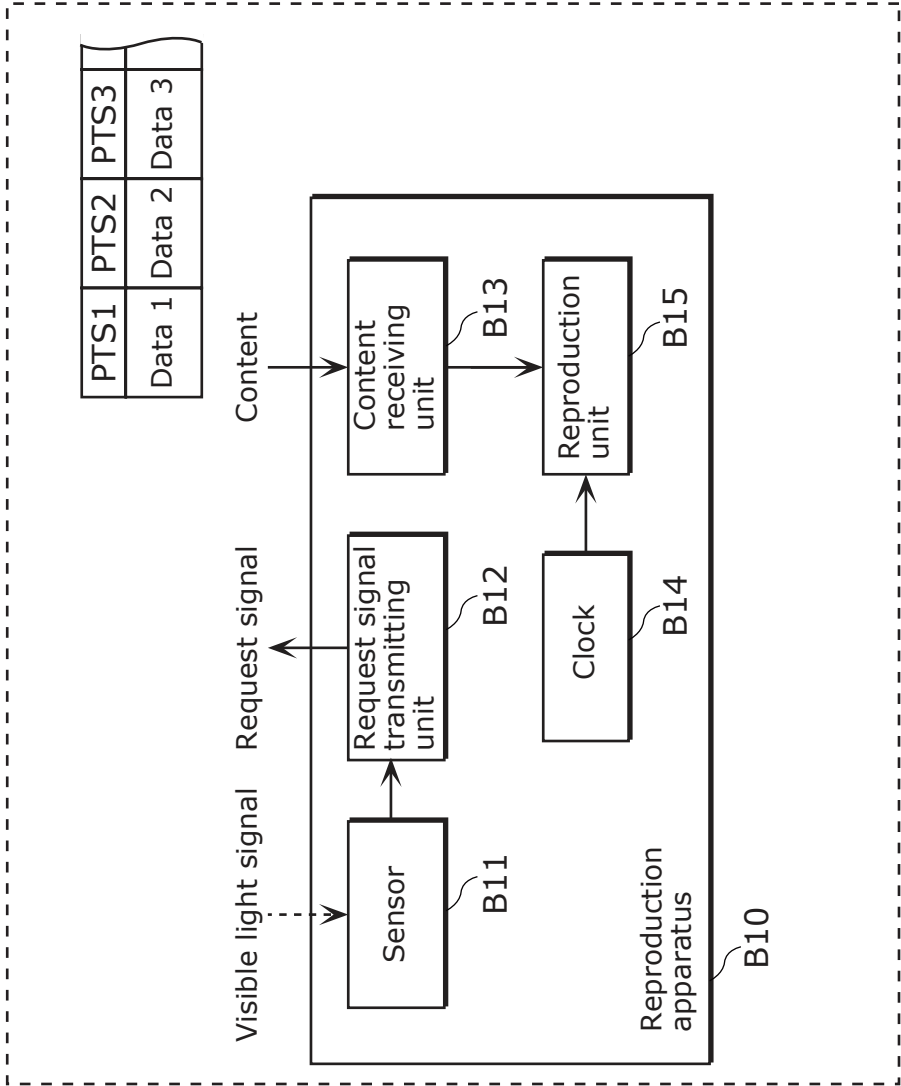


FIG. 402C

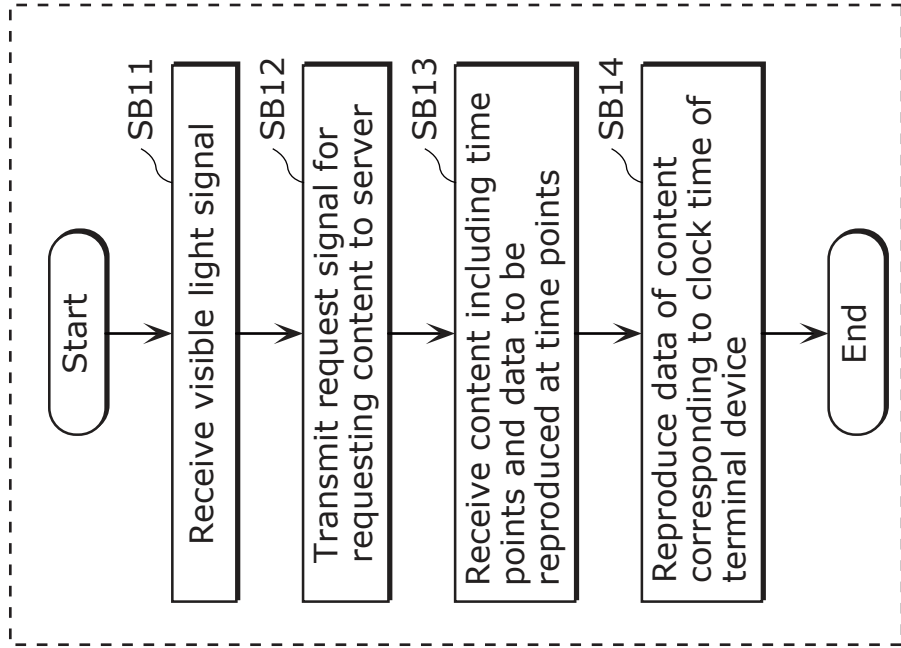


FIG. 403

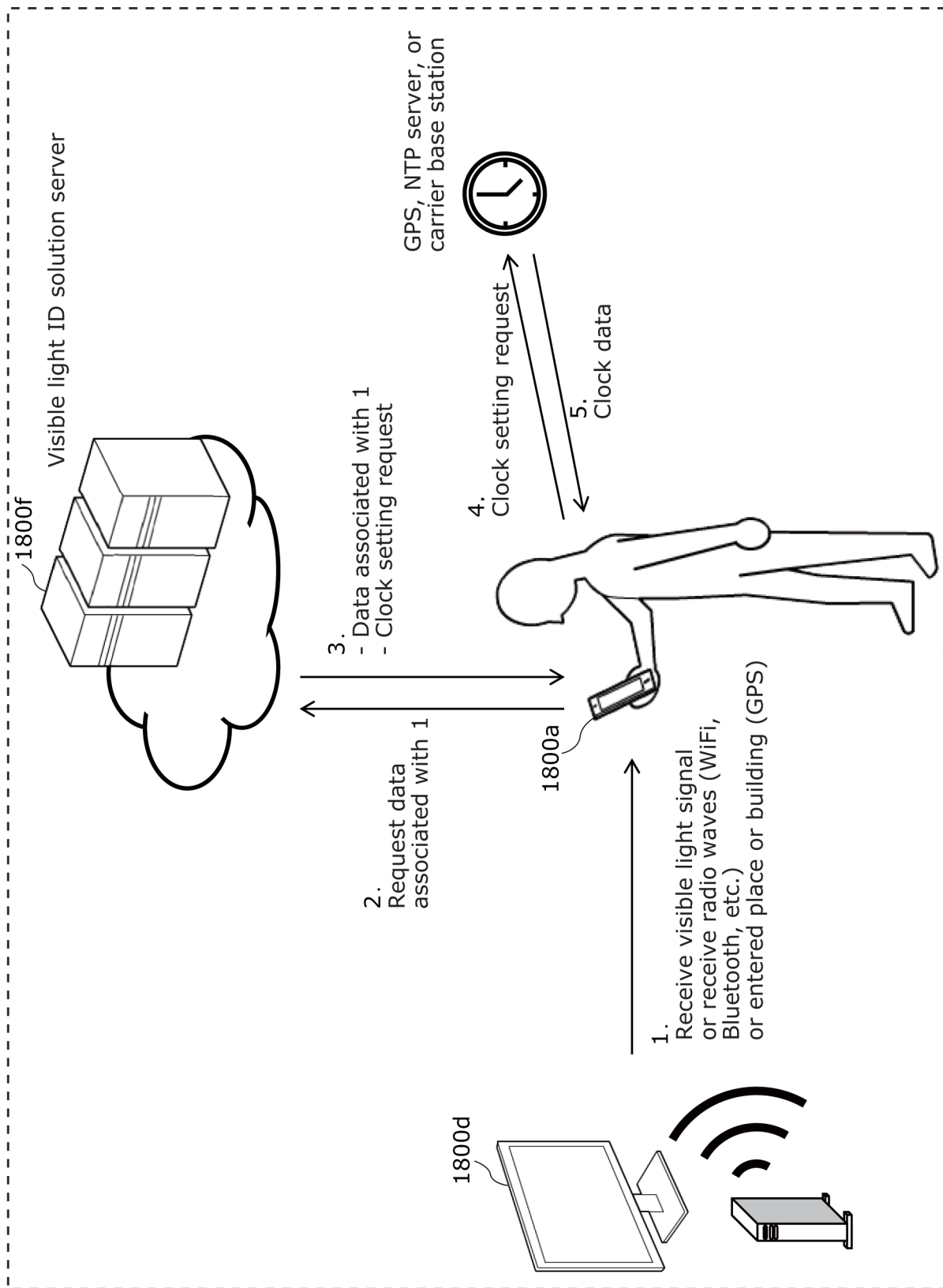


FIG. 404

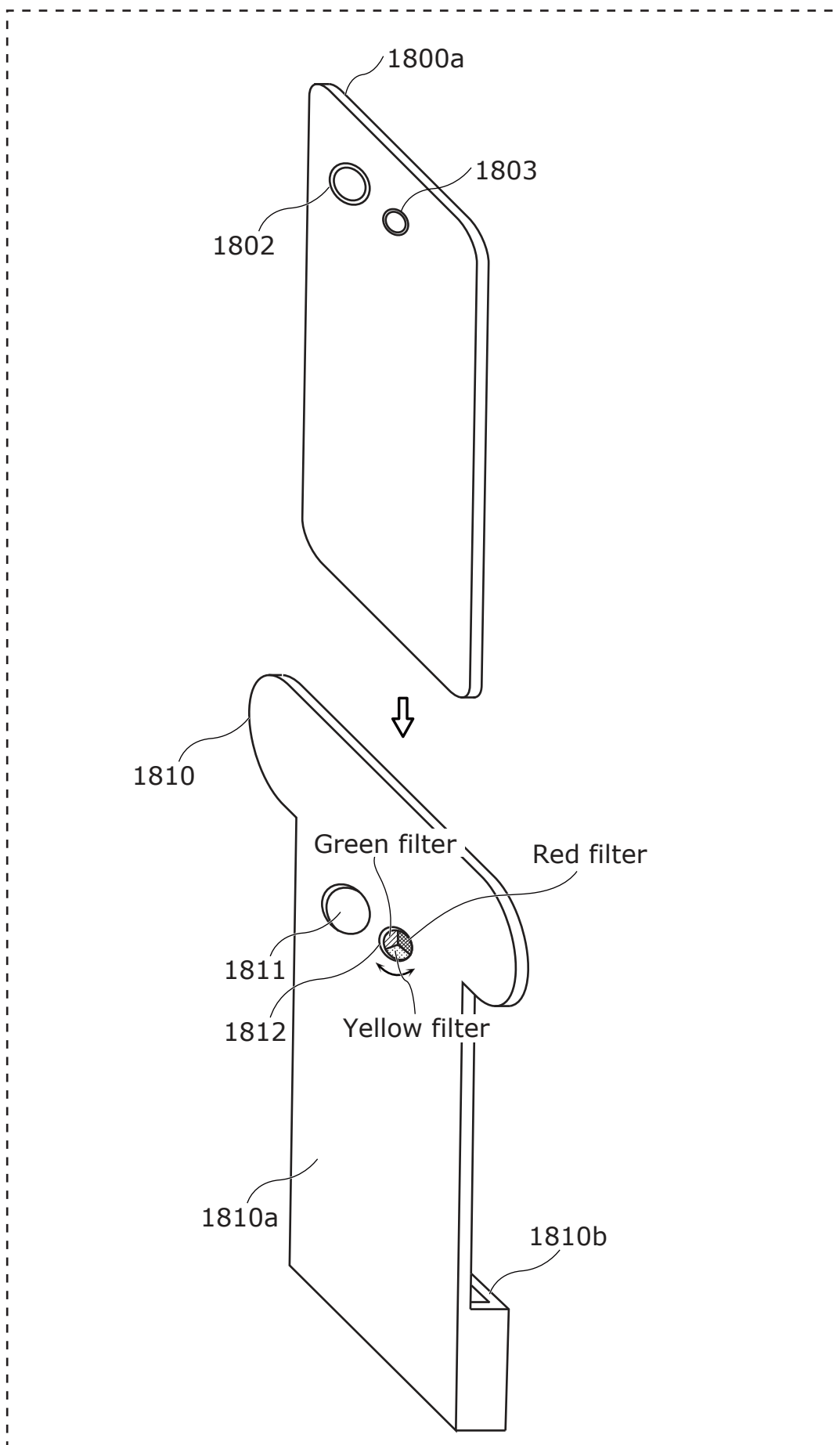


FIG. 405A

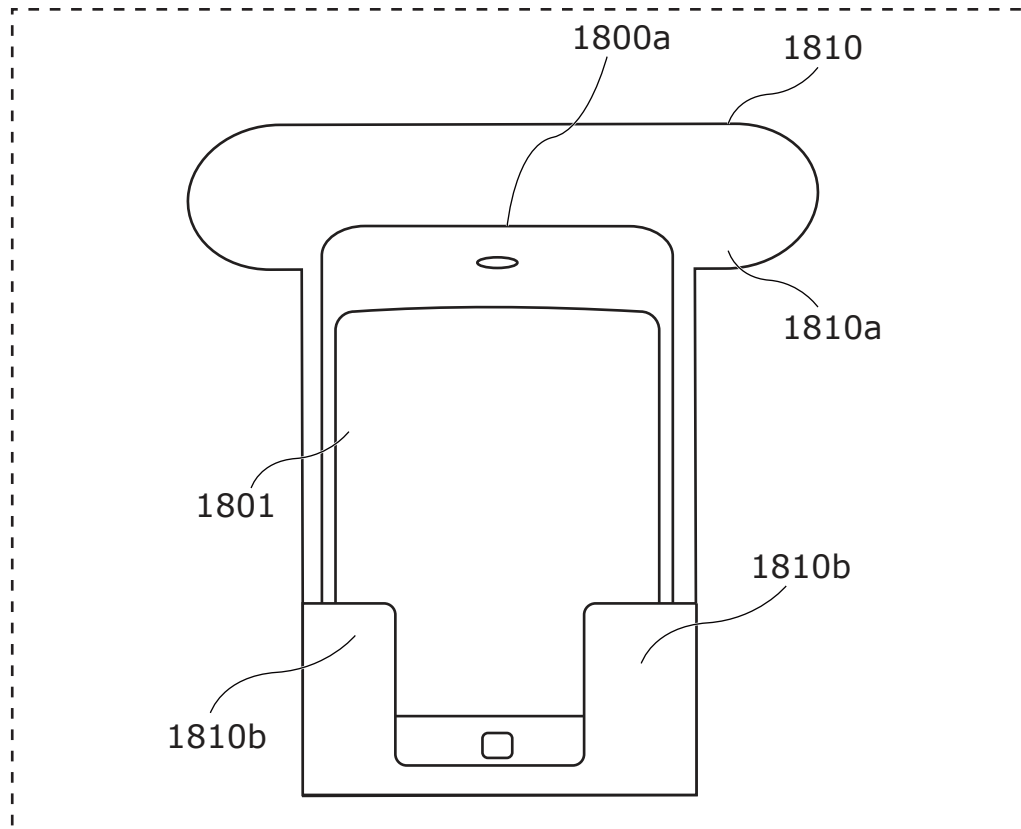


FIG. 405B

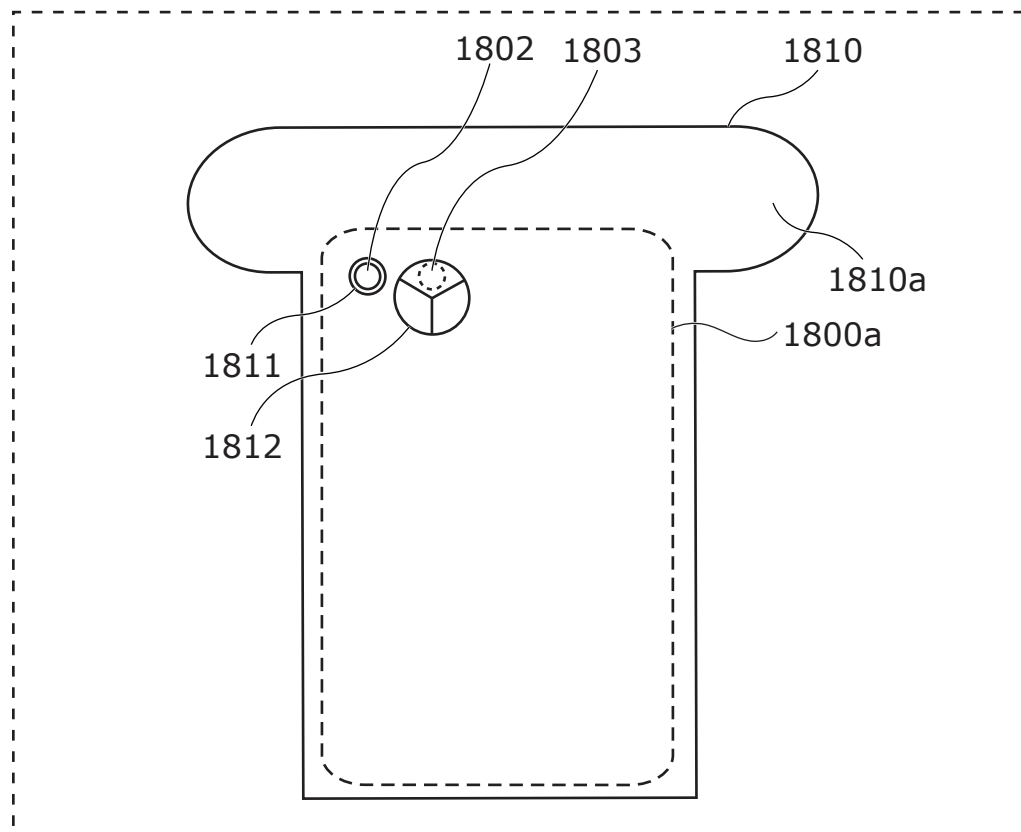


FIG. 406

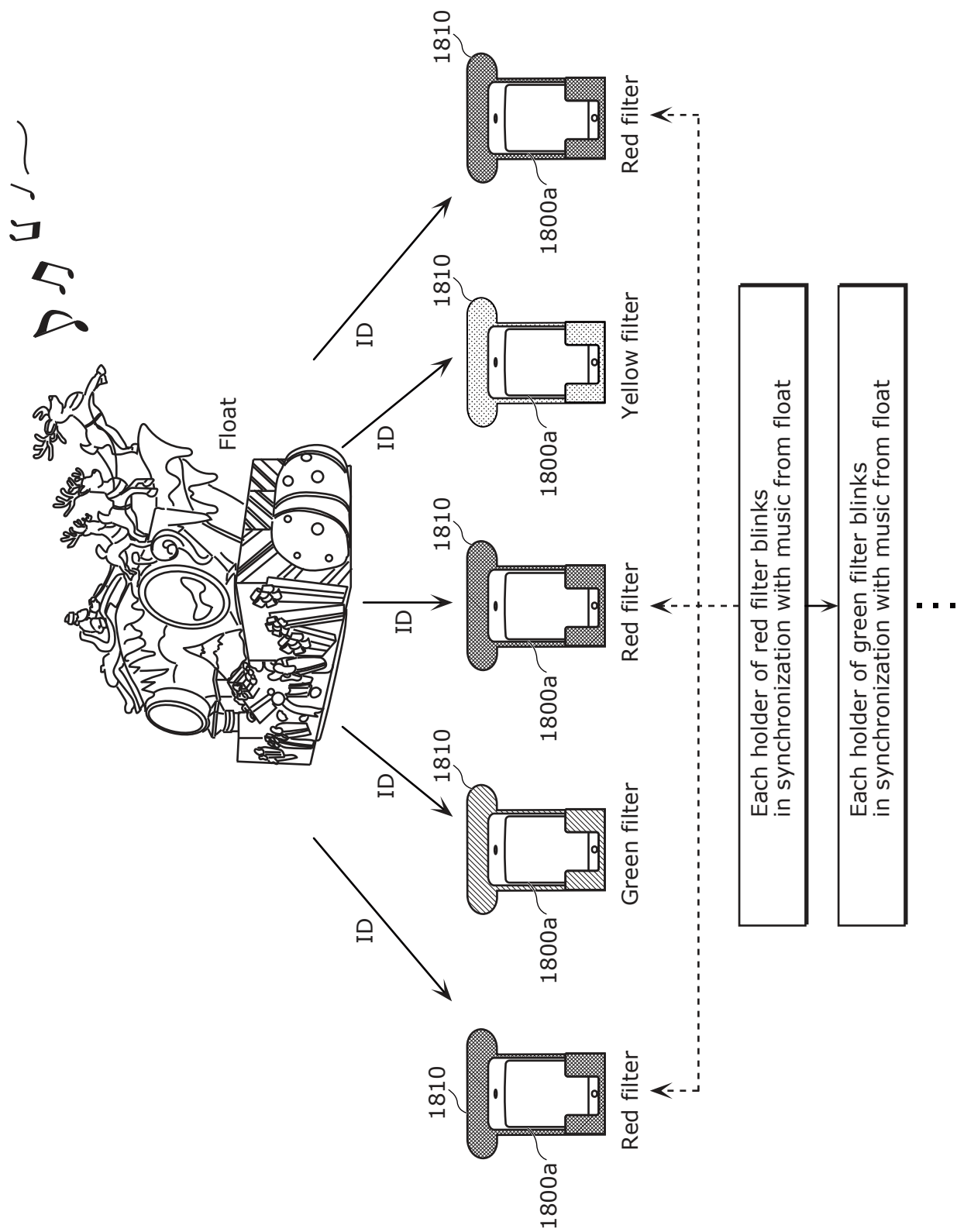


FIG. 407

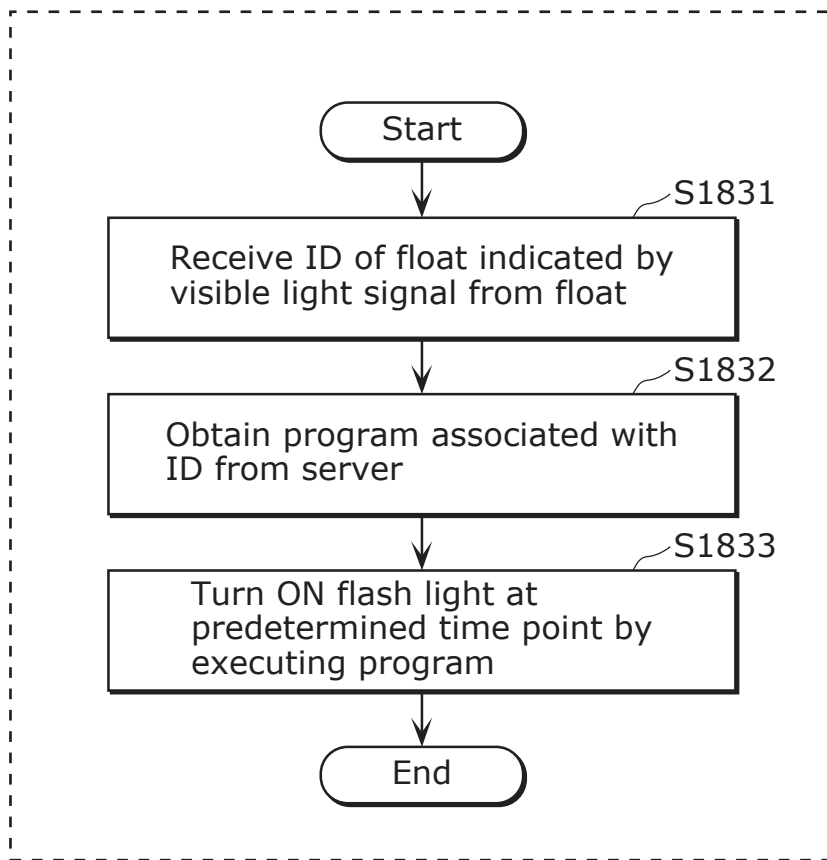


FIG. 408

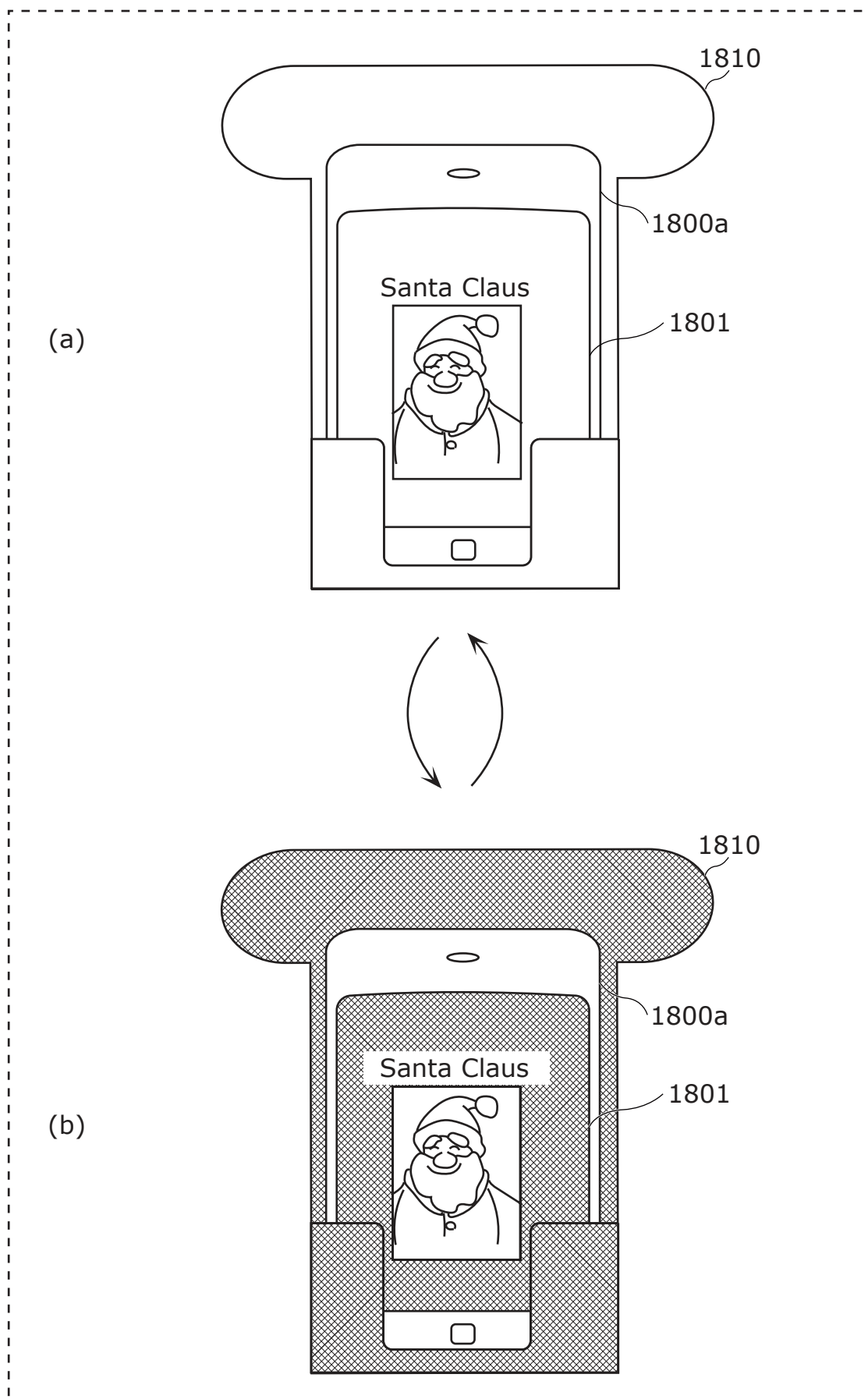


FIG. 409

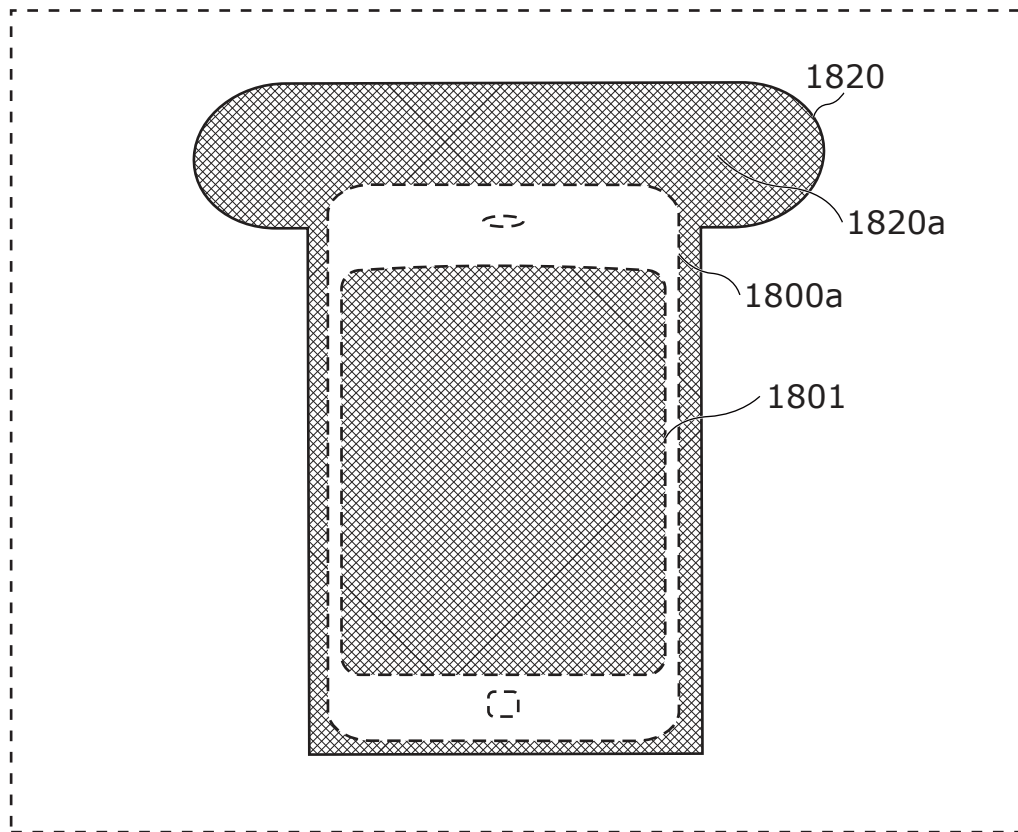


FIG. 410A

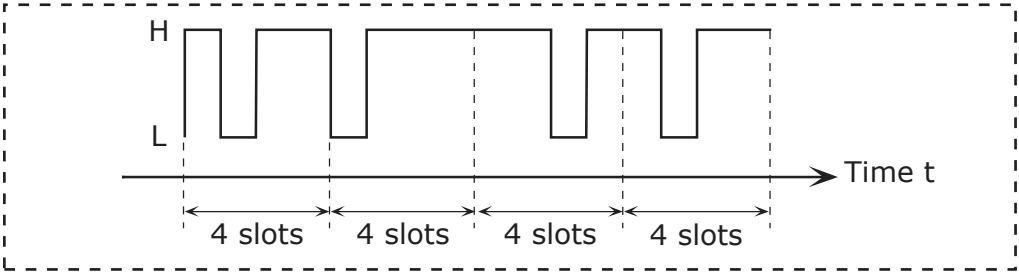


FIG. 410B

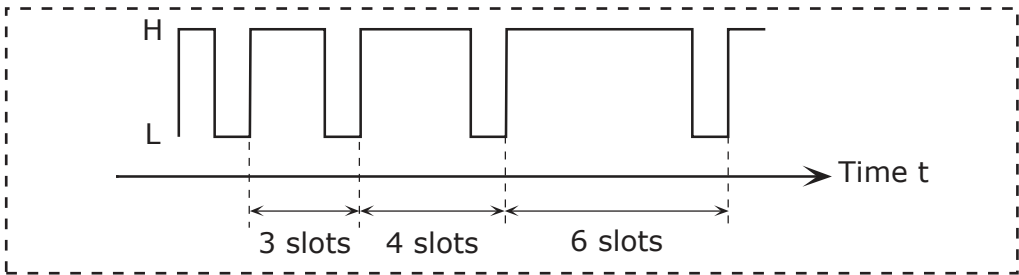


FIG. 410C

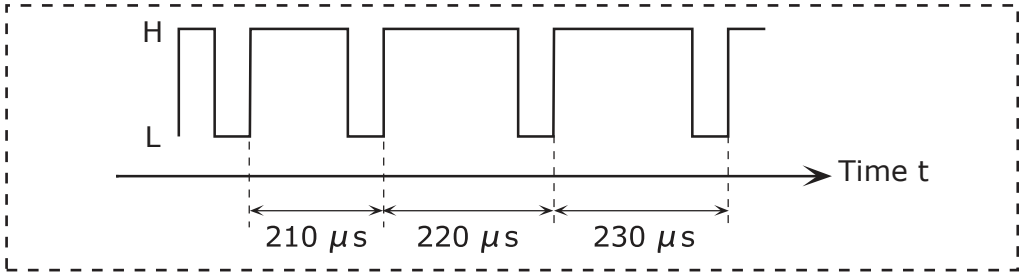


FIG. 410D

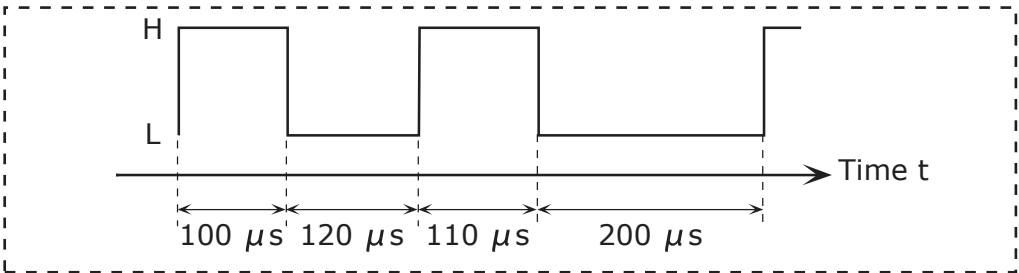


FIG. 411

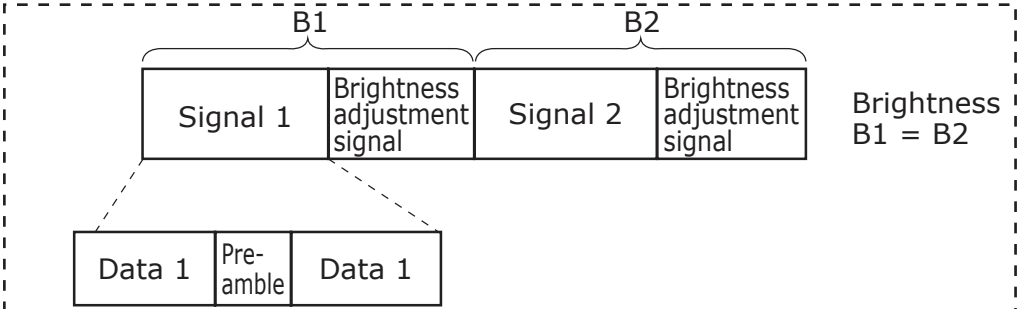


FIG. 412

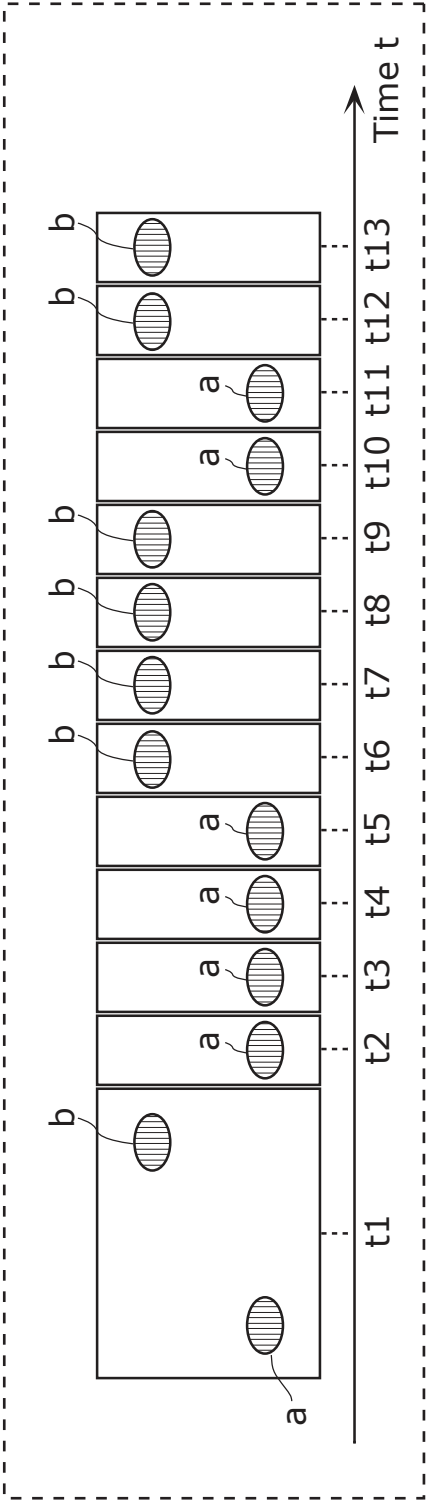


FIG. 413

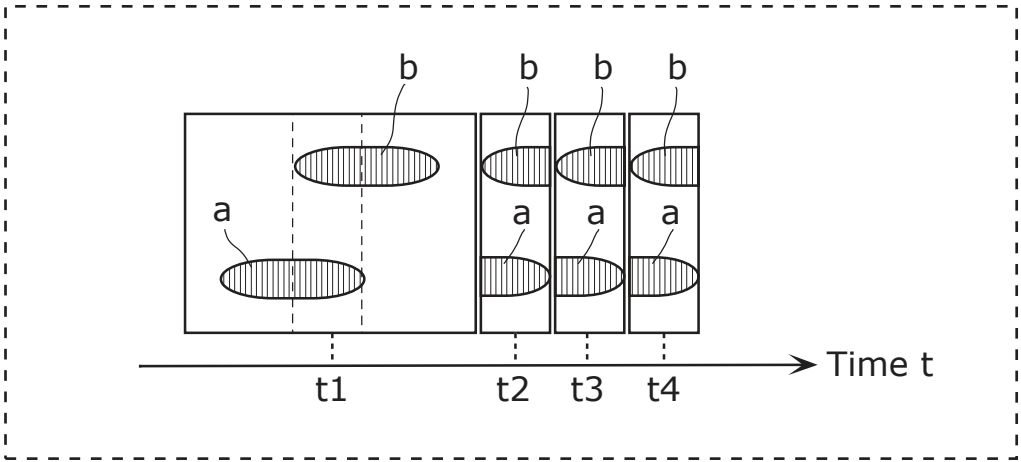


FIG. 414

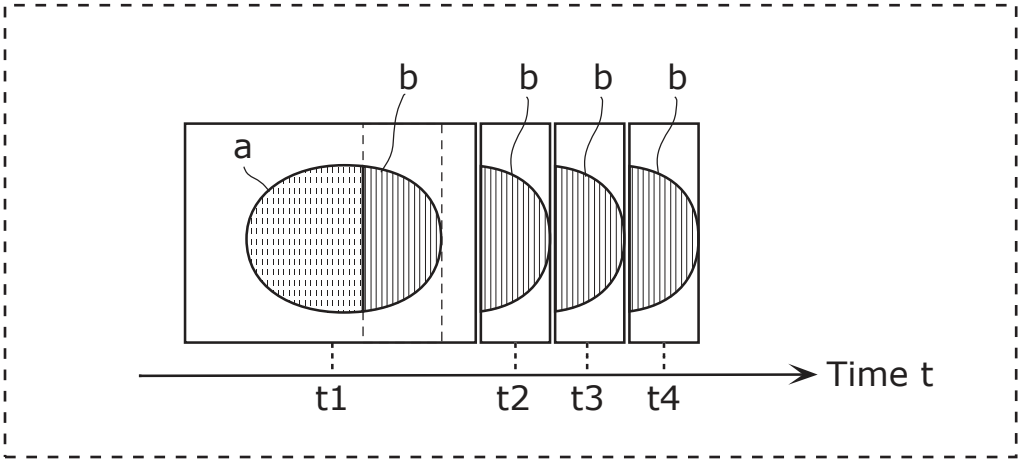


FIG. 415

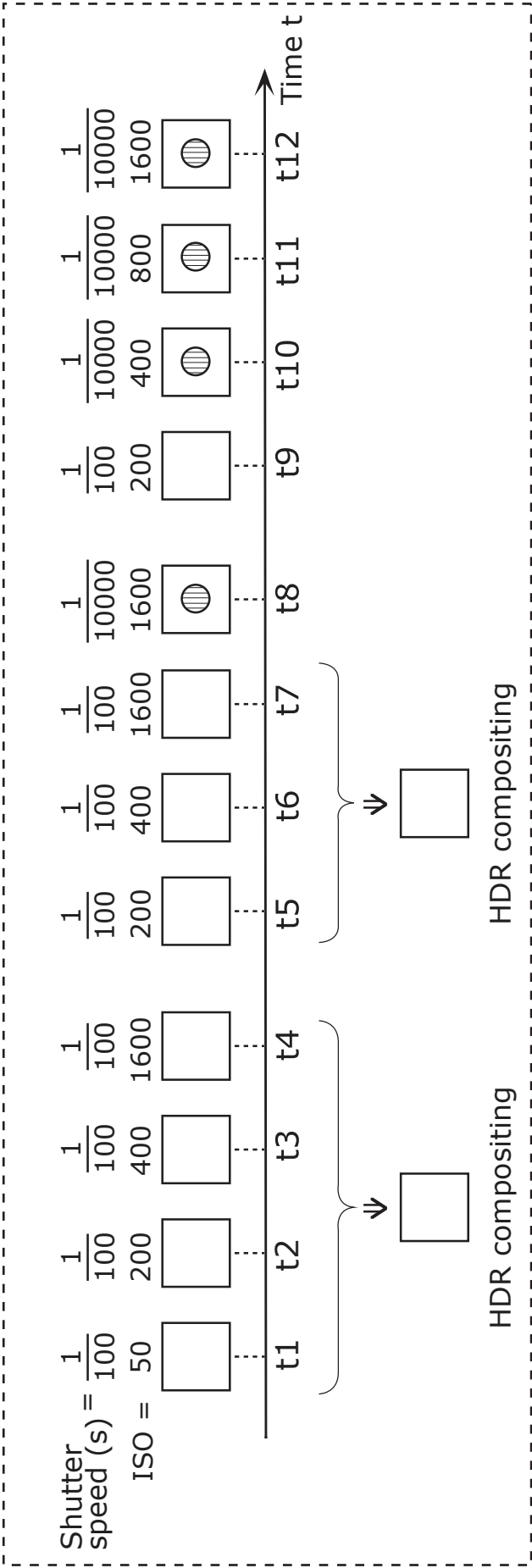


FIG. 416

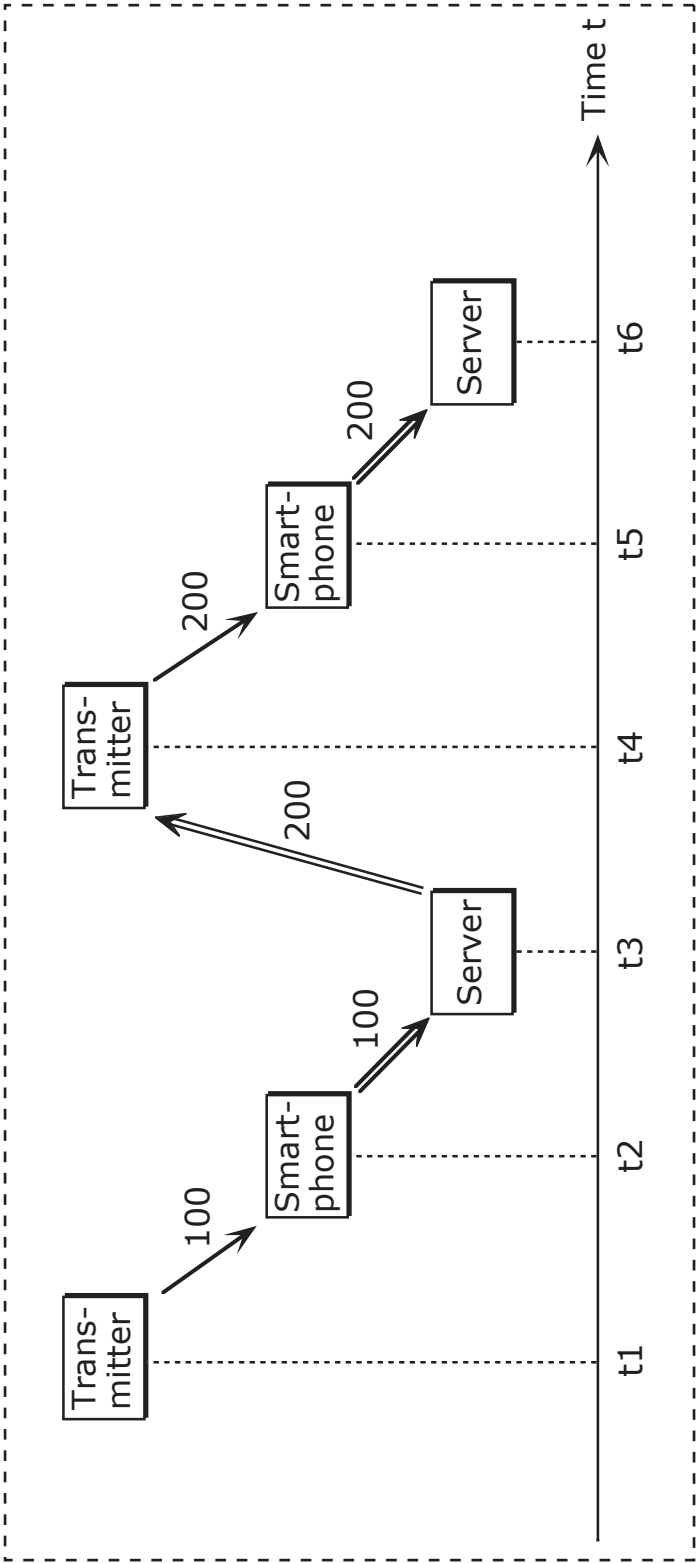


FIG. 417A

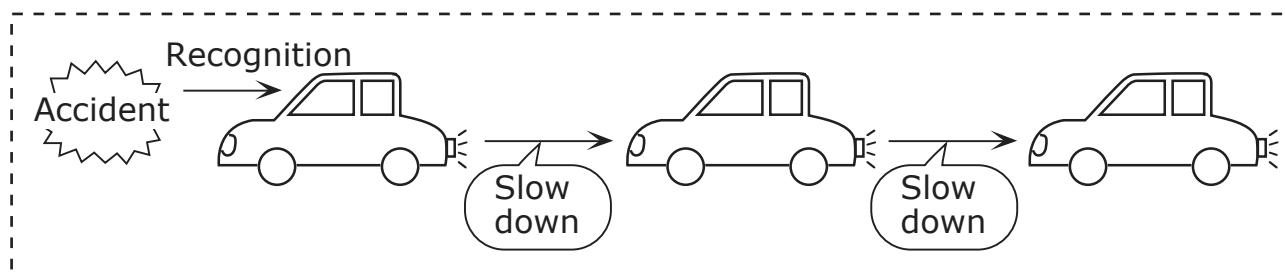


FIG. 417B

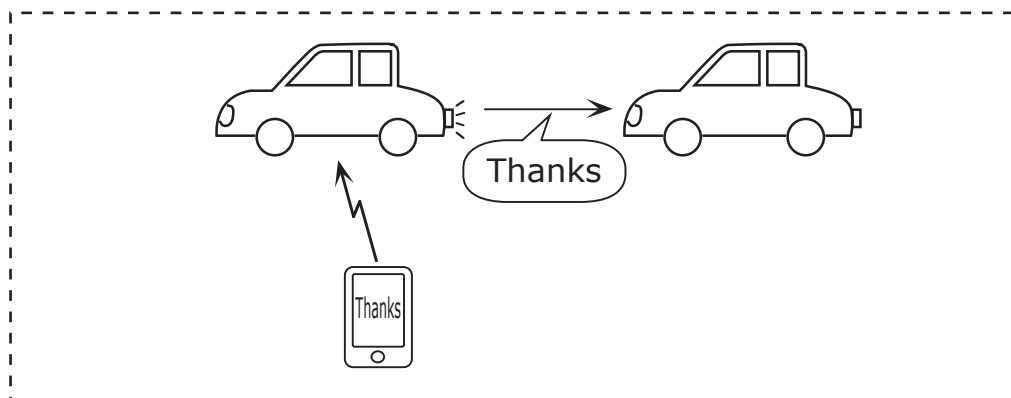


FIG. 418

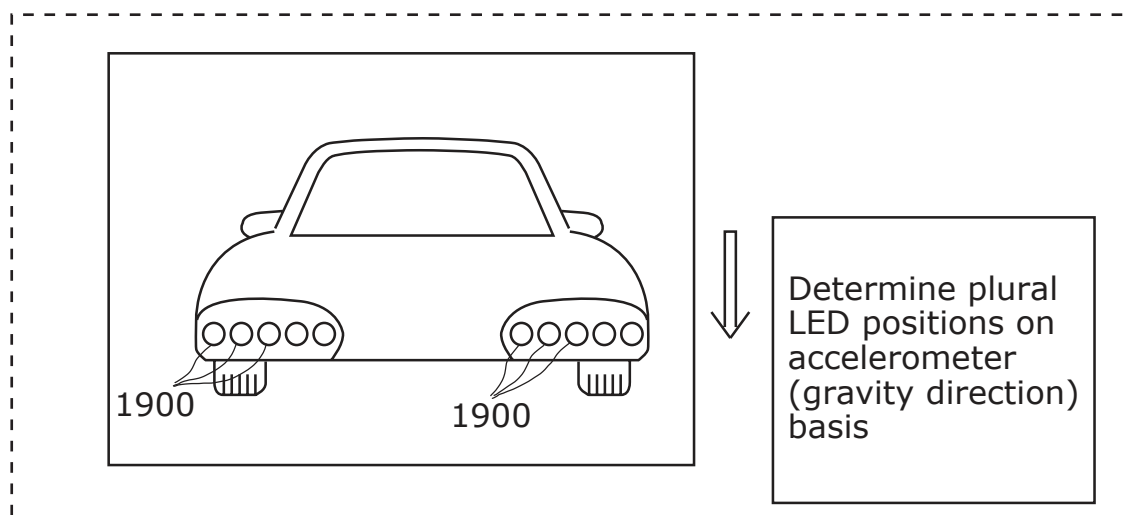


FIG. 419

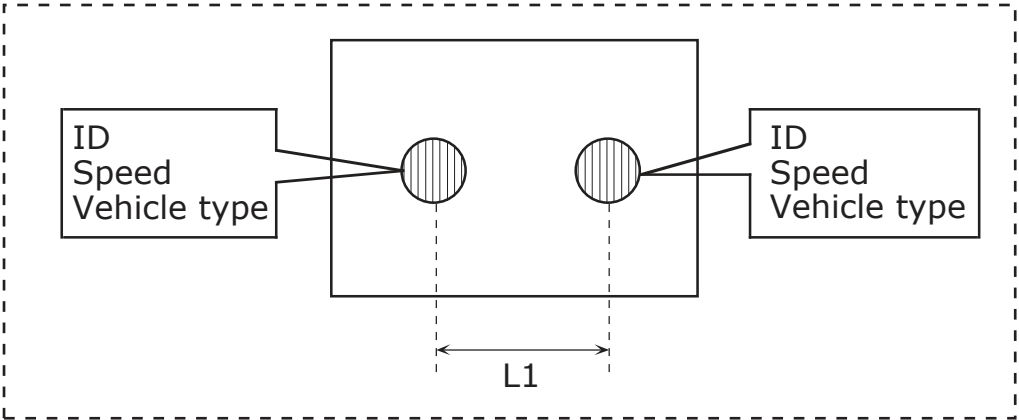


FIG. 420

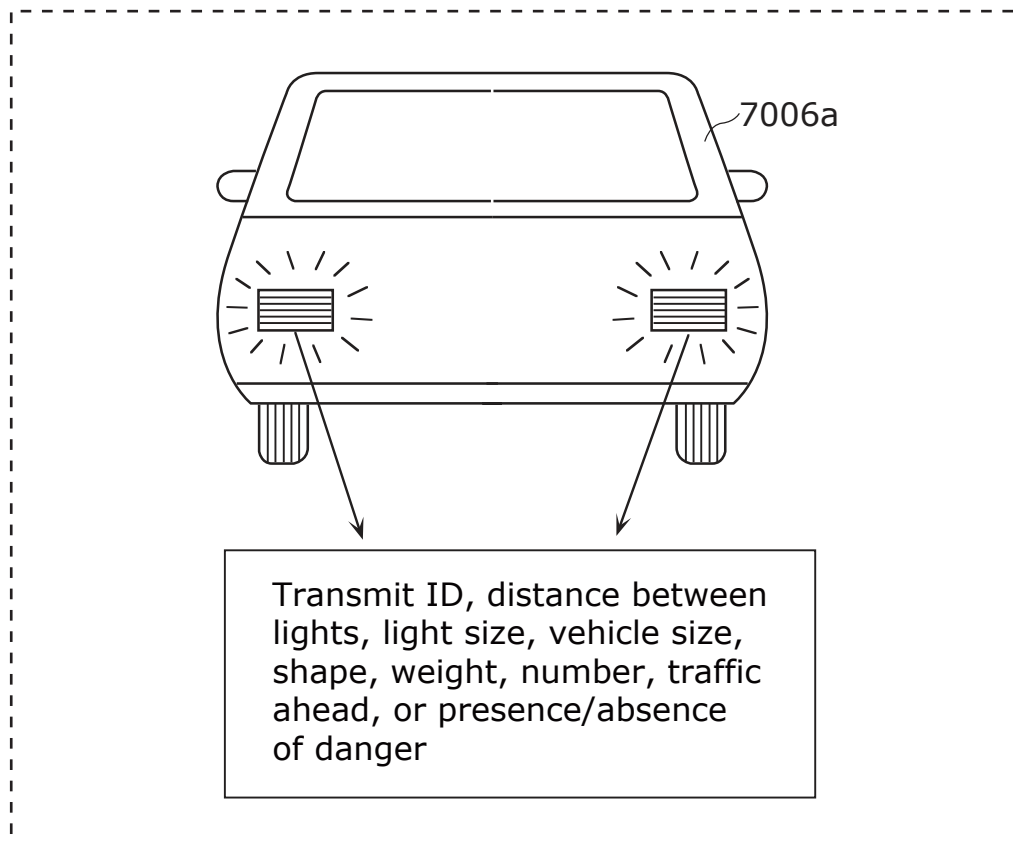


FIG. 421

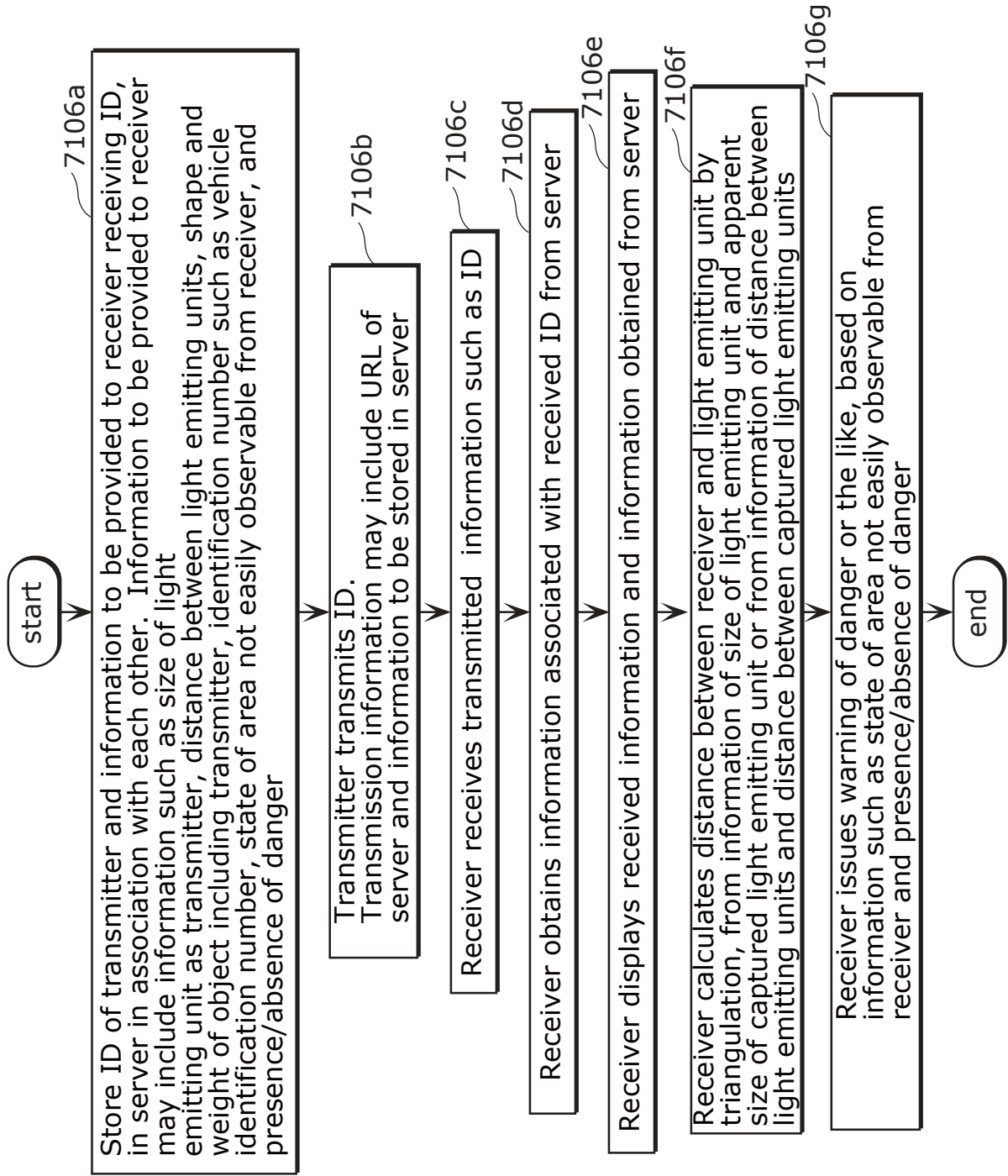


FIG. 422

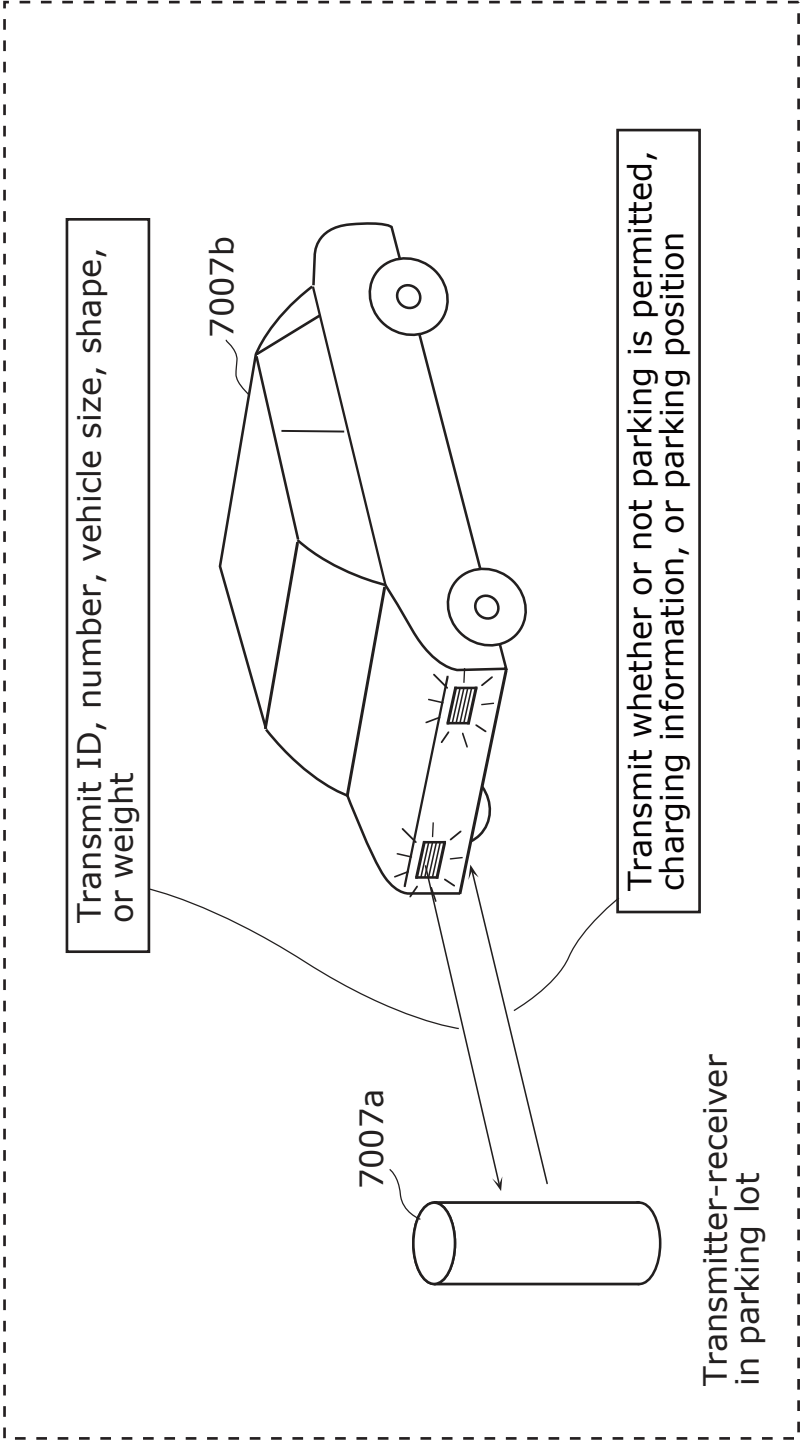


FIG. 423

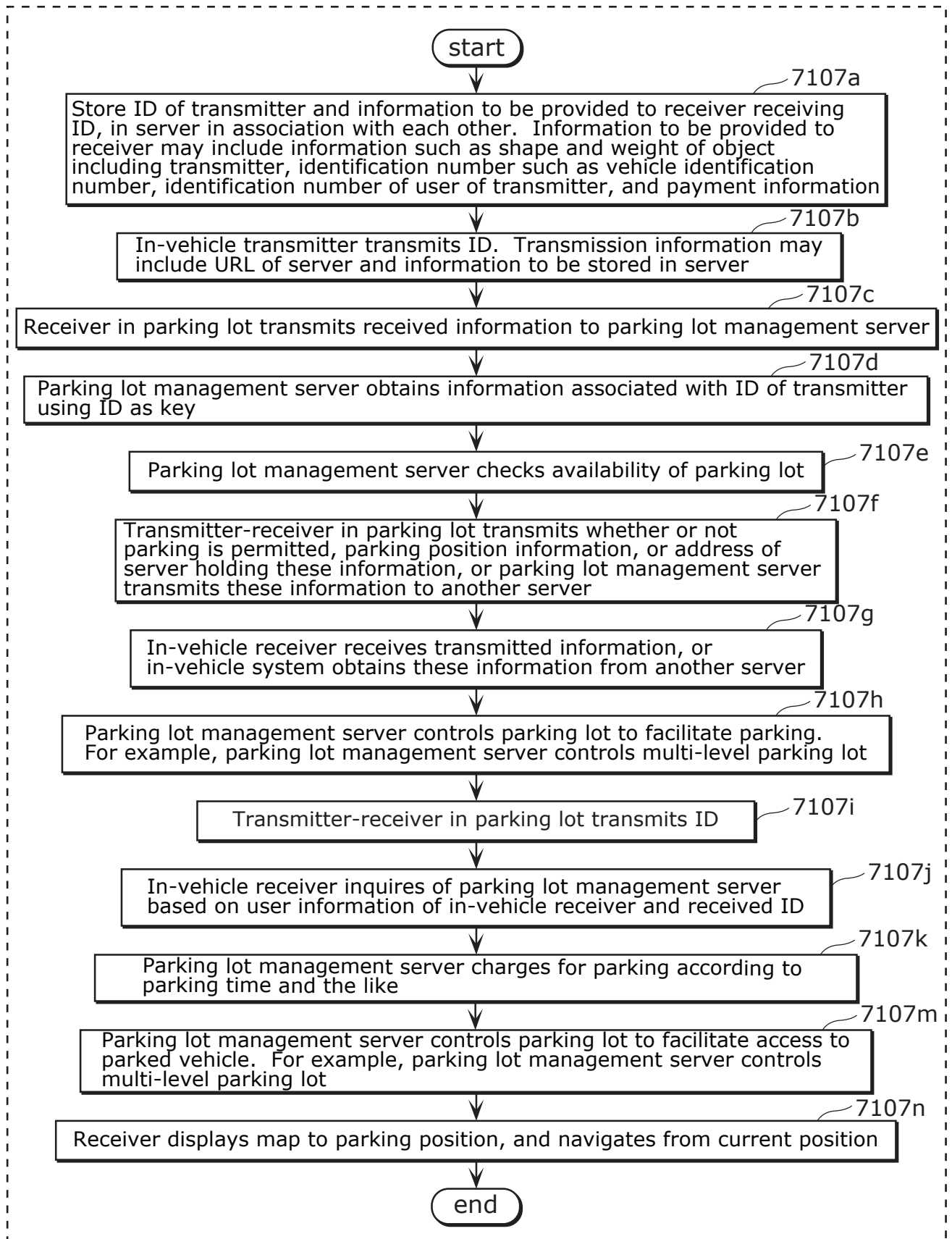


FIG. 424

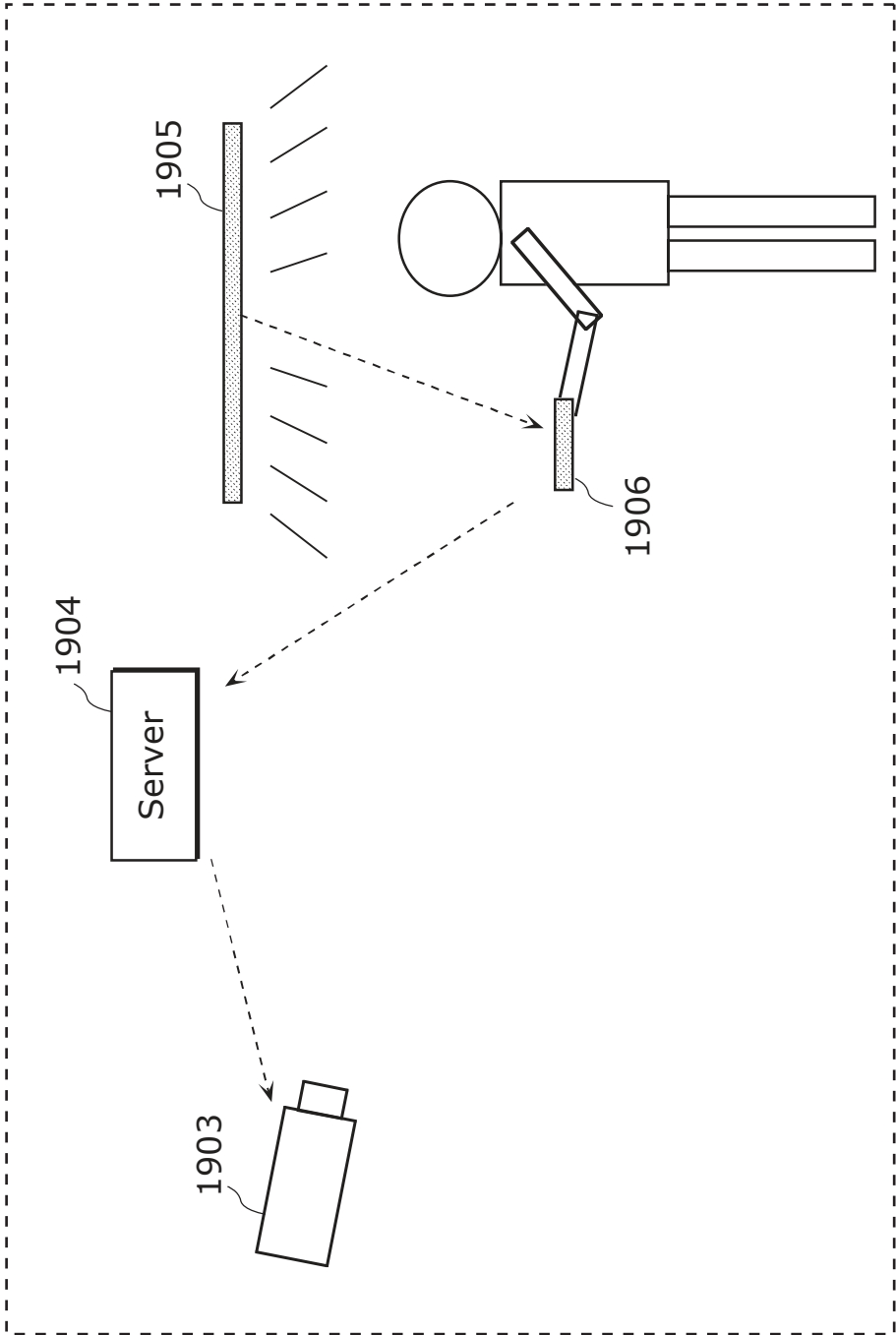


FIG. 425

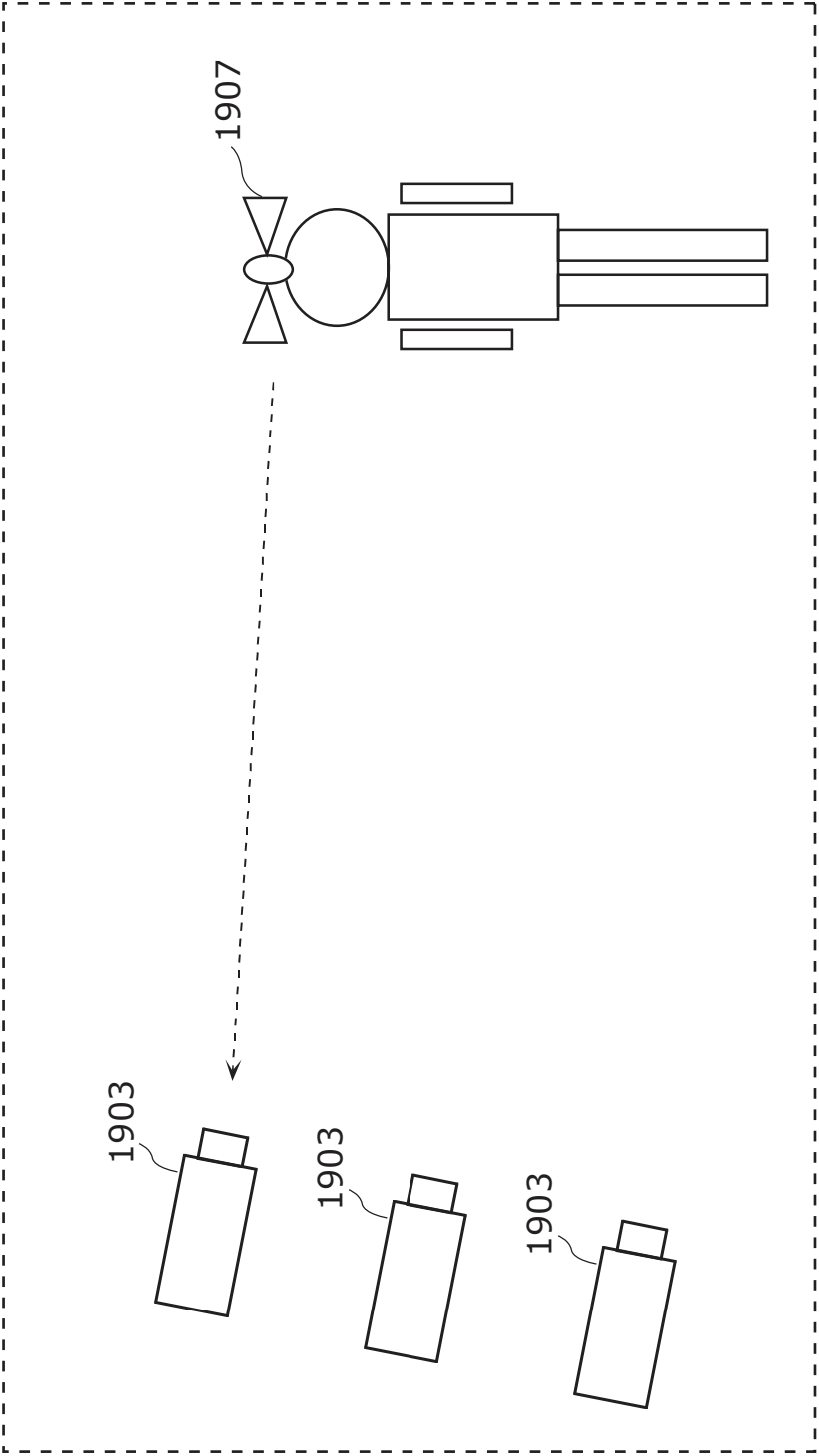


FIG. 426

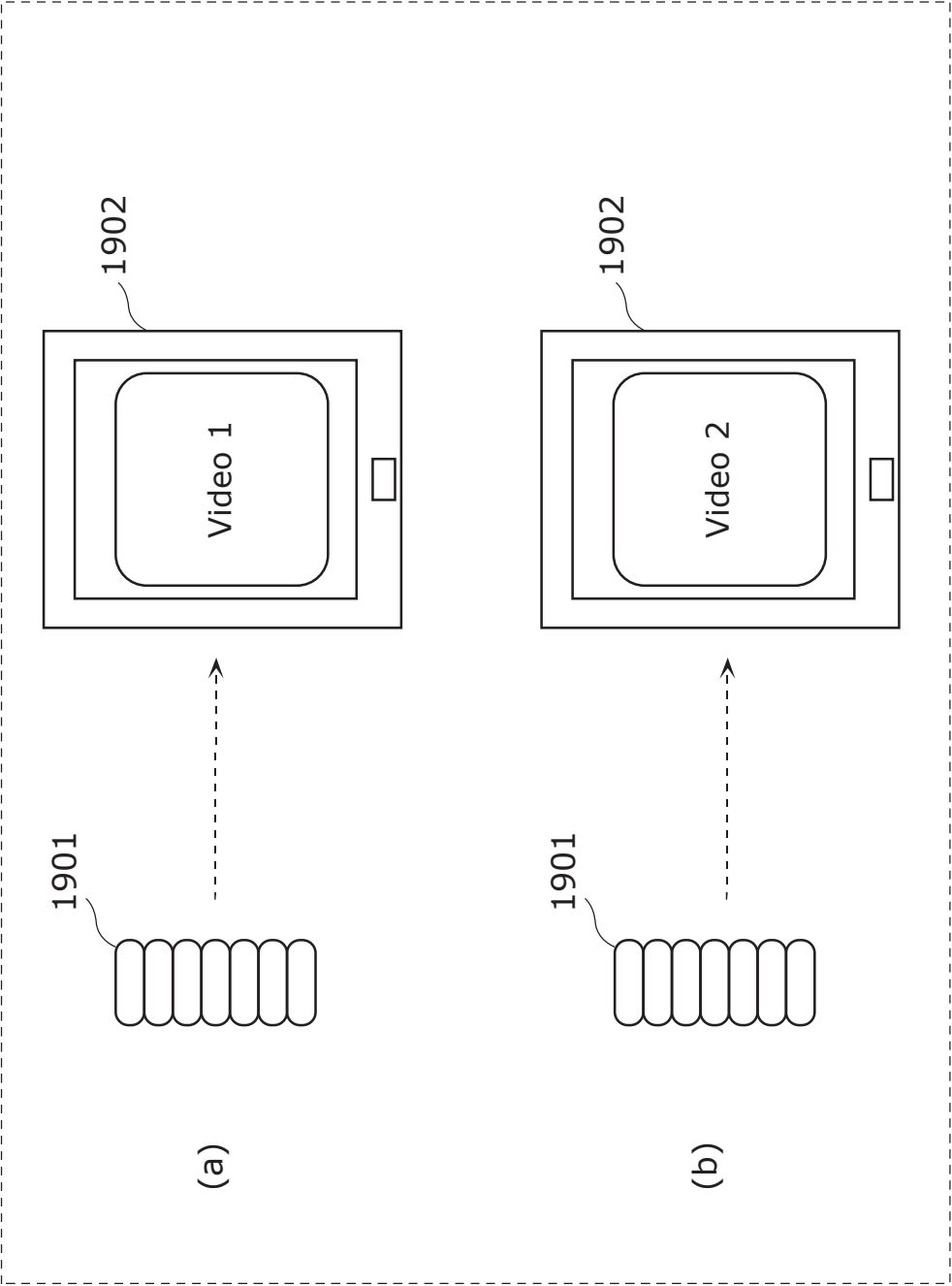


FIG. 427

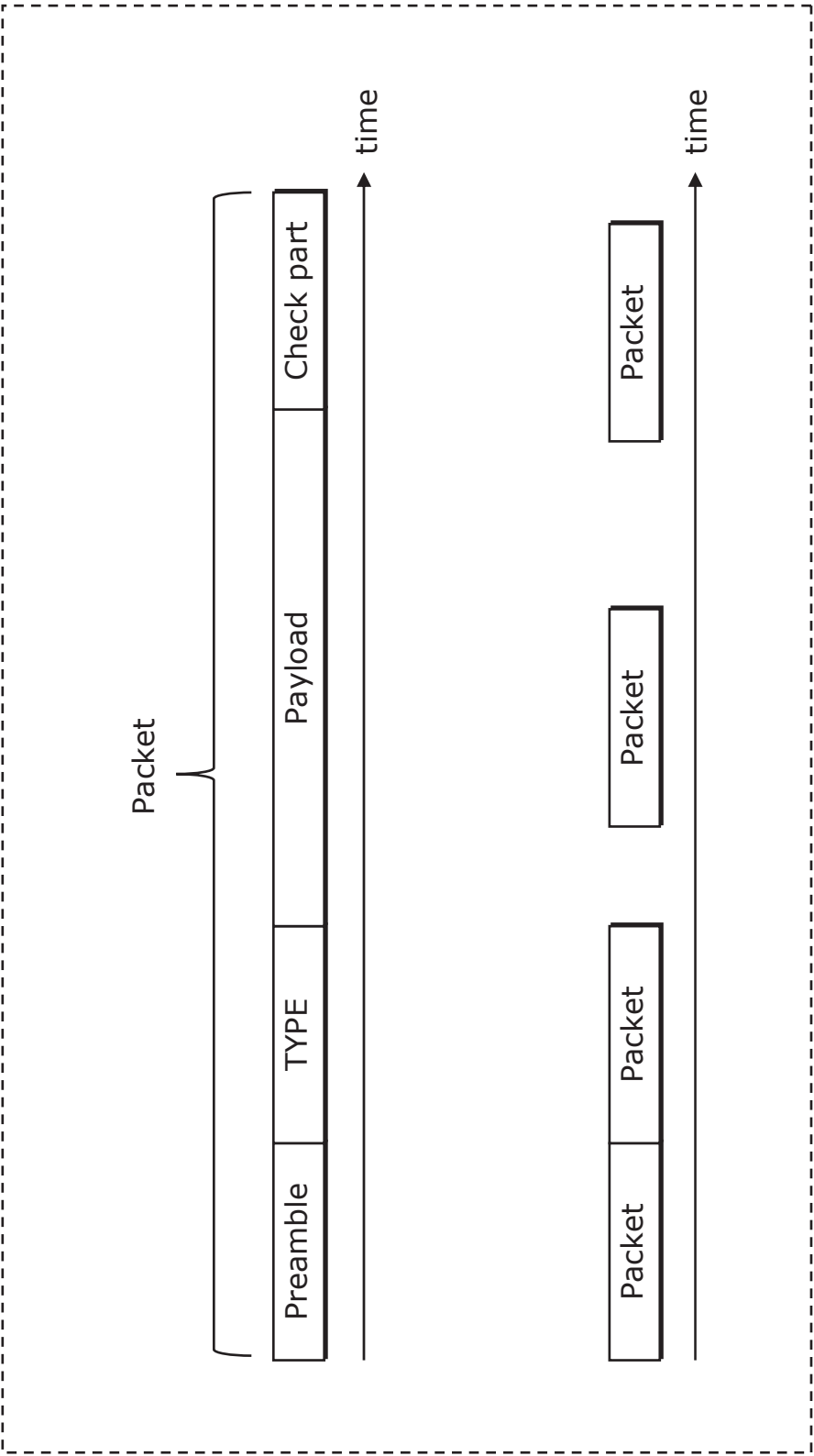


FIG. 428

Basic pattern	Preamble
000	0001 1111 1111
	1111 0001 1111
	1111 1111 1000
0010	0010 1111 1111
	1111 0010 1111
	1111 1111 0010
0100	0100 1111 1111
	1111 0100 1111
	1111 1111 0100
01010	0101 0111 1111
	1111 1110 1010
01100	0110 0111 1111
	1111 1110 1100
00110	0011 0111 1111
	1111 1110 0110
0111 1111 0	0111 1111 0011
	0111 1111 0101
	0111 1111 0110
	1100 1111 1110
	1010 1111 1110
	0110 1111 1110
0111 1111 10	0111 1111 1001
	0111 1111 1010
	1001 1111 1110
	0101 1111 1110
0111 1111 110	0111 1111 1100
	0011 1111 1110

FIG. 429

(a)			(b)			(c)		

FIG. 430

TYPE	Payload length [bit]
0000	-
0001	2
0010	4
0011	8
0100	16
0101	32
0110	64
0111	128
1000	256
1001	512
1010	1024
1011	2048
1100	4096
1101	8192
1110	16384
1111	reserved

(a)

TYPE	Data length
000	-
001	32
010	64
011	128
100	256
101	512
110	1024
111	-

(b)

Number of divisions	TYPE length [bit]
-	8
2	4
4	5
8	6
16	7
32	8
64	9
Streaming	3 or 4

(c)

FIG. 431

Payload [bit]	Check part A [bit]	Check part B [bit]
1	2	0
2	2	2
4	2	2
8	2	2
16	4	4
32	8	8
64	16	16
128	32	32
256	32	32
512	32	64
1024	32	64
2048	32	128
4096	32	128
8192	32	128
16384	32	256
32768	32	256

FIG. 432

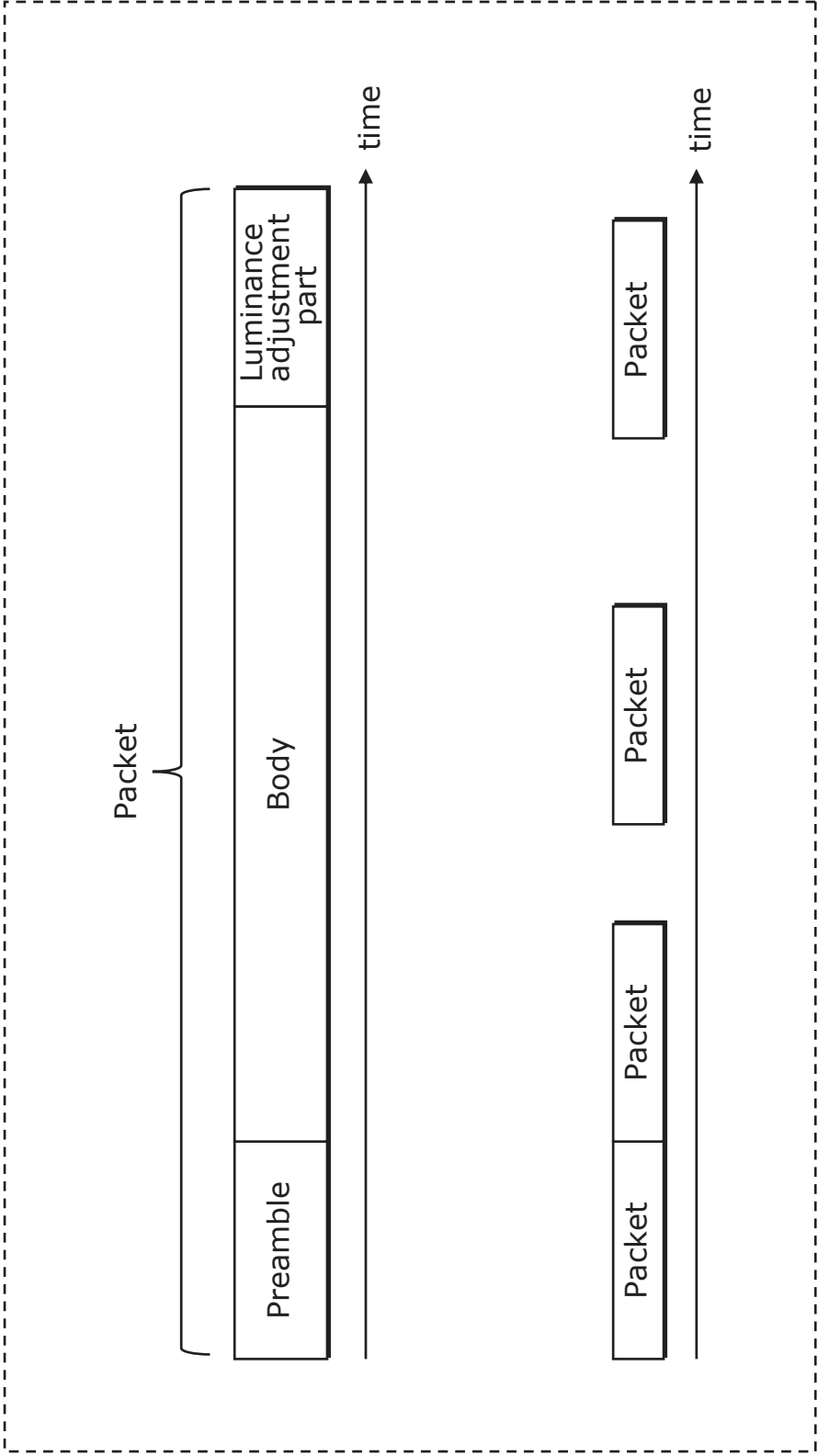


FIG. 433

Luminance	Preamble	Body	Luminance adjustment part
77.55%	111000111	4PPM (75%)	1 1111 111
┆	111000111	4PPM (75%)	┆
61.22%	111000111	4PPM (75%)	0 0000 000
61.22%	111000111	4PPM (50%)	1 1111 111
┆	111000111	4PPM (50%)	┆
44.90%	111000111	4PPM (50%)	0 0000 000
55.10%	000111000	4PPM (50%)	1 1111 111
┆	000111000	4PPM (50%)	┆
38.78%	000111000	4PPM (50%)	0 0000 000
38.78%	000111000	4PPM (25%)	1 1111 111
┆	000111000	4PPM (25%)	┆
22.45%	000111000	4PPM (25%)	0 0000 000
(a)			
Preamble: 9 slot			
Body: 32 slot			
Luminance adjustment part: 8 slot			

Luminance	Preamble	Body	Luminance adjustment part
77.27%	111000111	4PPM (75%)	1111 111
┆	111000111	4PPM (75%)	┆
61.36%	111000111	4PPM (75%)	0000 000
61.36%	111000111	4PPM (50%)	1111 111
┆	111000111	4PPM (50%)	┆
45.45%	111000111	4PPM (50%)	0000 000
54.55%	000111000	4PPM (50%)	1111 111
┆	000111000	4PPM (50%)	┆
38.64%	000111000	4PPM (50%)	0000 000
38.64%	000111000	4PPM (25%)	1111 111
┆	000111000	4PPM (25%)	┆
22.73%	000111000	4PPM (25%)	0000 000
(b)			
Preamble: 9 slot			
Body: 28 slot			
Luminance adjustment part: 7 slot			

FIG. 434

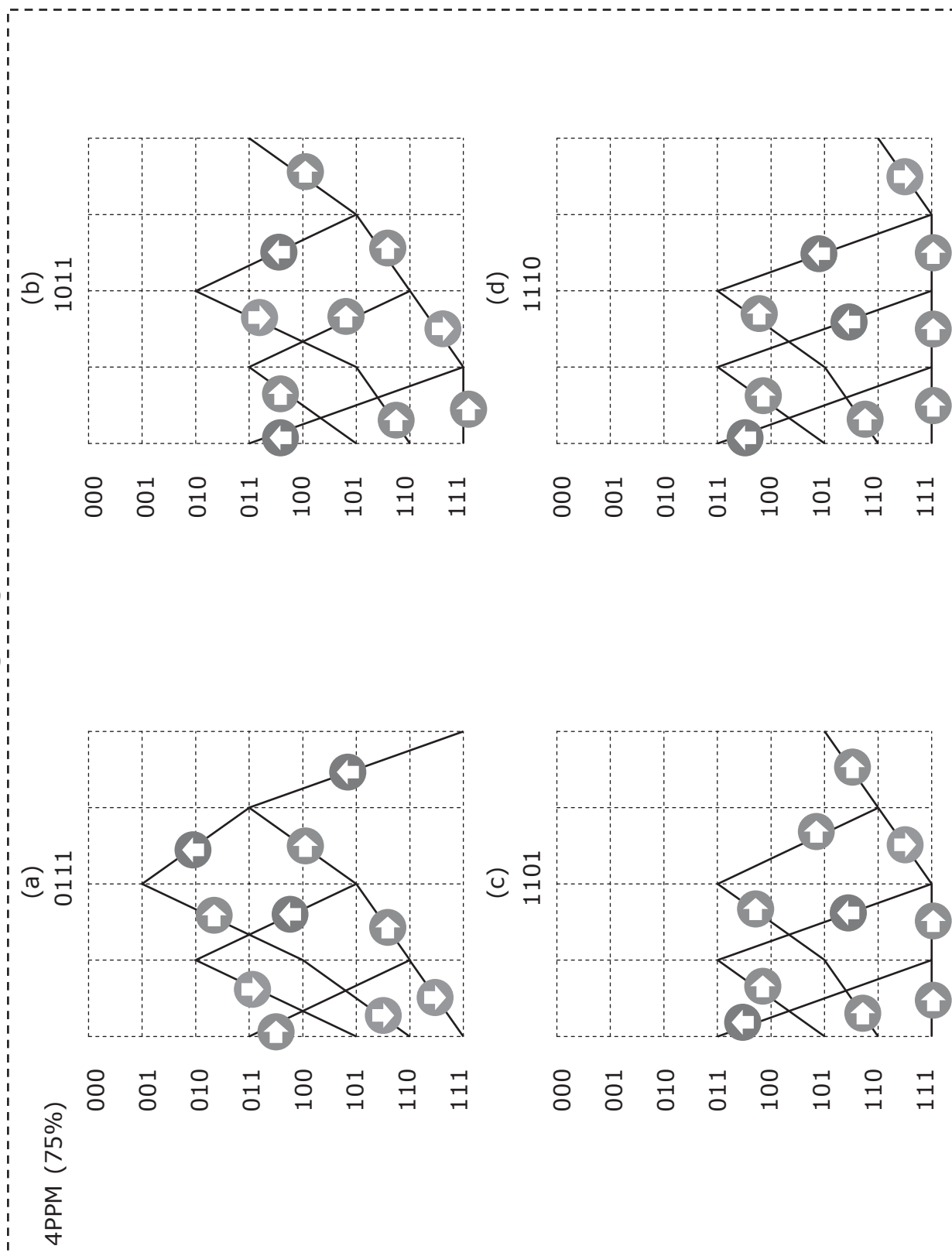


FIG. 435

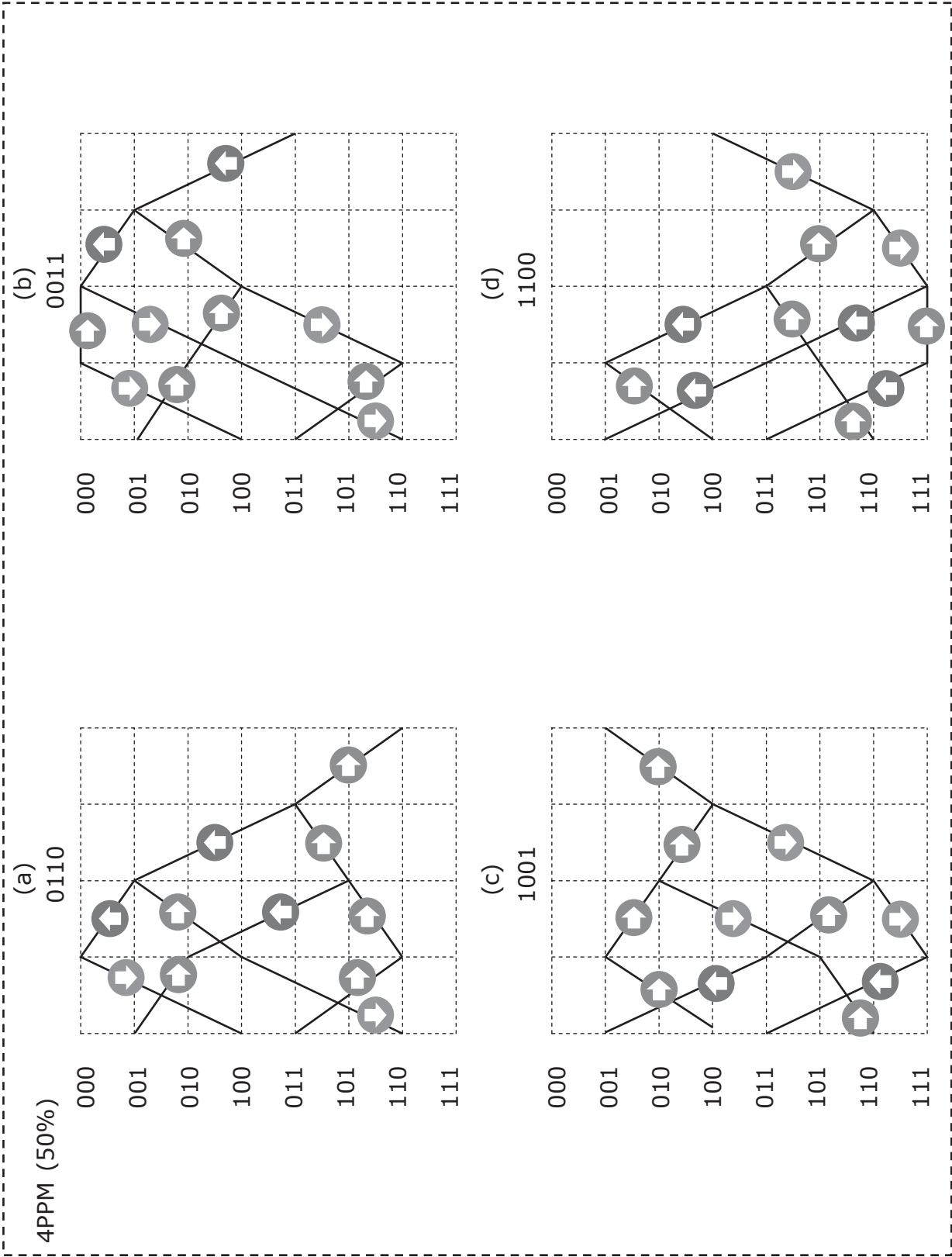


FIG. 436

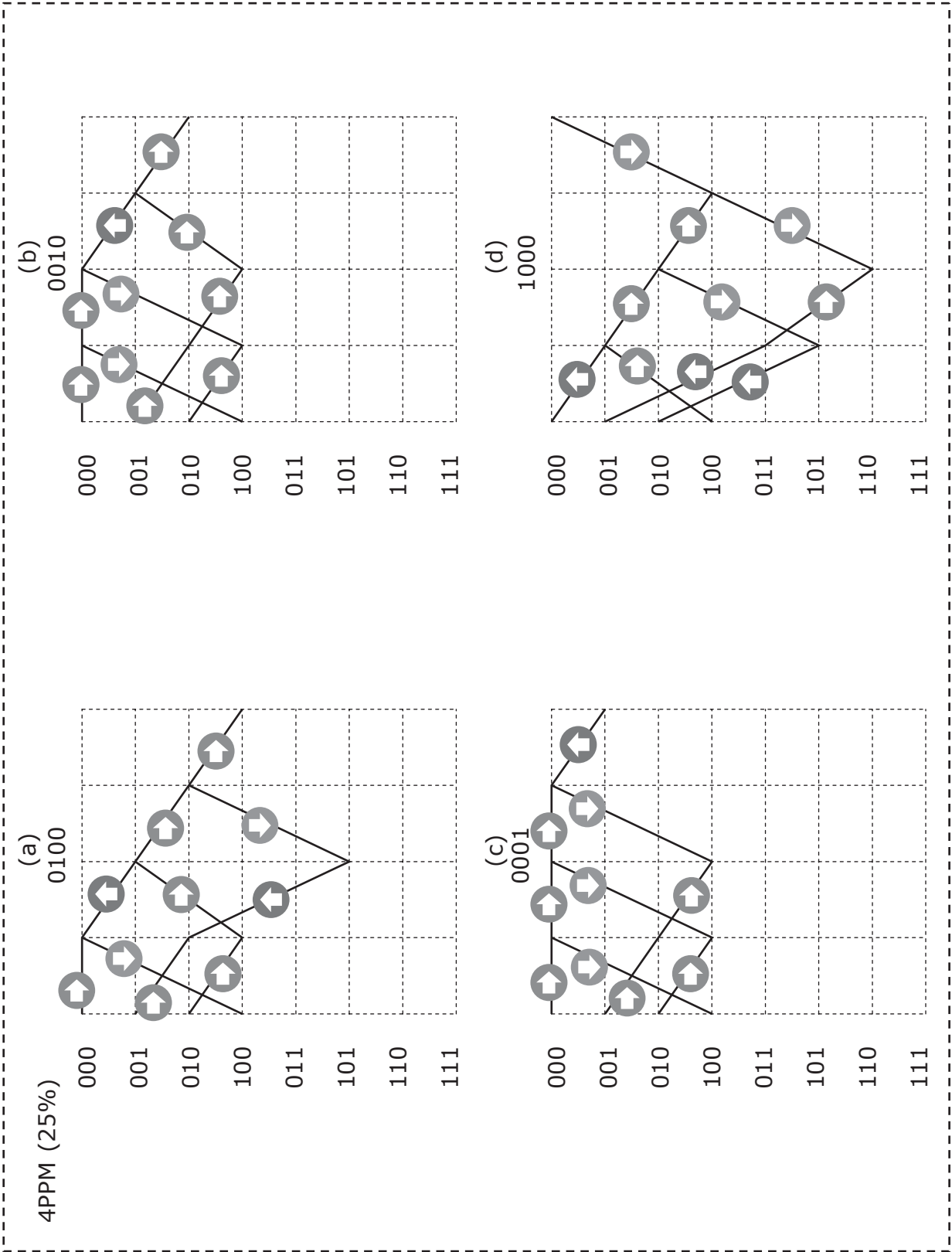


FIG. 437

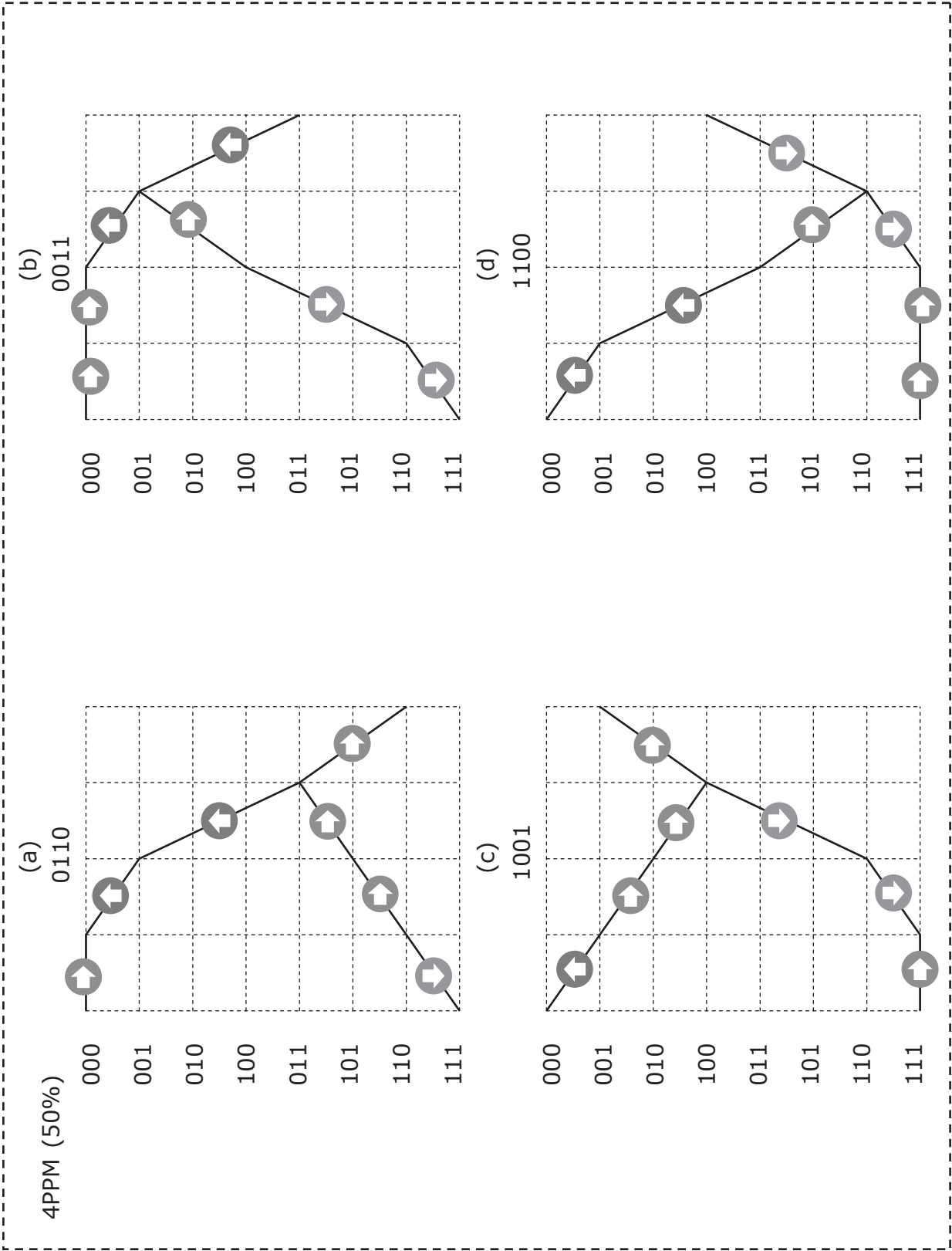


FIG. 438

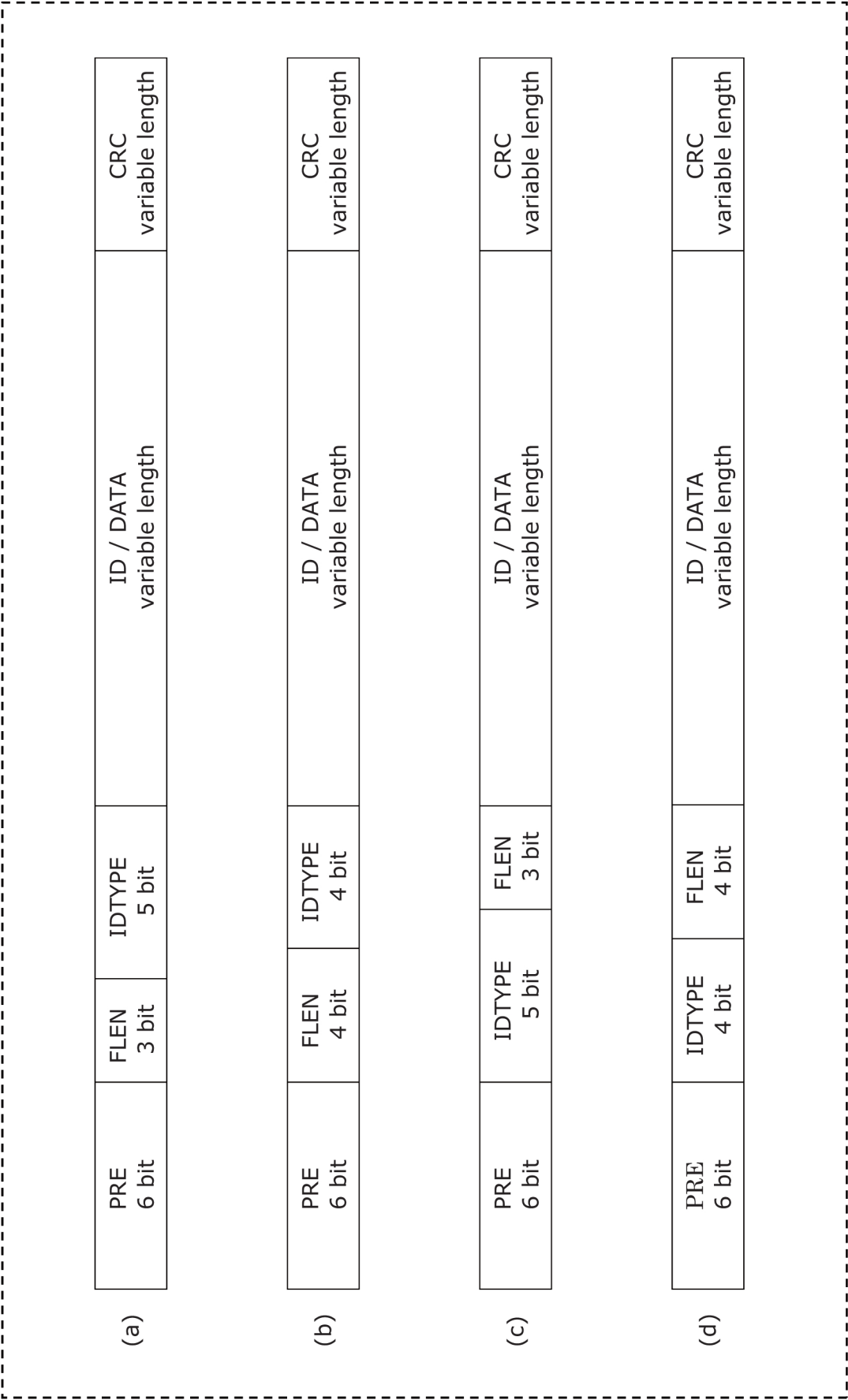


FIG. 439

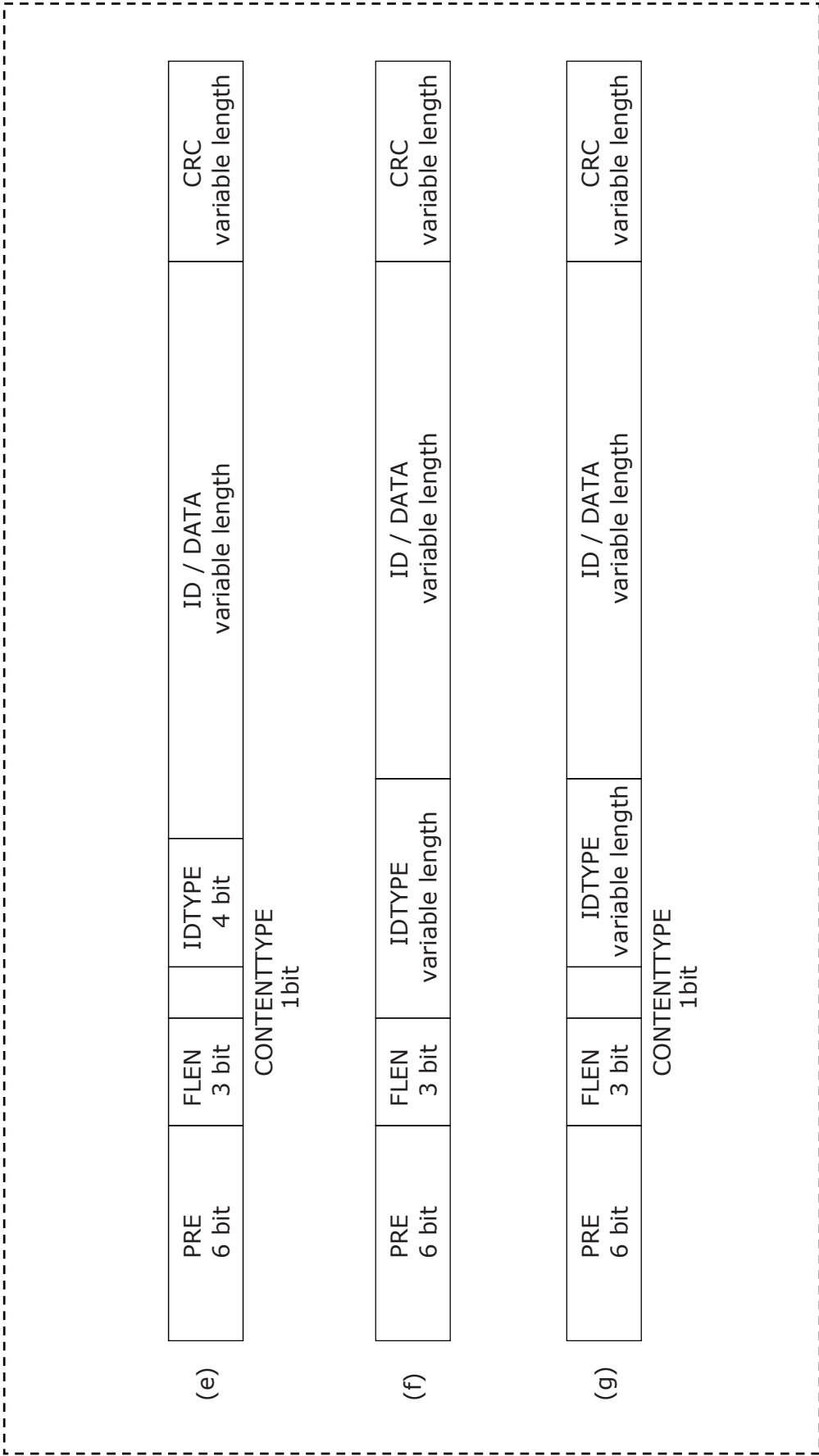


FIG. 440

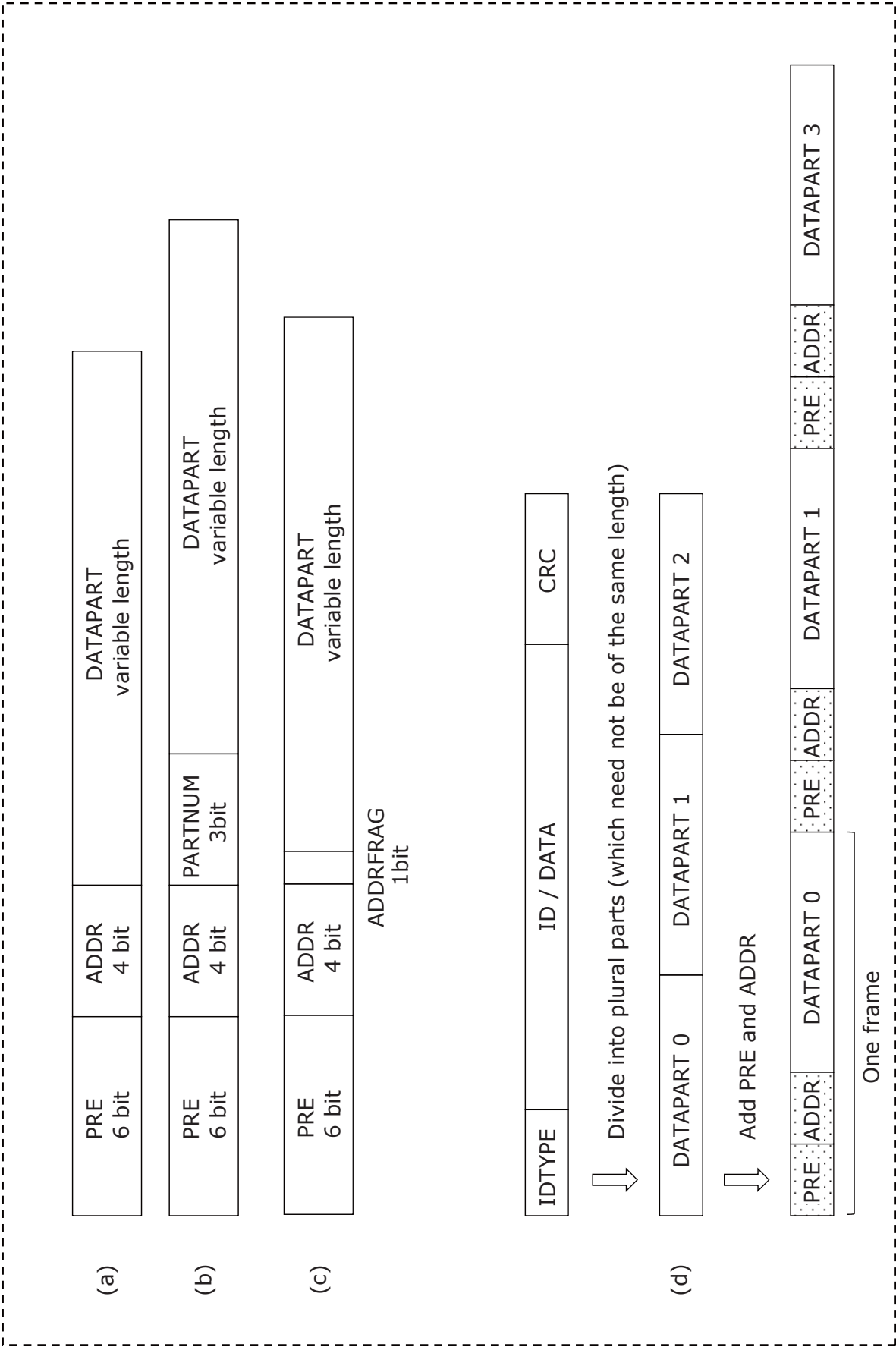


FIG. 441

FLEN [bit]	Lengthof ID/DATA [bit]
000	For experiment
001	32
010	64
011	128
100	256
101	512
110	1024
111	2048

(a)

FLEN [bit]	Lengthof ID/DATA [bit]
0000	For experiment
0001	For experiment
0010	4
0011	8
0100	16
0101	32
0110	64
0111	128
1000	256
1001	512
1010	1024
1011	2048
1100	4096
1101	8192
1110	16384
1111	32768

(b)

FIG. 442

FLEN [bit]	Lengthof ID/DATA [bit]
000	4096
001	2048
010	1024
011	512
100	256
101	128
110	64
111	32

(a)

FLEN [bit]	Lengthof ID/DATA [bit]
0000	4
0001	8
0010	16
0011	32
0100	64
0101	128
0110	256
0111	512
1000	1024
1001	2048
1010	4096
1011	8192
1100	16384
1101	32768
1110	65536
1111	reserved

(b)

FIG. 443

Length to be checked	Length of CRC	Generator polynomial
<= 16	8	$X^8 + X^7 + X^6 + X^4 + X^2 + 1$
<= 64	12	$X^{12} + X^{11} + X^3 + X^2 + X + 1$
<= 256	16	$X^{16} + X^{15} + X^2 + 1$
<= 1024	20	

(a)

Length to be checked	Length of CRC	Generator polynomial
<= 16	8	$X^8 + X^7 + X^6 + X^4 + X^2 + 1$
<= 64	12	$X^{12} + X^{11} + X^3 + X^2 + X + 1$
<= 256	16	$X^{16} + X^{15} + X^2 + 1$
>256	24	$X^{24} + X^{23} + X^{18} + X^{17} + X^{14} + X^{11} + X^{10} + X^7 + X^6 + X^5 + X^4 + X^3 + X + 1$

(b)

FIG. 444

PRE [slot]		Length of DATAPART [bit]
Last address	Not last address	
1101 0000 0000	0000 0000 1101	64
1011 0000 0000	0000 0000 1011	32
1100 1000 0000	0000 0001 1001	16
1001 1000 0000	0000 0001 0011	8
1010 1000 0000	0000 0001 0101	4

(a)

PRE [slot]	Length of DATAPART [bit]
1101 0000 0000	64 (Not last address)
1011 0000 0000	32 (Not last address)
1100 1000 0000	16 (Not last address)
1001 1000 0000	8 (Not last address)
1010 1000 0000	4 (Not last address)
0000 0001 0101	Last address

(b)

PRE [slot]	The number of divisions
1110 0000 0000	1 (Not divided)
0000 0000 0111	1 (Not divided)
0000 0000 1101	2
0000 0000 1011	4
0000 0001 0101	6
0000 0001 0011	8
0000 0001 1001	10

(c)

FIG. 445

ADDR [bit]	Address of DATAPART
0000	0
0001	1
0010	2
0011	3
0100	4
0101	5
0110	6
0111	7
1000	8
1001	9
1010	10
1011	11
1100	12
1101	13
1110	14
1111	15

(a)

PARTNUM [bit]	The number of divisions
000	2
001	4
010	6
011	8
100	10
101	12
110	14
111	16

(b)

FIG. 446

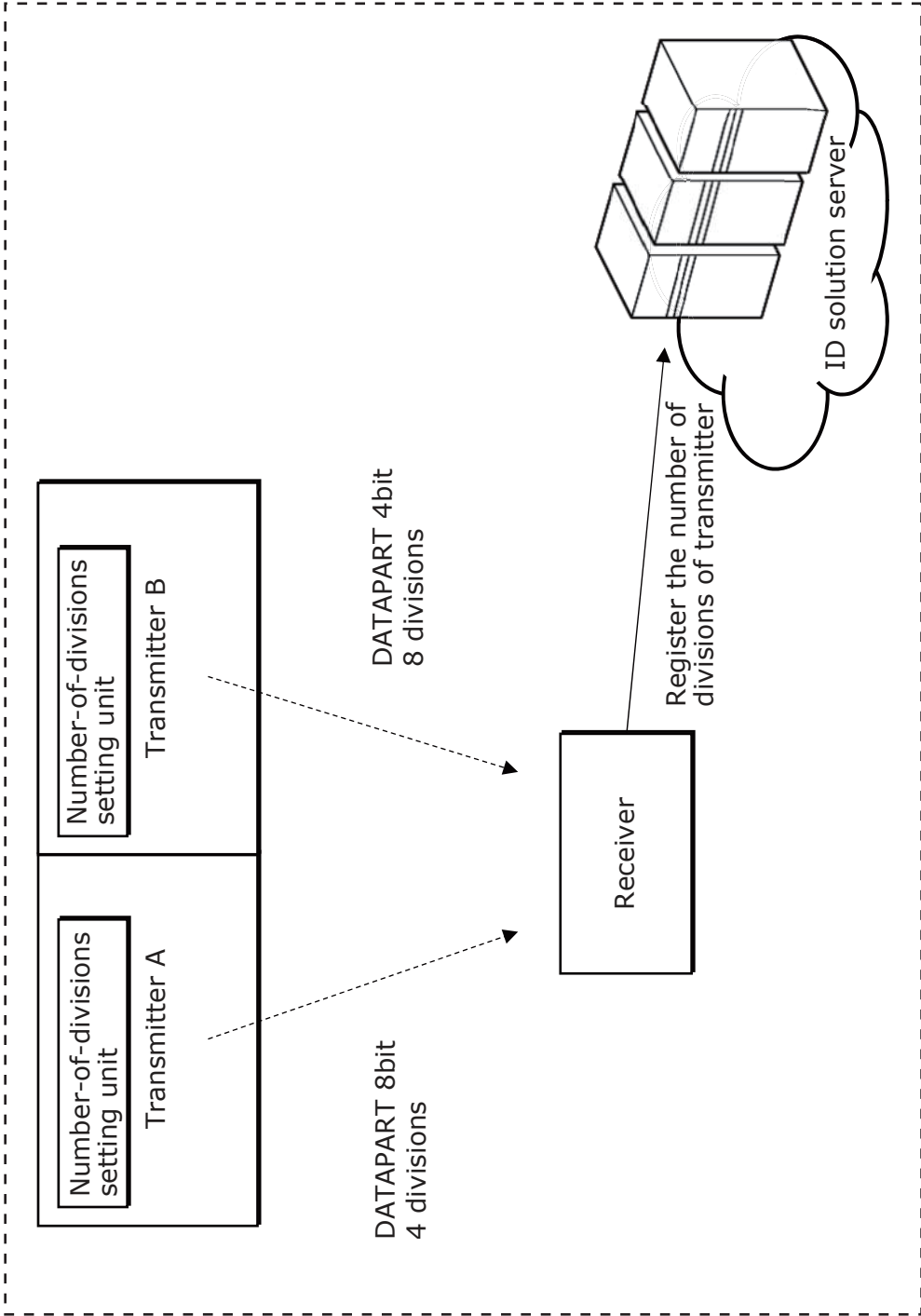


FIG. 447

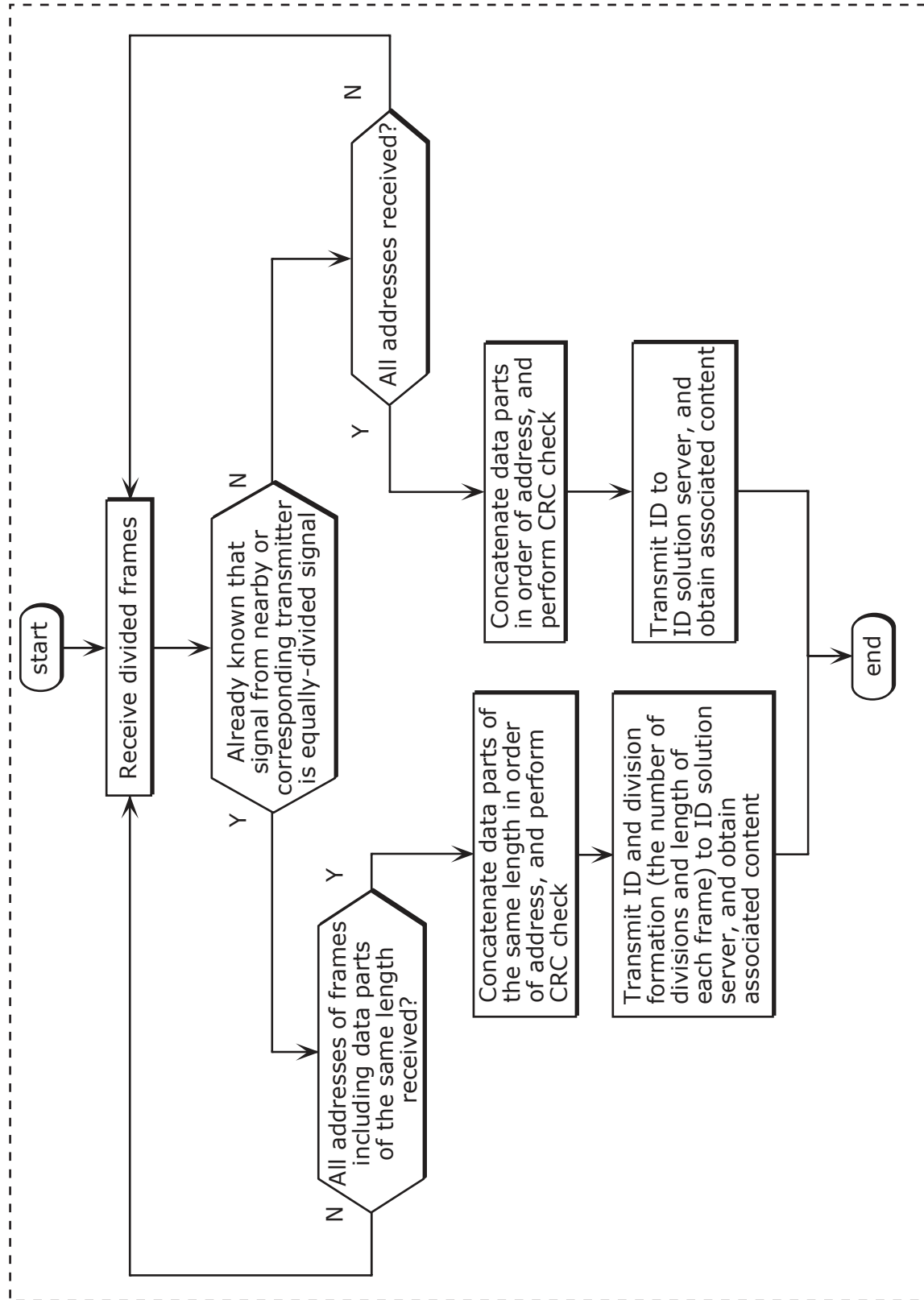


FIG. 448

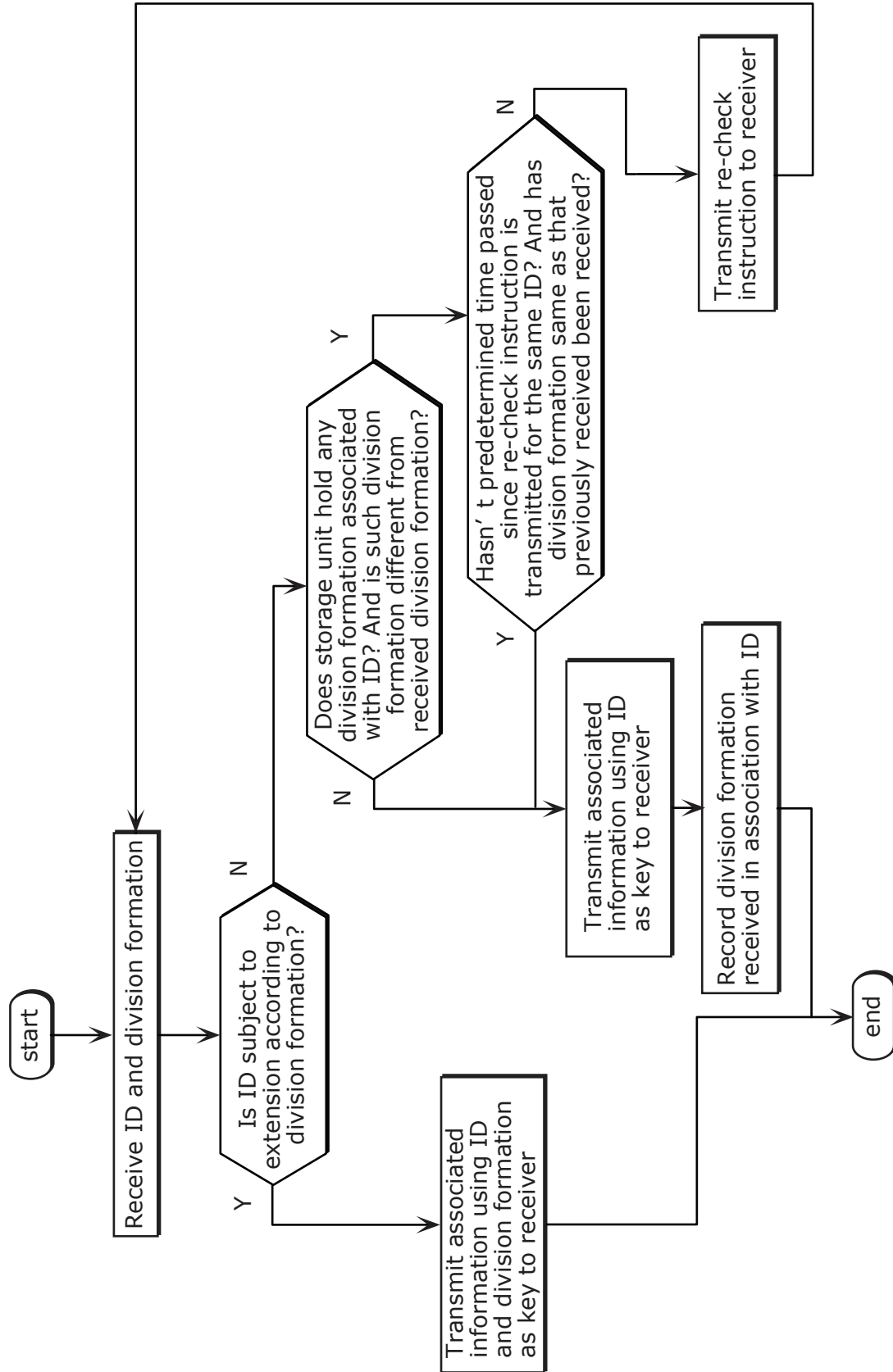


FIG. 449

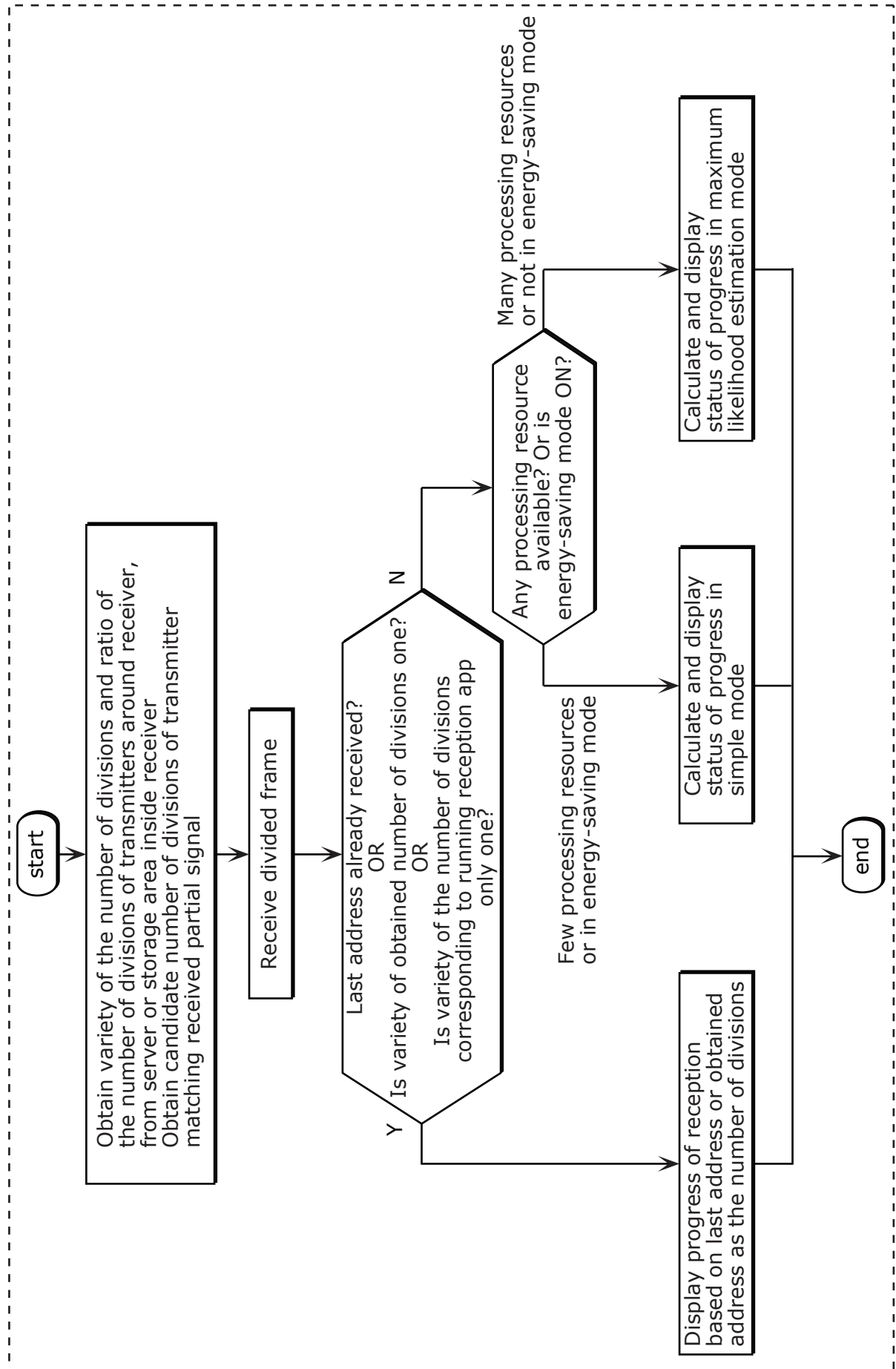


FIG. 450

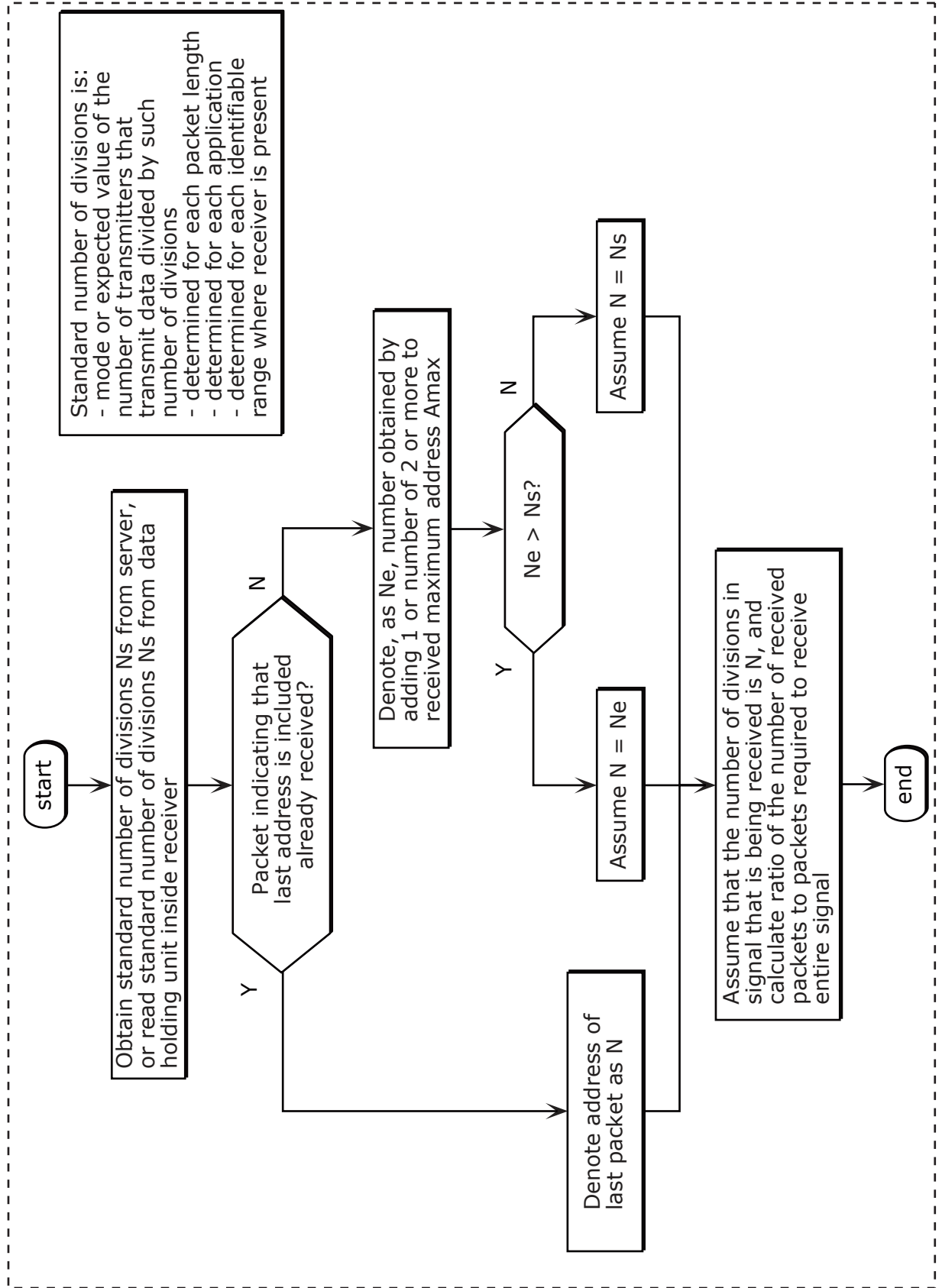


FIG. 451

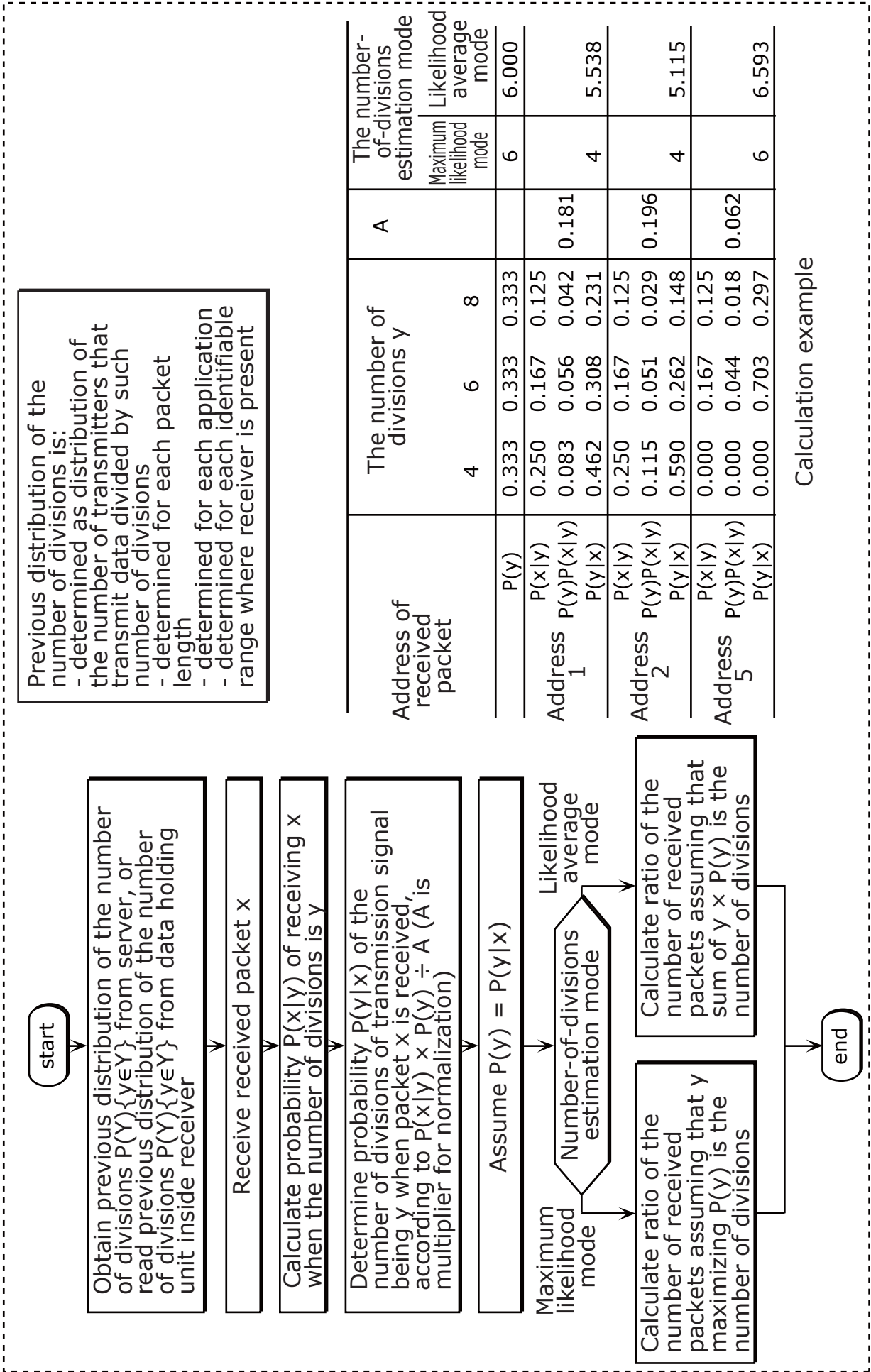


FIG. 452

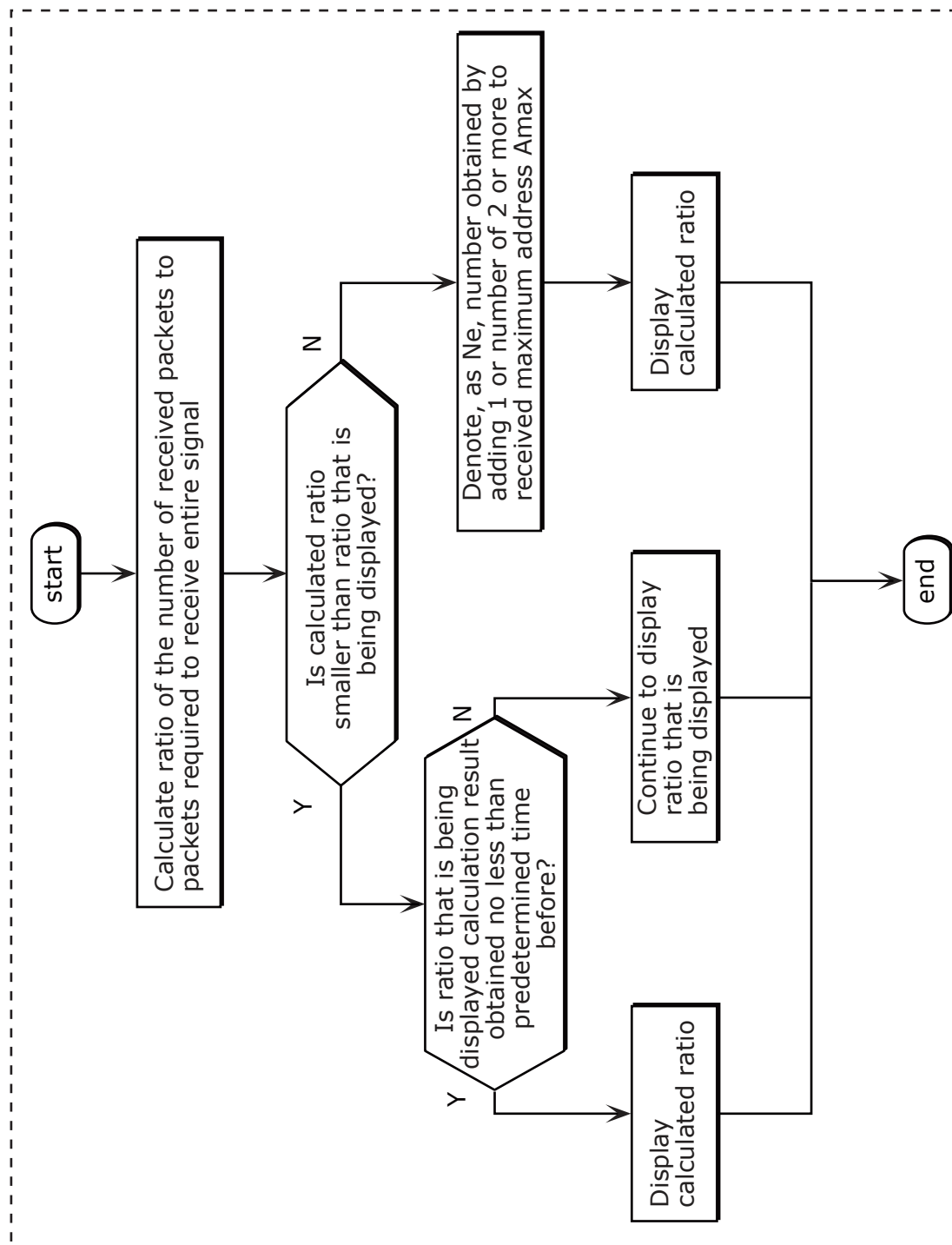


FIG. 453

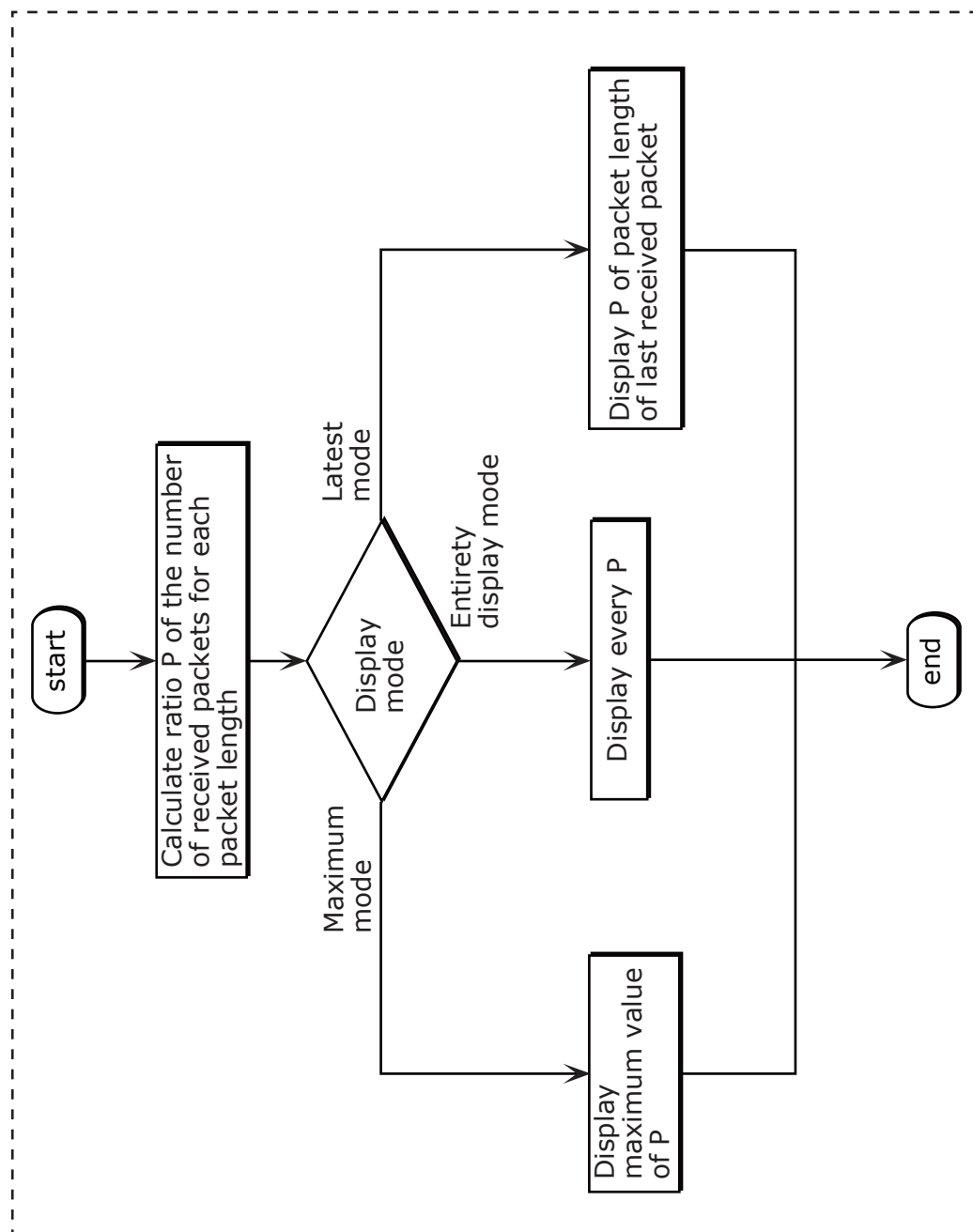


FIG. 454

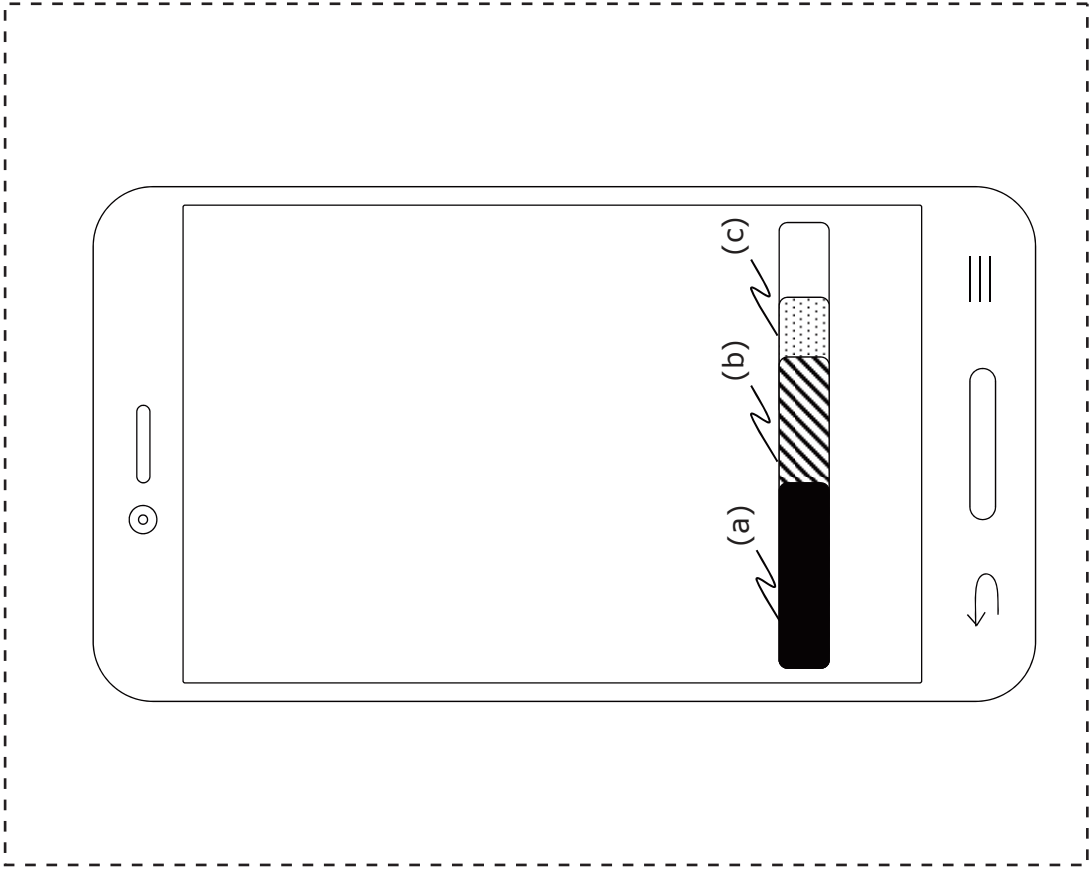


FIG. 455

Length to be checked	Length of CRC	Generator polynomial
<= 16	8	$X^8 + X^7 + X^6 + X^4 + X^2 + 1$
<= 64	12	$X^{12} + X^{11} + X^3 + X^2 + X + 1$
<= 256	16	$X^{16} + X^{15} + X^2 + 1$
<= 1024	20	
<= 4096	24	$X^{24} + X^{23} + X^{18} + X^{17} + X^{14} + X^{11} + X^{10} + X^7 + X^6 + X^5 + X^4 + X^3 + X + 1$
<= 16384	28	
<= 65536	32	$X^{32} + X^{26} + X^{23} + X^{22} + X^{16} + X^{12} + X^{11} + X^{10} + X^8 + X^7 + X^5 + X^4 + X^2 + X + 1$

(a)

Length to be checked	Length of CRC	Generator polynomial
<= 16	8	$X^8 + X^7 + X^6 + X^4 + X^2 + 1$
<= 32	10	$X^{10} + X^9 + X^5 + X^4 + X + 1$
<= 64	12	$X^{12} + X^{11} + X^3 + X^2 + X + 1$
<= 128	14	$X^{14} + X^{12} + X^8 + X^6 + X^4 + X + 1$
<= 256	16	$X^{16} + X^{15} + X^2 + 1$
<= 512	18	
<= 1024	20	
<= 2048	22	
<= 4096	24	$X^{24} + X^{23} + X^{18} + X^{17} + X^{14} + X^{11} + X^{10} + X^7 + X^6 + X^5 + X^4 + X^3 + X + 1$
<= 8192	26	
<= 16384	28	
<= 32768	30	$X^{30} + X^{29} + X^{21} + X^{20} + X^{15} + X^{13} + X^{12} + X^{11} + X^8 + X^7 + X^6 + X^2 + X + 1$
<= 65536	32	$X^{32} + X^{26} + X^{23} + X^{22} + X^{16} + X^{12} + X^{11} + X^{10} + X^8 + X^7 + X^5 + X^4 + X^2 + X + 1$
<= 131072	34	

(b)

FIG. 456

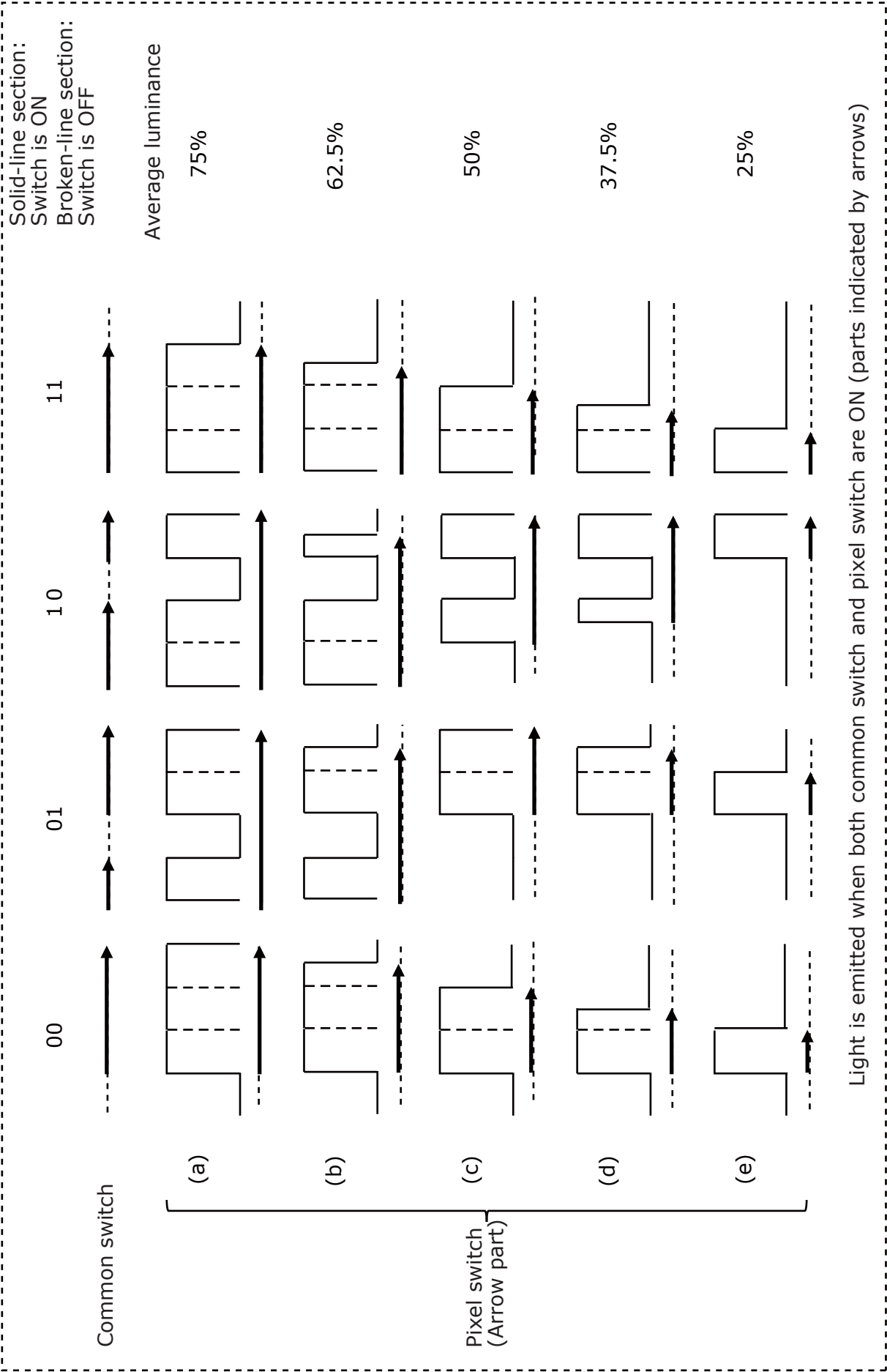


FIG. 457

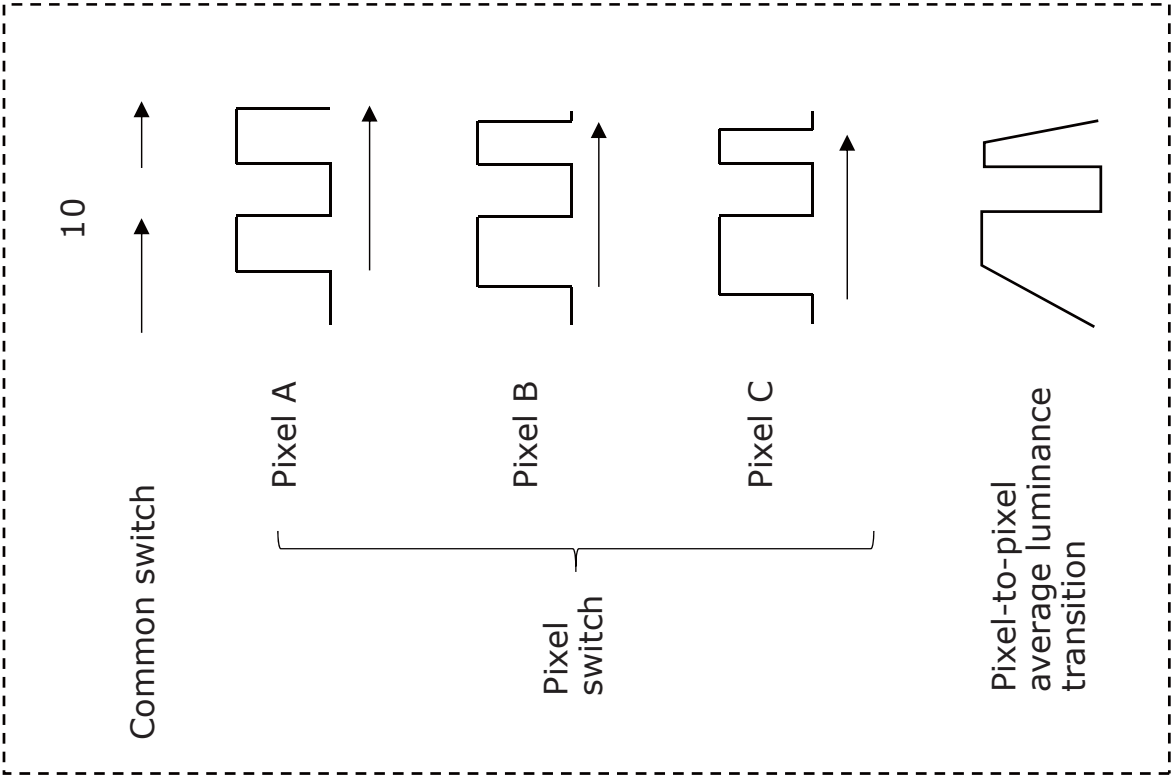


FIG. 458

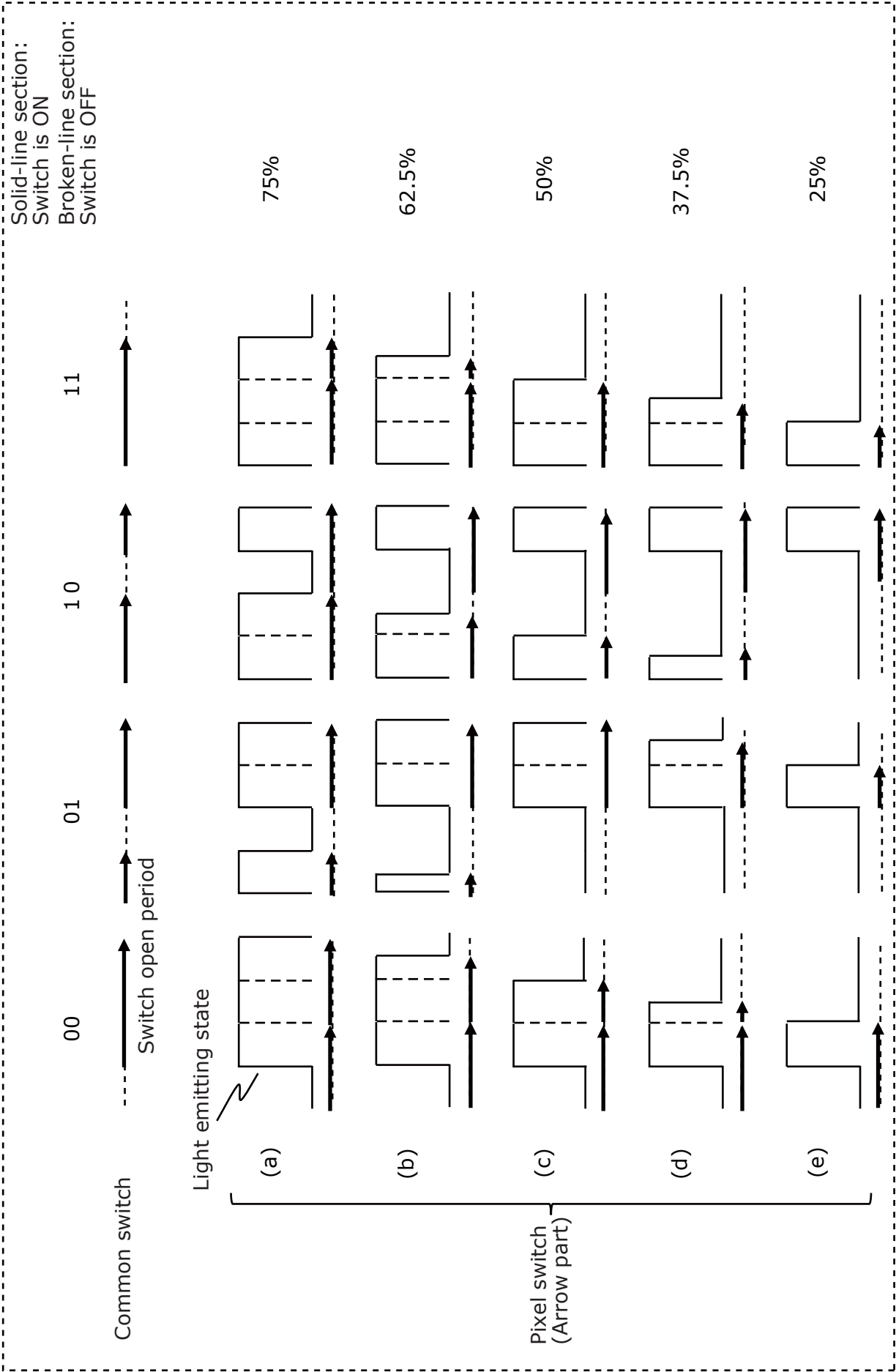


FIG. 459

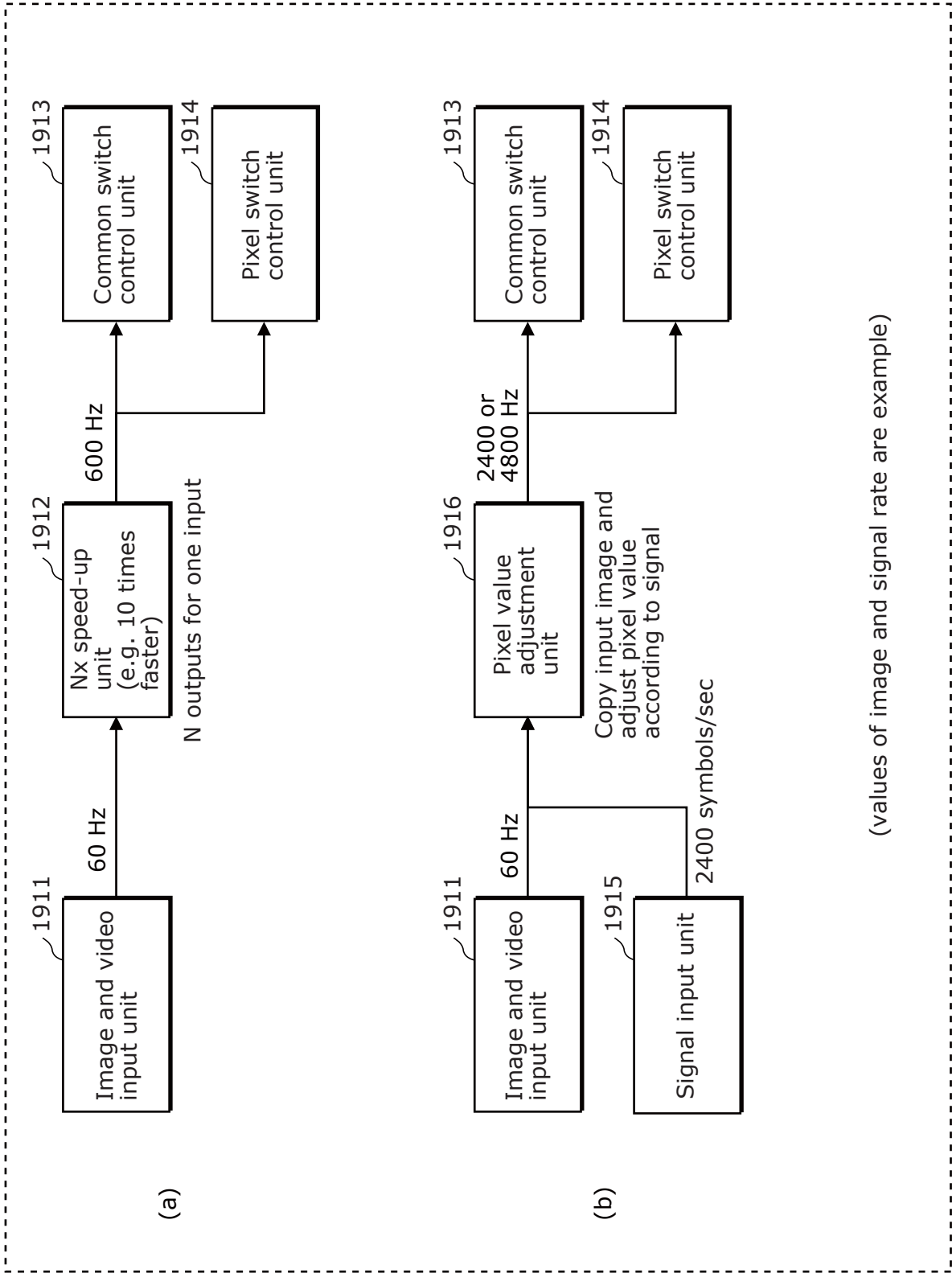


FIG. 460

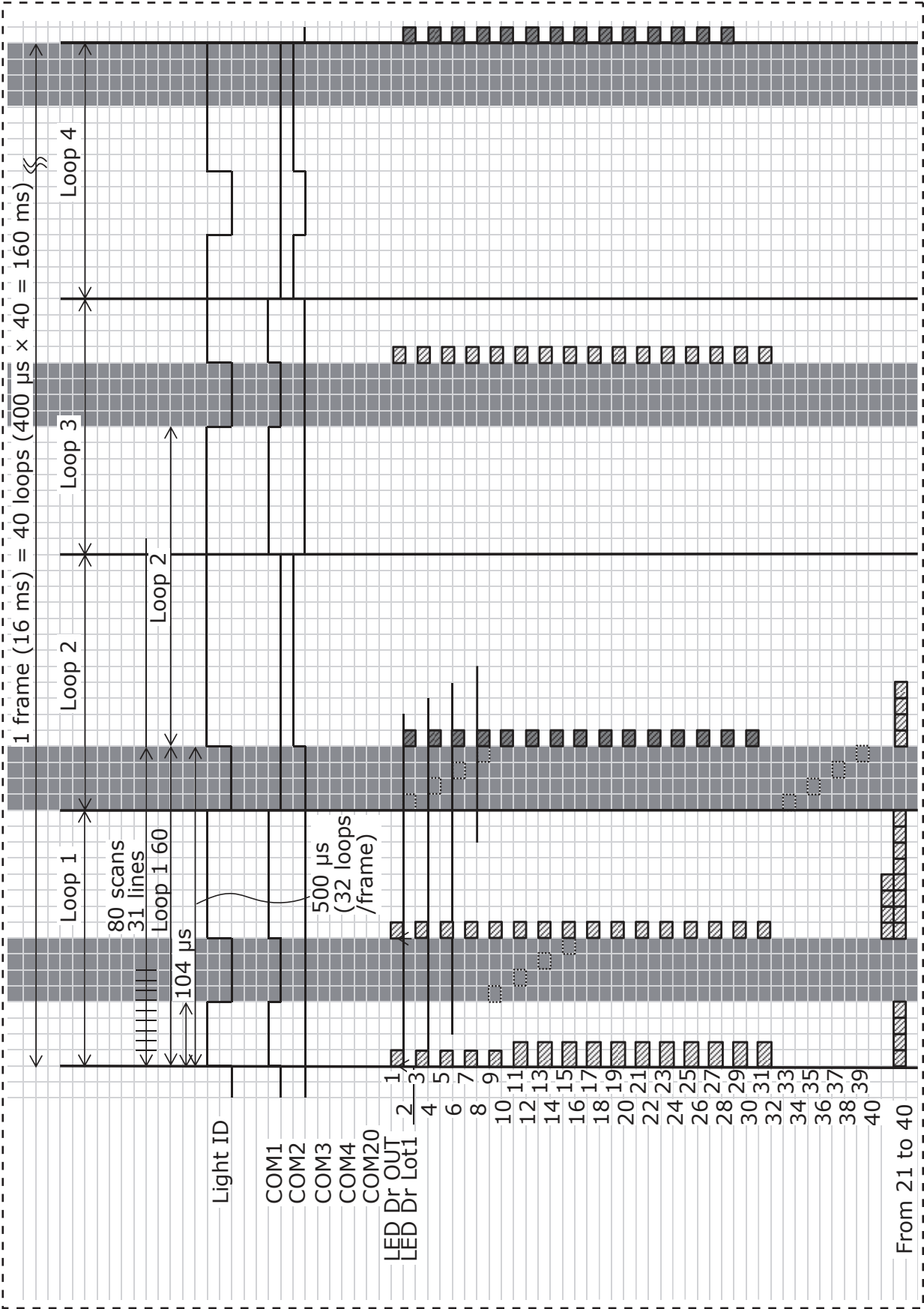


FIG. 461

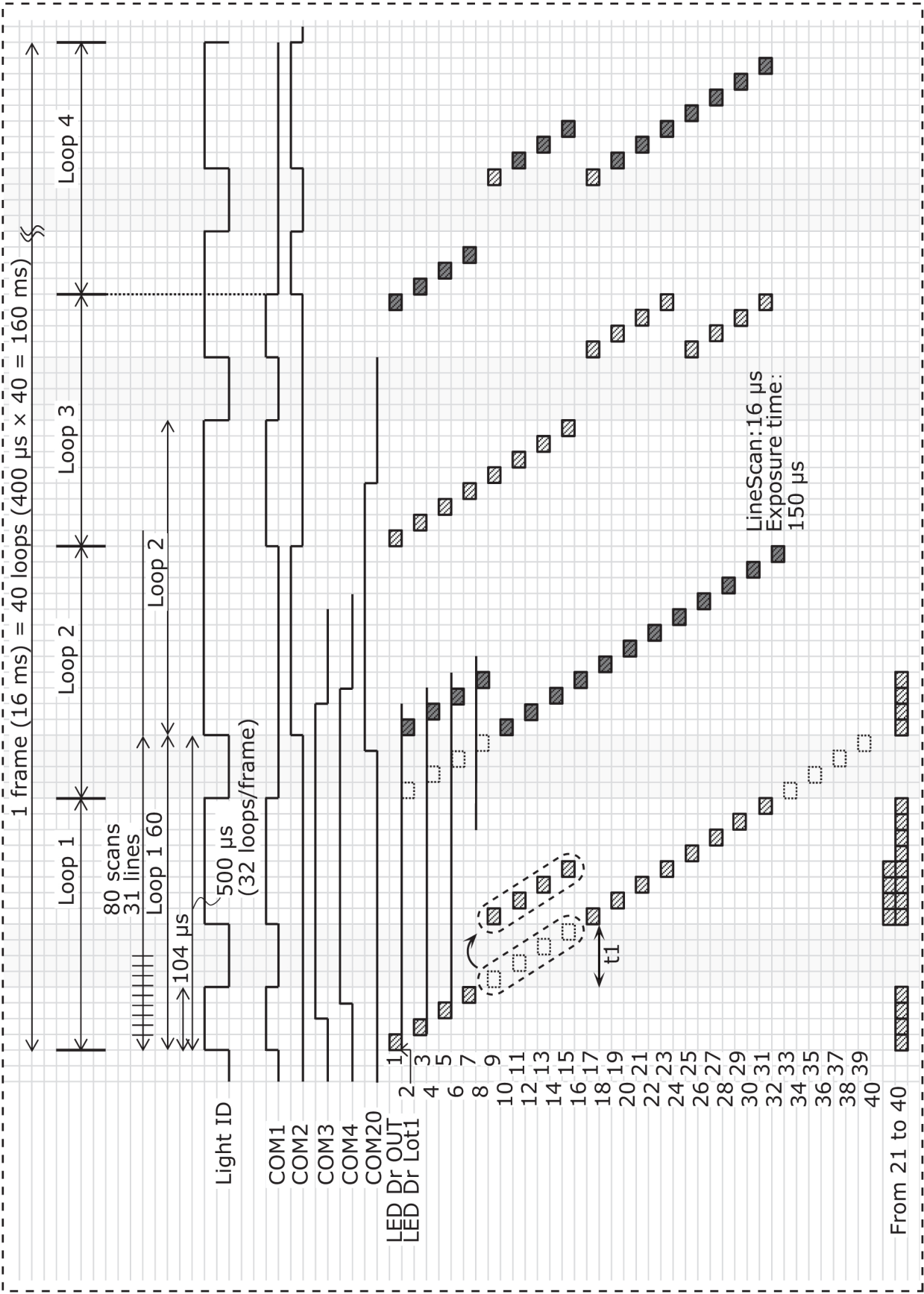


FIG. 462

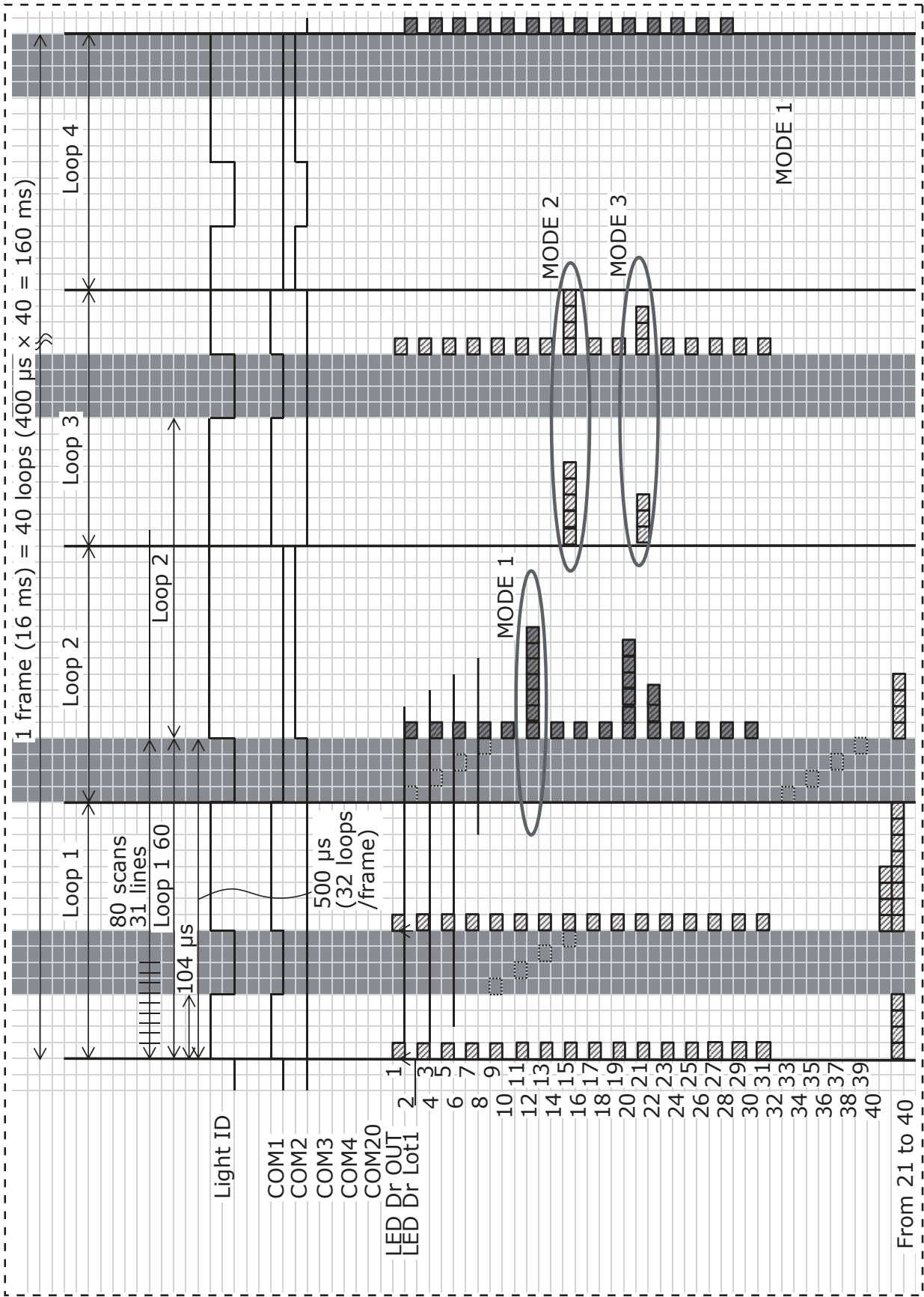


FIG. 463A

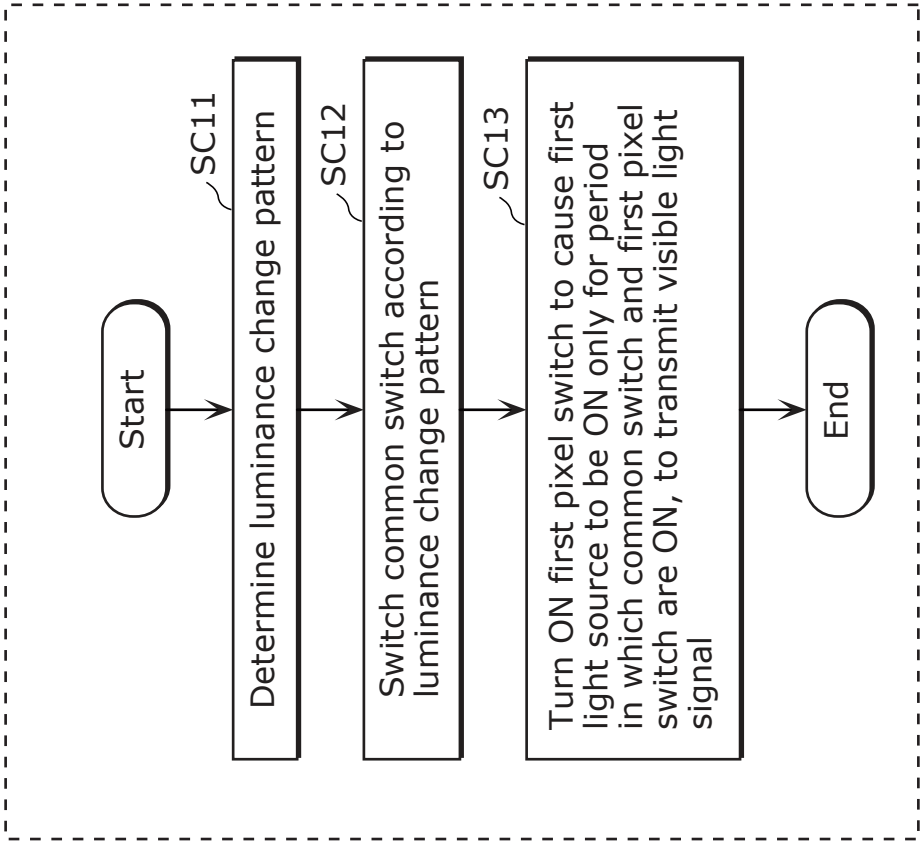


FIG. 463B

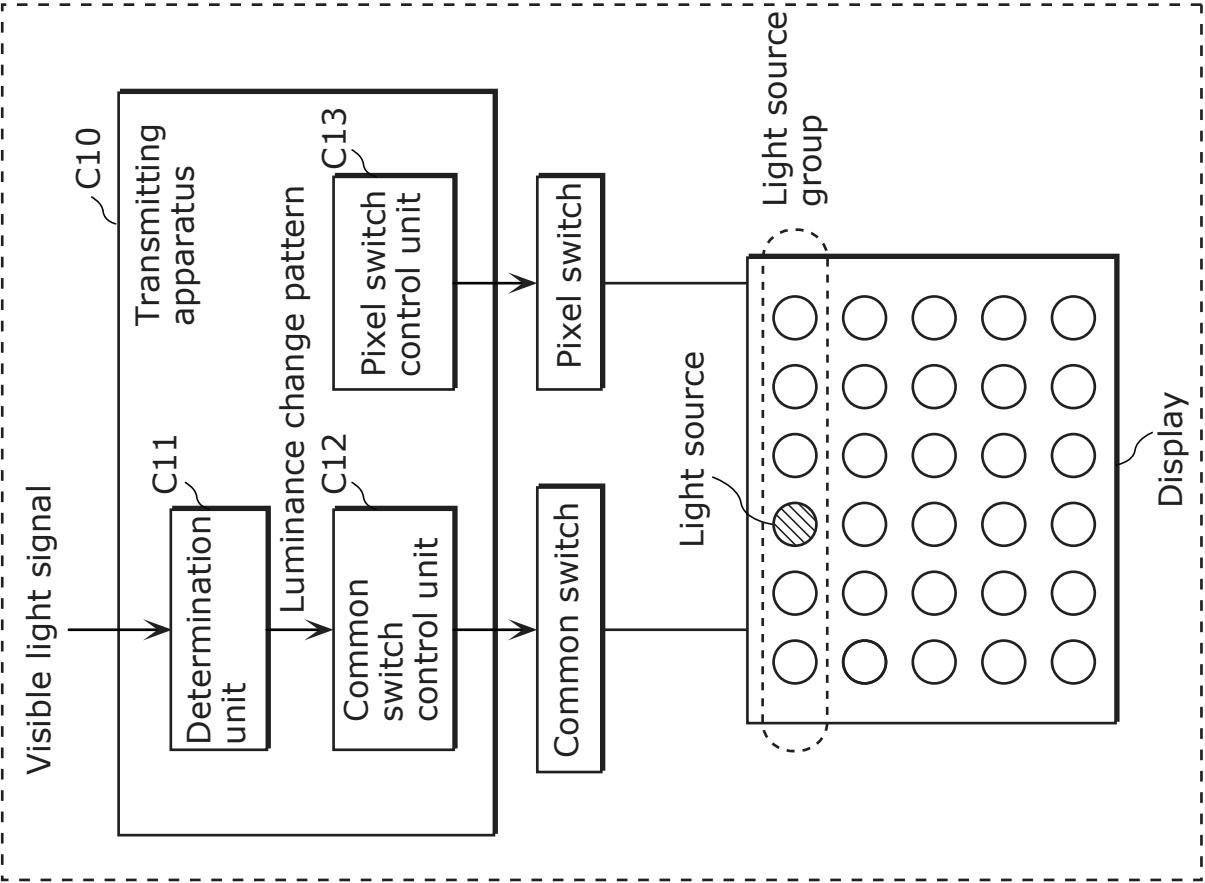
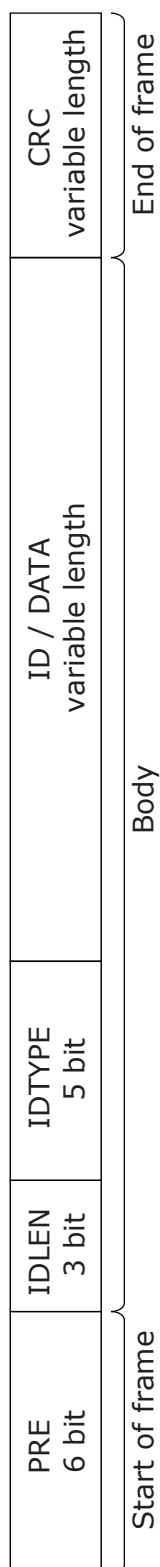
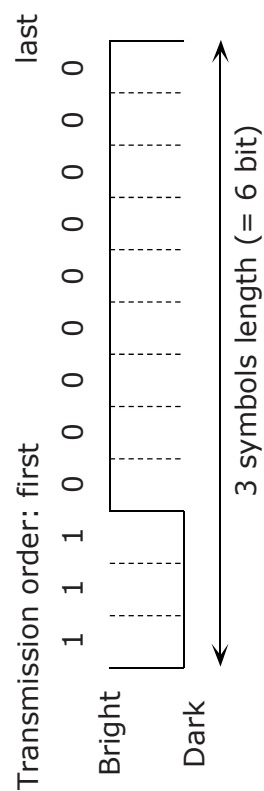


FIG. 464



(a)



(9)

IDLEN [bit]	Length of ID/DATA [bit]
000	reserved
001	32
010	64
011	128
100	256
101	512
110	1024
111	reserved

(c)

FIG. 465

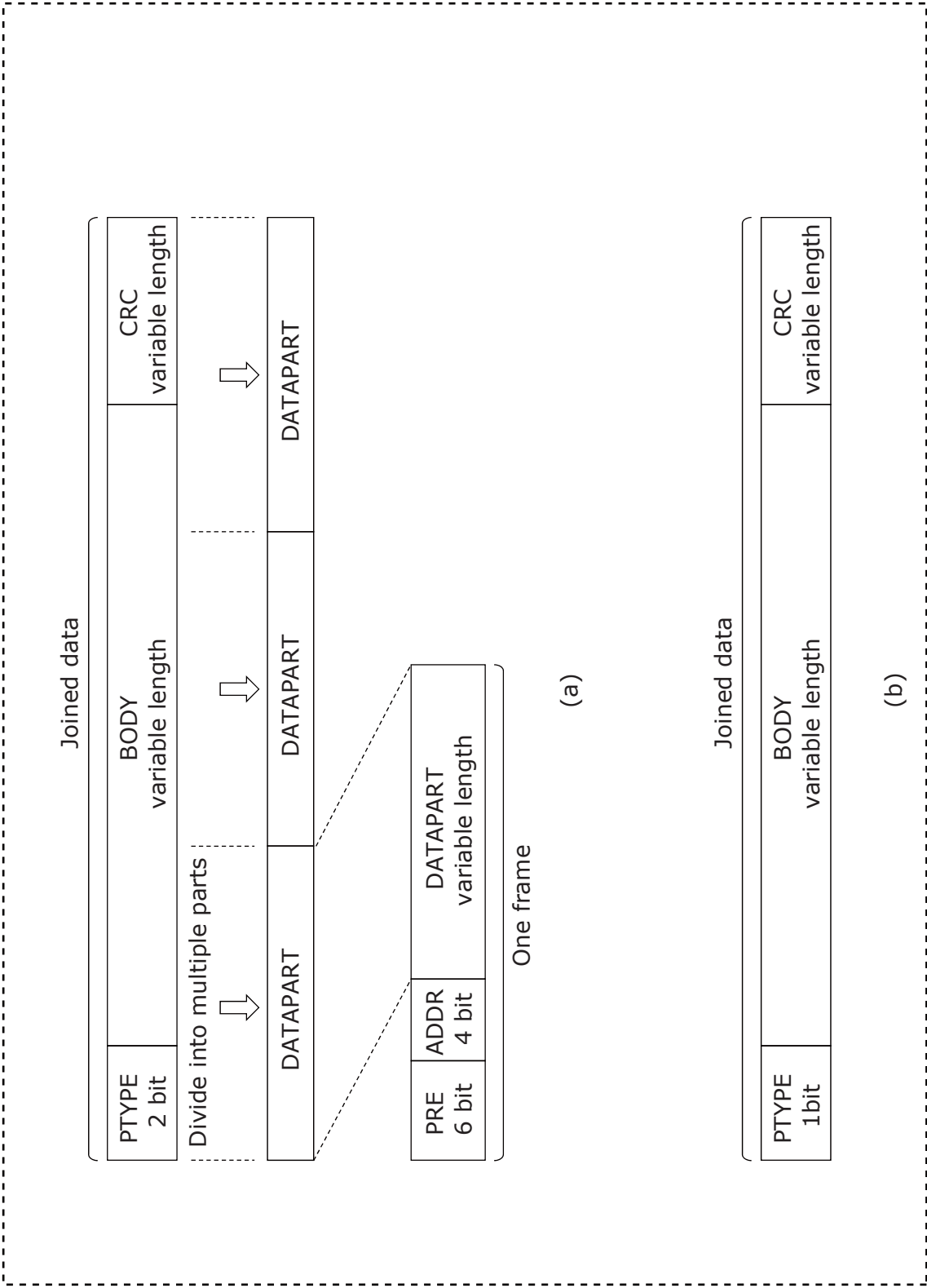


FIG. 466

Number of frames	Length of DATAPART [bit]			
	4	8	16	32
1	4	8	16	32
2	8	16	32	64
3	12	24	48	96
4	16	32	64	128
5	20	40	80	160
6	24	48	96	192
7	28	56	112	224
8	32	64	128	256
9	36	72	144	288
10	40	80	160	320
11	44	88	176	352
12	48	96	192	384
13	52	104	208	416
14	56	112	224	448
15	60	120	240	480
16	64	128	256	512

1024

FIG. 467

BODY			
IDLEN 3bit	IDTYPE 5 bit	ID variable length	PADDING

(a)

BODY		
IDTYPE 5 bit	ID variable length	PADDING

(b)

BODY	
IDTYPE 5 bit	ID variable length

(c)

BODY	
IDTYPE variable length	ID variable length

(d)

FIG. 468

PTYPE [bit]	Partitiontype of BODY
00	Singleframe compatible
01	Data stream
10	reserved
11	reserved

(a)

PTYPE [bit]	Partitiontype of BODY
0	Singleframe compatible
1	Data stream

(b)

FIG. 469

Lengthof ID [bit]	Number of frames	DATAPART [bit]	Joined data [bit]	IELEN [bit]	IDTYPE [bit]	CRC [bit]	PADDING [bit]
32	7	8	56	3	5	12	2
	14	4					
64	6	16	96	3	5	16	6
	12	8					
128	5	32	160	3	5	16	6
	10	16					
256	5	64	320	3	5	24	30
	10	32					
512	9	64	576	3	5	24	30

FIG. 470A

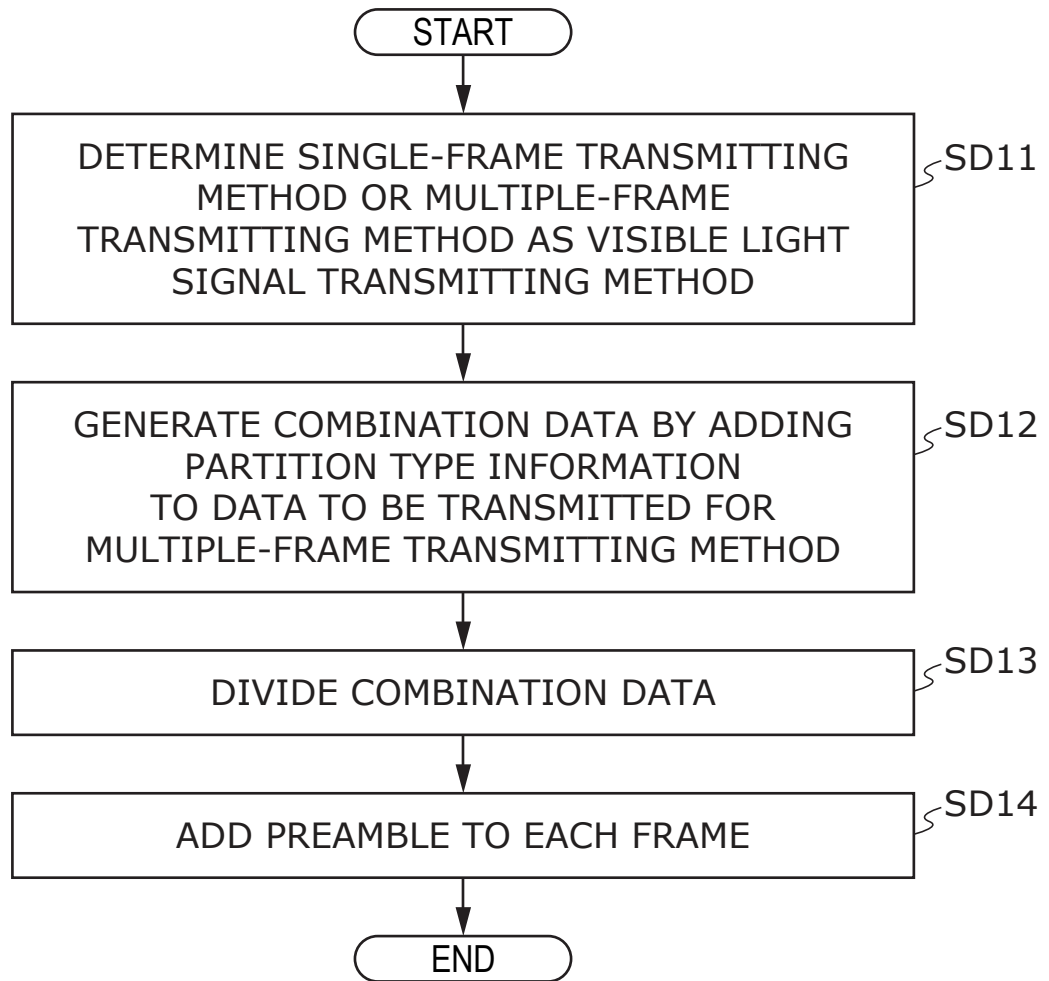


FIG. 470B

