# METHOD AND APPARATUS FOR IMPLEMENTING A NOISE GENERATOR IN AN INTEGATED CIRCUIT DISK DRIVE READ CHANNEL

## ABSTRACT OF THE DISCLOSURE

A method and apparatus for implementing a noise generator in an integrated circuit read channel to optimize the performance of the signal channel. In the preferred embodiment resistors are used to generate noise. The noise source is buffered and the noise signal passes through a pre-amplifier stage. A differential current digital-to-analog converter controlled multiplier cell controls the amplitude of the noise signal. A switch connects the noise signal to a differential current output buffer, which is coupled to a signal channel.

Figure 1