

ABSTRACT:

Discrete-time signal processing system.

The invention relates to a discrete-time signal processing system in which a clock signal is derived from an oscillation signal having a higher frequency. A switchable frequency divider is then used which is driven by a sigma-delta modulator. By alternately switching from one dividend n to the other dividend $n+1$ and *vice versa*,
5 an effective dividend m , where $n \leq m \leq n+1$ is realised, so that a very fine frequency tuning can take place.

The use of the Σ - Δ modulator is advantageous in that the frequency spectrum of the sampled signal is not corrupted by the frequency spectrum of the sampling signal (clock signal) generated by way of the switching frequency divider.

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(Figure 2).

This Sheet Has Been Replaced

