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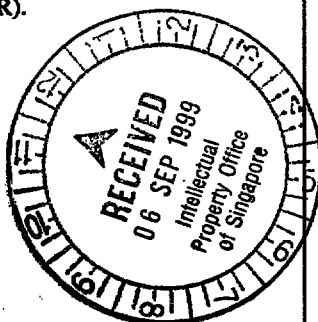
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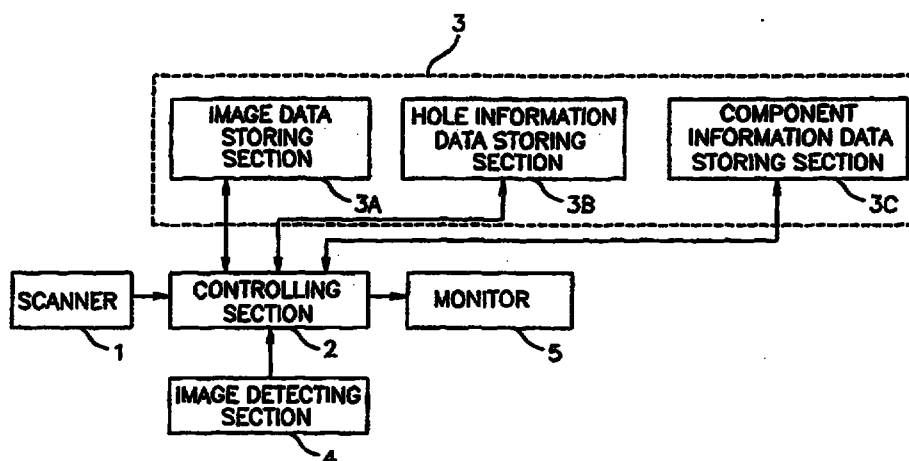
(71) Applicant: DAEWOO ELECTRONICS CO., LTD. [KR/KR];
541, 5-Ga, Namdaemoon-ro, Jung-ku, Seoul 100-095 (KR).(72) Inventor: JIN, Sang-Hoon; 1553-11, Daemyoung 11-dong,
Nam-ku, Taegu 705-041 (KR).(74) Agent: NAM, Sang-Sun; The Korea Chamber of Commerce
& Industry Building, 8th floor, 45, Namdaemoonno 4-ga,
Chung-ku, Seoul 100-094 (KR).(81) Designated States: CN, ID, JP, SG, VN, European patent (DE,
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(54) Title: A PCB TESTING CIRCUIT FOR AN AUTOMATIC INSERTING APPARATUS AND A TESTING METHOD THEREFOR



(57) Abstract

A PCB testing circuit for an automatic inserting apparatus and method thereof can minimize an inferior rate of the PCB. The circuit includes a scanner (1) for converting an image signal of a reference PCB into image data, an image detecting section (4) in an o-line state, a storing section (3A, 3B, 3C) for storing the image data, hole information data and component information data by being formed with an image data storage for storing the image data of the scanner (1) and image data detecting section (4), a hole information data storage (3B) and a component information data storage (3C), a monitor (5) for displaying the image data of the reference and modified PCBs on a screen, and a controlling section (2) which receives the image data of the reference PCB and modified PCB received via the scanner (1) and image data detecting section (4) to detect the hole information data of the reference PCB and modified PCB, compares detected hole information data to detect the hole information data of the modified PCB which does not correspond to the hole information data of the reference PCB, and stores the unpaired hole information data in the hole information data storing section.