

ABSTRACT

MEMORY DEVICE AND OPERATING METHOD OF THE MEMORY DEVICE

5

A memory device and a method of operating the memory device are provided. The memory device includes a memory cell array including memory cells that are programmed into a plurality of program states, a peripheral circuit configured to
10 perform a read operation on the memory cell array, and control logic configured to control the peripheral circuit to perform the read operation and to control the peripheral circuit to perform a masking process on first memory cells having a threshold voltage level higher than a first read level and second memory
15 cells having a threshold voltage level lower than a second read level among the memory cells during the read operation.

FIG. 7