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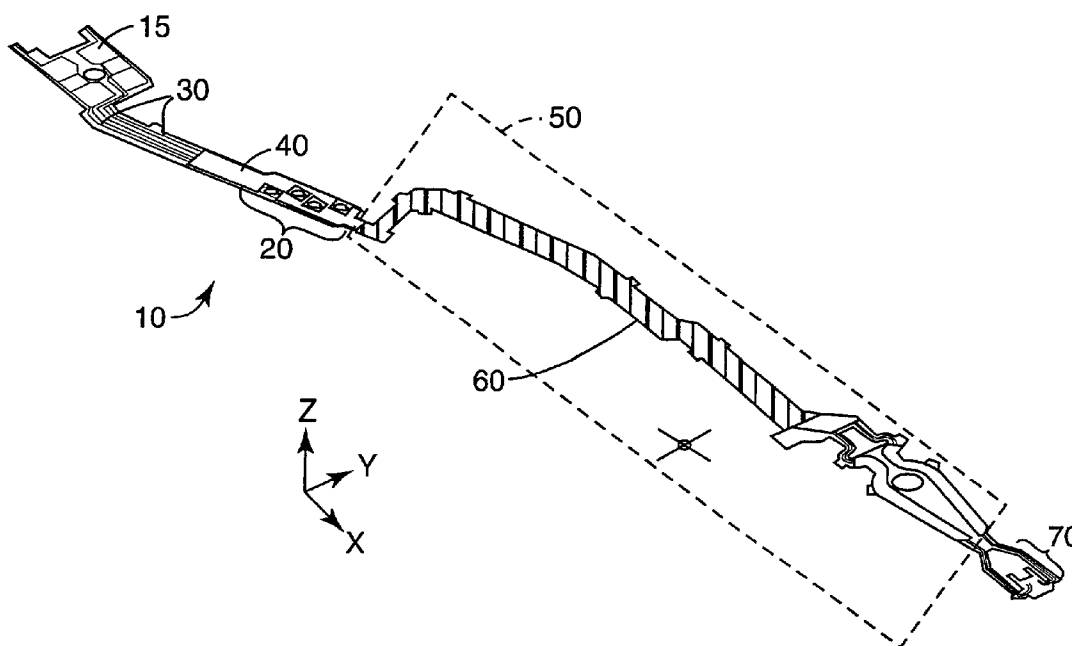
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(54) Title: FLEXIBLE CIRCUIT WITH ELECTROSTATIC DAMAGE LIMITING FEATURE



(57) Abstract: A flexible circuit (10) incorporating an electrostatic discharge limiting feature, comprising a dielectric substrate selected from polyimide or liquid crystal polymer film having at least one conductive trace (30) coated on at least one surface of the substrate wherein the discharge limiting feature includes a thin conductive polymer coating (60) selectively applied over a at least a portion of the non-critical region (50) of the circuit, said feature reducing the surface resistivity of the circuit to about 10^4 ohms to about 10^8 ohms and having tribocharging of less than about 50 V.

WO 2004/030427 A1



SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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