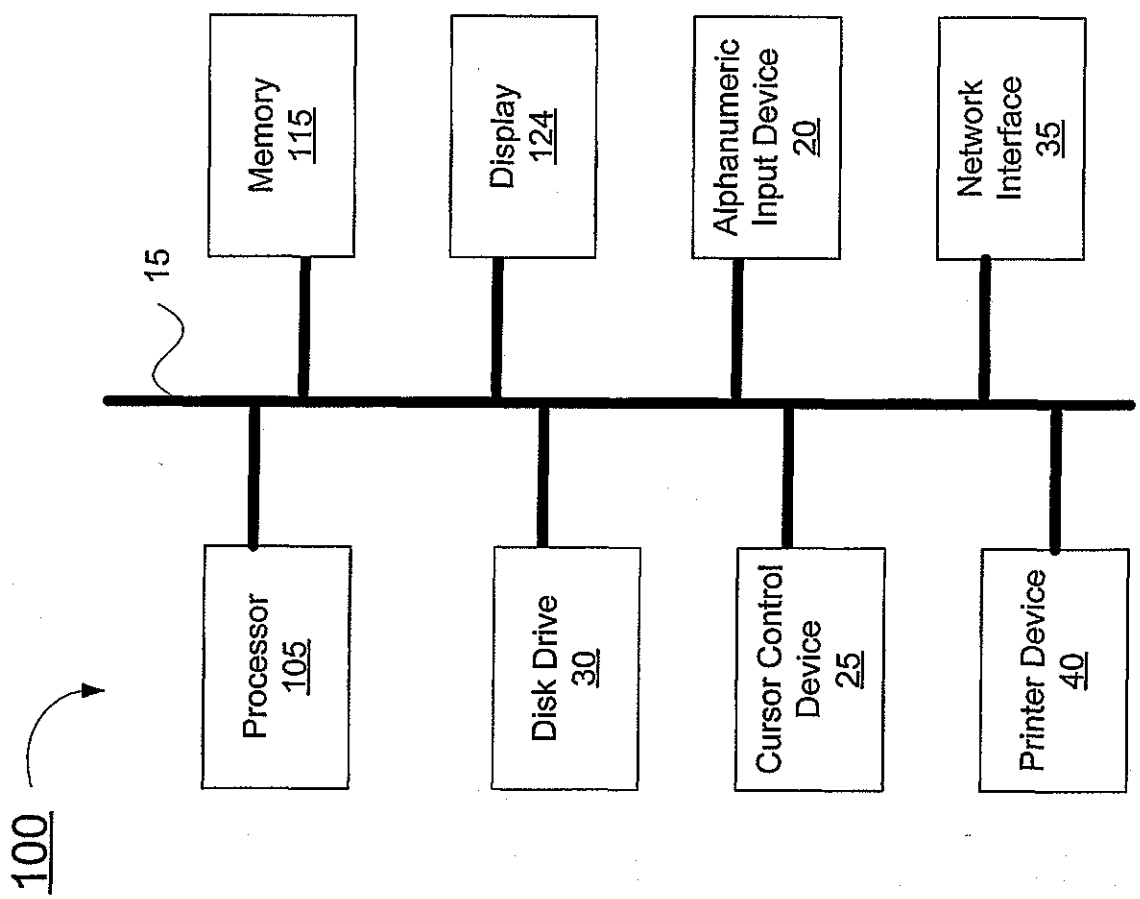
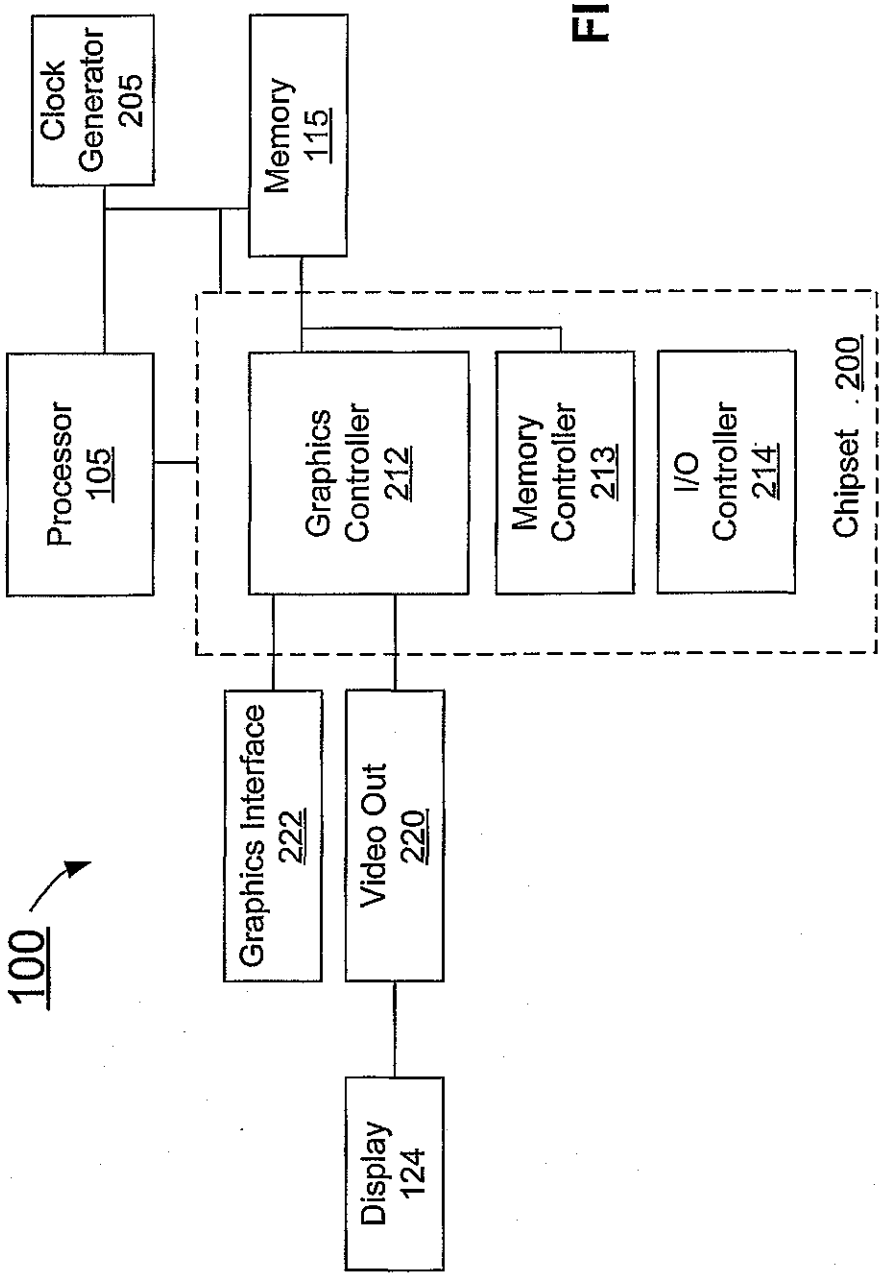

100
15
Processor 105
Disk Drive 30
Cursor Control Device 25
Printer Device 40
Memory 115
Display 124
Alphanumeric Input Device 20
Network Interface 35

FIG. 1

FIG. 2


Clock Generator 205
Memory 115
Processor 105
Graphics Controller 212
Memory Controller 213
I/O Controller 214
Chipset 200
Graphics Interface 222
Video Out 220
Display 124
100

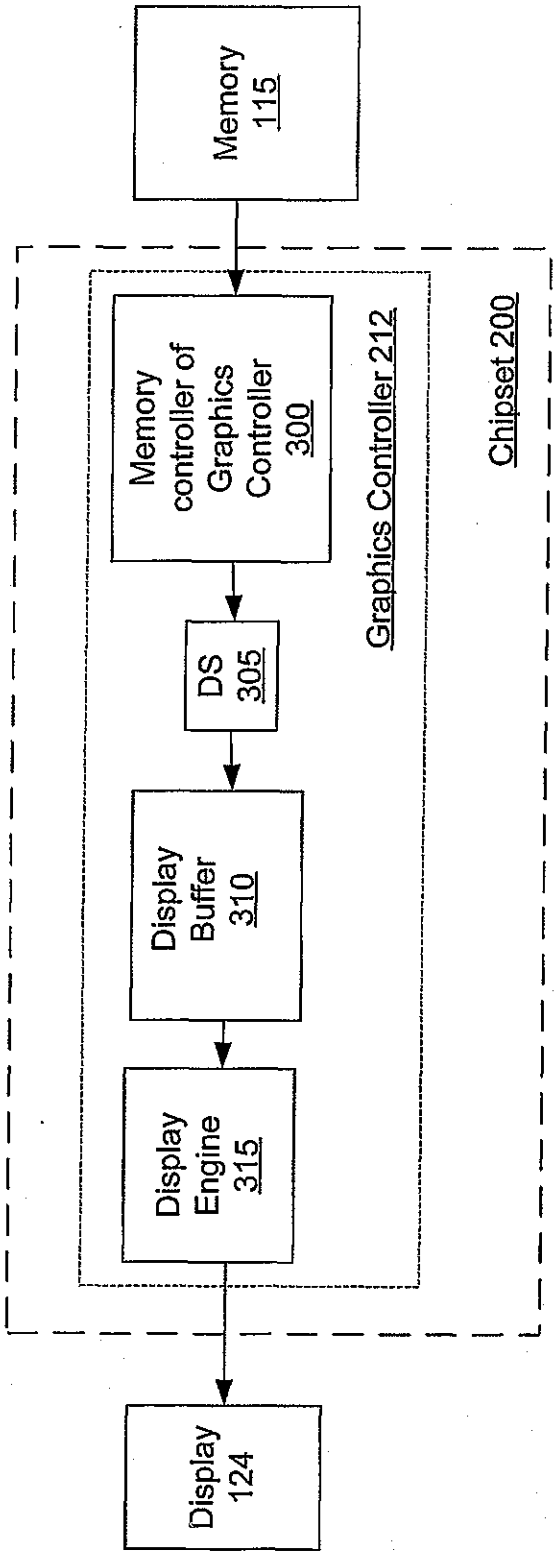

Memory 115
Chipset 200
Graphics Controller 212
Memory controller of Graphics Controller 300
DS 305
Display Buffer 310
Display Engine 315
Display 124

FIG. 3

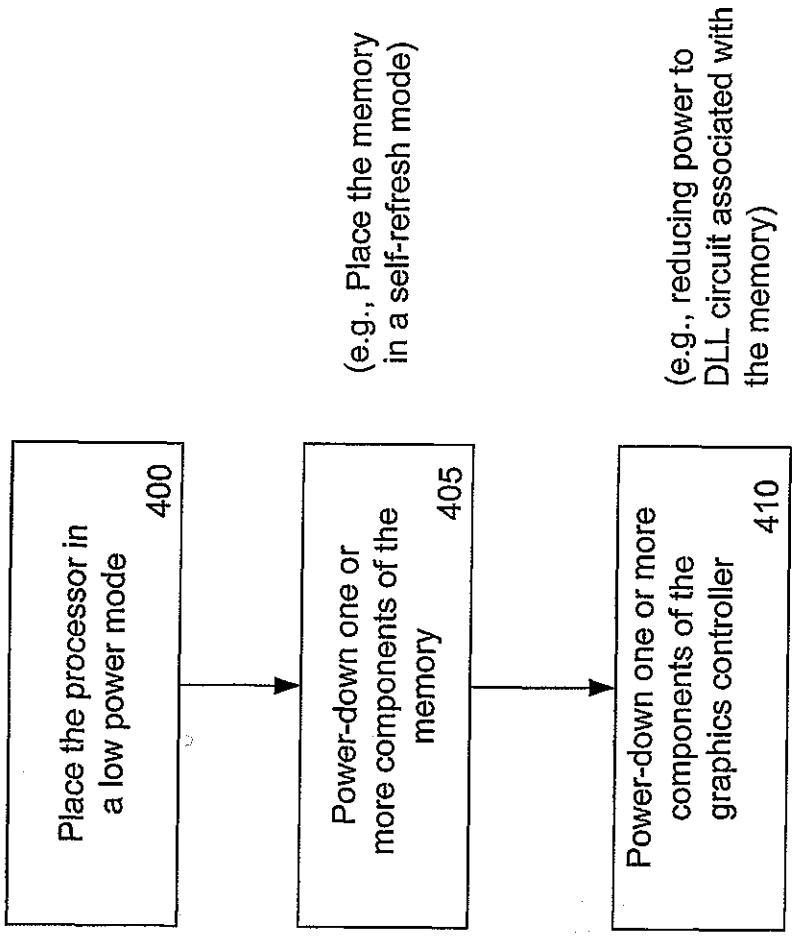

Place the processor in a low power mode
400
Power-down one or more components of the memory
405
(e.g., Place the memory in a self-refresh mode)
Power-down one or more components of the graphics controller
410
(e.g., reducing power to DLL circuit associated with the memory)

FIG. 4

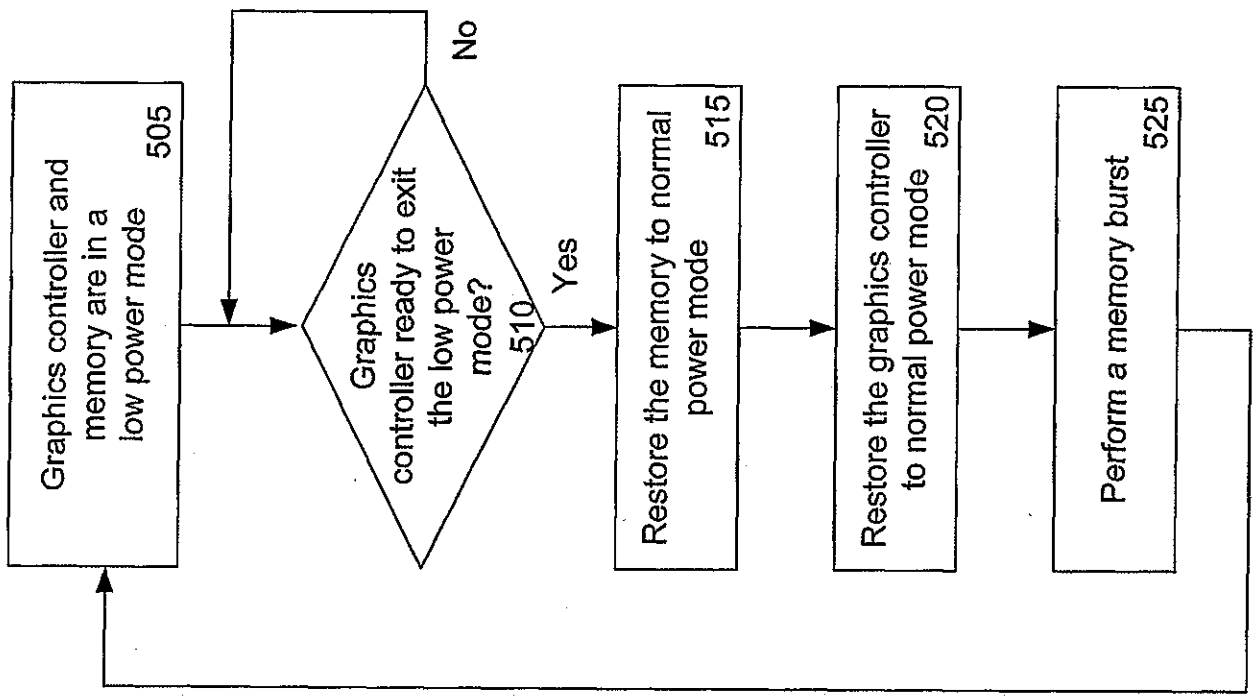

Graphics controller and memory are in a low power mode
505
Graphics controller ready to exit the low power mode?
510
No
Yes
Restore the memory to normal power mode
515
Restore the graphics controller to normal power mode
520
Perform a memory burst
525

FIG. 5