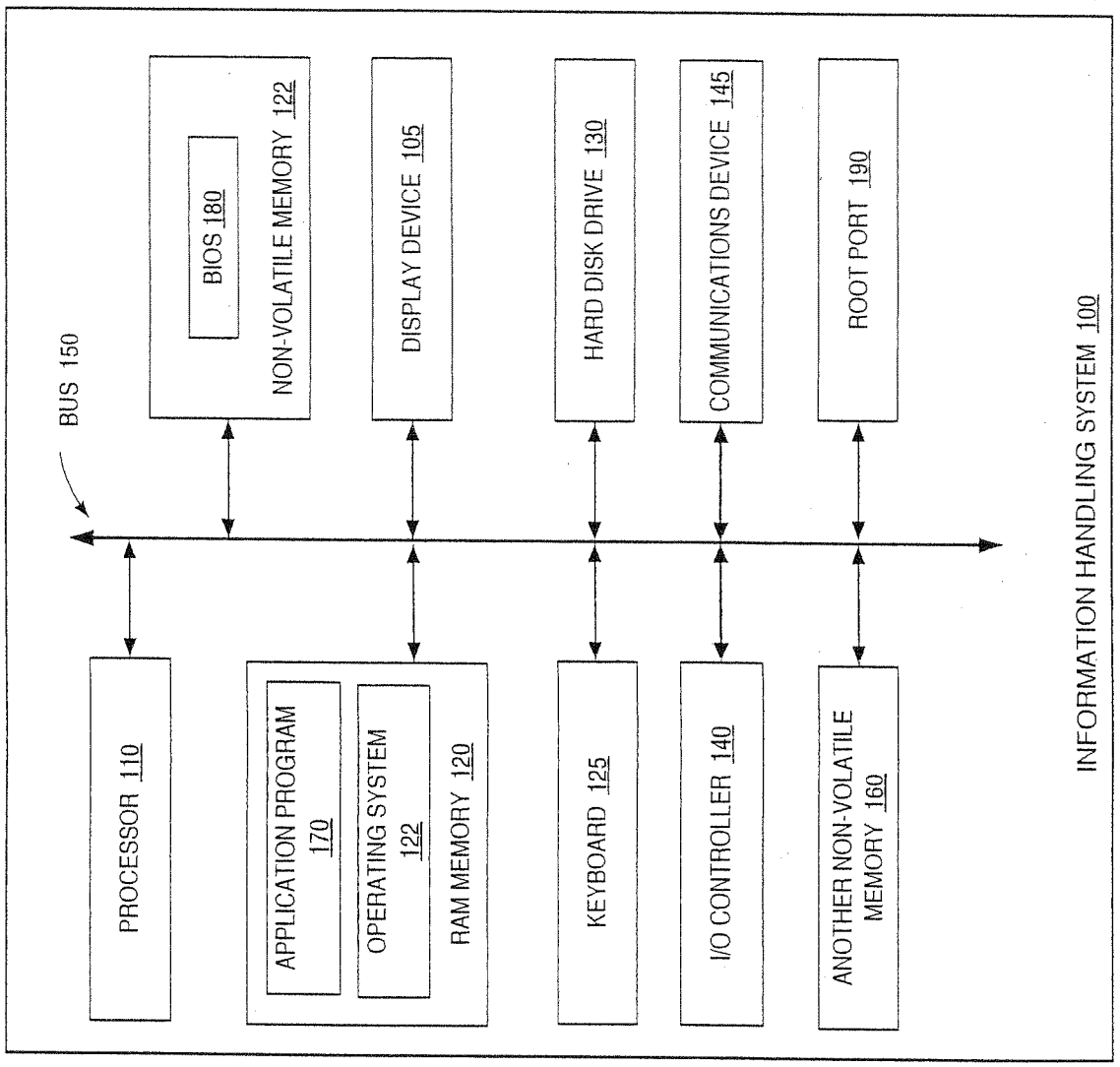

BUS 150
BIOS 180
NON-VOLATILE MEMORY 122
DISPLAY DEVICE 105
HARD DISK DRIVE 130
COMMUNICATIONS DEVICE 145
ROOT PORT 190
PROCESSOR 110
APPLICATION PROGRAM 170
OPERATING SYSTEM 122
RAM MEMORY 120
KEYBOARD 125
I/O CONTROLLER 140
ANOTHER NON-VOLATILE MEMORY 160
INFORMATION HANDLING SYSTEM 100

FIG. 1

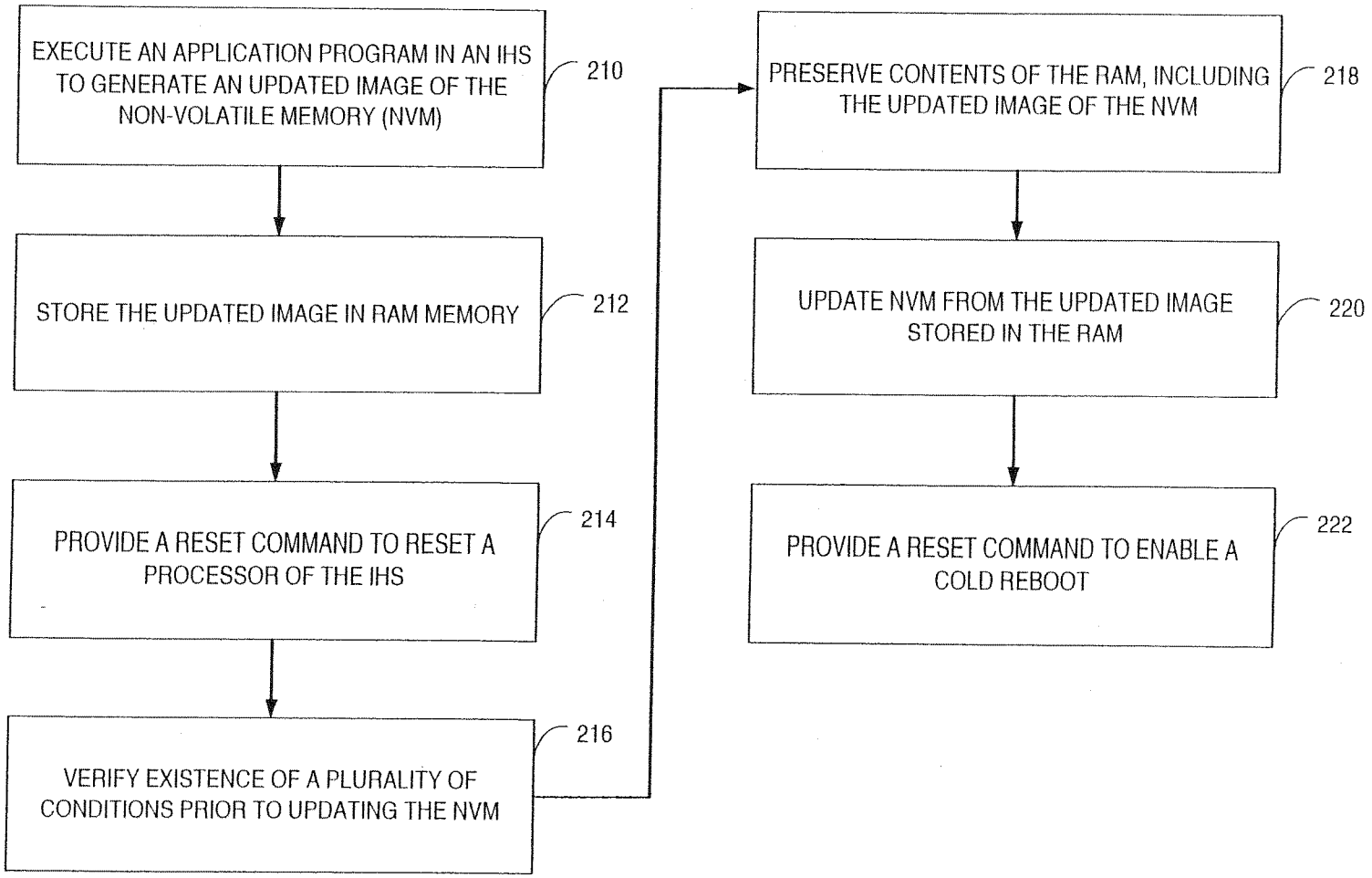

EXECUTE AN APPLICATION PROGRAM IN AN IHS TO GENERATE AN UPDATED IMAGE OF THE NON-VOLATILE MEMORY (NVM)
210
STORE THE UPDATED IMAGE IN RAM MEMORY
212
PROVIDE A RESET COMMAND TO RESET A PROCESSOR OF THE IHS
214
VERIFY EXISTENCE OF A PLURALITY OF CONDITIONS PRIOR TO UPDATING THE NVM
216
PRESERVE CONTENTS OF THE RAM, INCLUDING THE UPDATED IMAGE OF THE NVM
218
UPDATE NVM FROM THE UPDATED IMAGE STORED IN THE RAM
220
PROVIDE A RESET COMMAND TO ENABLE A COLD REBOOT
222

FIG. 2A

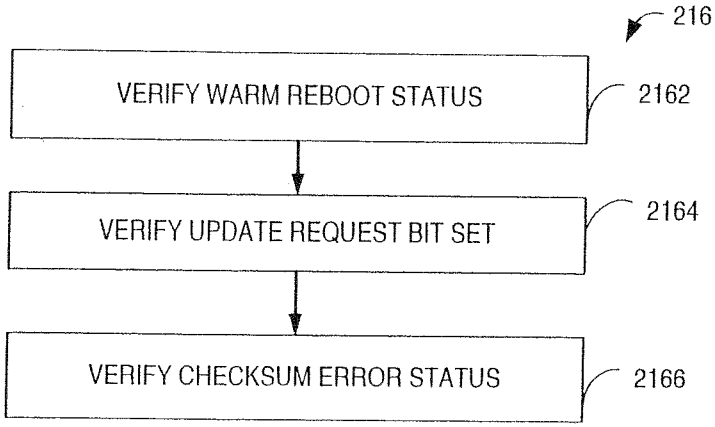


FIG. 2B

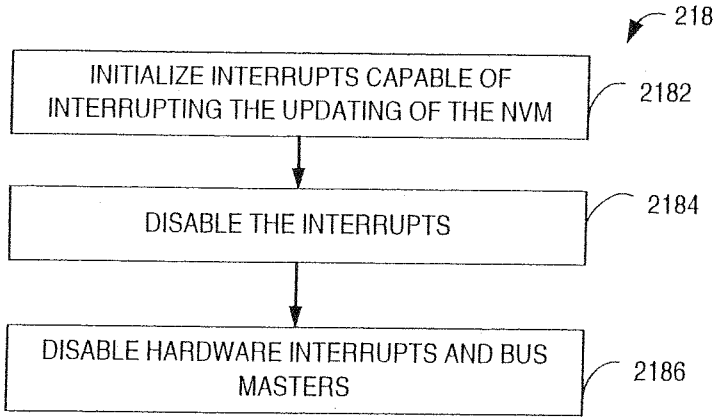


FIG. 2C

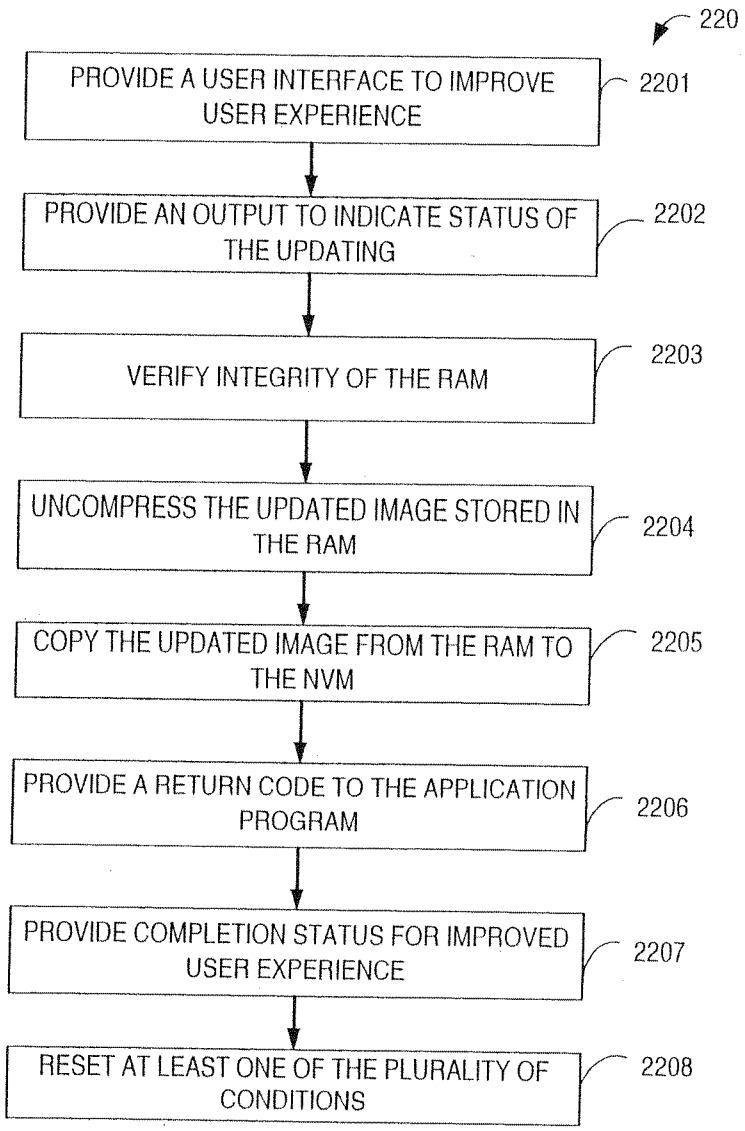


FIG. 2D

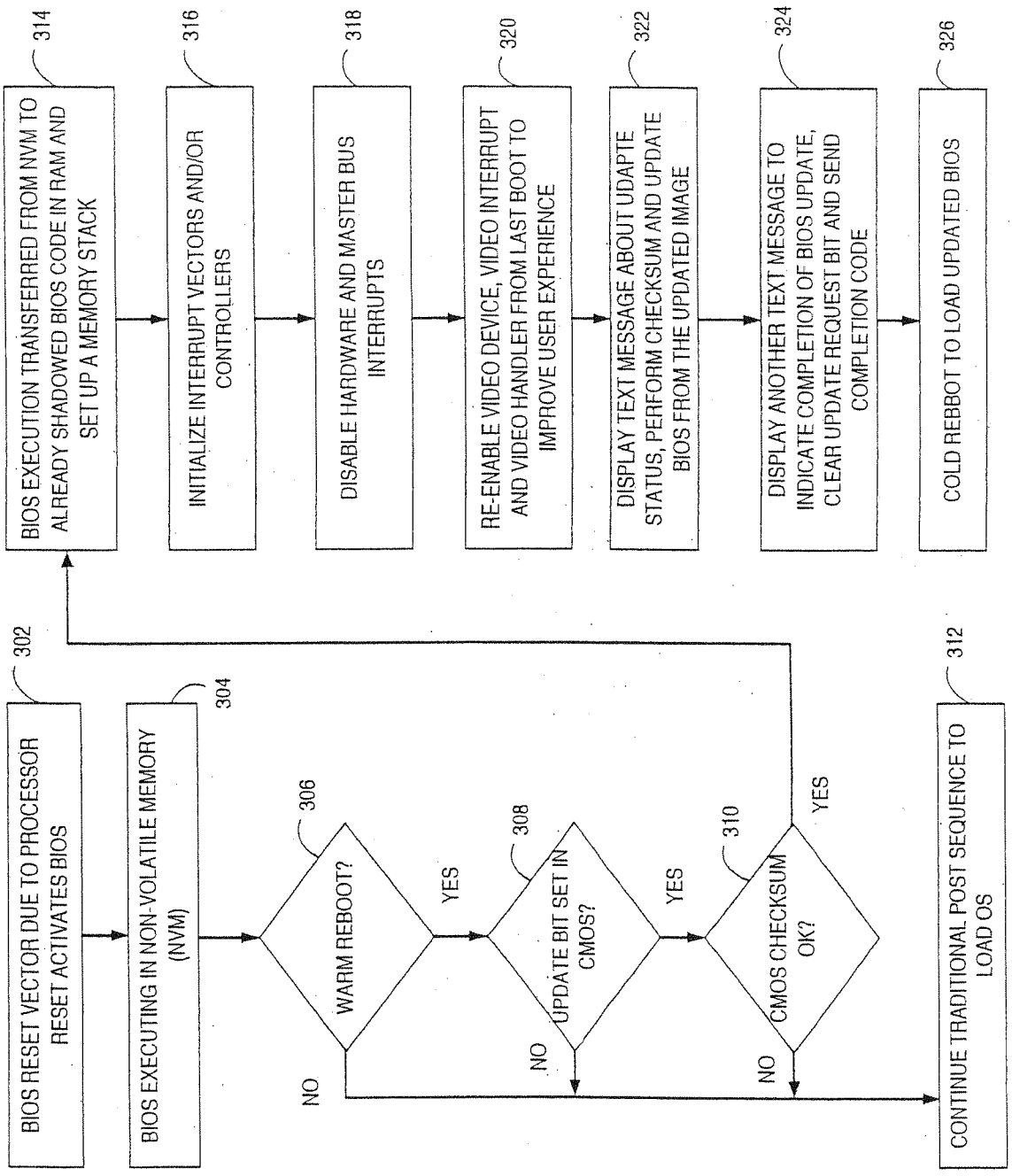

BIOS RESET VECTOR DUE TO PROCESSOR RESET ACTIVATES BIOS
302
BIOS EXECUTING IN NON-VOLATILE MEMORY (NVM)
304
WARM REBOOT?
306
YES
NO
UPDATE BIT SET IN CMOS?
308
YES
NO
CMOS CHECKSUM OK?
310
YES
NO
CONTINUE TRADITIONAL POST SEQUENCE TO LOAD OS
312
BIOS EXECUTION TRANSFERRED FROM NVM TO ALREADY SHADOWED BIOS CODE IN RAM AND SET UP A MEMORY STACK
314
INITIALIZE INTERRUPT VECTORS AND/OR CONTROLLERS
316
DISABLE HARDWARE AND MASTER BUS INTERRUPTS
318
RE-ENABLE VIDEO DEVICE, VIDEO INTERRUPT AND VIDEO HANDLER FROM LAST BOOT TO IMPROVE USER EXPERIENCE
320
DISPLAY TEXT MESSAGE ABOUT UDAPTE STATUS, PERFORM CHECKSUM AND UPDATE BIOS FROM THE UPDATED IMAGE
322
DISPLAY ANOTHER TEXT MESSAGE TO INDICATE COMPLETION OF BIOS UPDATE, CLEAR UPDATE REQUEST BIT AND SEND COMPLETION CODE
324
COLD REBBOT TO LOAD UPDATED BIOS
326

FIG. 3