

FIG.1

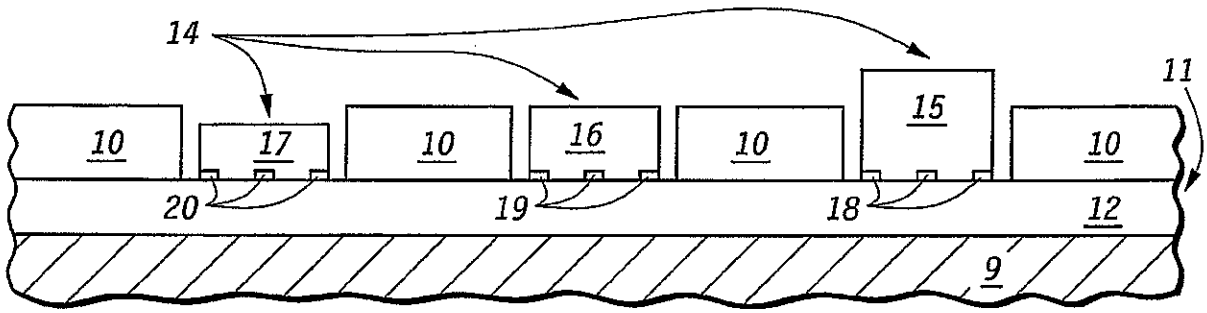


FIG.2

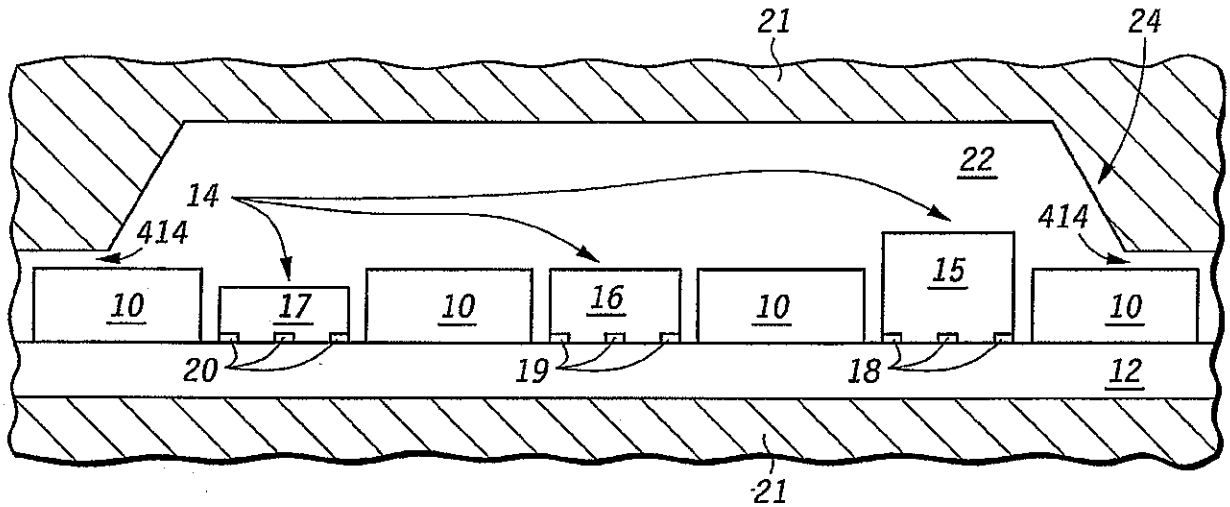


FIG.3

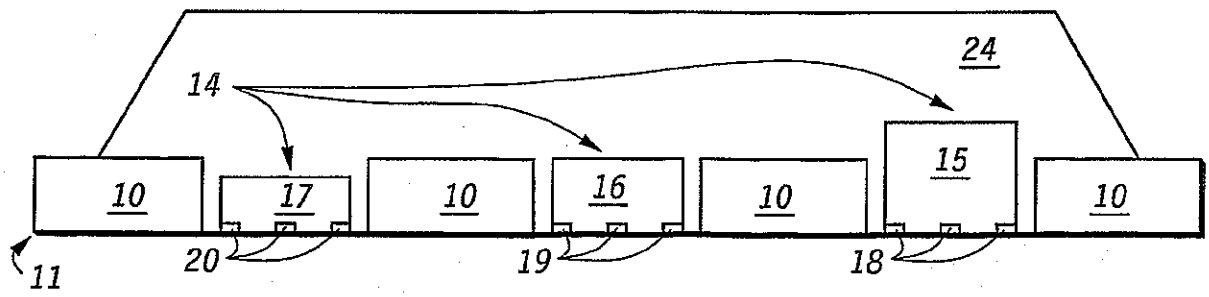


FIG.4

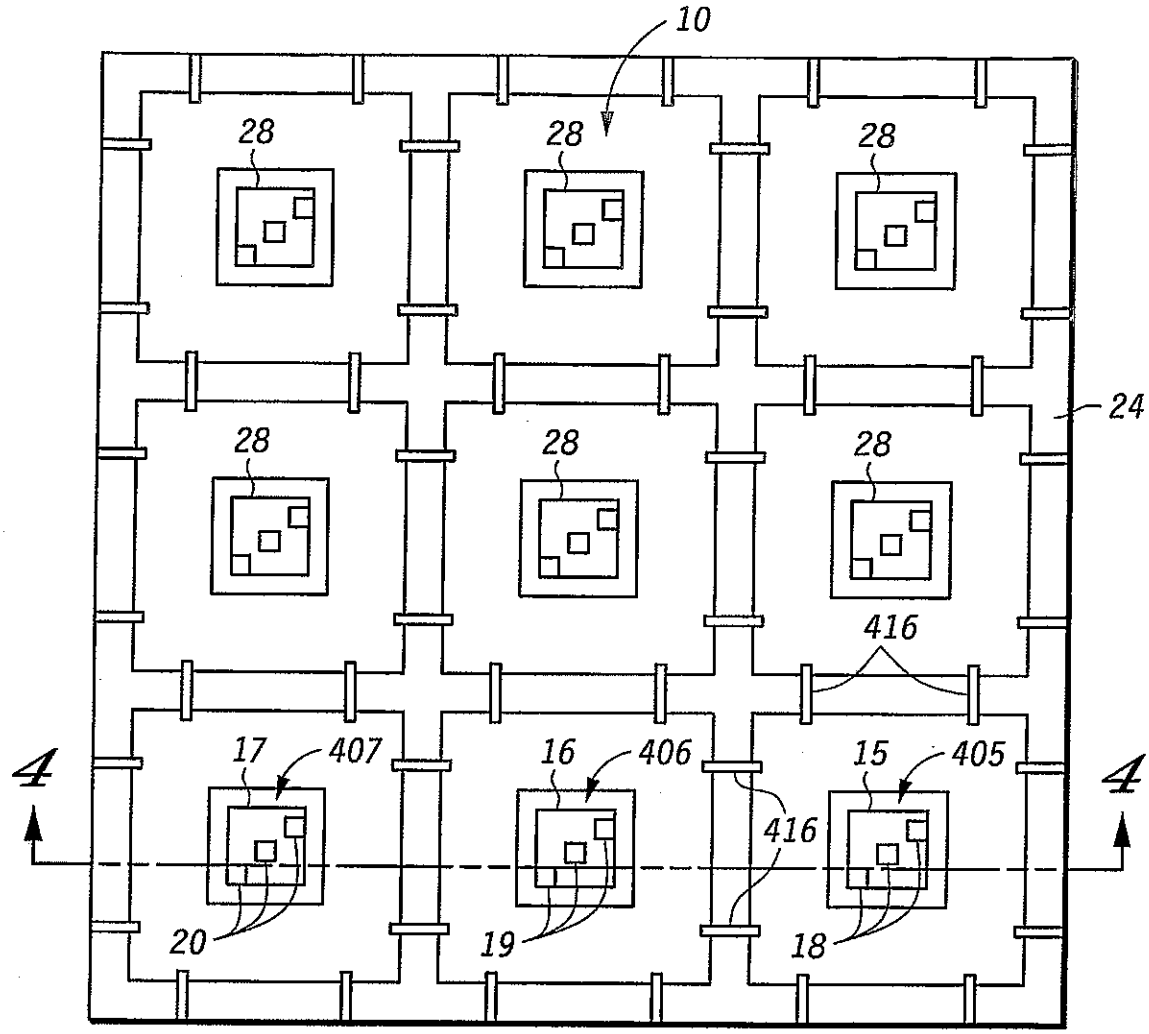

10
28
28
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416
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405
4
416
20
19
18

FIG.5

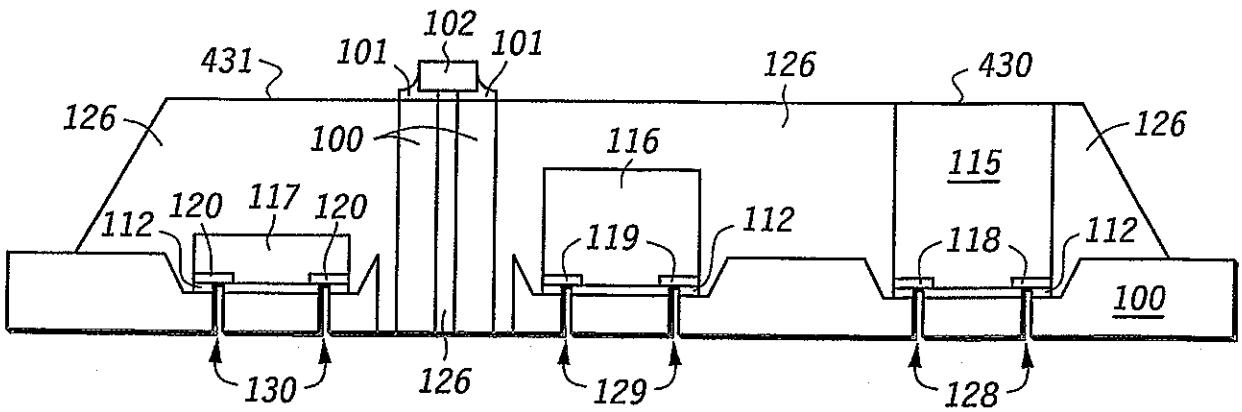

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116
115
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130
126
129
128

FIG.6

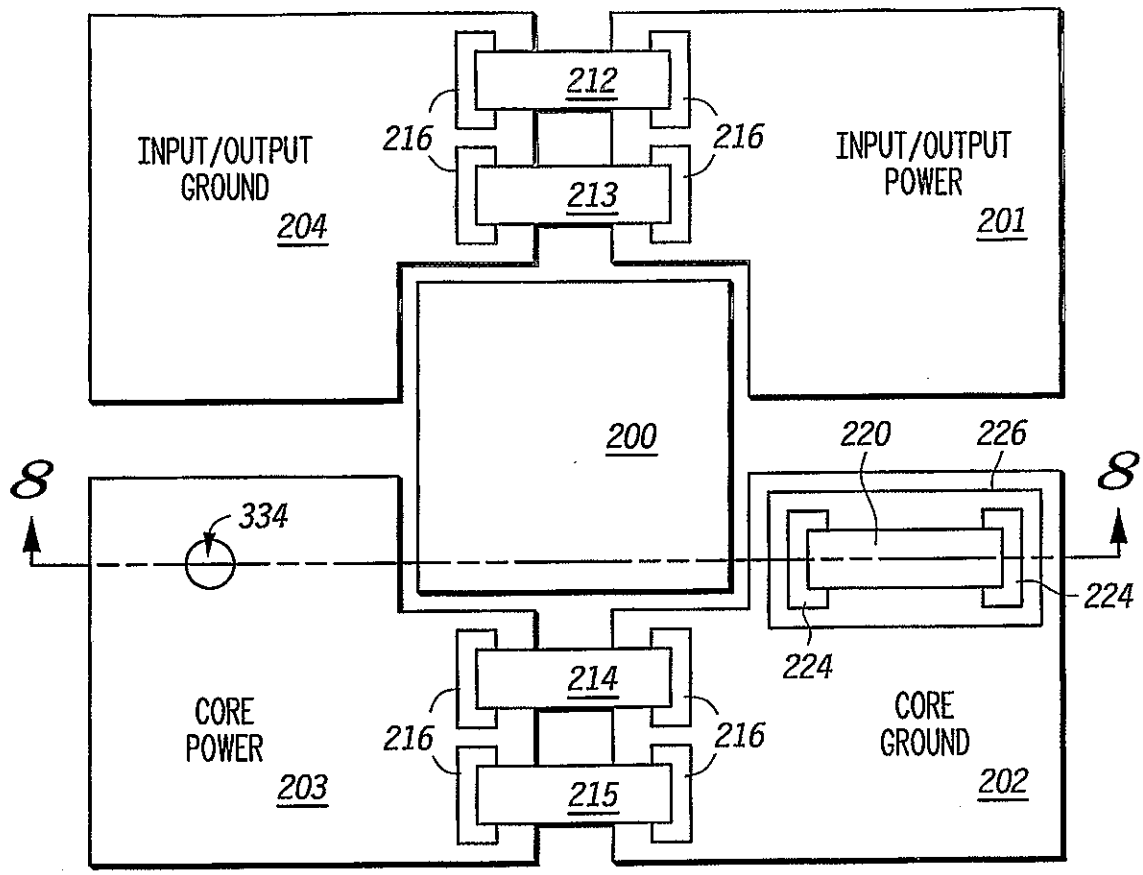

212
216
216
213
INPUT/OUTPUT
GROUND
204
INPUT/OUTPUT
POWER
201
200
220
226
8
8
334
224
224
214
CORE
POWER
203
216
216
CORE
GROUND
202
215

FIG. 7

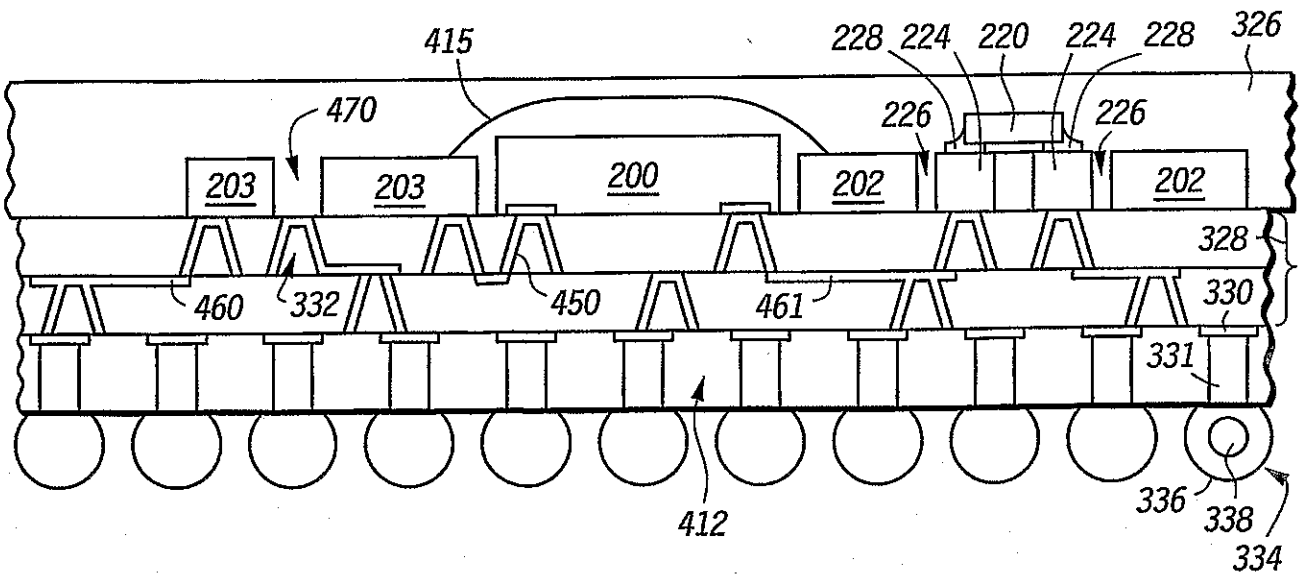

415
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326
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200
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328
460
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331
412
336
338
334

FIG. 8