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T. [US/US]; 2564 N.W. Pettygrove Street, Portland, OR 97210 (US).

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(74) Agents: MALLIE, Michael, J. et al.; Blakely, Sokoloff, Taylor & Zafman LLP, 12400 Wilshire Boulevard, 7th Floor, Los Angeles, CA 90025 (US).

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(71) Applicant (*for all designated States except US*): INTEL CORPORATION [US/US]; 2200 Mission College Boulevard, Santa Clara, CA 95052 (US).

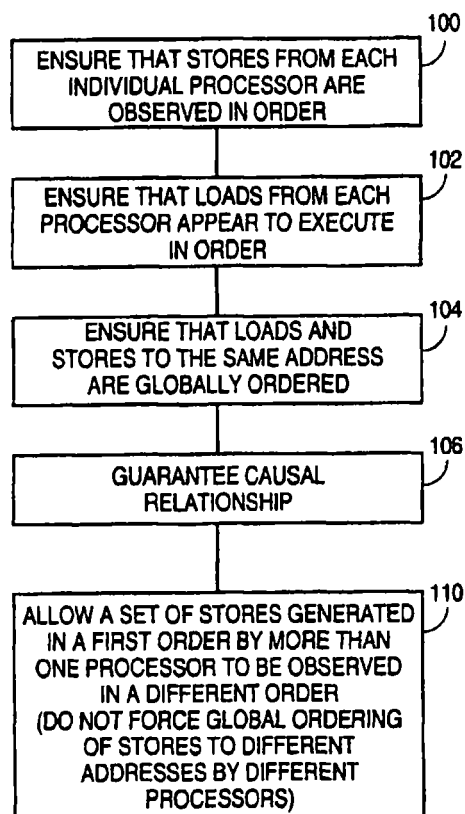
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(72) Inventor; and

(75) Inventor/Applicant (*for US only*): MARR, Deborah,

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(54) Title: CAUSALITY-BASED MEMORY ACCESS ORDERING IN A MULTIPROCESSING ENVIRONMENT



(57) Abstract: Causality-based memory ordering in a multiprocessing environment. A disclosed embodiment includes a plurality of processors and arbitration logic coupled to the plurality of processors. The processors and arbitration logic maintain processor consistency yet allow stores generated in a first order by any two or more of the processors to be observed in a different order by at least one other of the other processors.



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