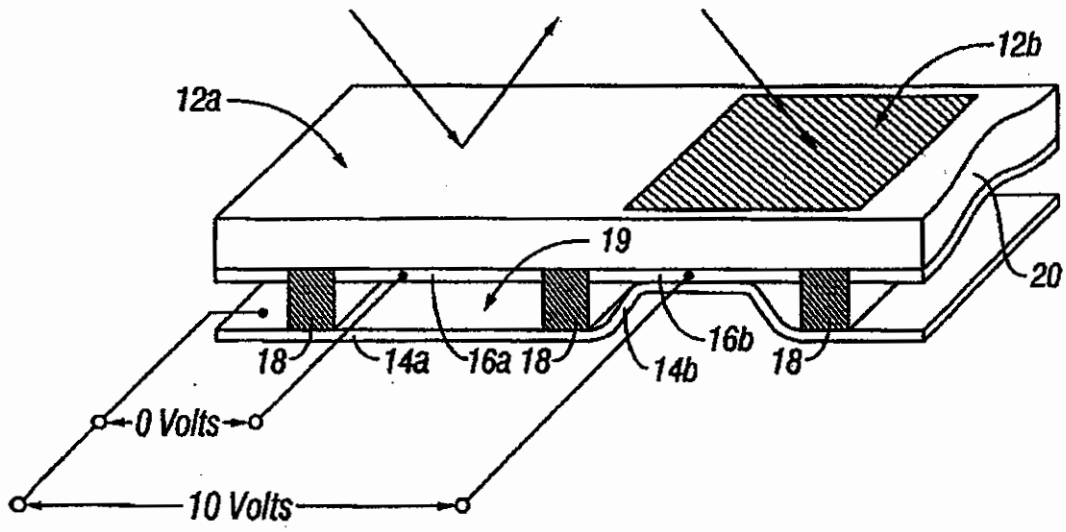


FIG. 1

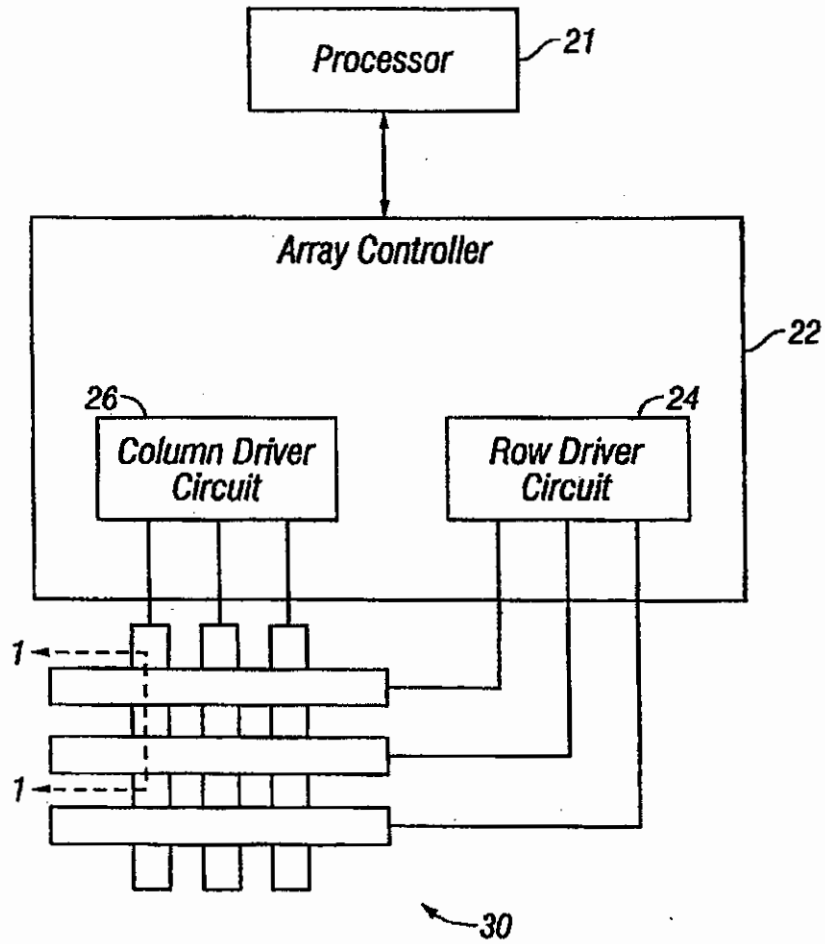

Processor
21
Array Controller
22
26
Column Driver Circuit
24
Row Driver Circuit
1
1
30

FIG. 2

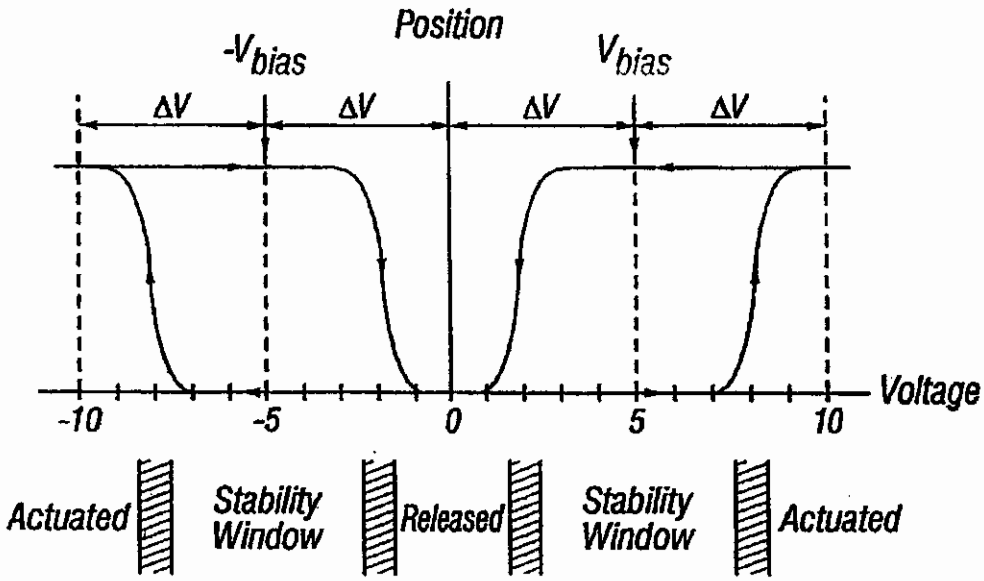


FIG. 3

Row Output Signals	Column Output Signals: $+V_{bias}$	Column Output Signals: $-V_{bias}$
0	Stable	Stable
$+\Delta V$	Release	Actuate
$-\Delta V$	Actuate	Release

FIG. 4

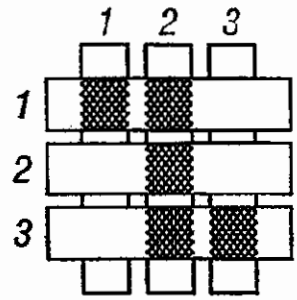

1
2
3
1
2
3

FIG. 5A

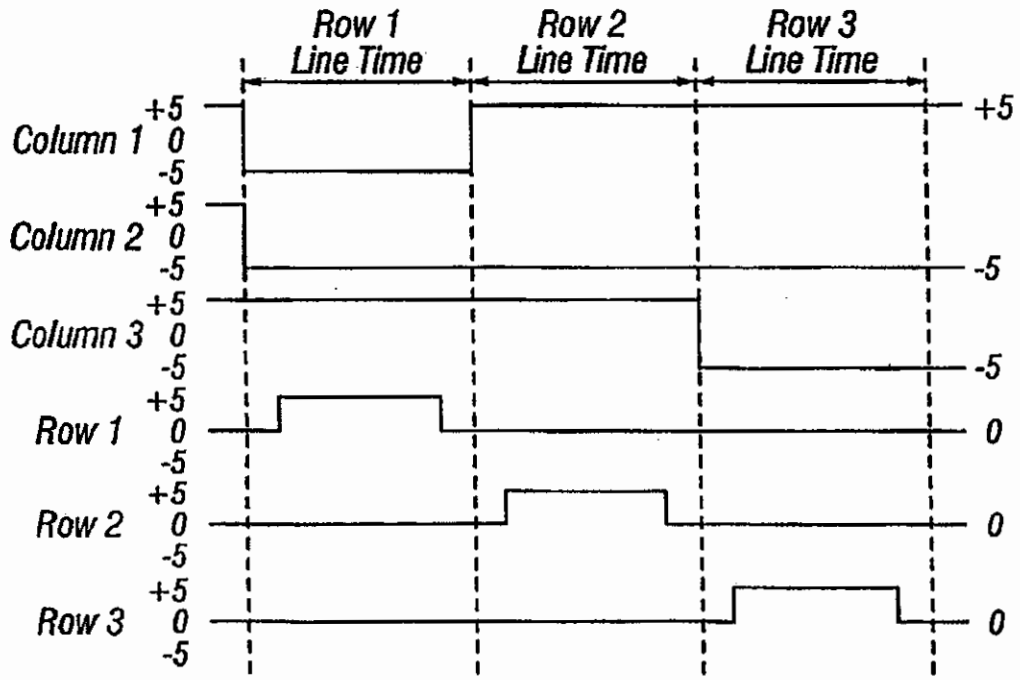

Row 1 Line Time
Row 2 Line Time
Row 3 Line Time
Column 1
Column 2
Column 3
Row 1
Row 2
Row 3
+5
0
-5
+5
-5
-5
0
0
0

FIG. 5B

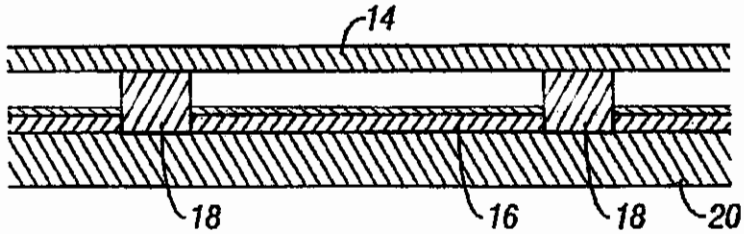


FIG. 6A

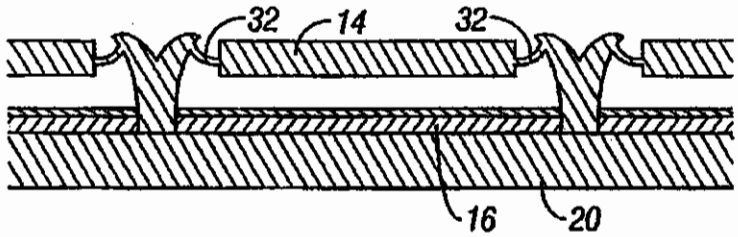


FIG. 6B

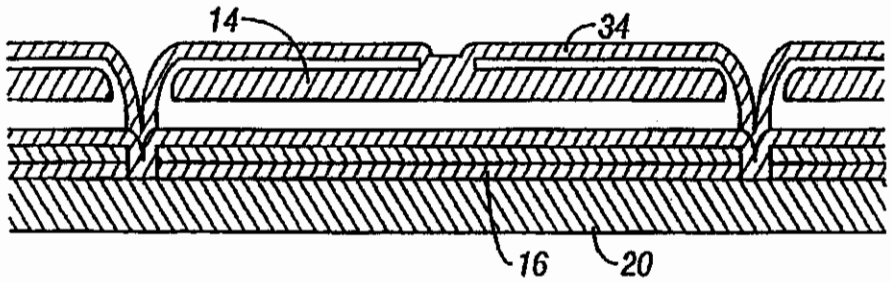


FIG. 6C

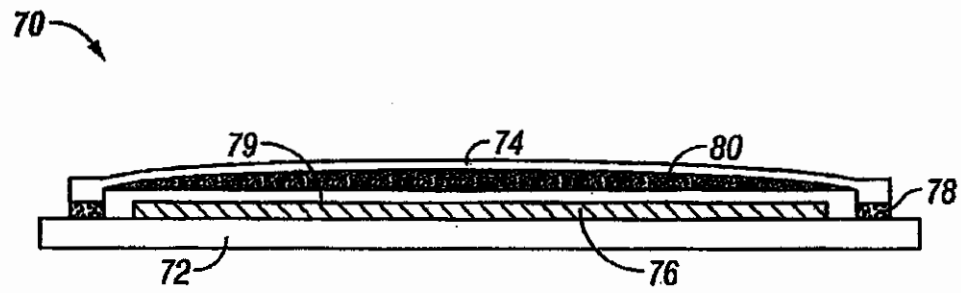

70
79
74
80
78
72
76

FIG. 7

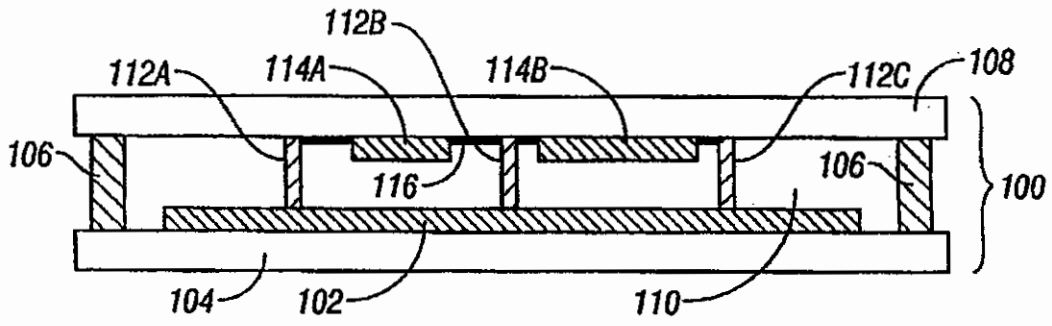

112B
112A
114A
114B
112C
108
106
116
106
100
104
102
110

FIG. 8

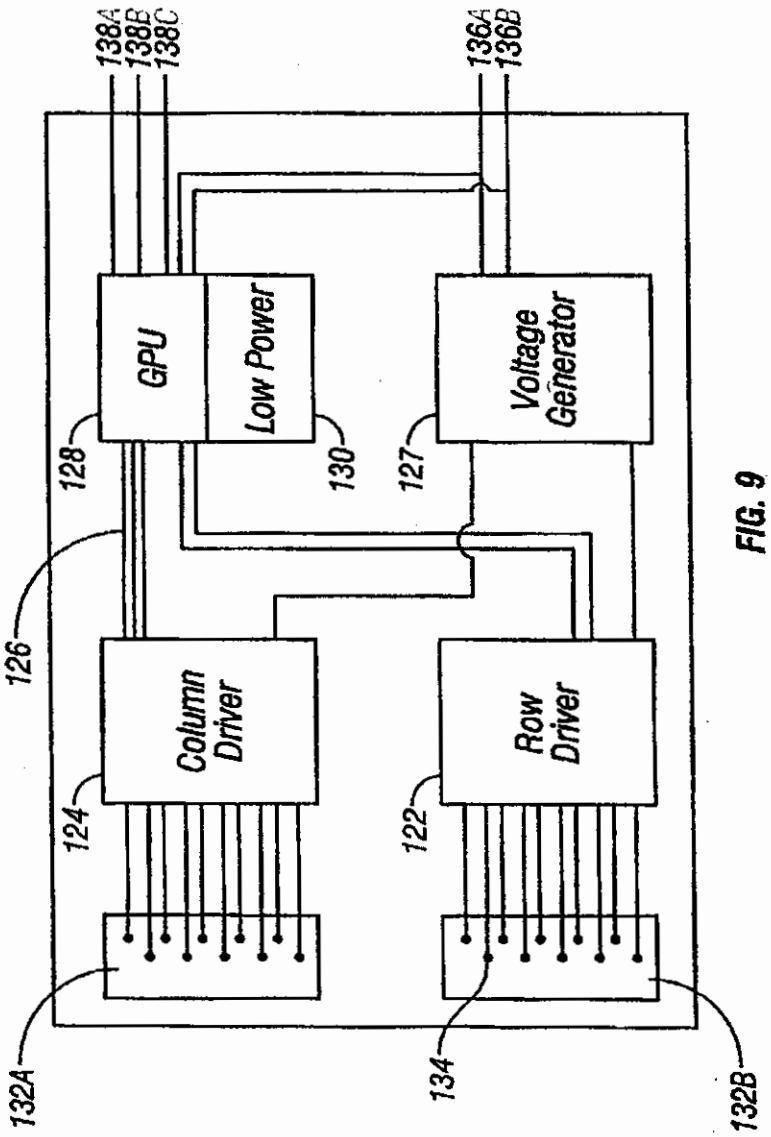

138A
138B
138C
136A
136B
GPU
Low Power
Voltage Generator
128
130
127
126
Column Driver
Row Driver
124
122
132A
134
132B

FIG. 9

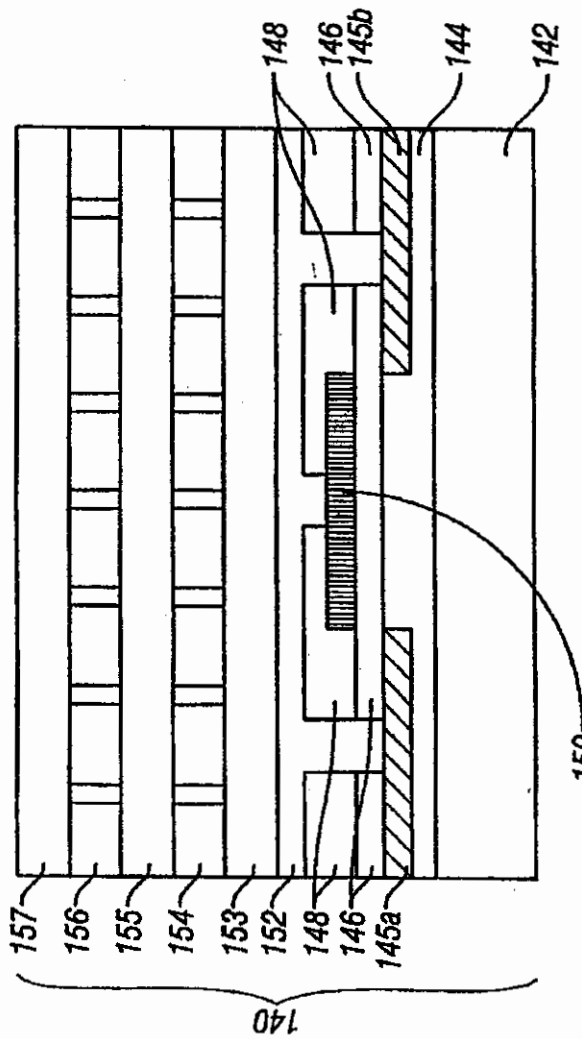

148
146
145b
144
142
150
157
156
155
154
153
152
148
146
145a
140

FIG. 10

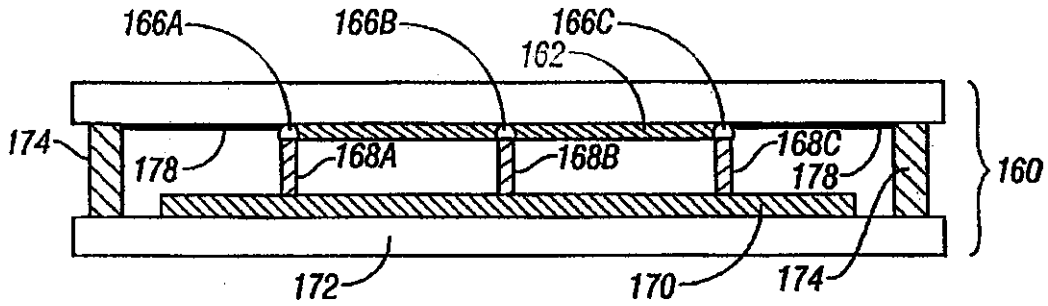


FIG. 11

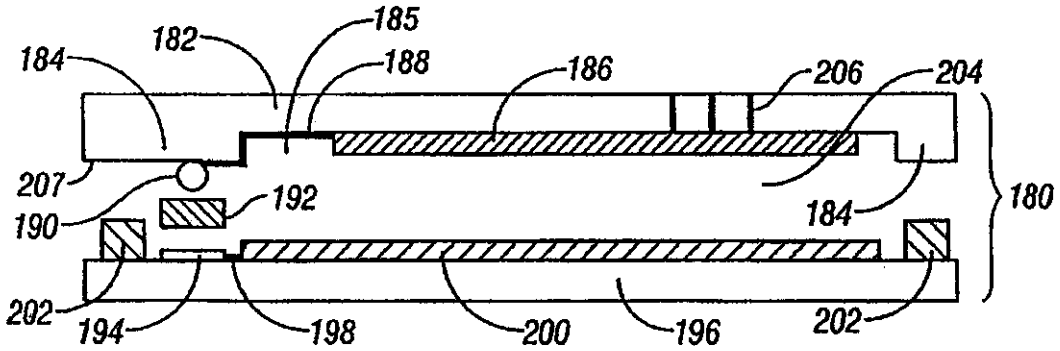


FIG. 12A

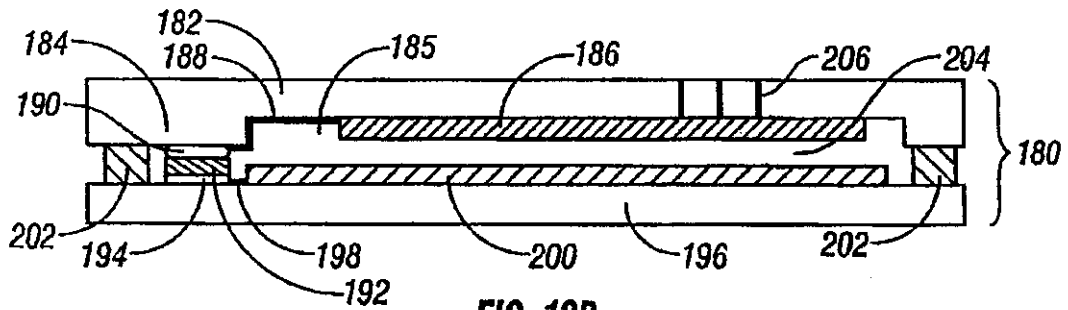


FIG. 12B

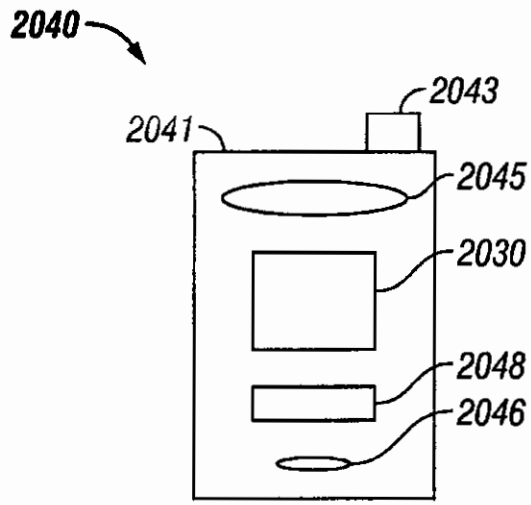


FIG. 13A

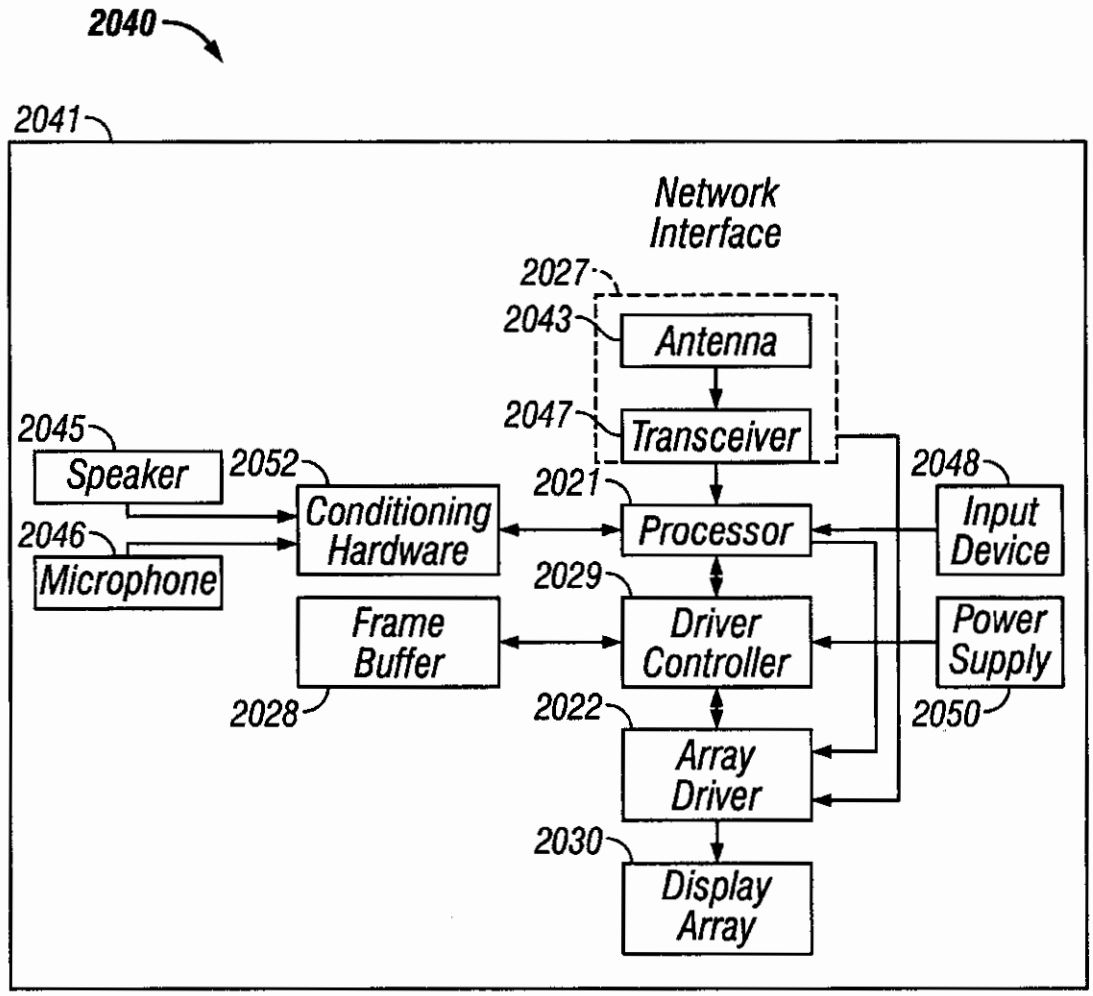


FIG. 13B