

RECORDING AND REPRODUCING APPARATUS

ABSTRACT OF THE DISCLOSURE

The object of the present invention is to convert a signal from an arbitrary equipment into a signal at a data rate for used in a device.

A semiconductor memory (7) is controlled by a control signal from a memory control section (8), thereby repeatedly reproducing recorded still image signals each corresponding to one frame. If a reproduction signal has a data rate of 4 : 4 : 4, the reproduction signal is supplied to a rate conversion circuit (9) for converting the data rate into 4 : 2 : 2 or 4 : 1 : 1. A digital video signal thus converted is supplied to a VTR section (6) through a change-over switch (5). One of the digital video signal from the rate conversion circuit (9) and the reproduction digital video signal from the VTR section (6) is selected by a switch (10). The selected digital video signal is supplied to a D/A conversion circuit (11) and fetched a digital output terminal (12). An analog video signal converted by the D/A conversion circuit (11) is supplied to a display section (13) consisting of a liquid crystal display unit or the like and built in the device and fetched at an analog output terminal (14).

Fig. 1