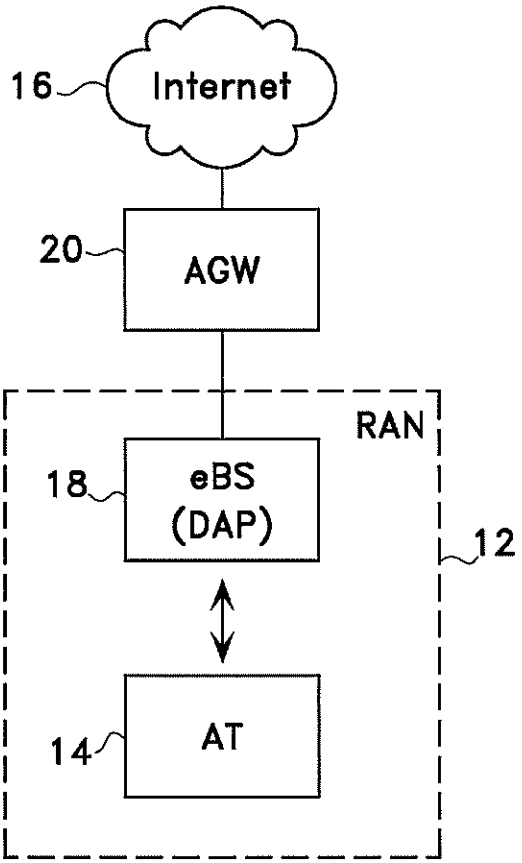

16
Internet
20
AGW
RAN
18
eBS
(DAP)
12
14
AT

FIG. 1

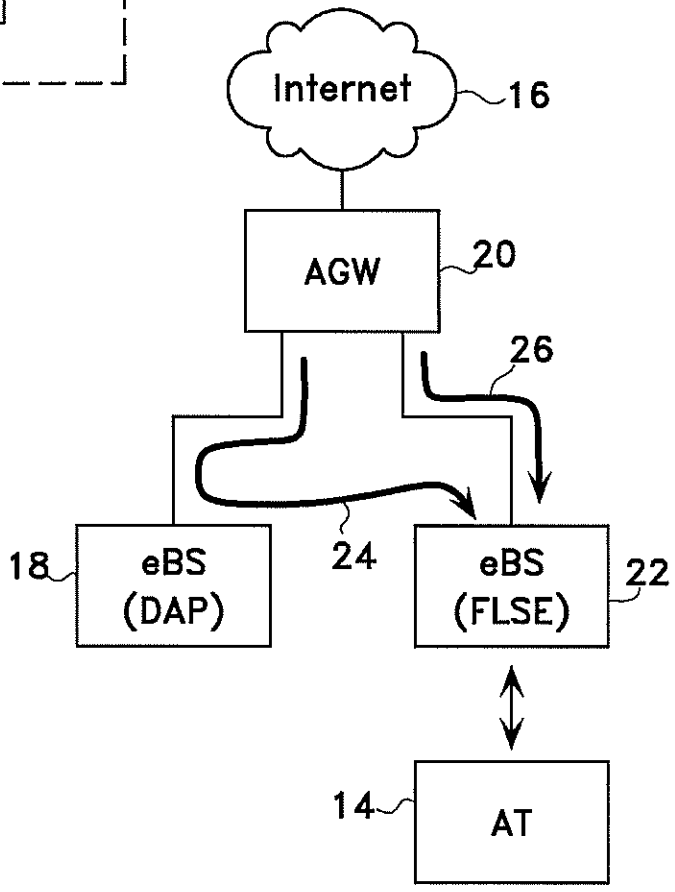

Internet
16
AGW
20
26
18
eBS
(DAP)
24
eBS
(FLSE)
22
14
AT

FIG. 2

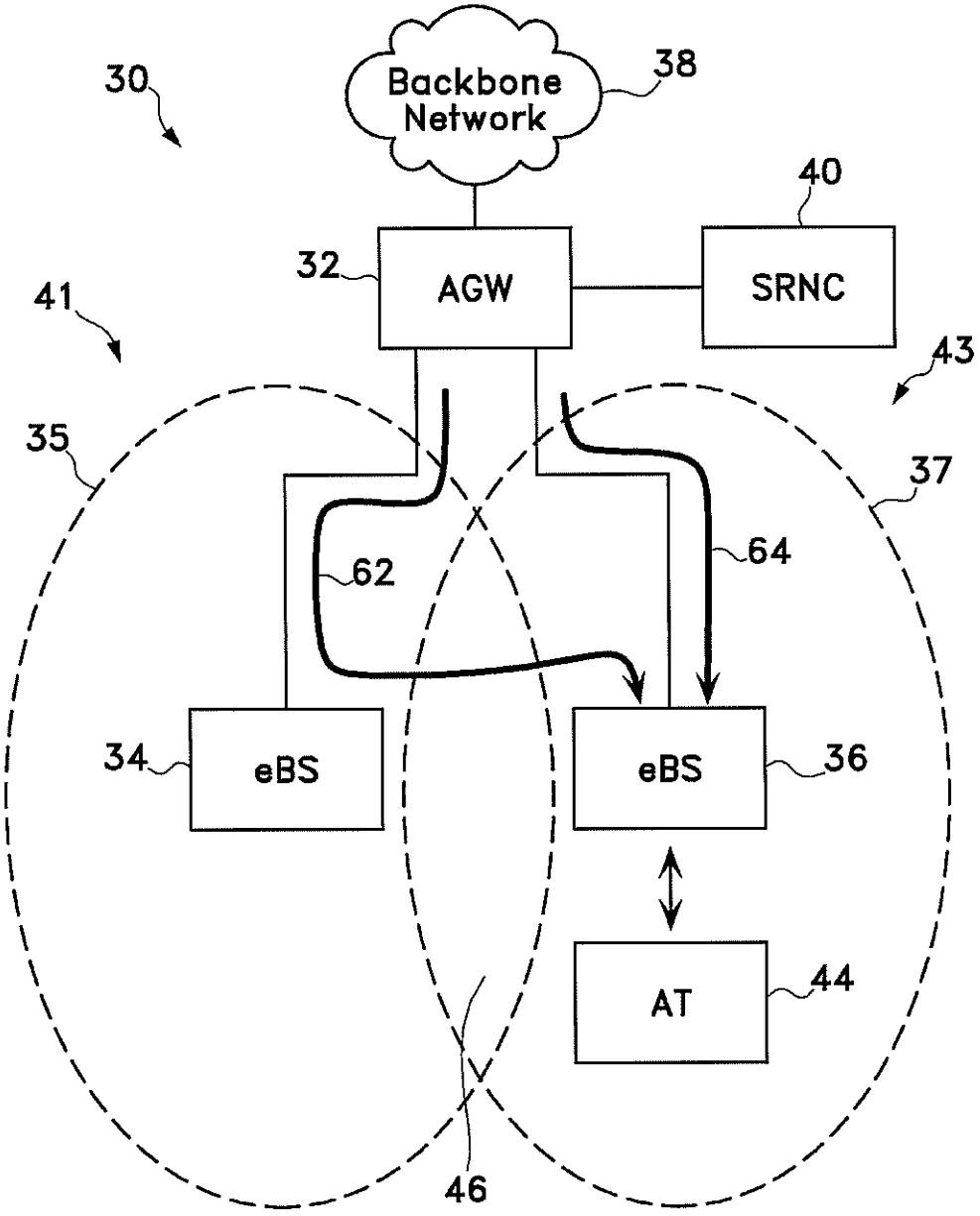

30
Backbone Network
38
40
32
AGW
SRNC
41
43
35
37
62
64
34
eBS
eBS
36
AT
44
46

FIG. 3

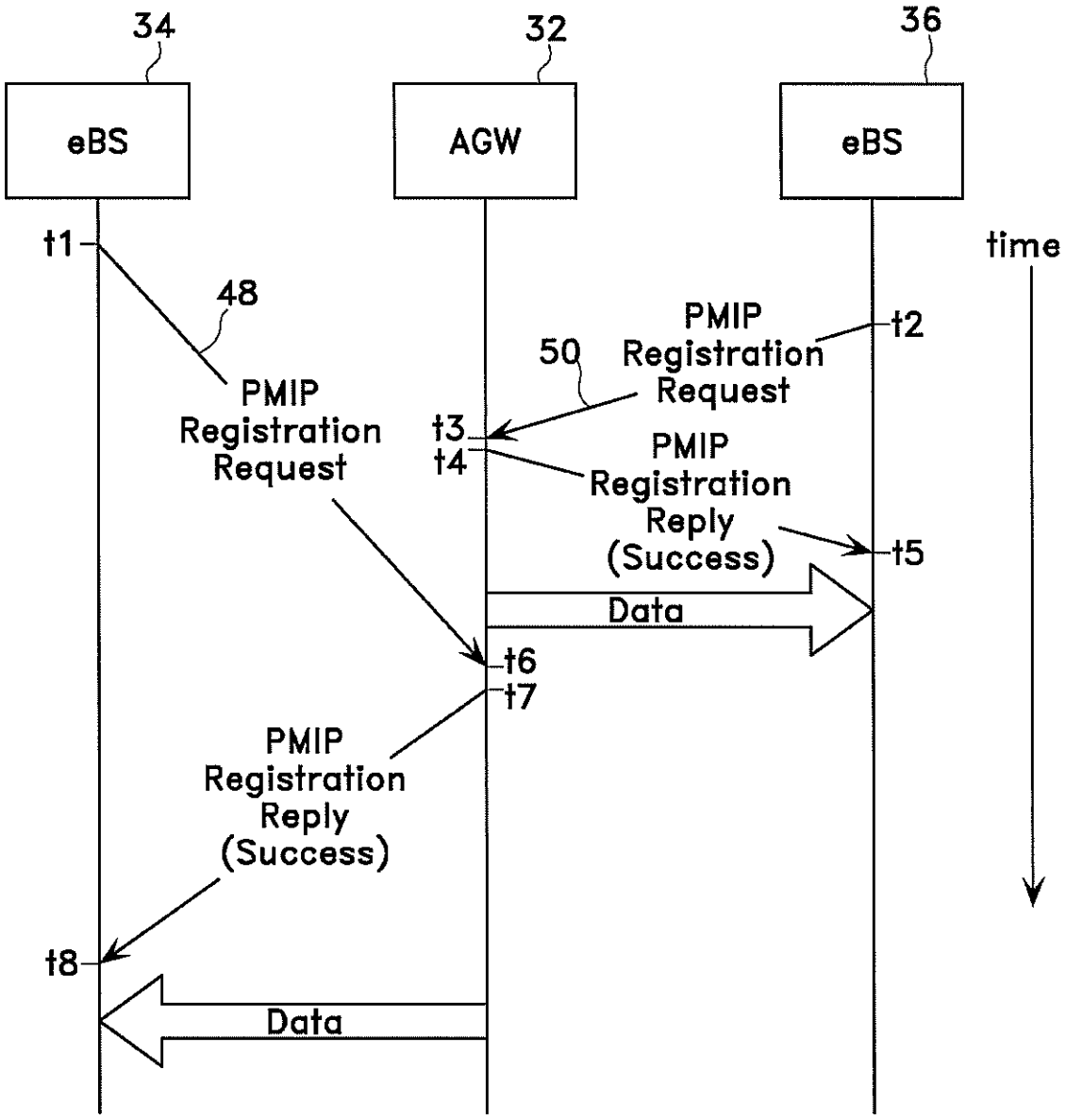

34
32
36
eBS
AGW
eBS
time
t1
48
PMIP
Registration
Request
PMIP
Registration
Request
50
t2
t3
t4
PMIP
Registration
Reply
(Success)
t5
Data
t6
t7
PMIP
Registration
Reply
(Success)
t8
Data

FIG. 4

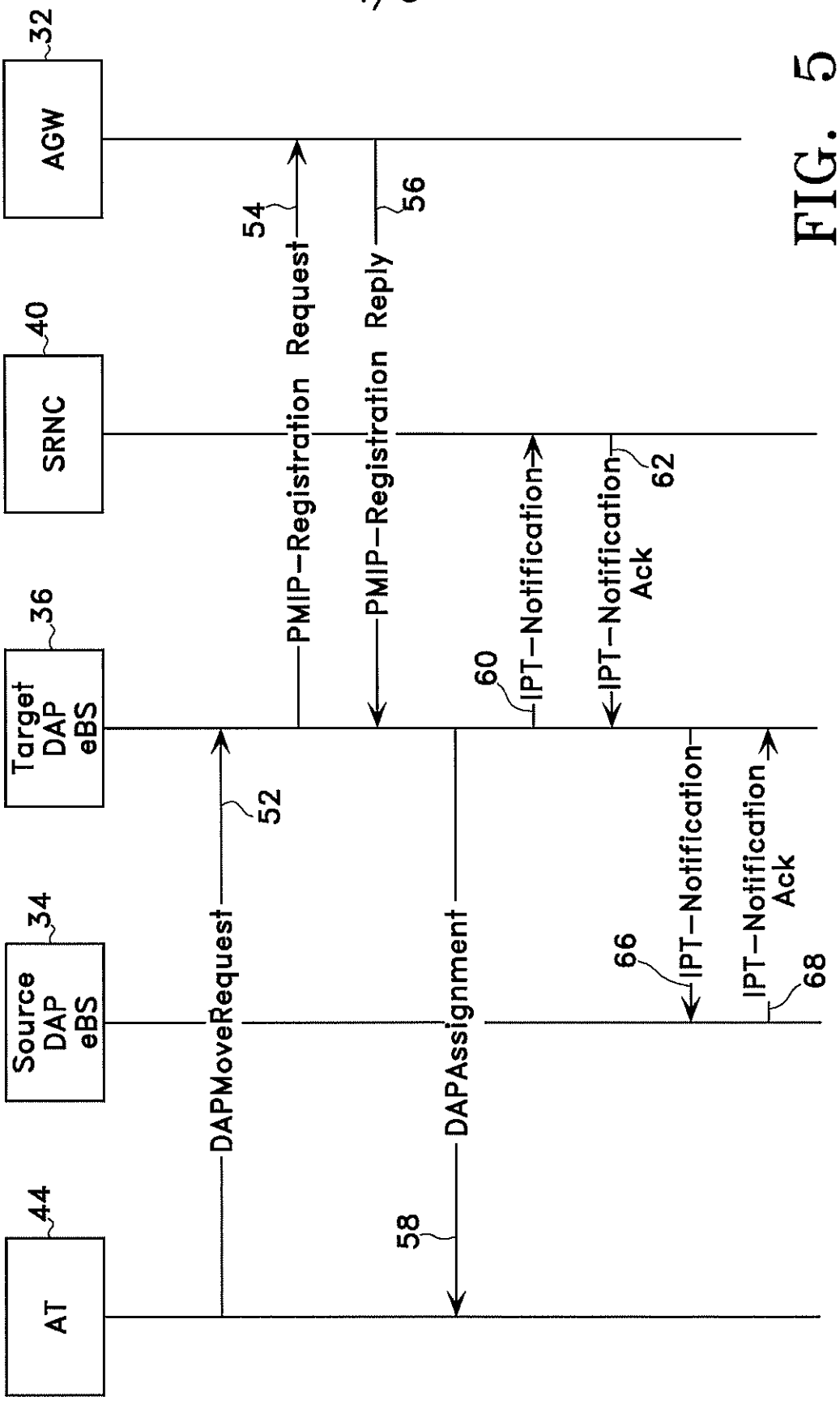

AGW
32
SRNC
40
Target DAP eBS
36
Source DAP eBS
34
AT
44
54
PMIP-Registration Request
56
PMIP-Registration Reply
60
IPT-Notification
62
IPT-Notification Ack
52
DAPMoveRequest
58
DAPAssignment
66
IPT-Notification
68
IPT-Notification Ack

FIG. 5

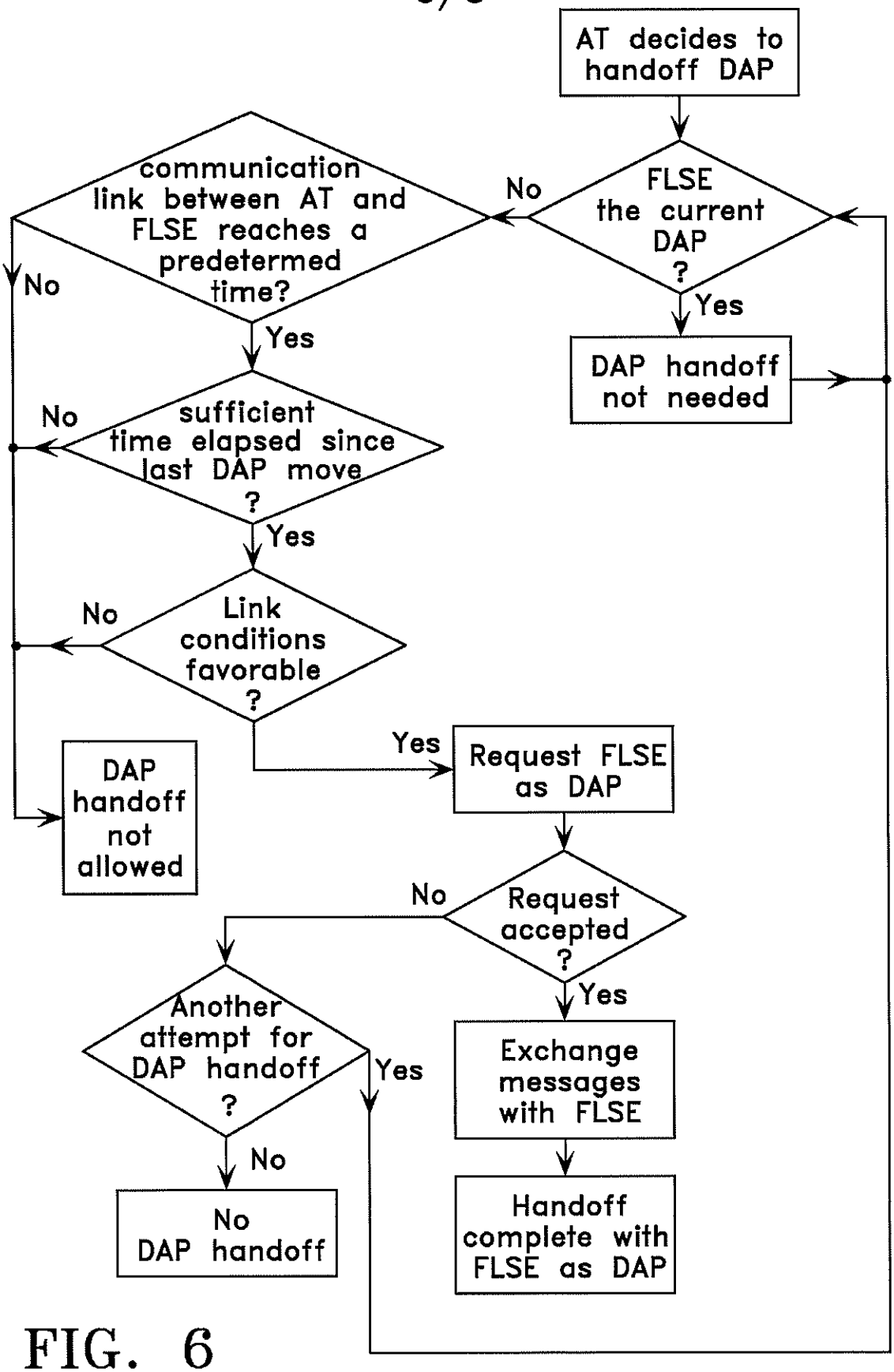

AT decides to handoff DAP
FLSE the current DAP ?
No
Yes
DAP handoff not needed
communication link between AT and FLSE reaches a predetermed time?
No
Yes
sufficient time elapsed since last DAP move ?
No
Yes
Link conditions favorable ?
No
Yes
Request FLSE as DAP
DAP handoff not allowed
Request accepted ?
No
Yes
Another attempt for DAP handoff ?
Yes
No
Exchange messages with FLSE
Handoff complete with FLSE as DAP
No DAP handoff

FIG. 6

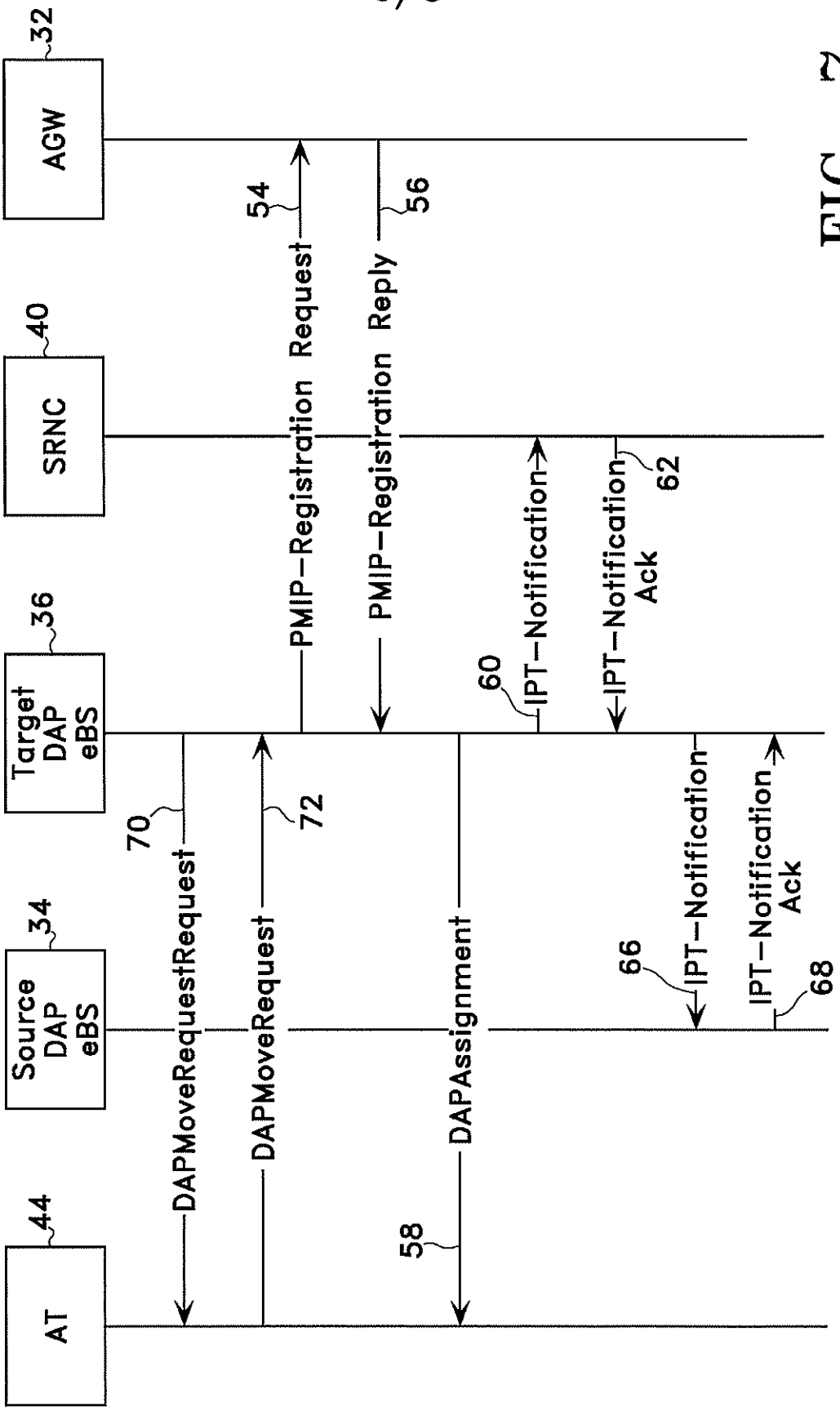

AGW
32
SRNC
40
Target DAP eBS
36
Source DAP eBS
34
AT
44
54
PMIP–Registration Request
56
PMIP–Registration Reply
60
IPT–Notification
62
IPT–Notification Ack
70
DAPMoveRequestRequest
72
DAPMoveRequest
58
DAPAssignment
66
IPT–Notification
68
IPT–Notification Ack

FIG. 7

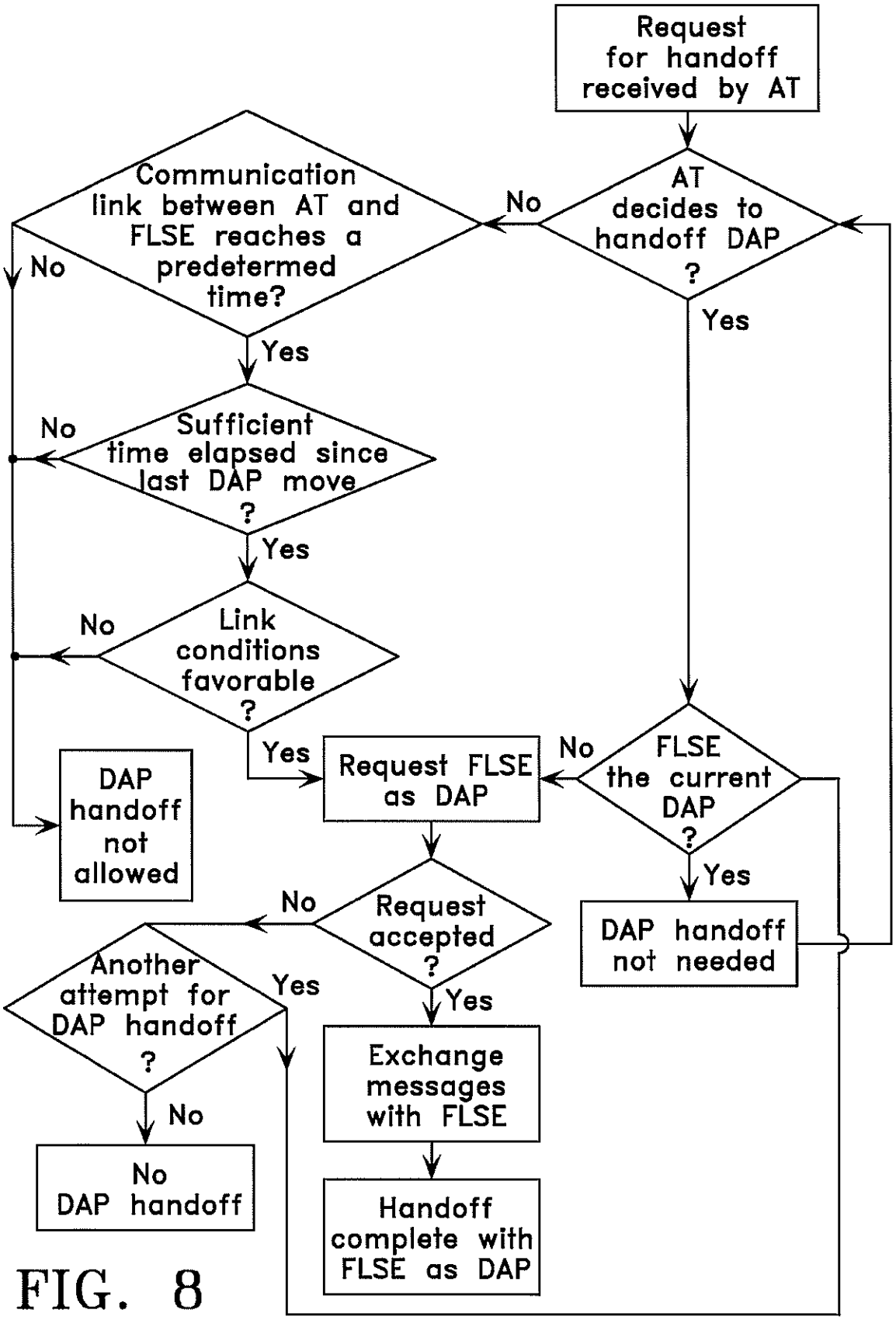

Request for handoff received by AT
AT decides to handoff DAP ?
No
Yes
Communication link between AT and FLSE reaches a predetermed time?
No
Yes
Sufficient time elapsed since last DAP move ?
No
Yes
Link conditions favorable ?
No
Yes
DAP handoff not allowed
Request FLSE as DAP
FLSE the current DAP ?
No
Yes
DAP handoff not needed
Request accepted ?
No
Yes
Another attempt for DAP handoff ?
Yes
No
No DAP handoff
Exchange messages with FLSE
Handoff complete with FLSE as DAP

FIG. 8

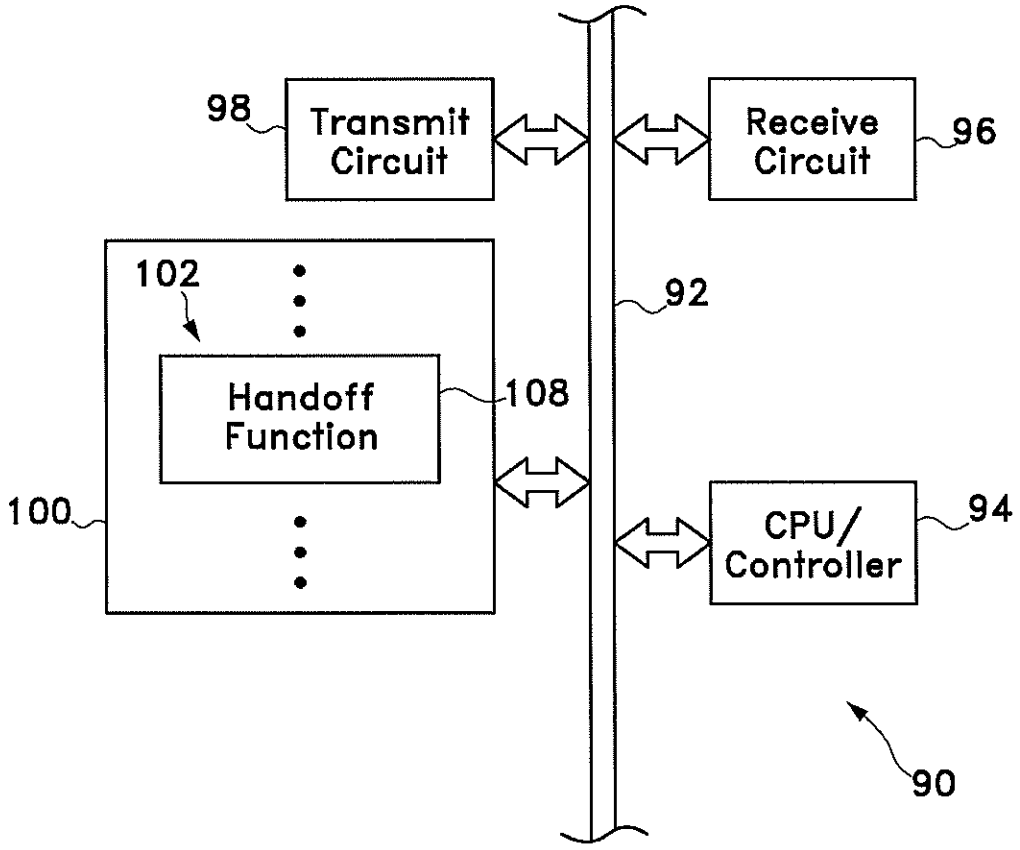

98
Transmit Circuit
Receive Circuit
96
102
92
Handoff Function
108
100
CPU/ Controller
94
90

FIG. 9