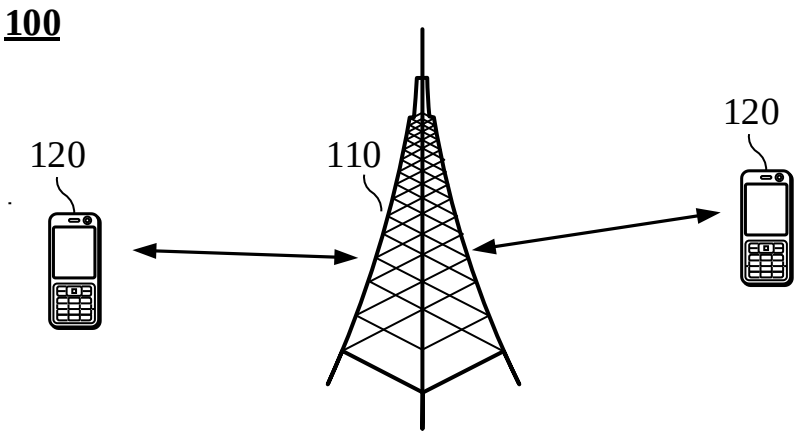

FIG. 1

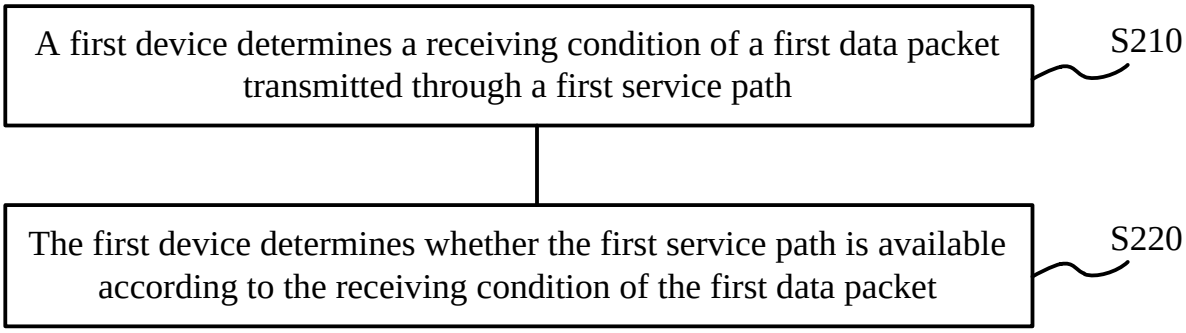

FIG. 2

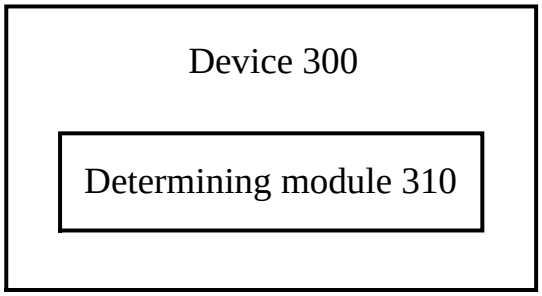

FIG. 3

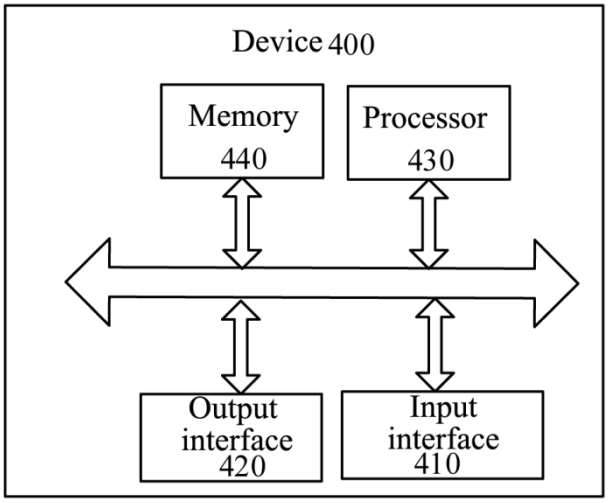
Device 400
Memory
440
Processor
430
Output
interface
420
Input
interface
410

FIG. 4