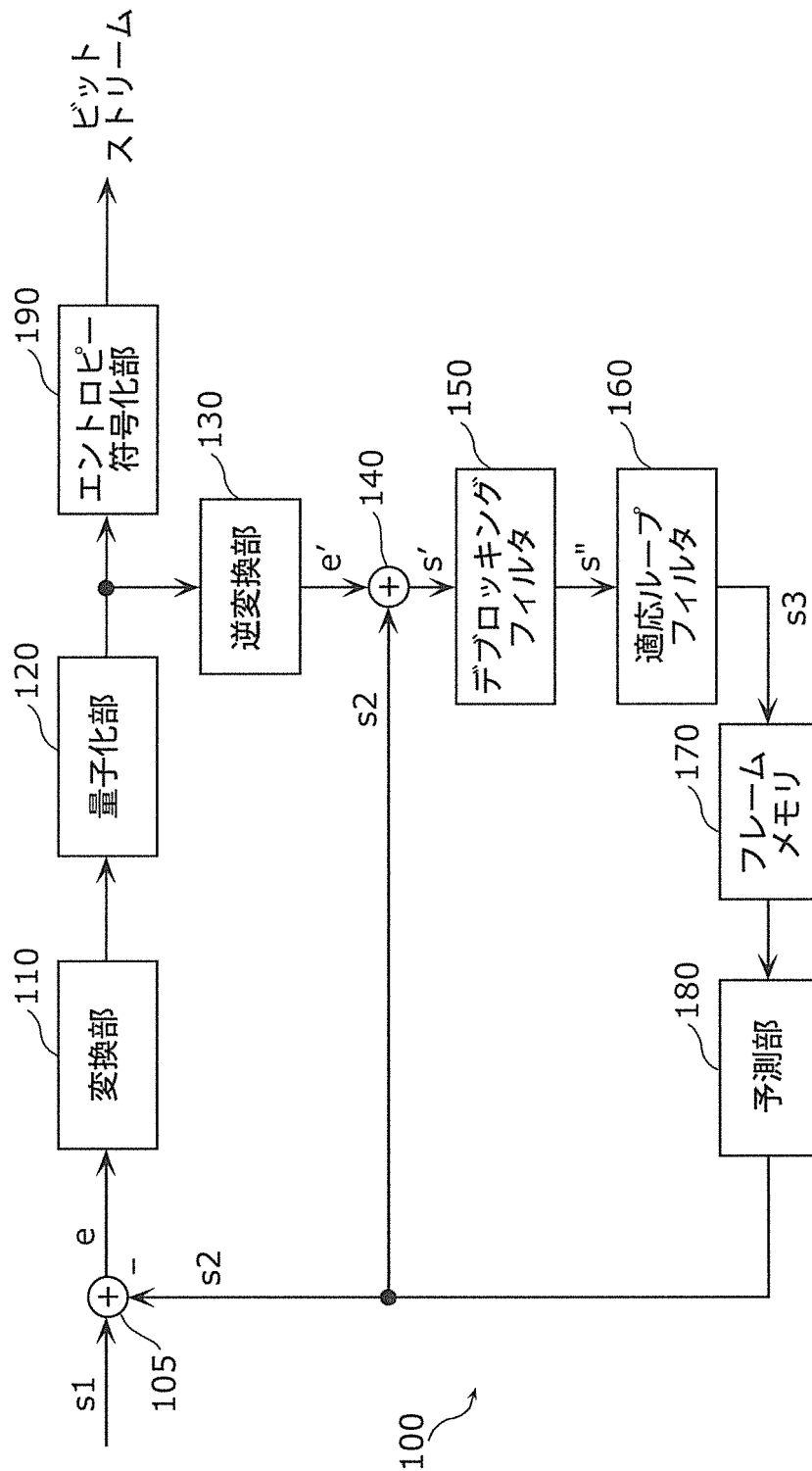
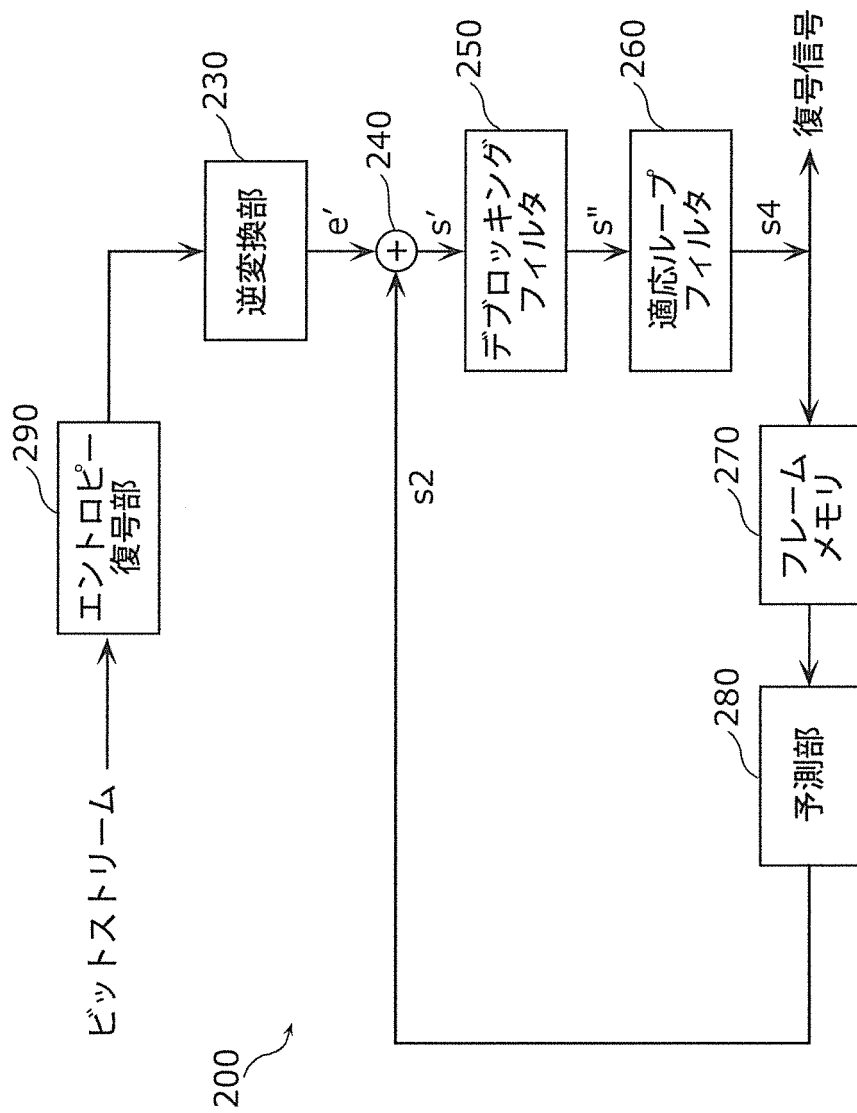


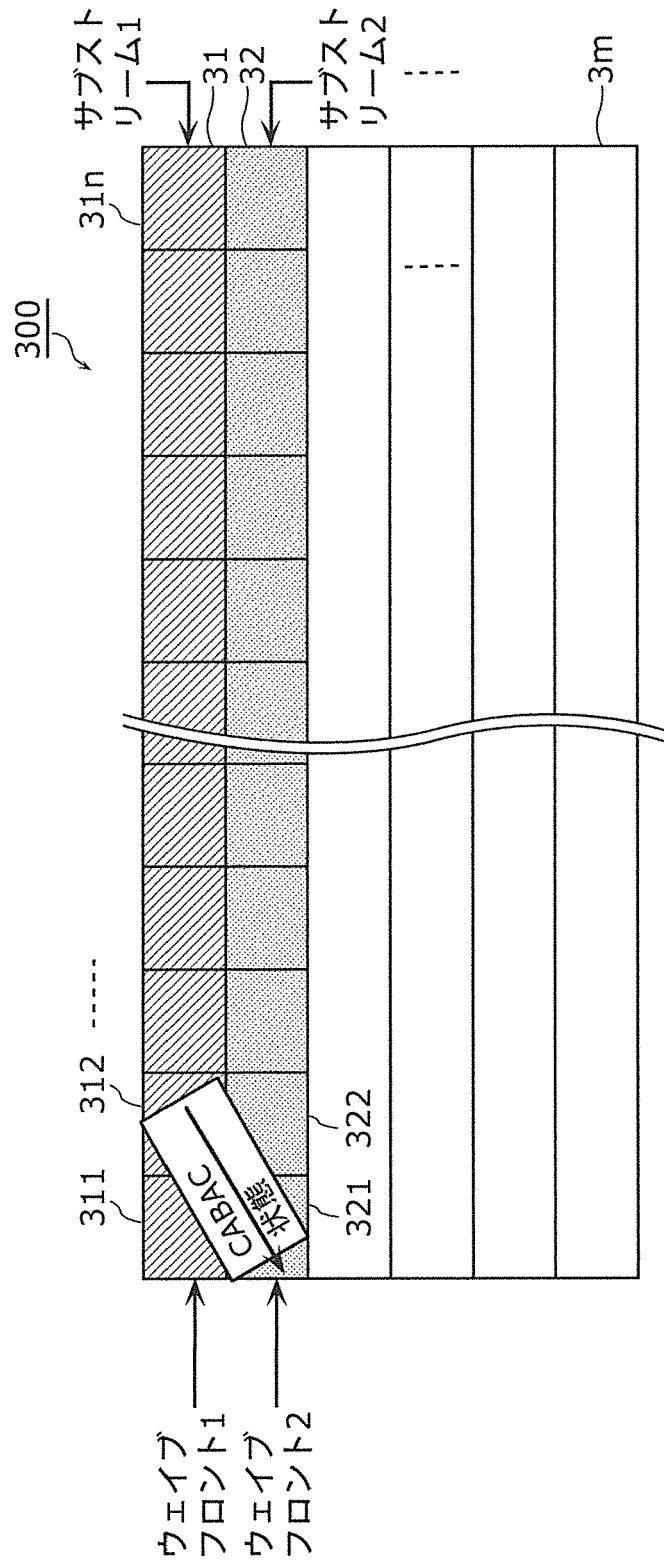
[図1]



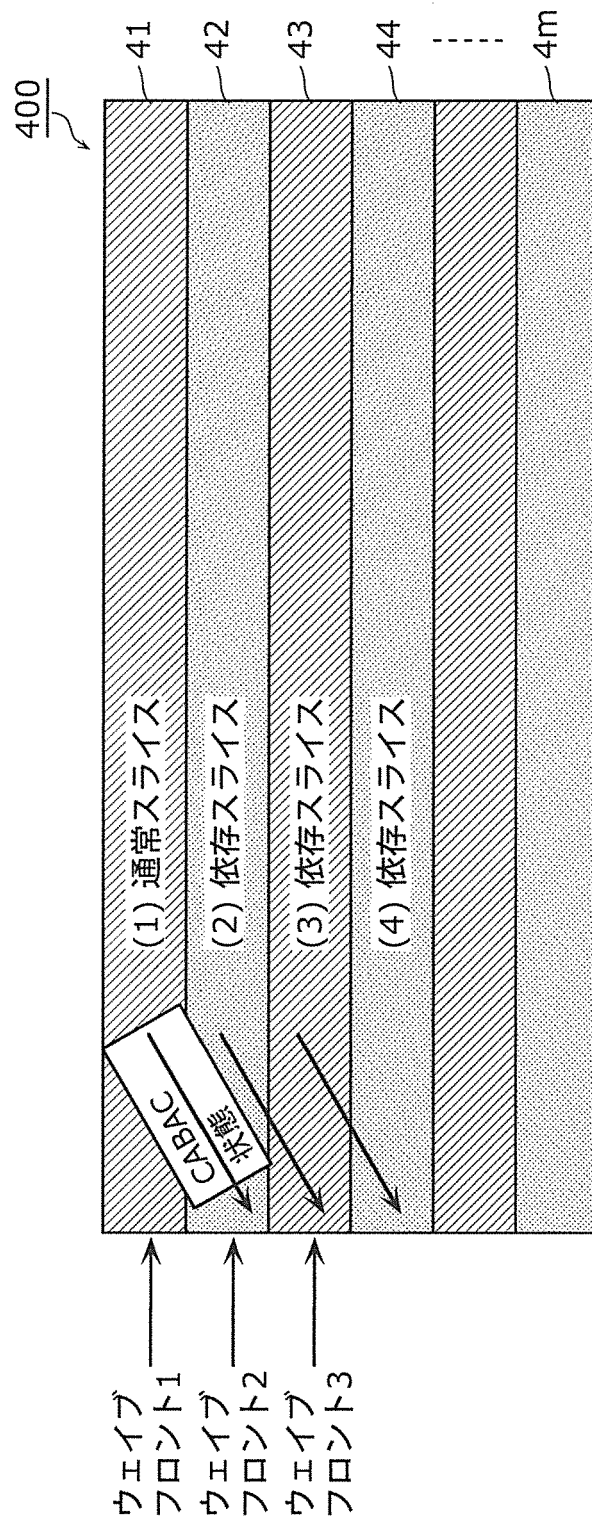
[図2]



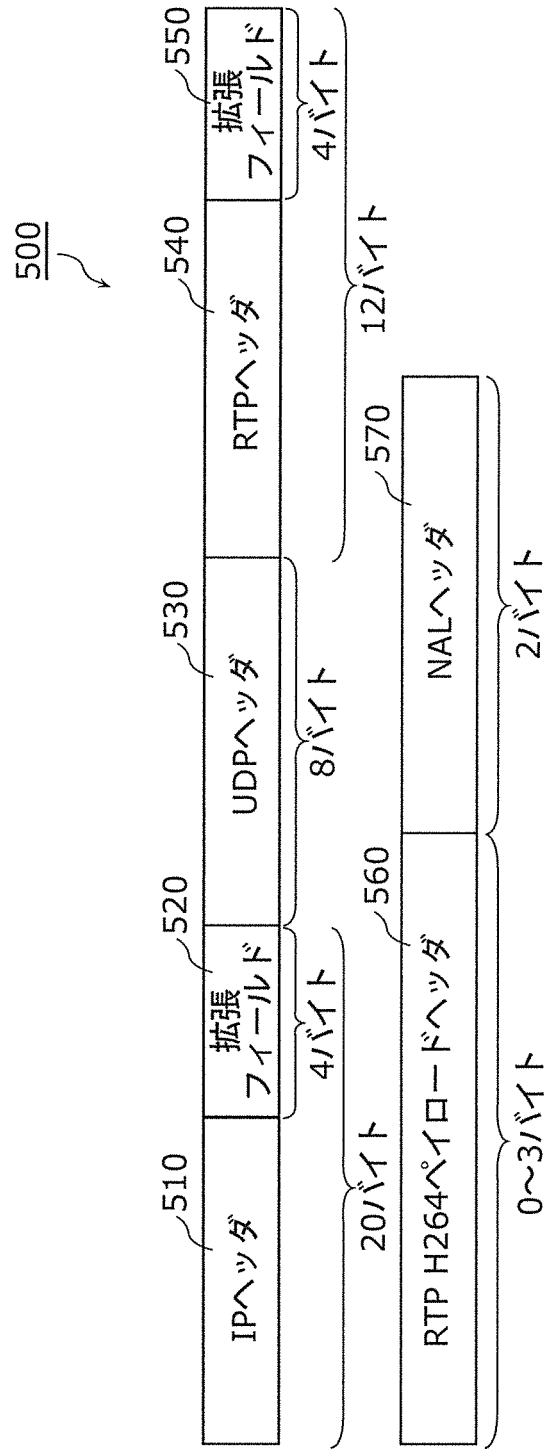
[図3]



[図4]



[図5]



[図6]

600

	slice_header() {	Descriptor
	first_slice_in_pic_flag	u(1)
	if(RapPicFlag)	
	no_output_of_prior_pics_flag	u(1)
	pic_parameter_set_id	ue(v)
	if(!first_slice_in_pic_flag)	
	slice_address	u(v)
	if(dependent_slice_enabled_flag && !first_slice_in_pic_flag)	
	dependent_slice_flag	u(1)
	if (dependent_slice_flag == 0) slice_header_info	
	if(tiles_or_entropy_coding_sync_idc == 1	
	tiles_or_entropy_coding_sync_idc == 2) {	
	num_entry_point_offsets	ue(v)
	if(num_entry_point_offsets > 0) {	
	offset_len_minus1	ue(v)
	for(i = 0; i < num_entry_point_offsets; i++)	
	entry_point_offset[i]	u(v)
	}	
	}	

依存スライスフラグ

601

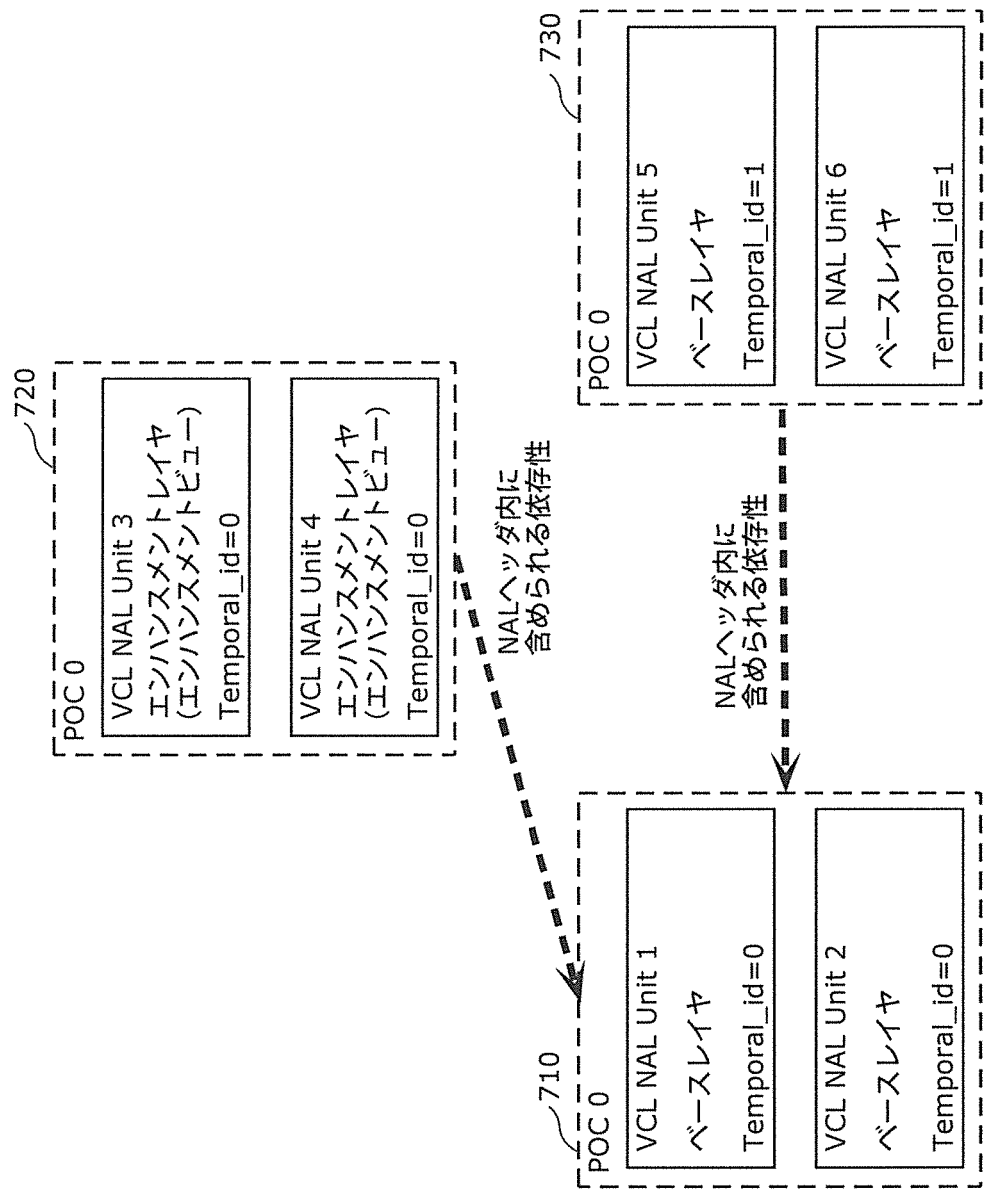
スライス内のCABAC
ストリームの数

602

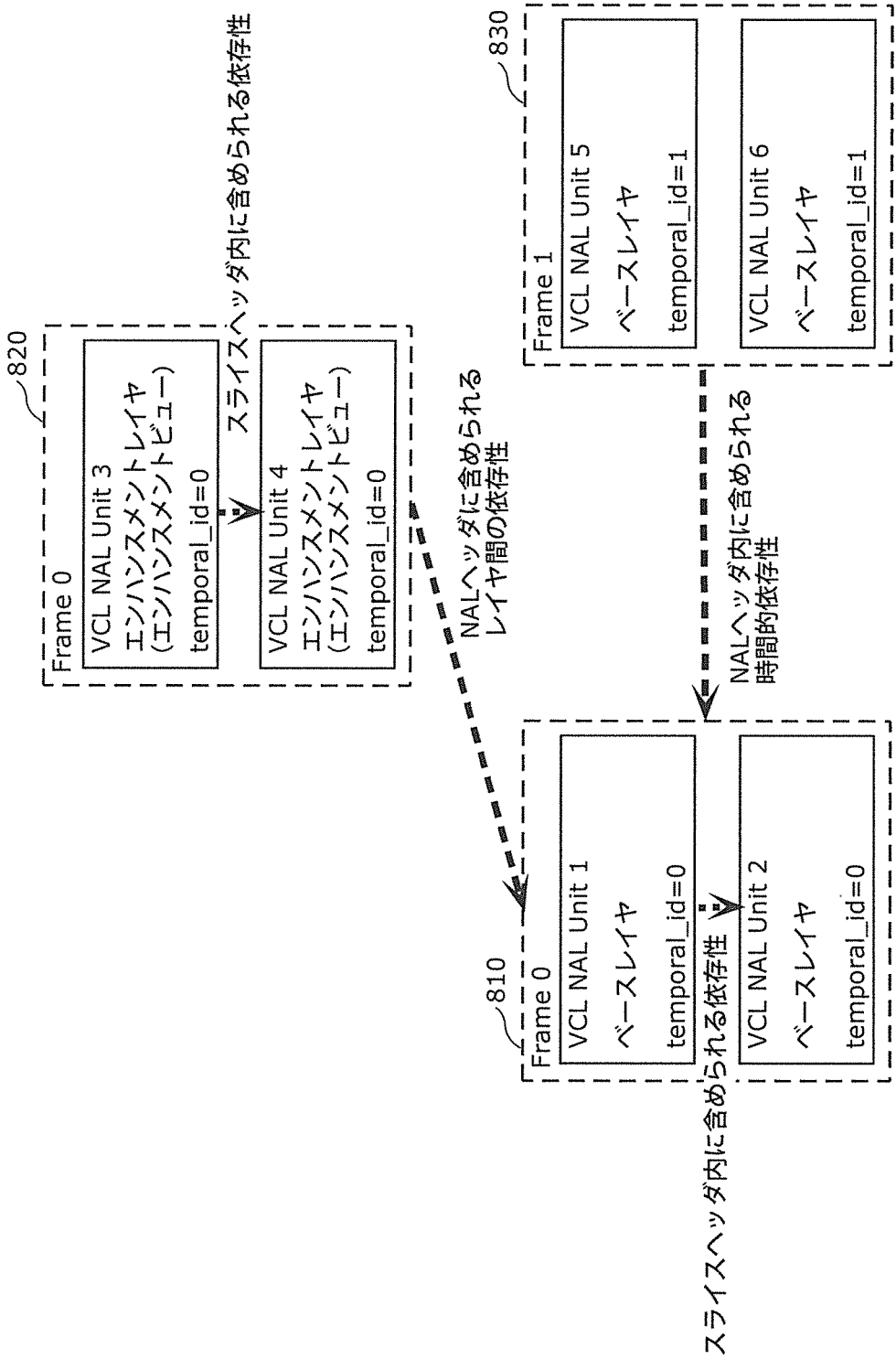
サブストリームのバイト数
(ビットストリーム内の
ナビゲーションが容易になる)

603

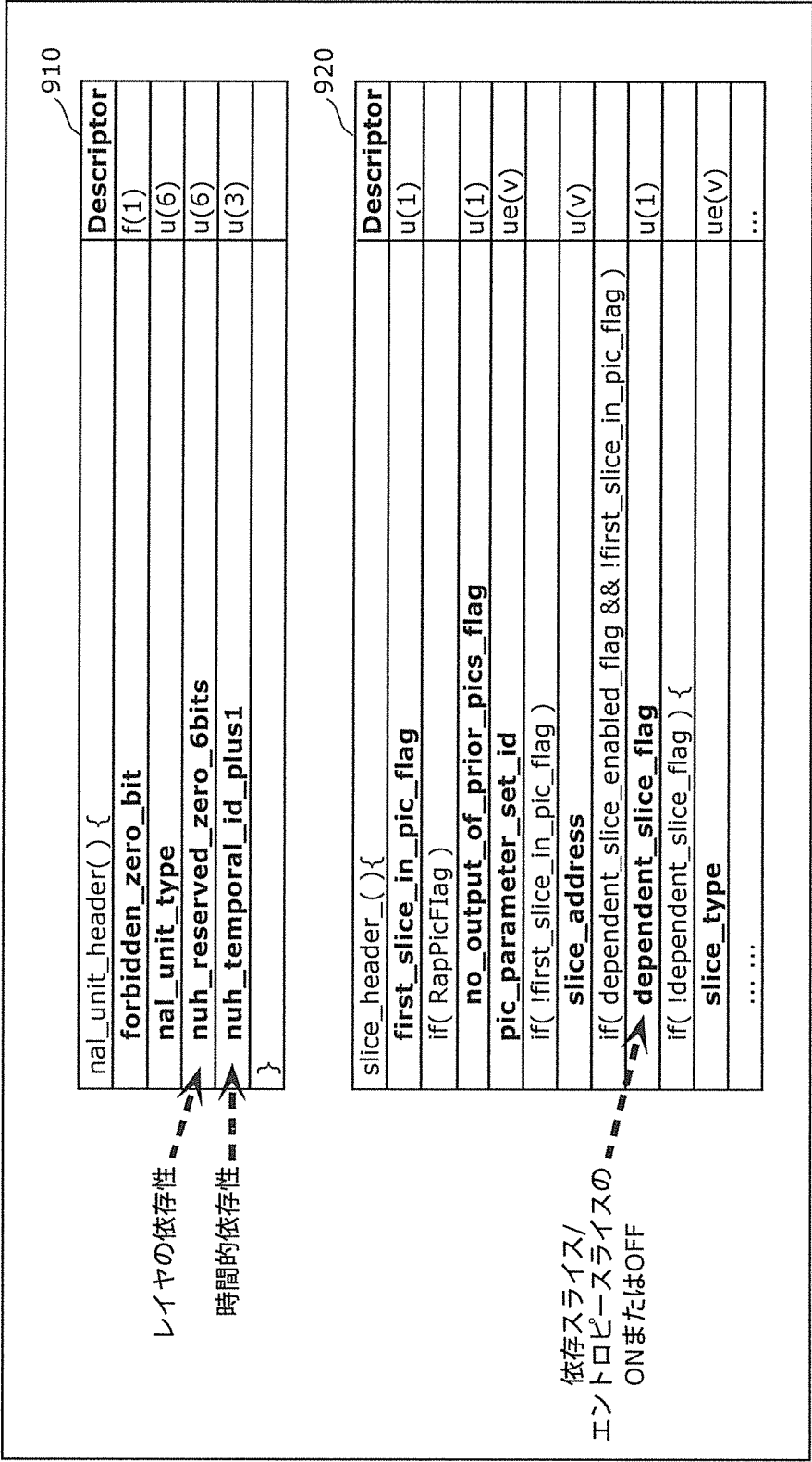
[図7]



[図8]



[図9A]



[図9B]

seq_parameter_set_rbsp() {	Descriptor
video_parameter_set_id	u(4)
sps_max_sub_layers_minus1	u(3)
sps_reserved_zero_bit	u(1)
profile_and_level(1, sps_max_sub_layers_minus1)	
seq_parameter_set_id	ue(v)
chroma_format_idc	ue(v)
if(chroma_format_idc == 3)	
separate_colour_plane_flag	u(1)
pic_width_in_luma_samples ~931	ue(v)
pic_height_in_luma_samples ~932	ue(v)
pic_cropping_flag	u(1)
if(pic_cropping_flag) {	
pic_crop_left_offset	ue(v)
pic_crop_right_offset	ue(v)
pic_crop_top_offset	ue(v)
pic_crop_bottom_offset	ue(v)
}	
bit_depth_luma_minus8	ue(v)
bit_depth_chroma_minus8	ue(v)
pcm_enabled_flag	u(1)
if(pcm_enabled_flag) {	
pcm_sample_bit_depth_luma_minus1	u(4)
pcm_sample_bit_depth_chroma_minus1	u(4)
}	
log2_max_pic_order_cnt_lsb_minus4	ue(v)
for(i=0; i <= sps_max_sub_layers_minus1; i++) {	
sps_max_dec_pic_buffering[i]	ue(v)
sps_max_num_reorder_pics[i]	ue(v)
sps_max_latency_increase[i]	ue(v)
}	
restricted_ref_pic_lists_flag	u(1)
if(restricted_ref_pic_lists_flag)	
lists_modification_present_flag	u(1)
log2_min_coding_block_size_minus3 ~933	ue(v)
log2_diff_max_min_coding_block_size ~934	ue(v)
...	
...	
...	
}	

920

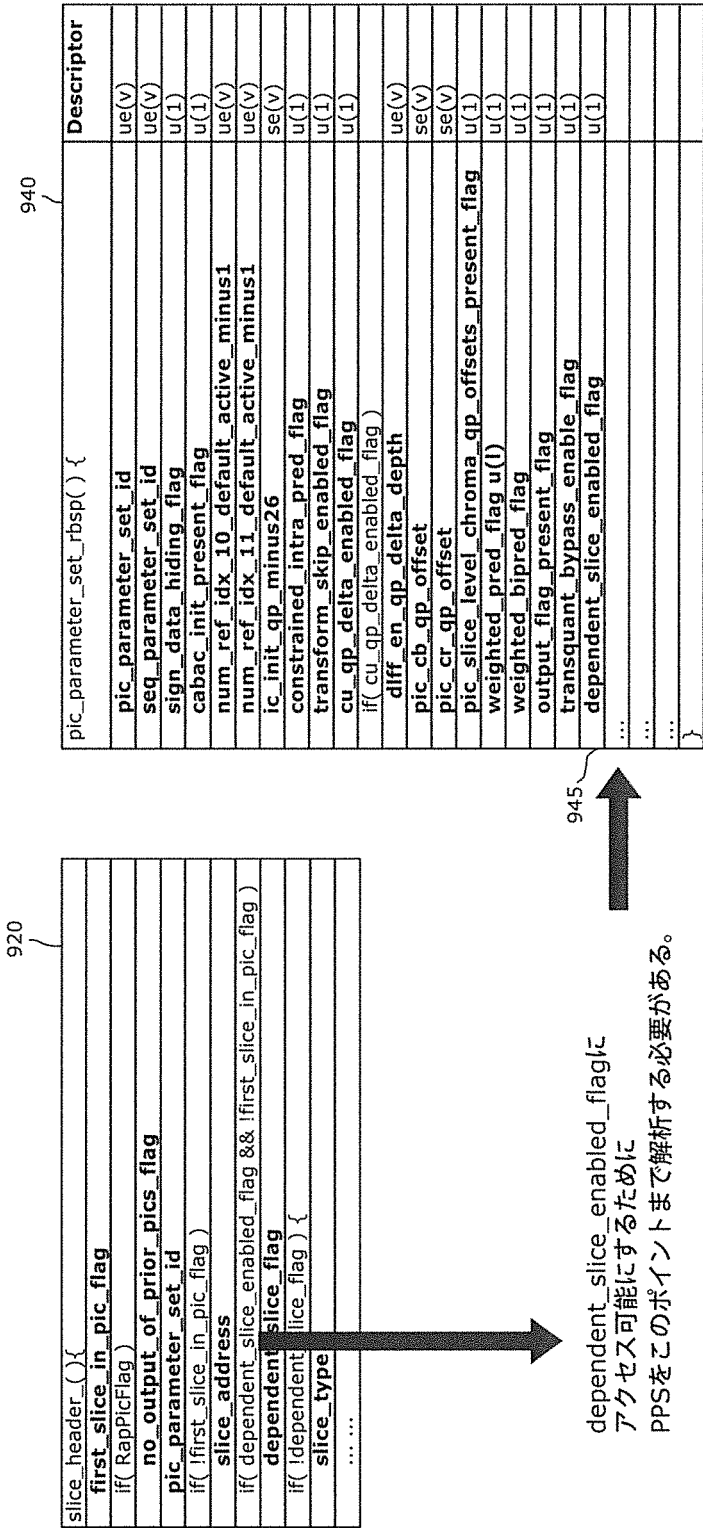
slice_header() {
first_slice_in_pic_flag
if(RapPicFlag)
no_output_of_prior_pics_flag
pic_parameter_set_id
if(!first_slice_in_pic_flag)
slice_address
if(dependent_slice_enabled_flag && !first_slice_in_pic_flag)
dependent_slice_flag
if(!dependent_slice_flag) {
slice_type
... ..

slice_addressの解析のために、SPSに含まれる
シンタックスエレメントが必要。
SPSをこのポイントまで解析する必要がある。

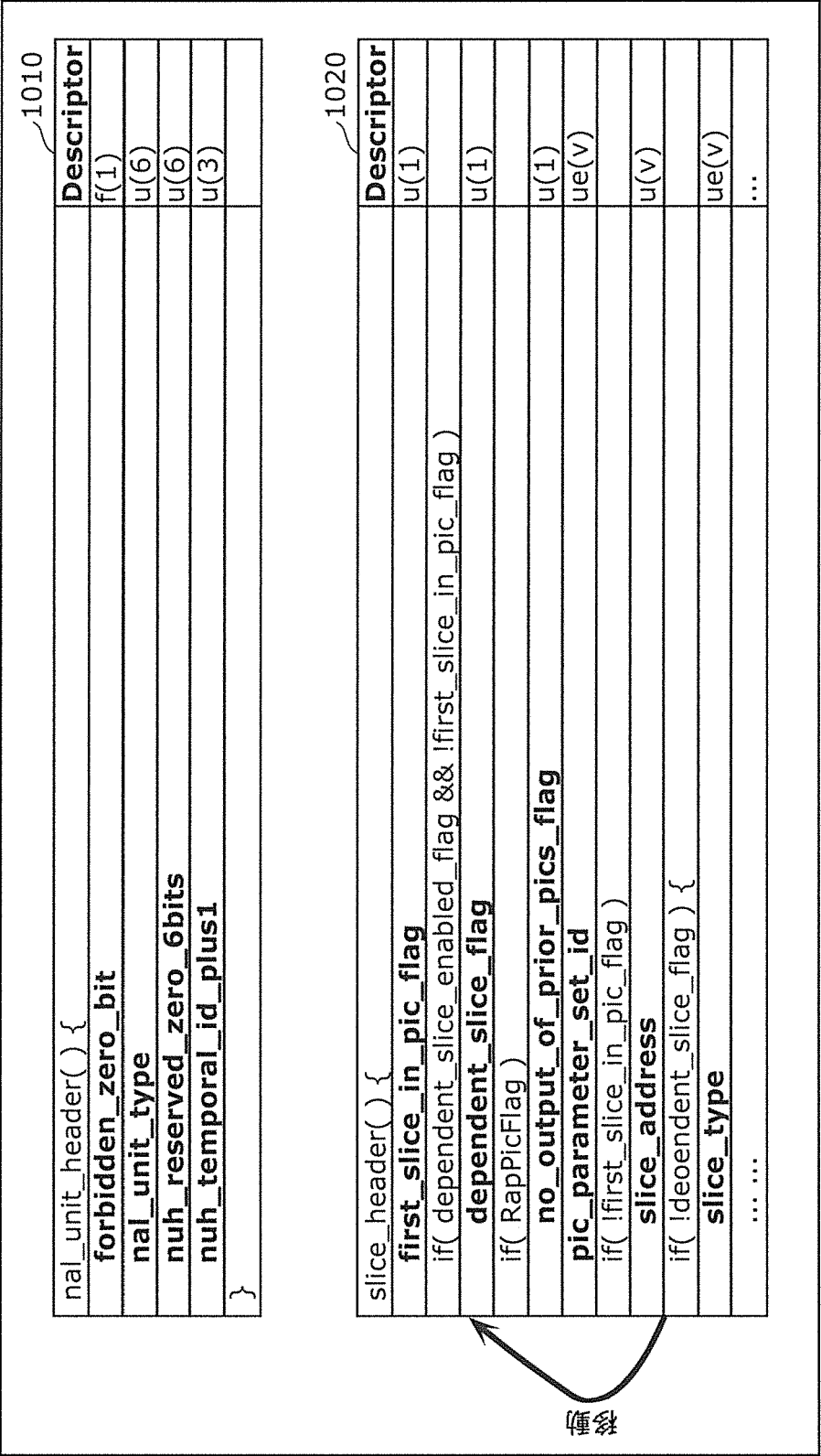
930

935

[図9C]



[図10]

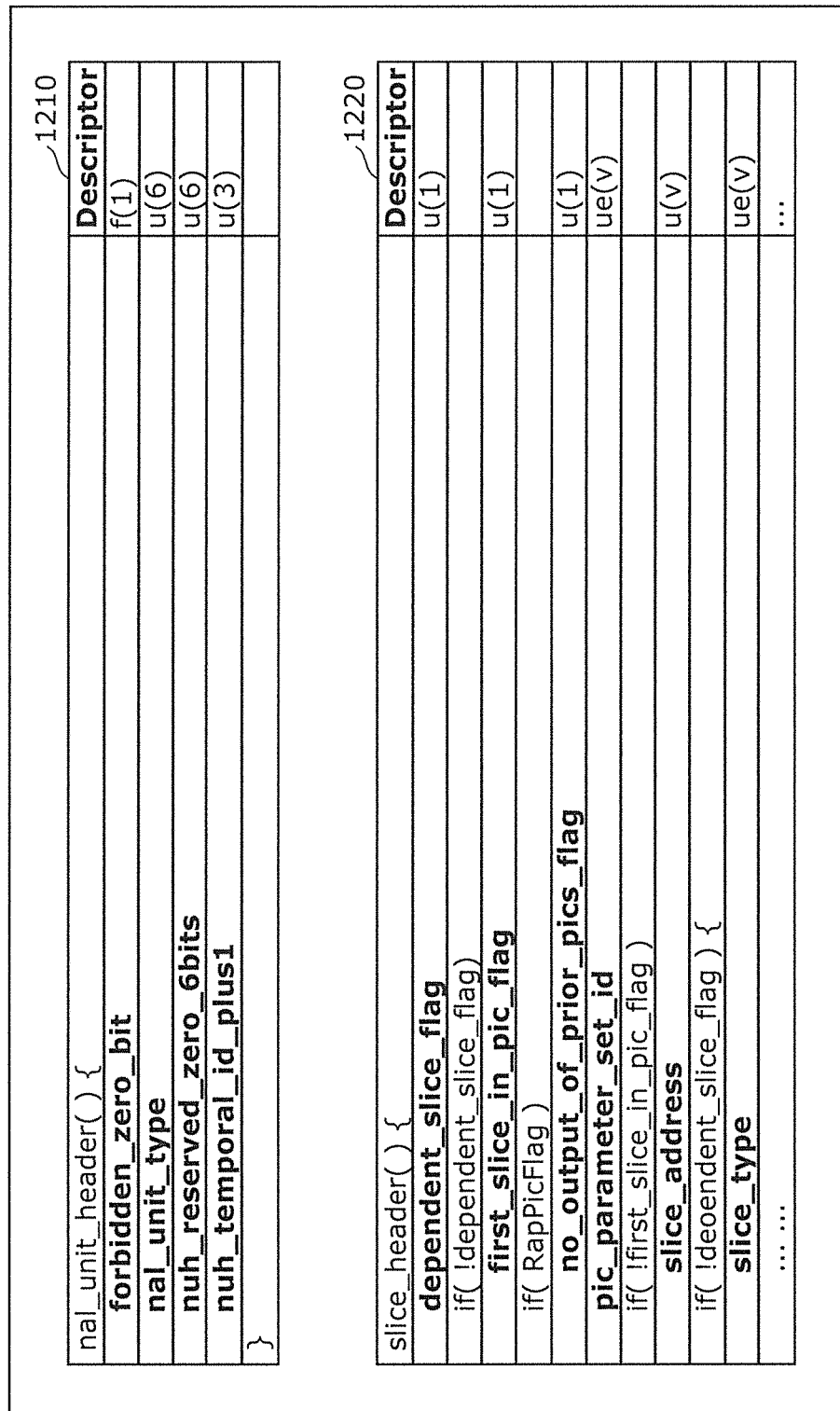


[図11]

1110

slice_header() {	Descriptor
first_slice_in_pic_flag	u(1)
if(dependent_slice_enabled_flag && !first_slice_in_pic_flag)	
dependent_slice_flag	u(1)
if(RapPicFlag)	
no_output_of_prior_pics_flag	u(1)
pic_parameter_set_id	ue(v)
if(!first_slice_in_pic_flag)	
slice_address	u(v)
if(!dependent_slice_flag) {	
slice_type	ue(v)
... ..	...

[図12]



[図13]

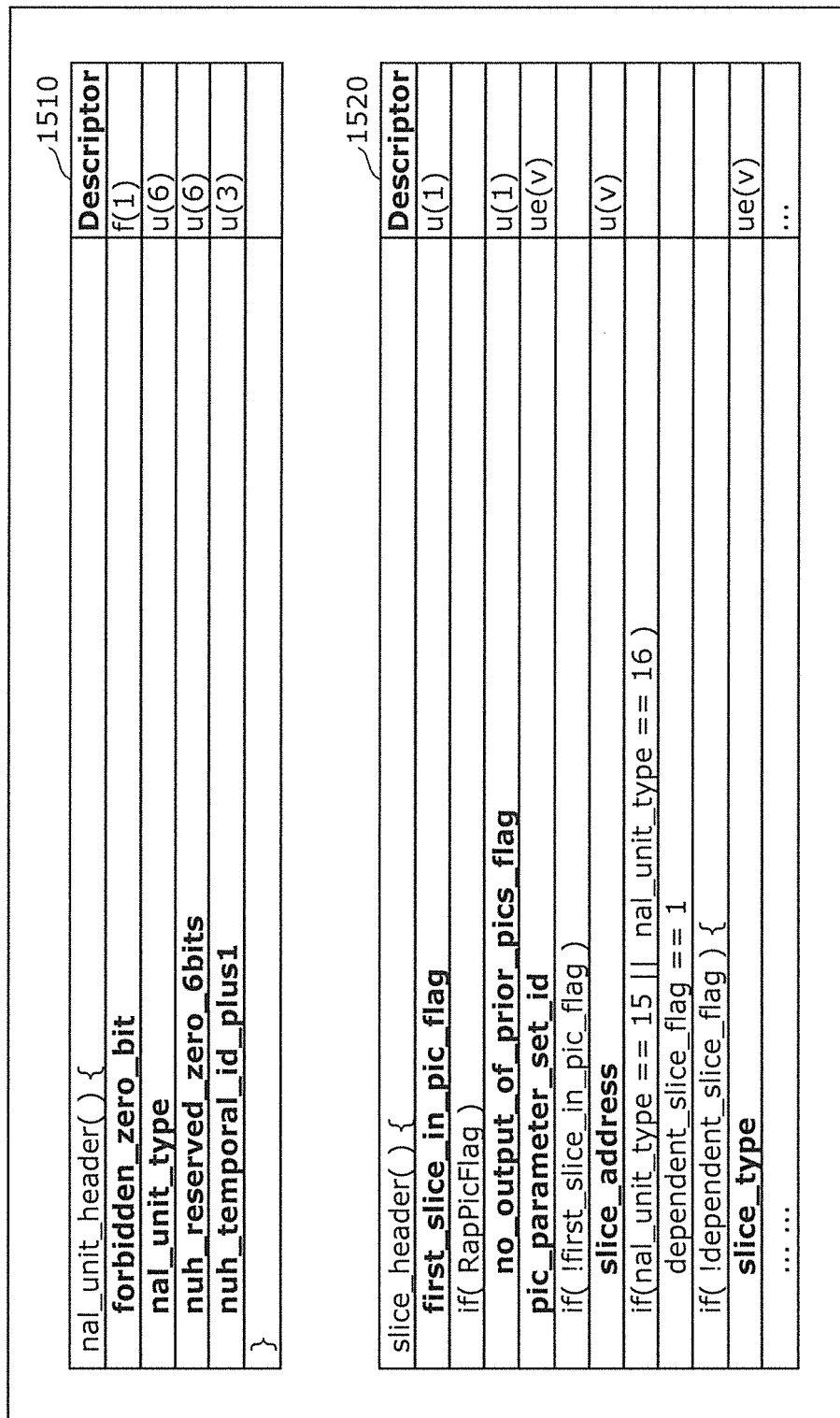
1310

slice_header_() {	Descriptor
first_slice_in_pic_flag	u(1)
if(RapPicFlag)	
no_output_of_prior_pics_flag	u(1)
pic_parameter_set_id	ue(v)
if(dependent_slice_enabled_flag && !first_slice_in_pic_flag)	
dependent_slice_flag	u(1)
if(!first_slice_in_pic_flag)	
slice_address	u(v)
if(!dependent_slice_flag) {	
slice_type	ue(v)
... ..	

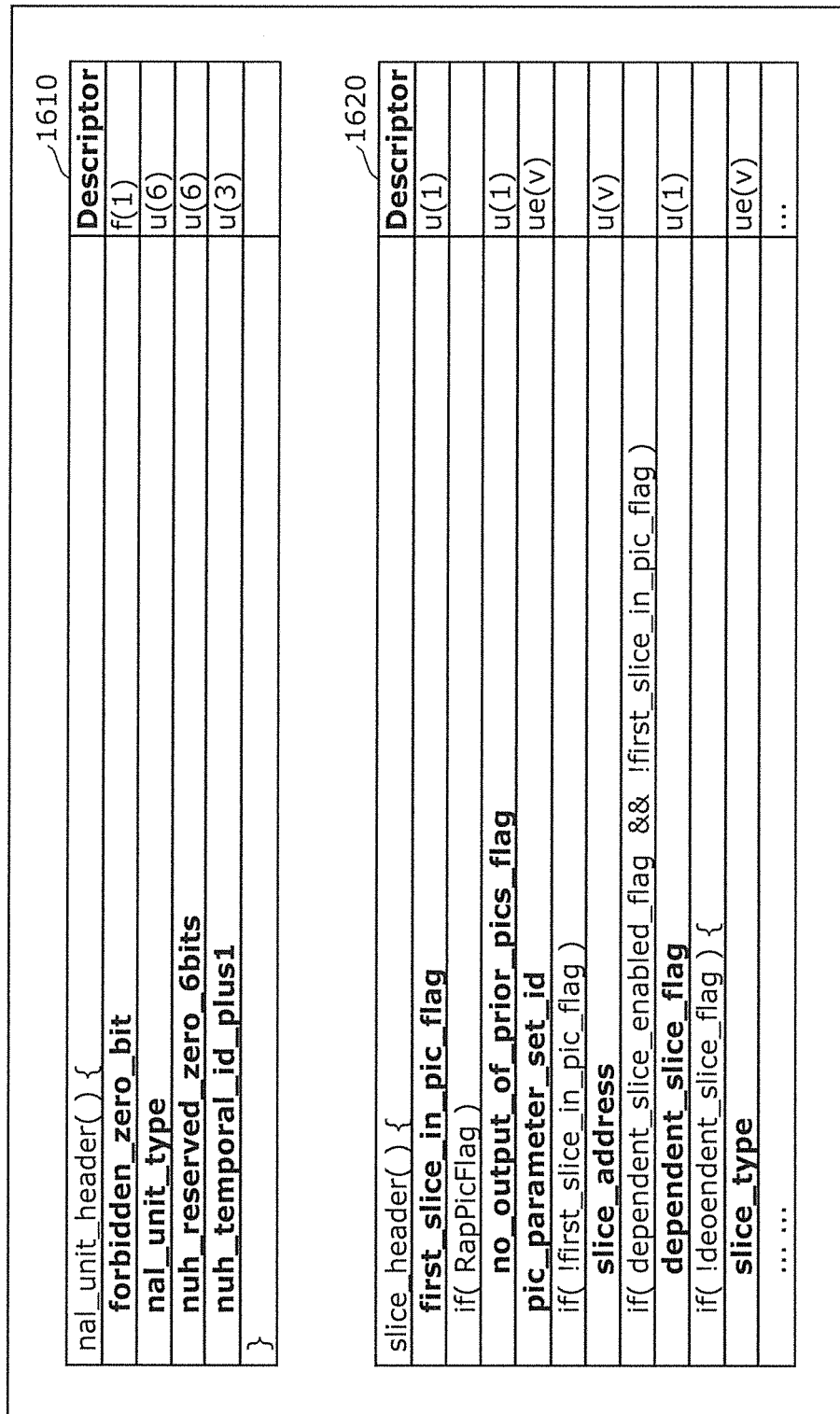
[図14]

nal_unit_header() {	
forbidden_zero_bit	Descriptor f(1)
nal_unit_type	u(6)
nuh_reserved_zero_6bits	u(5)
nuh_temporal_id_plus1	u(3)
dependent_slice_flag	u(1)
}	

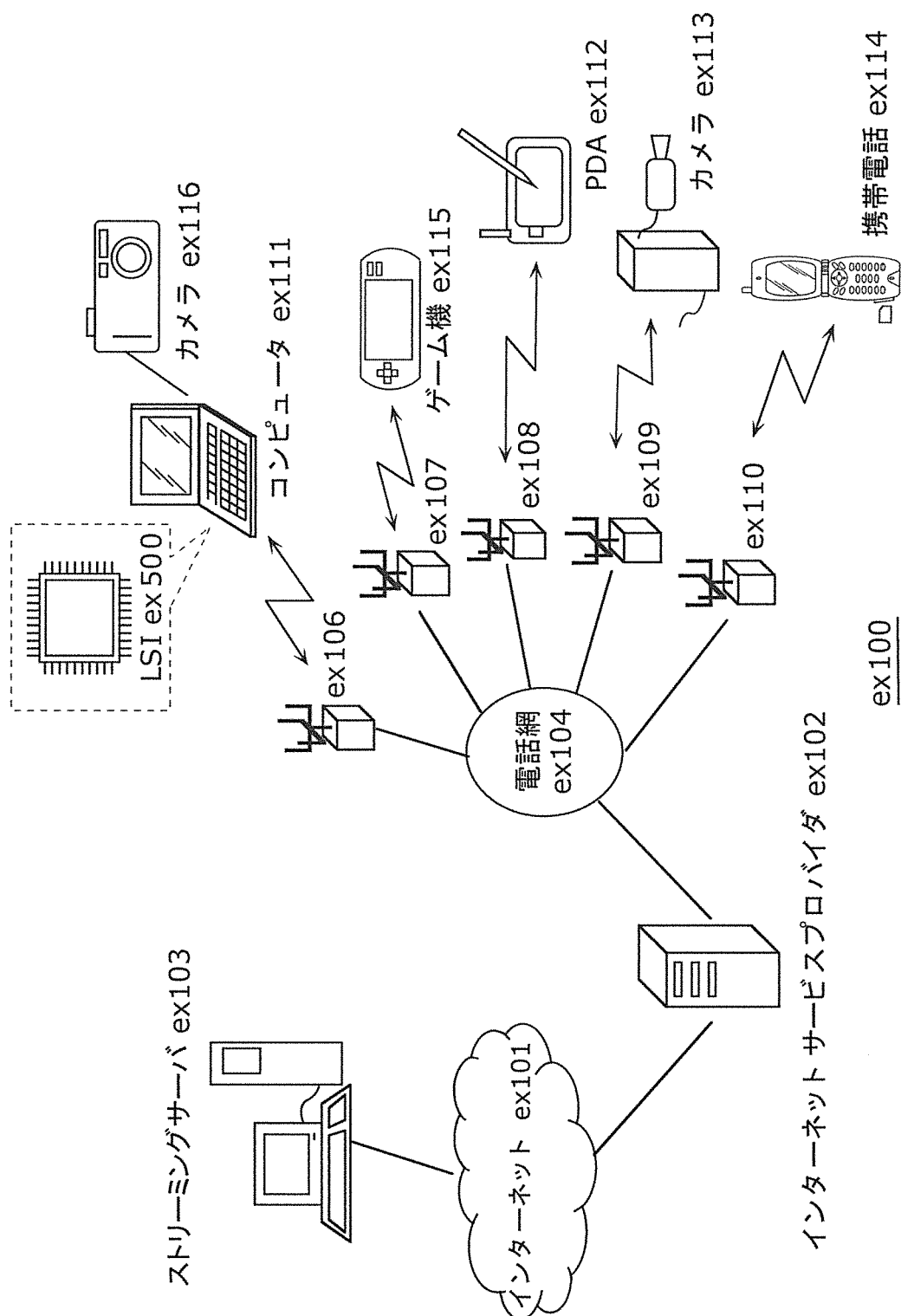
[図15]

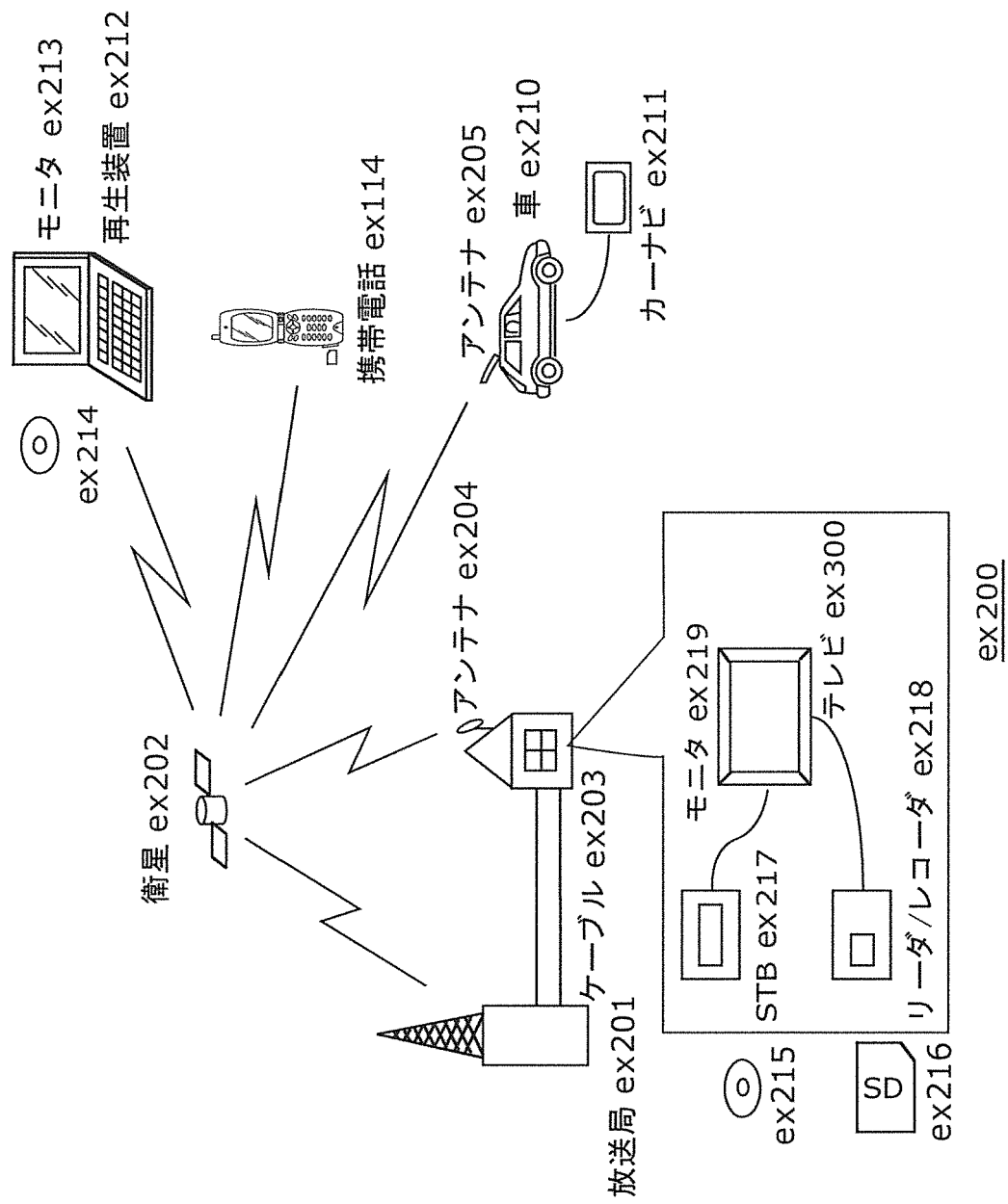


[図16]

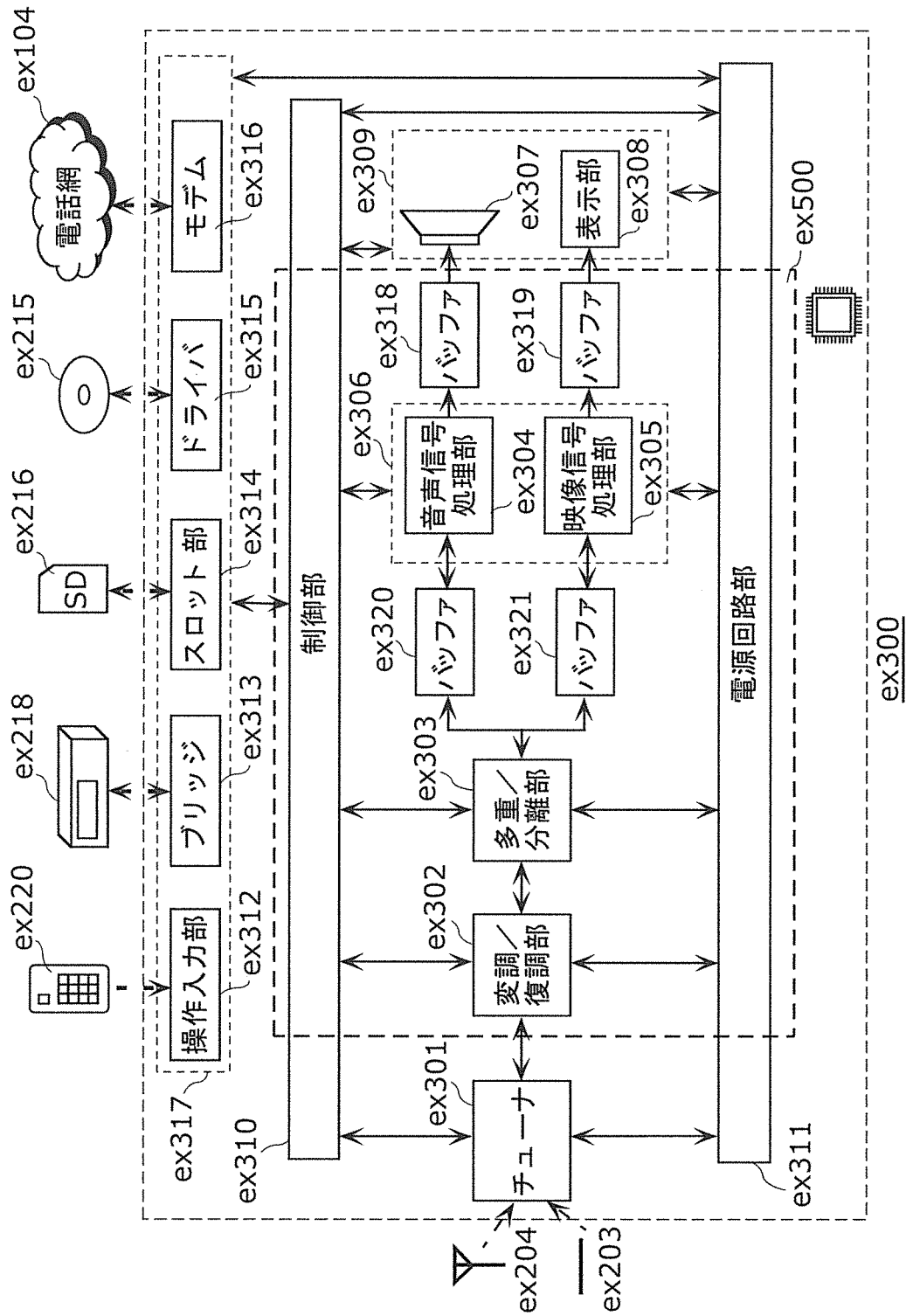


[図17]

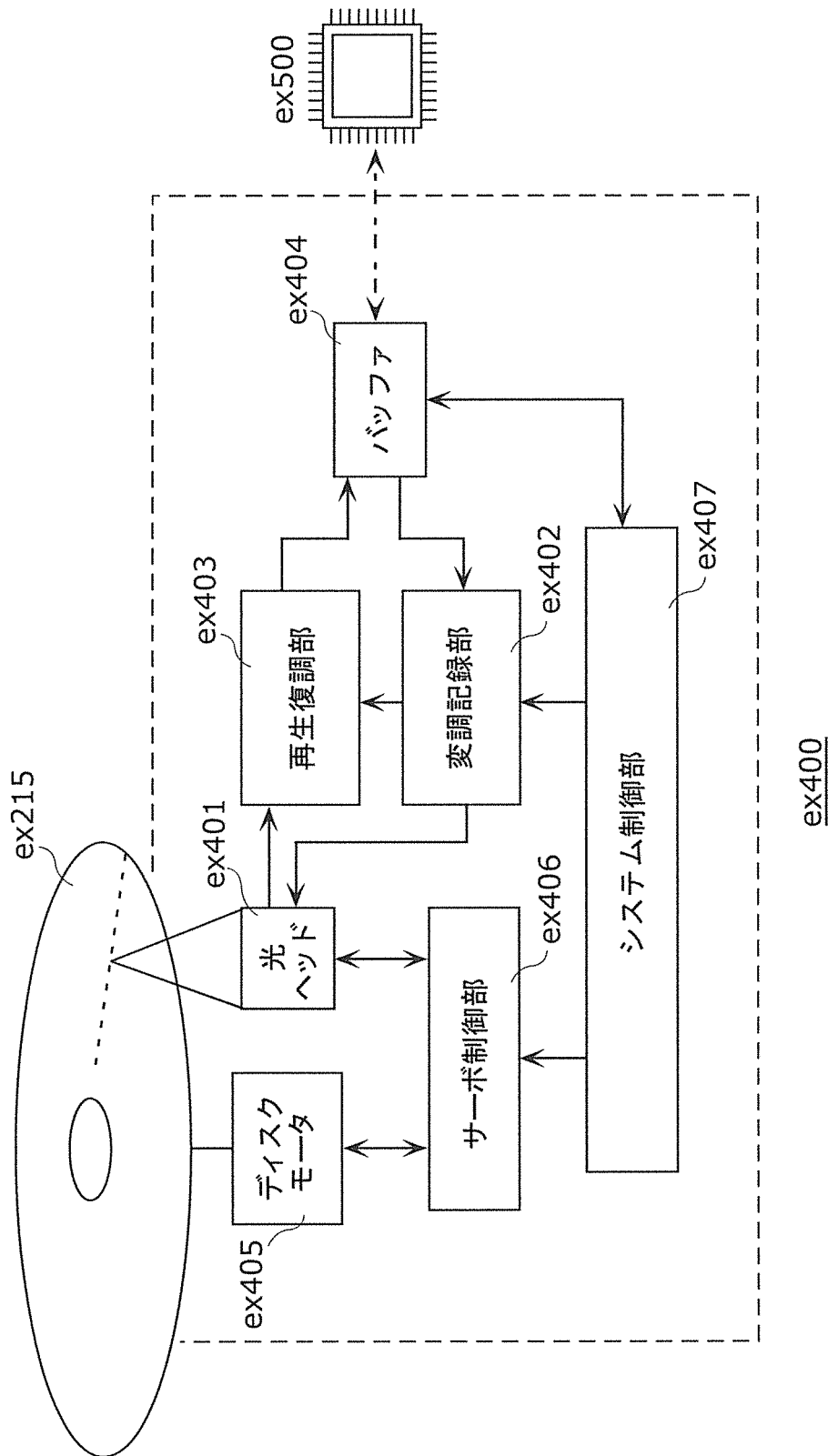




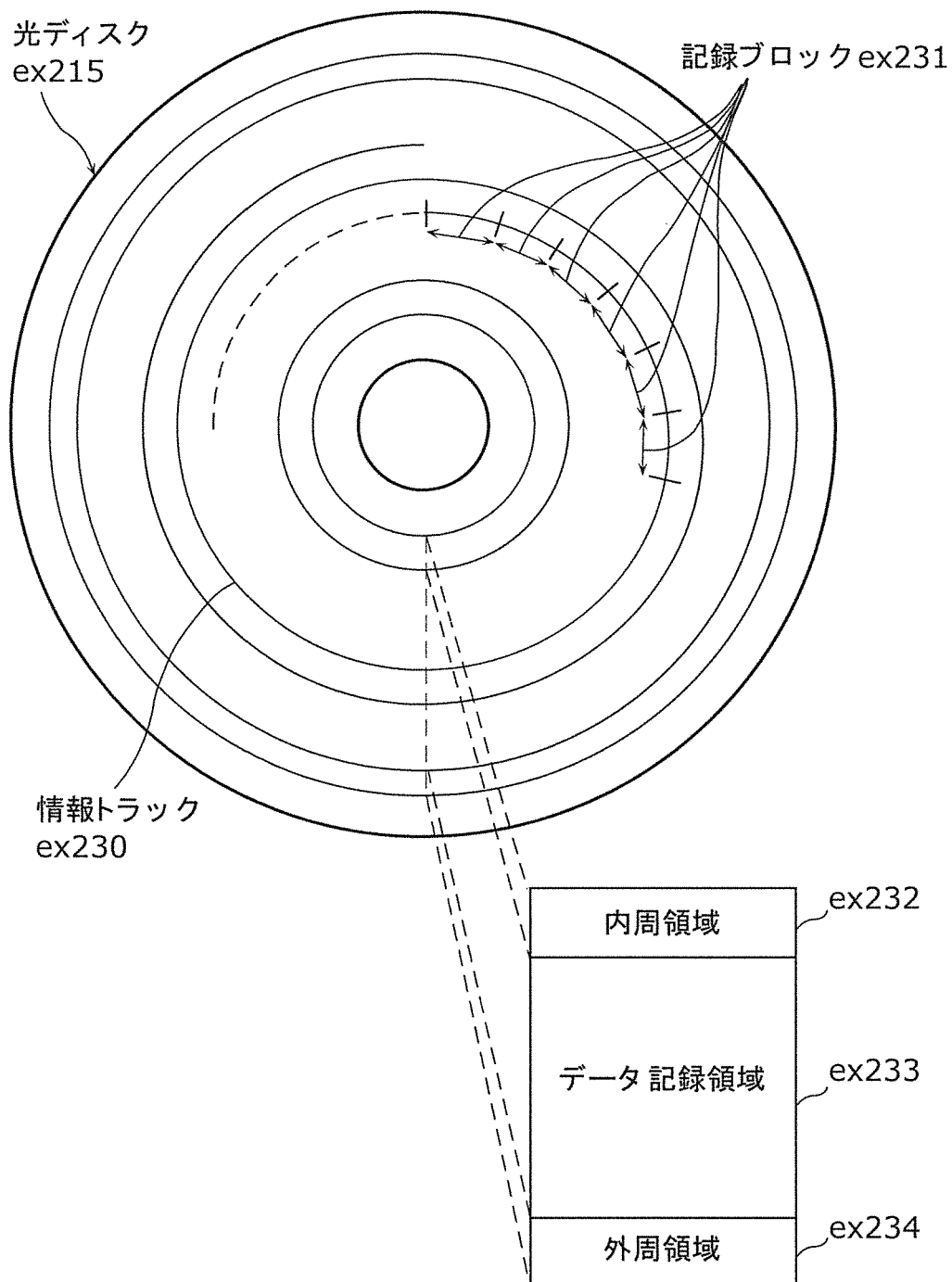
[図19]



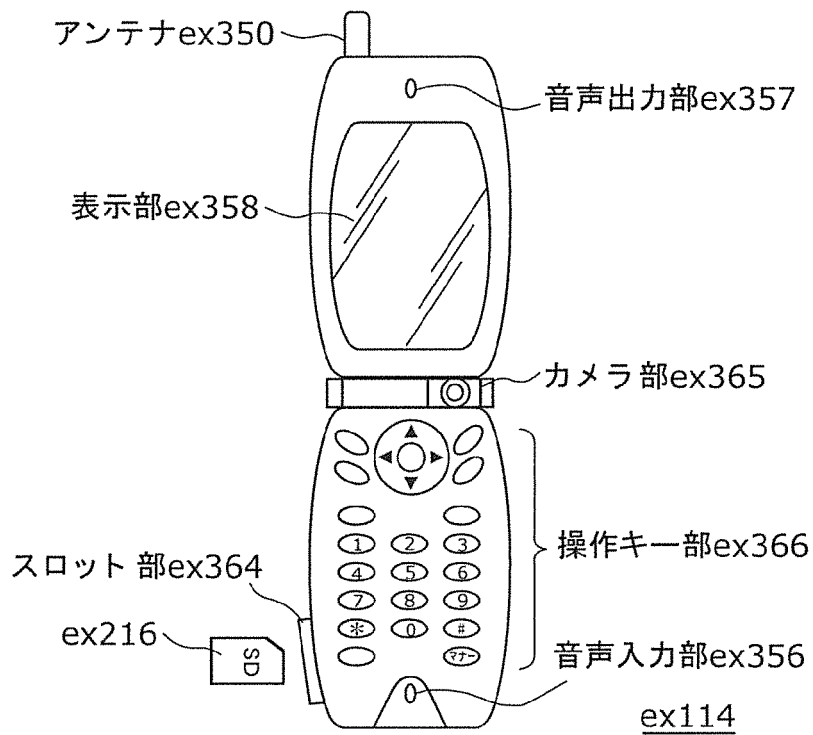
[図20]



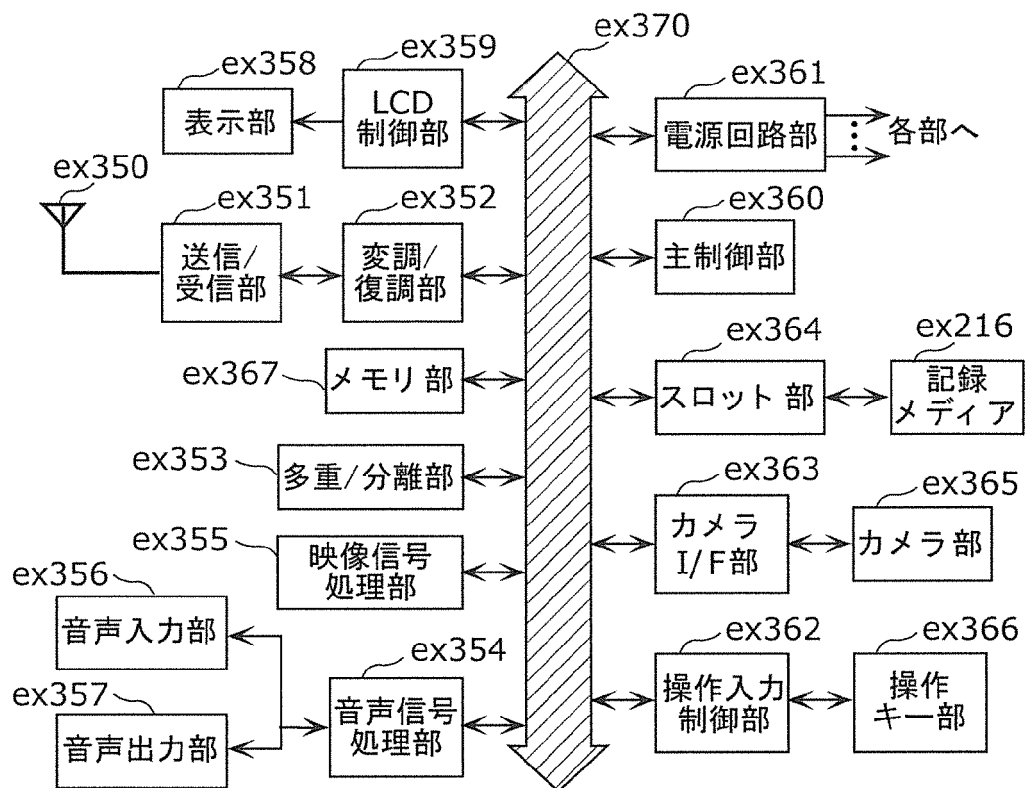
[図21]



[図22A]



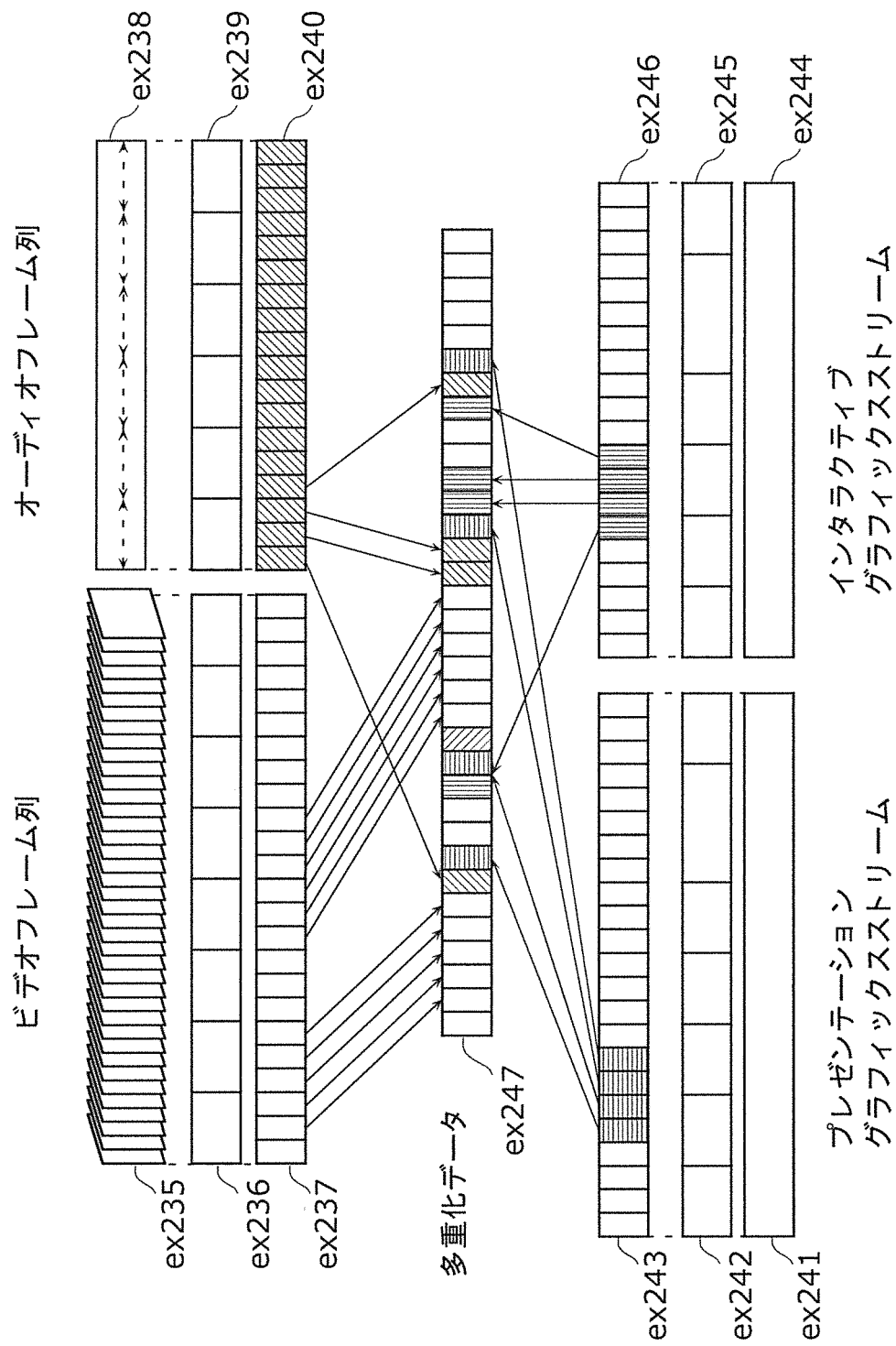
[図22B]



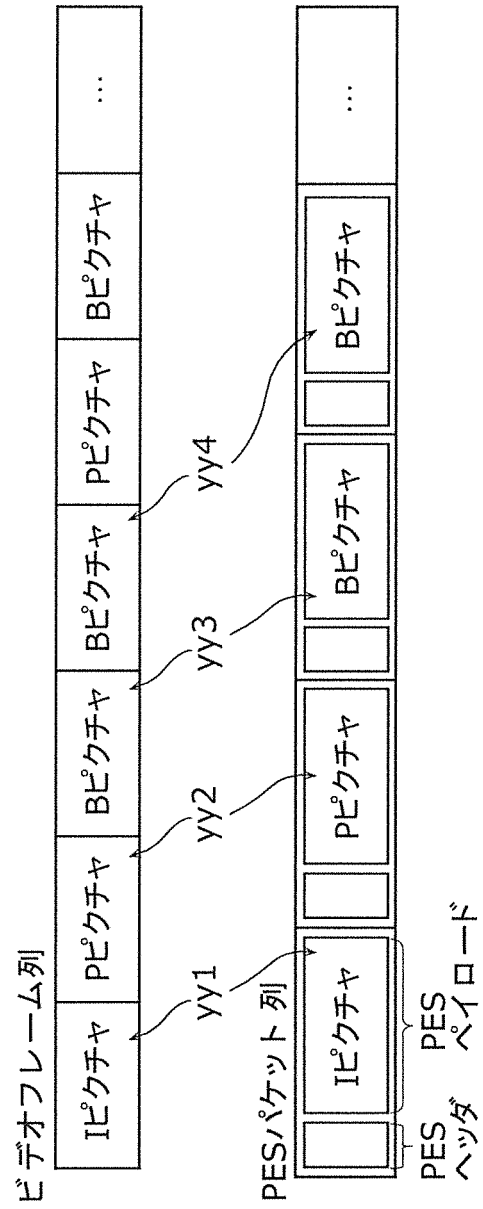
[図23]

ビデオストリーム(PID=0x1011 主映像)
オーディオストリーム(PID=0x1100)
オーディオストリーム(PID=0x1101)
プレゼンテーショングラフィックスストリーム(PID=0x1200)
プレゼンテーショングラフィックスストリーム(PID=0x1201)
インタラクティブグラフィックスストリーム(PID=0x1400)
ビデオストリーム(PID=0x1B00 副映像)
ビデオストリーム(PID=0x1B01 副映像)

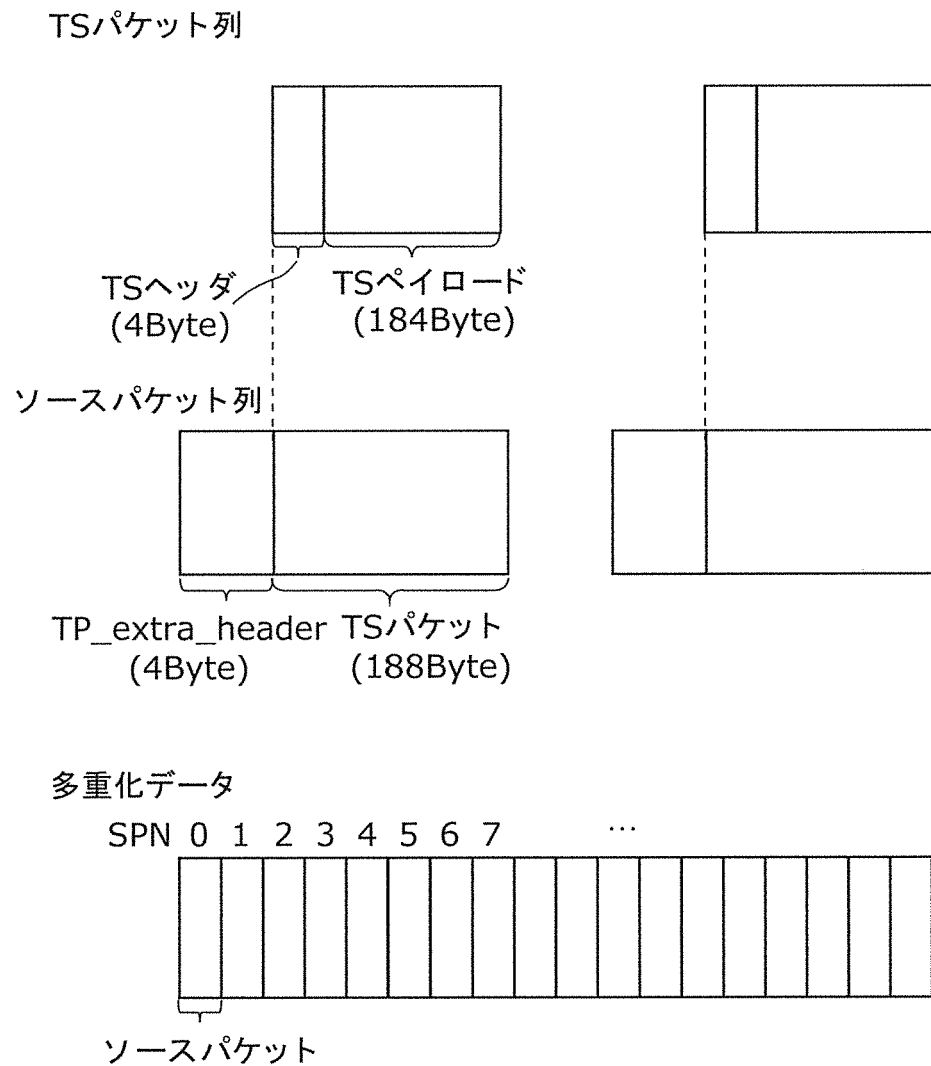
[図24]



[図25]

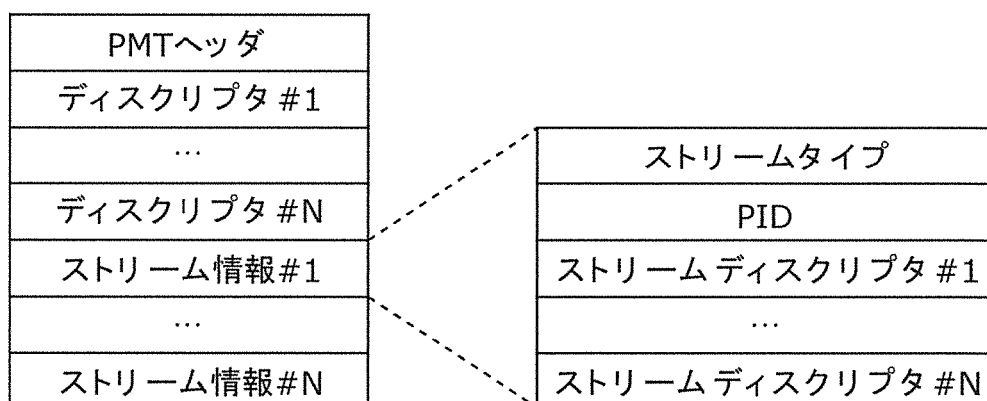


[図26]

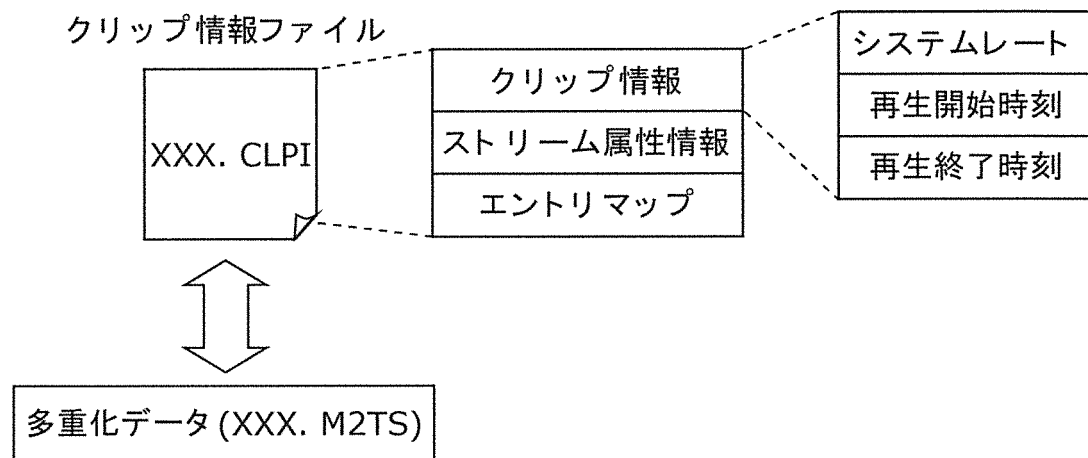


[図27]

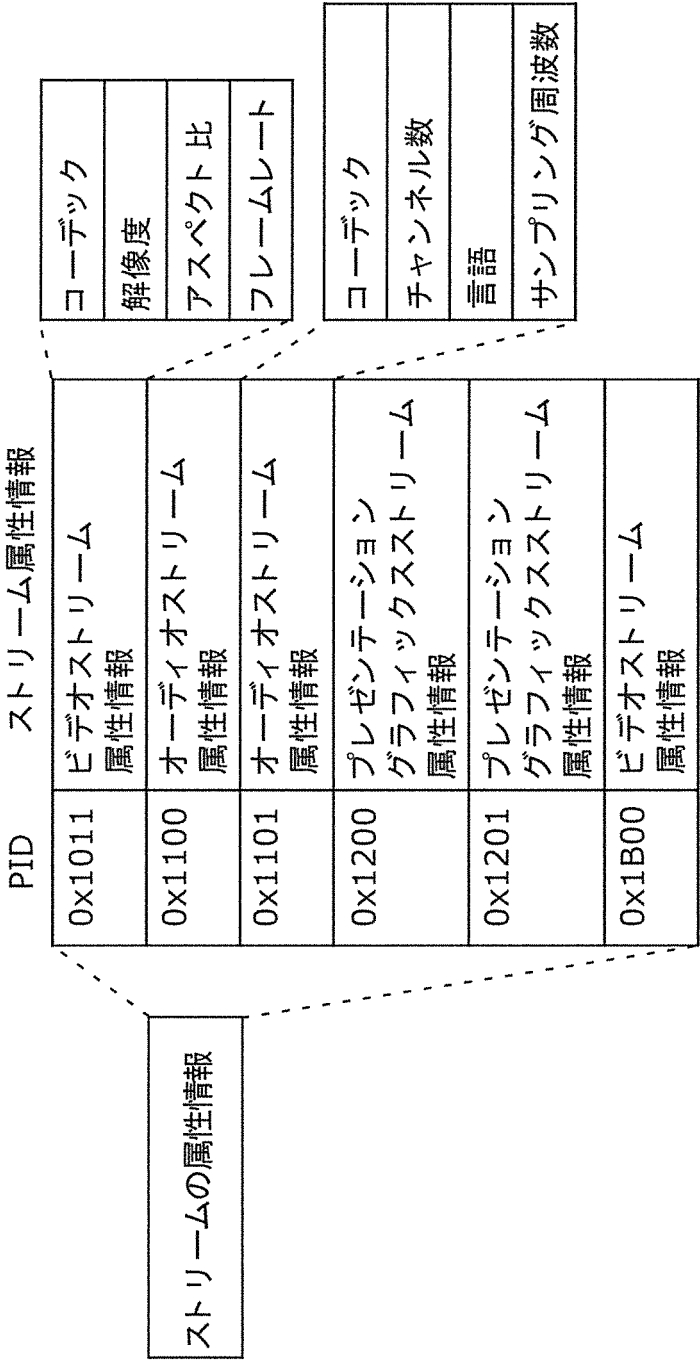
PMTのデータ構造



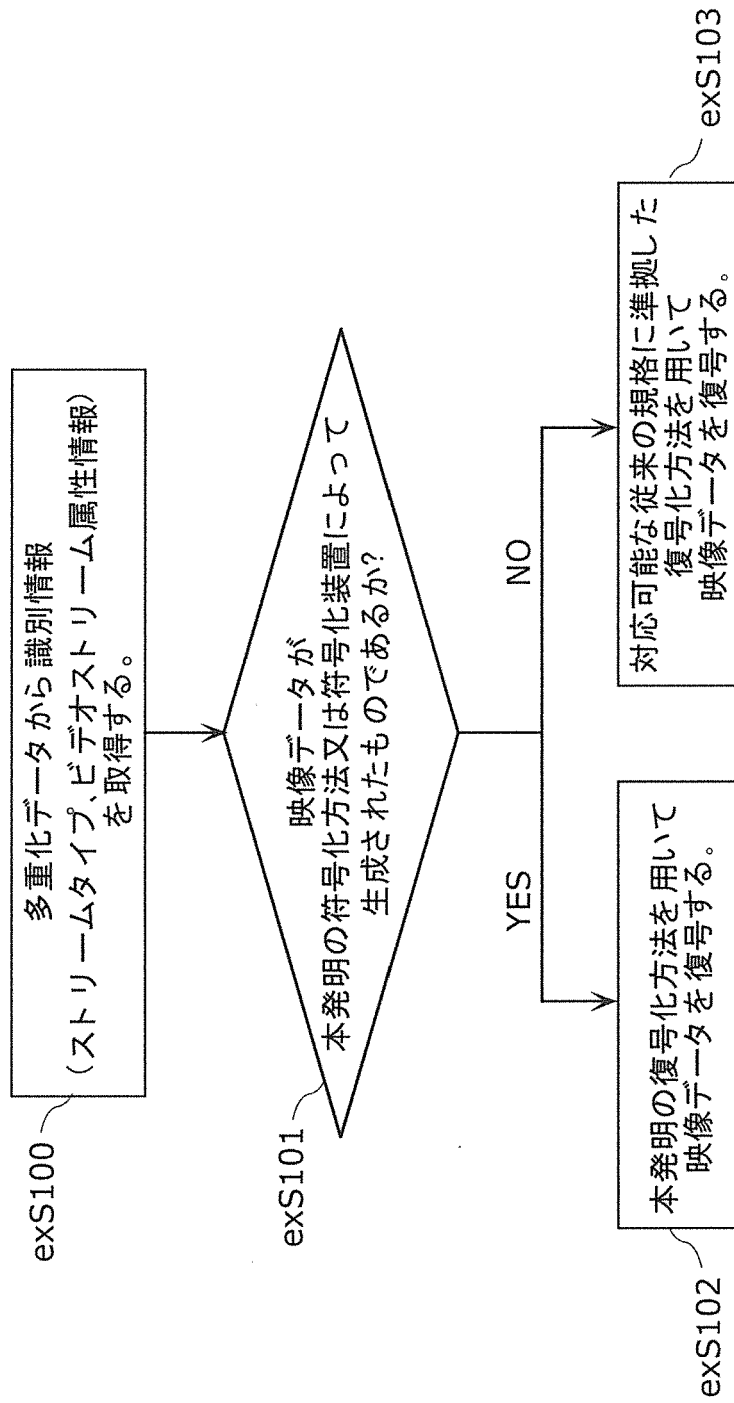
[図28]



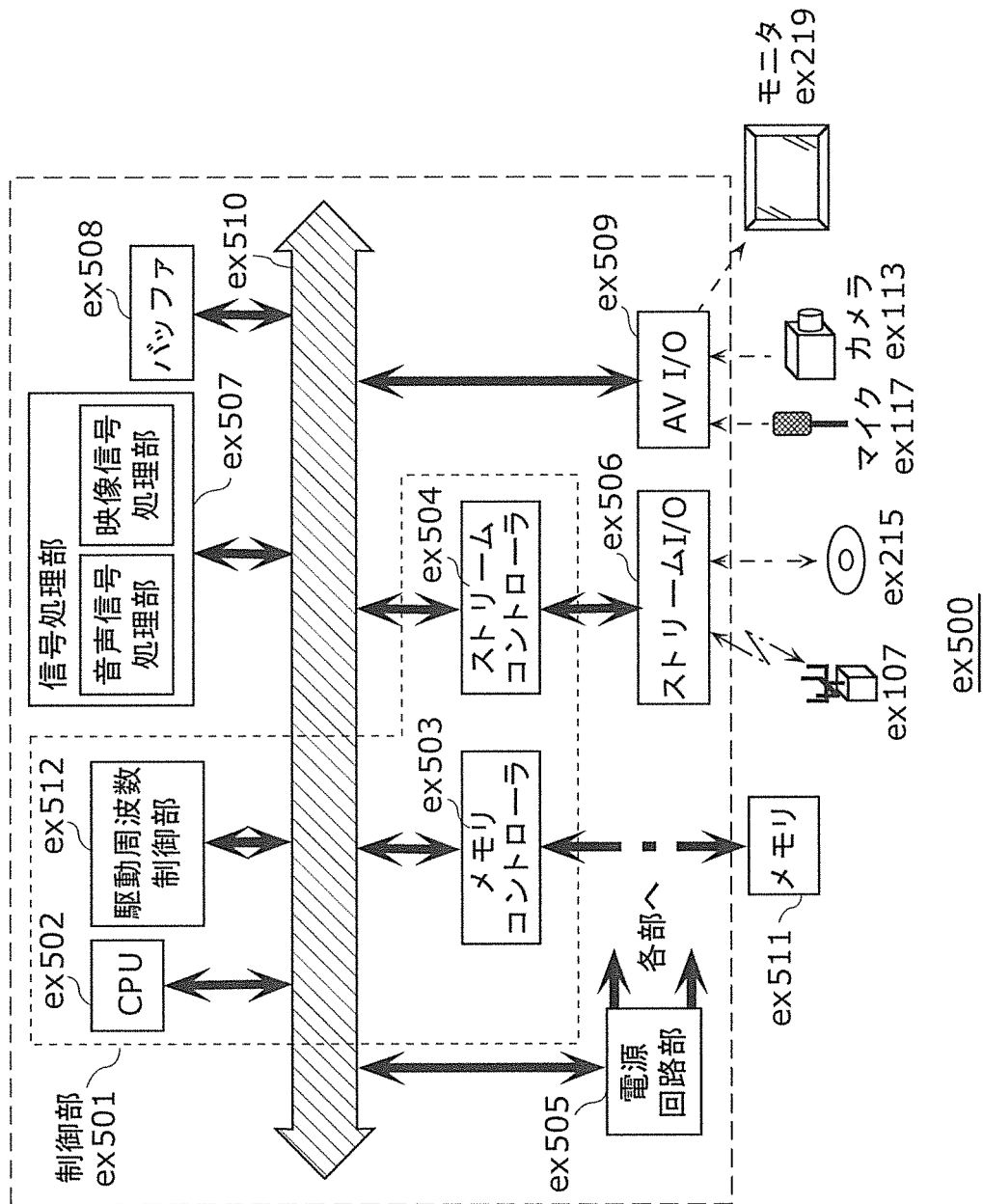
[図29]



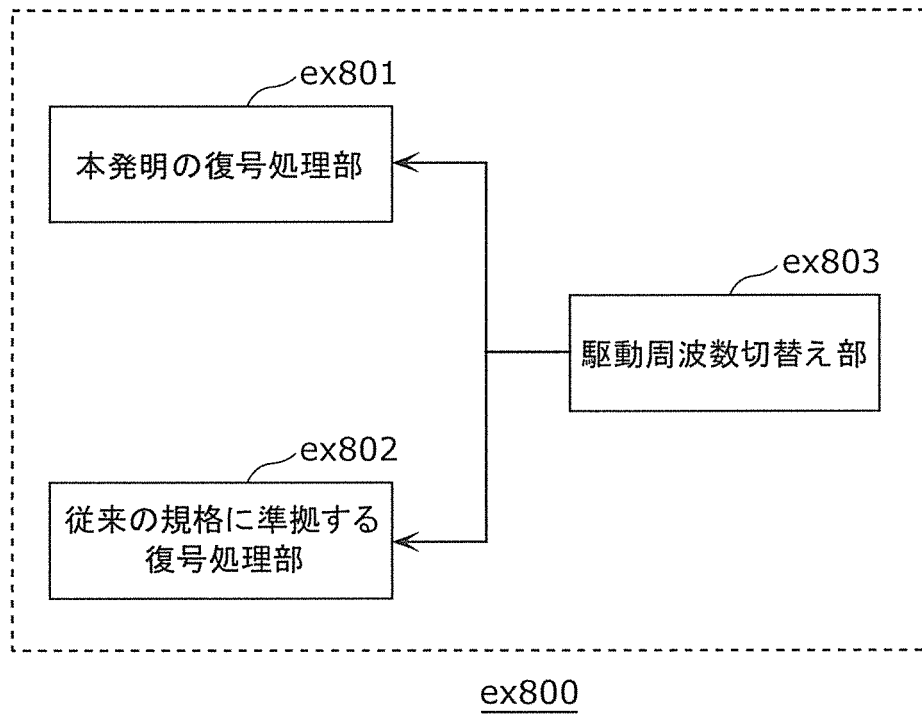
[図30]



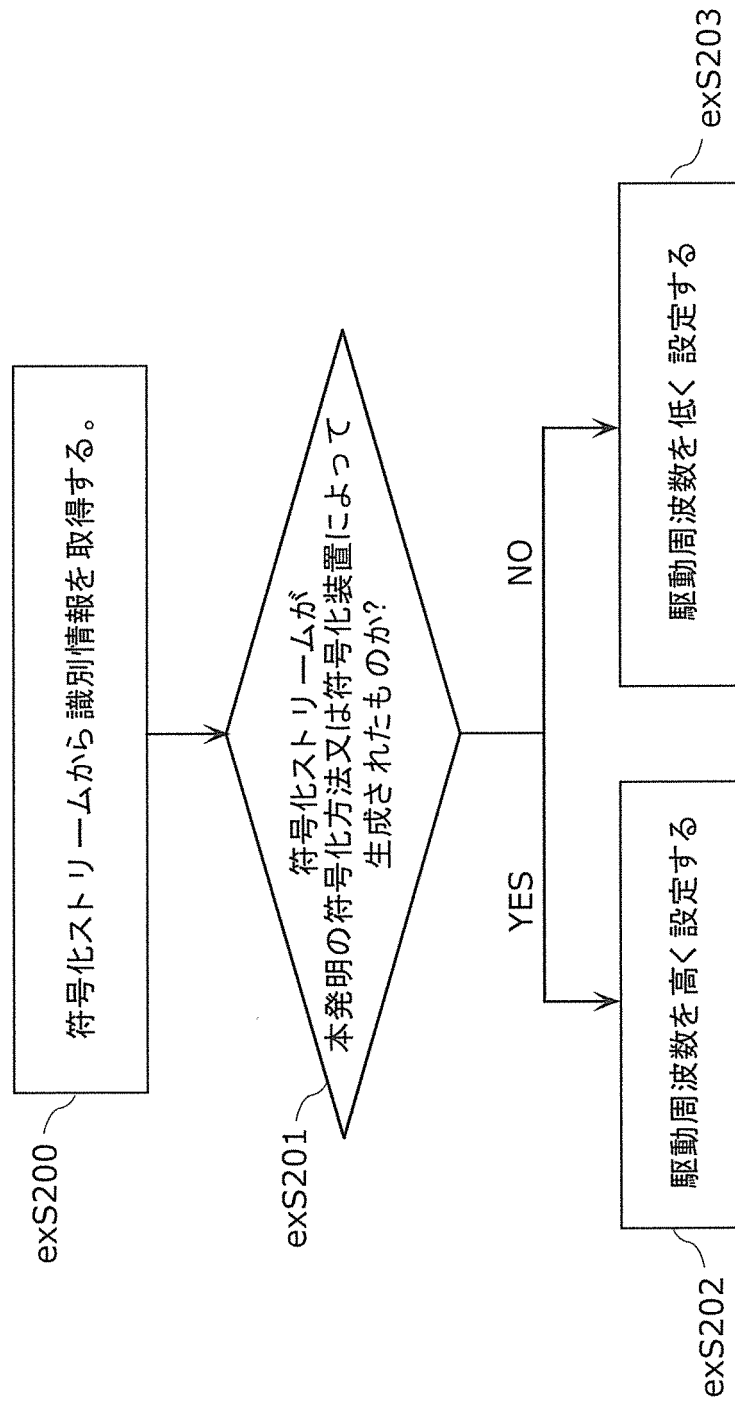
[図31]



[図32]



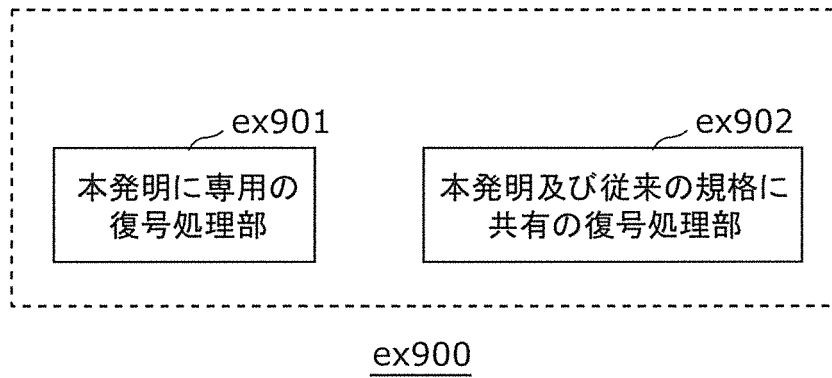
[図33]



[図34]

対応規格	駆動周波数
MPEG4.AVC	500MHz
MPEG2	350MHz
...	...

[図35A]



[図35B]

