

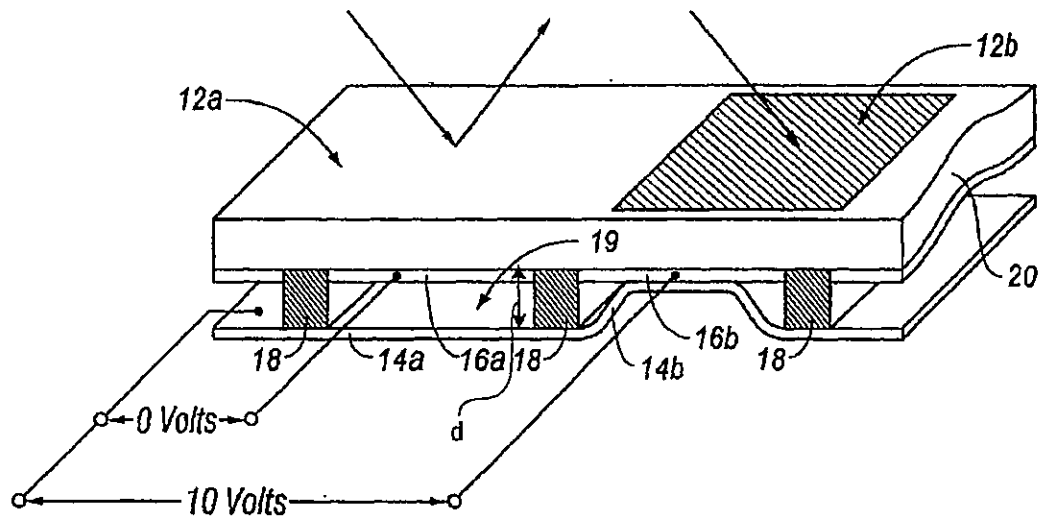


FIG. 1

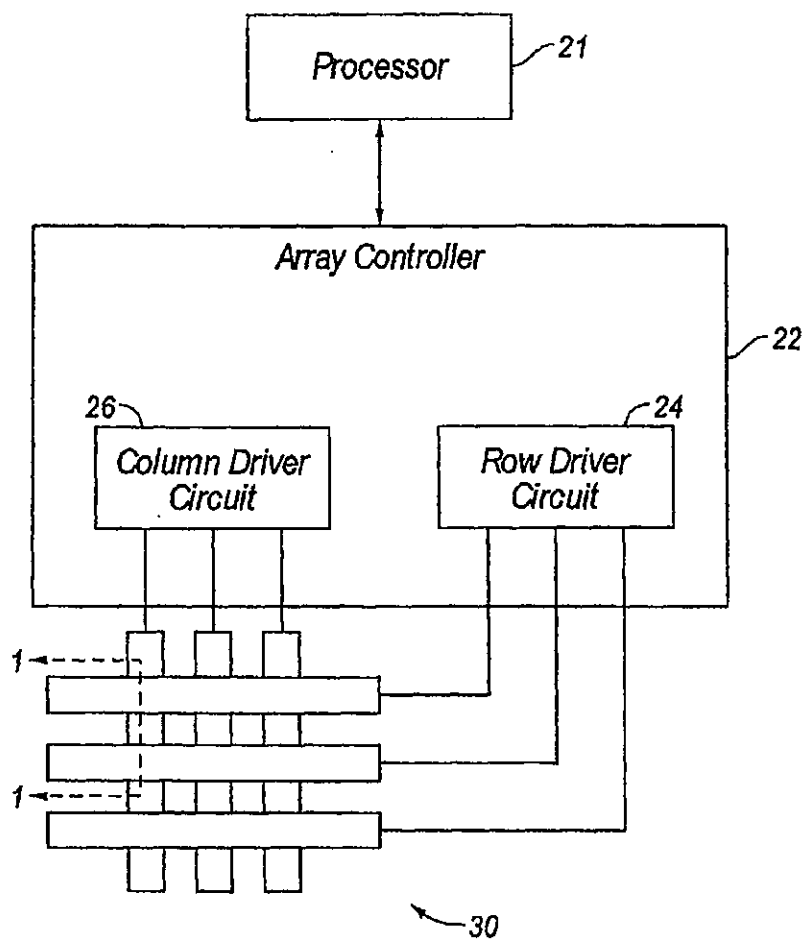


FIG. 2

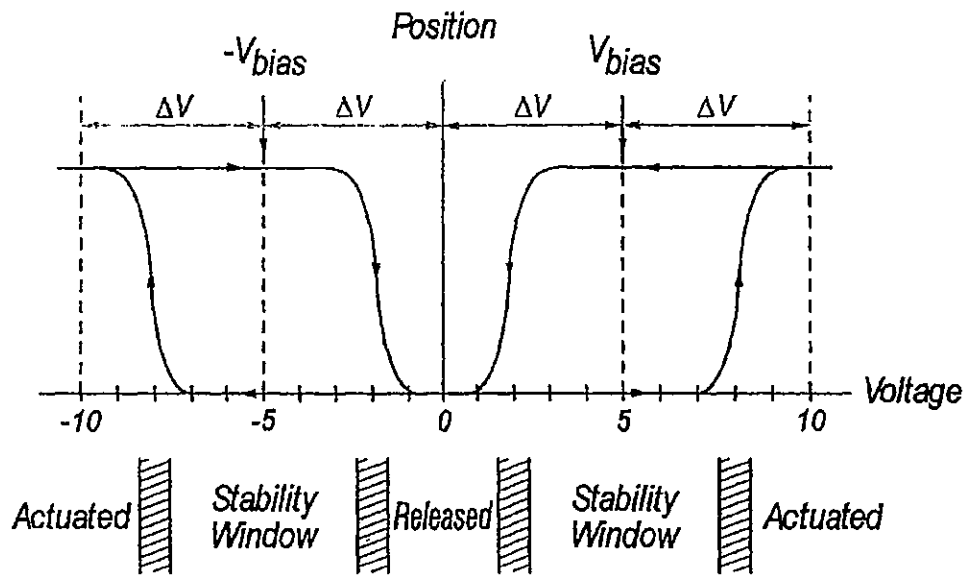


FIG. 3

		Column Output Signals	
		$+ V_{bias}$	$-V_{bias}$
Row Output Signals	0	Stable	Stable
	$+ \Delta V$	Release	Actuate
	$-\Delta V$	Actuate	Release

FIG. 4

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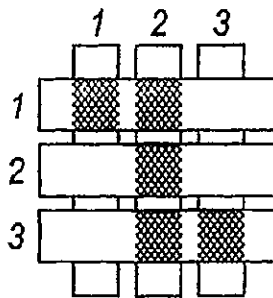


FIG. 5A

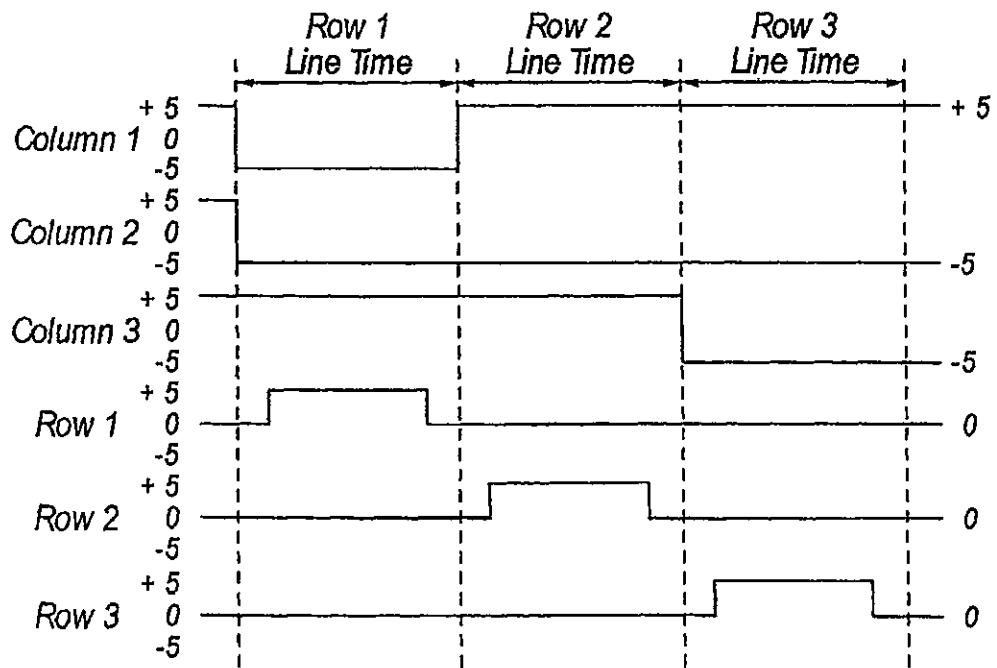


FIG. 5B

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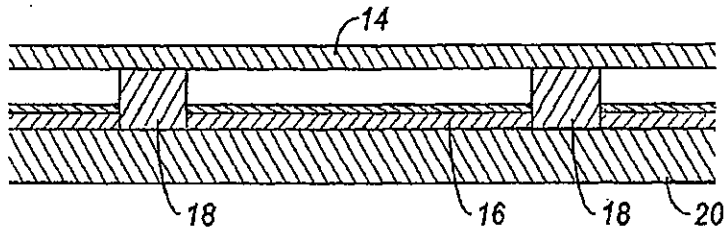


FIG. 6A

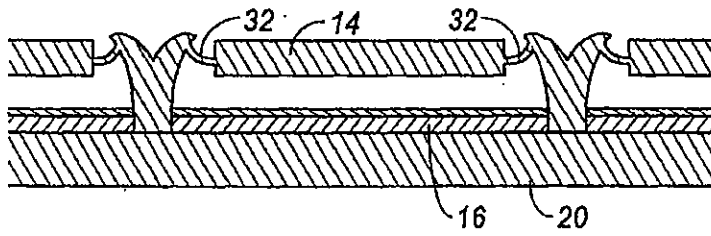


FIG. 6B

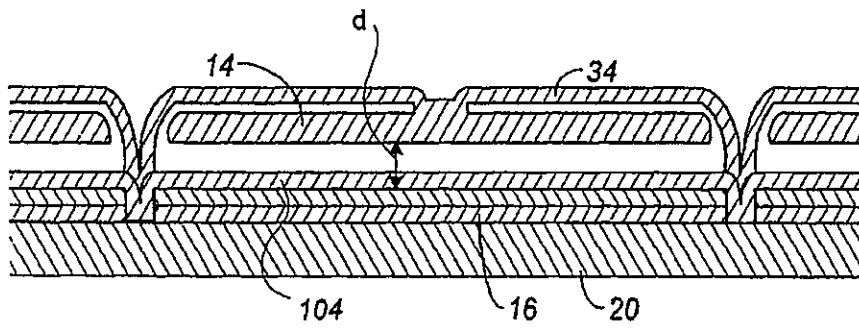


FIG. 6C

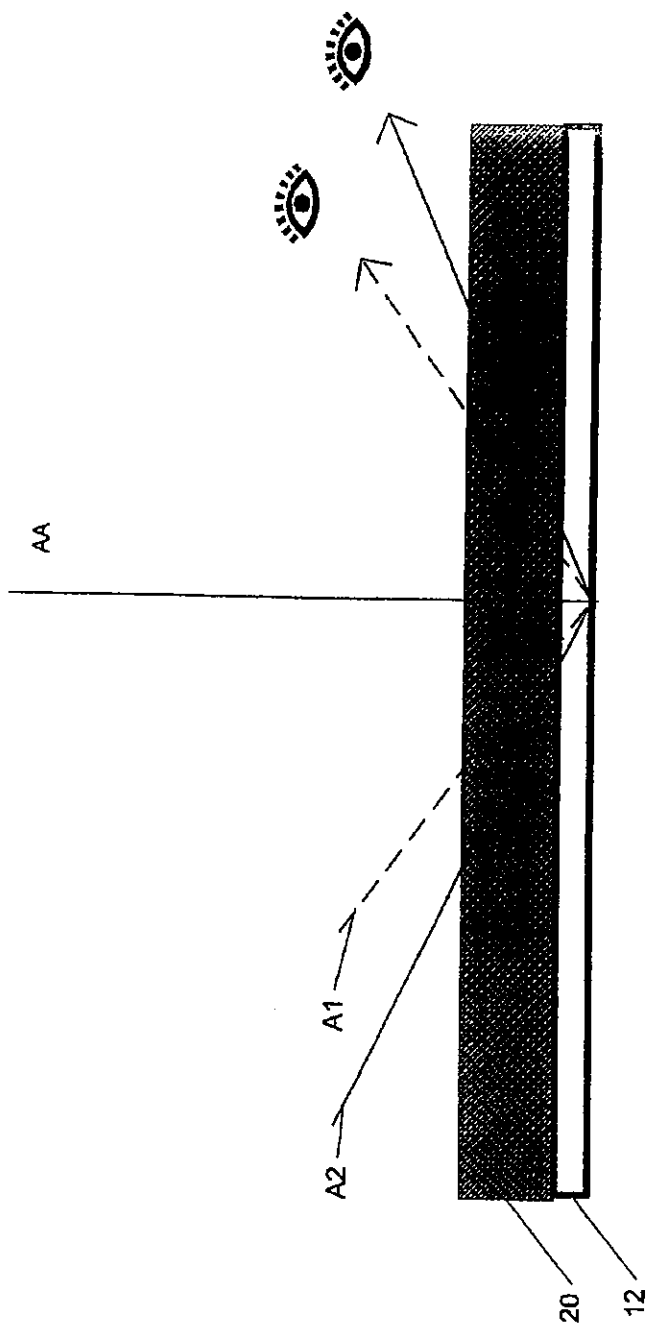


Fig. 7

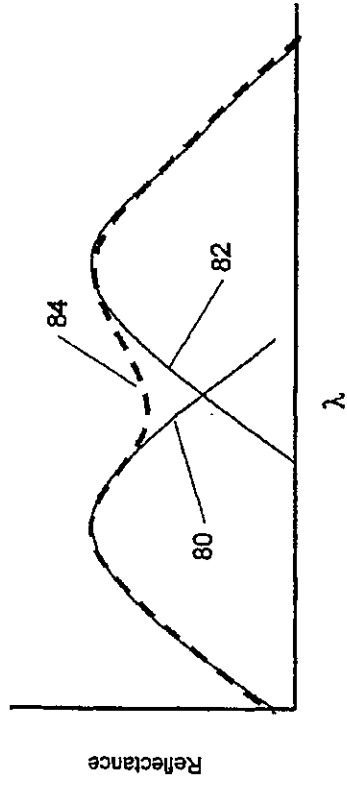


Fig. 8

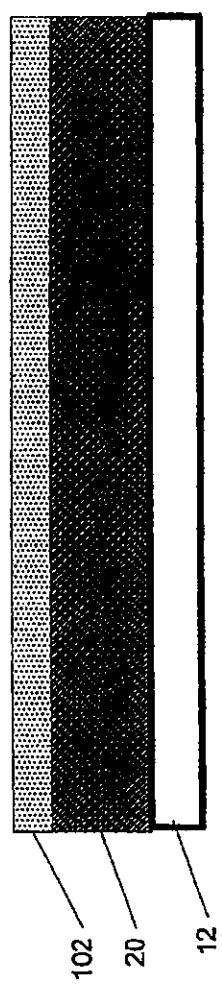


Fig. 9

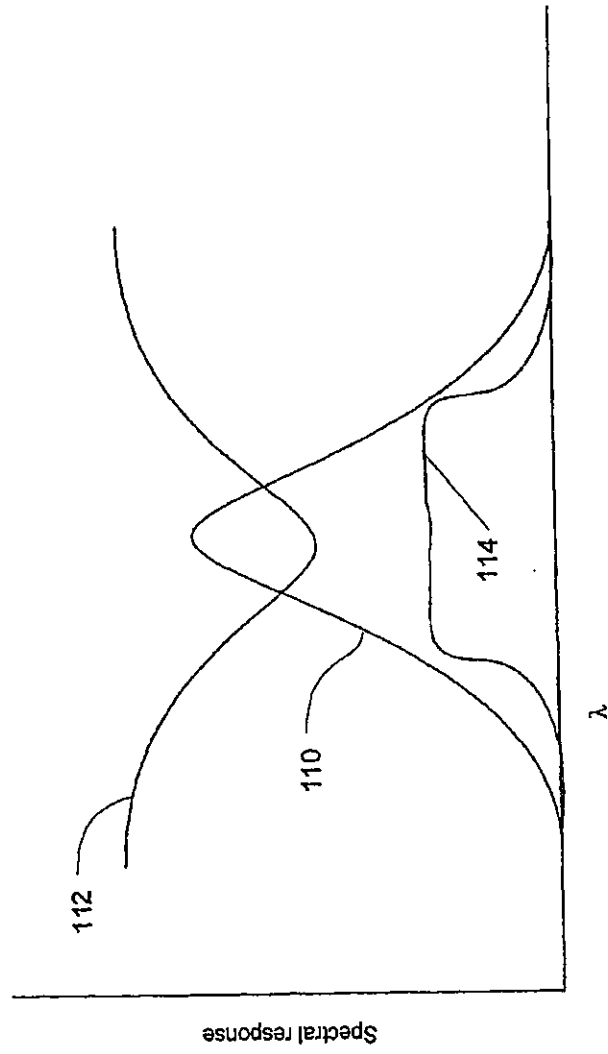


Fig. 10

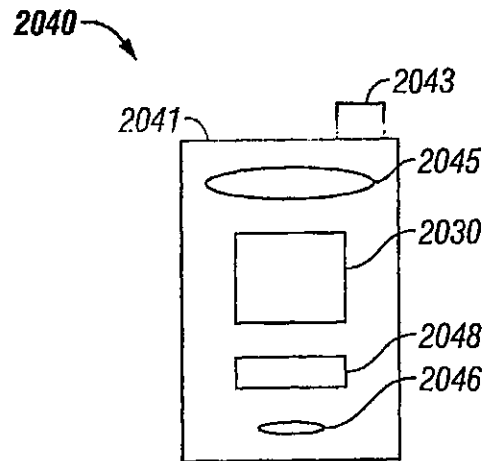


Fig. 11A

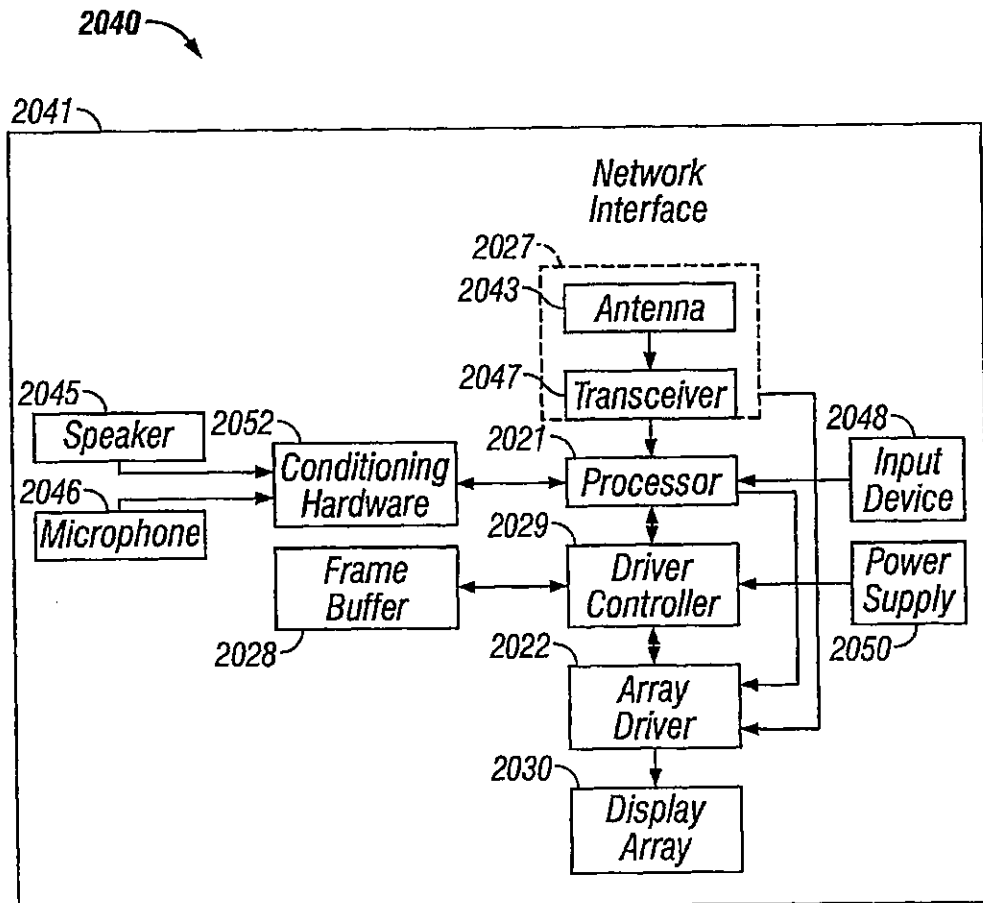


Fig. 11B