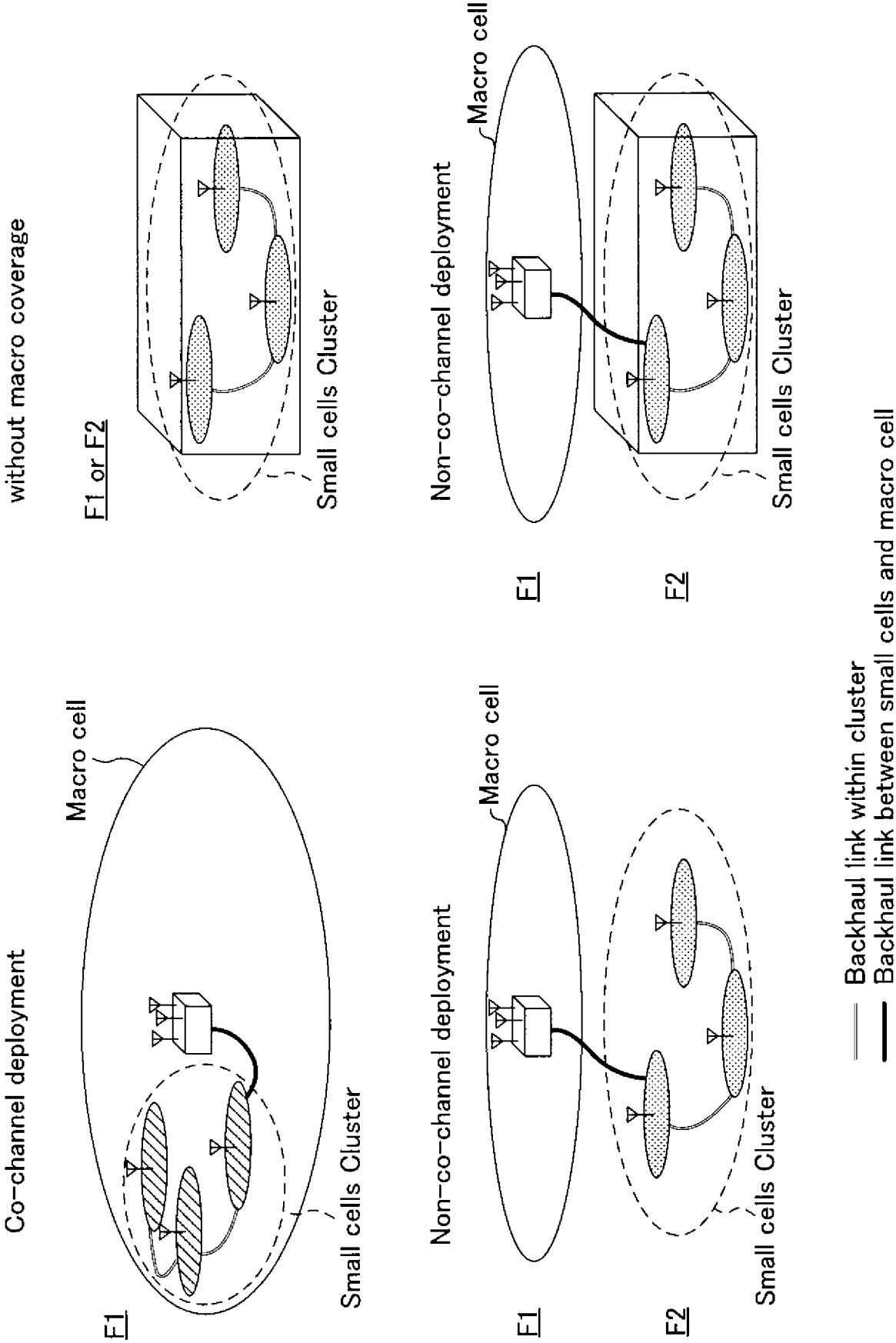


[図1]



[図2]

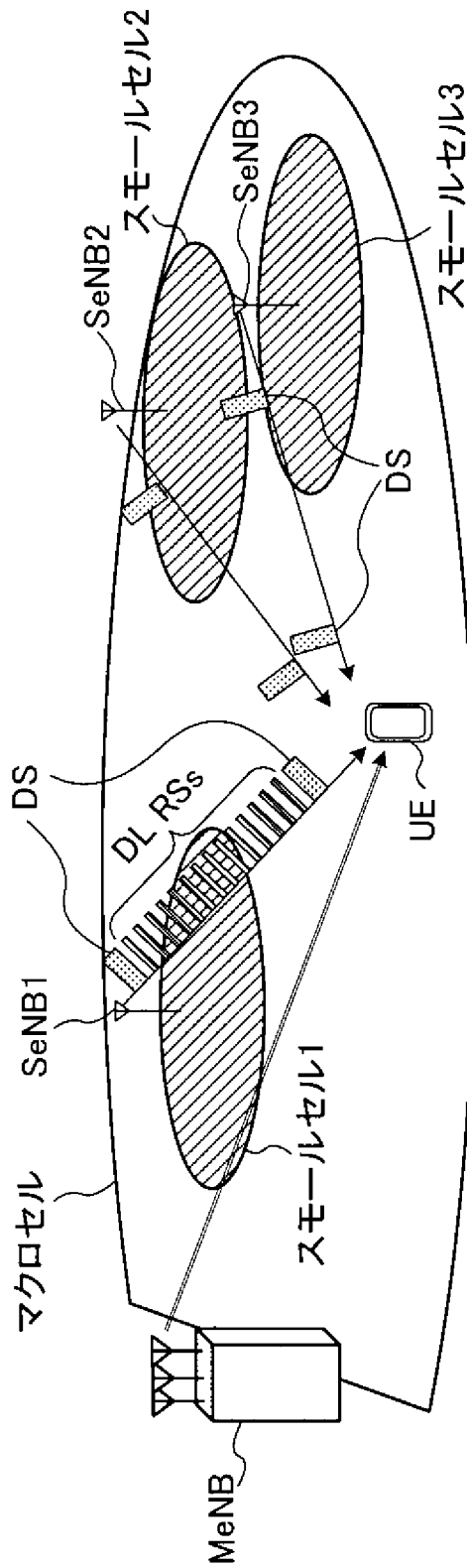


図2A

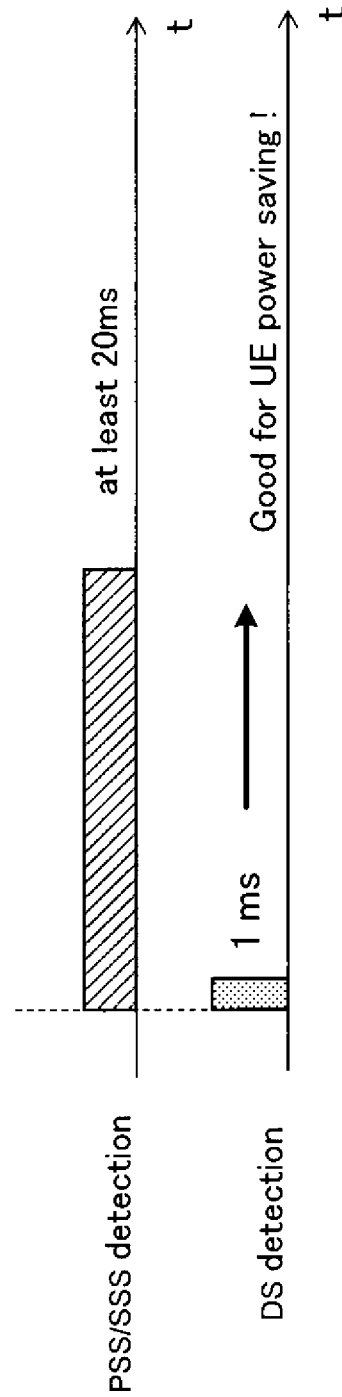
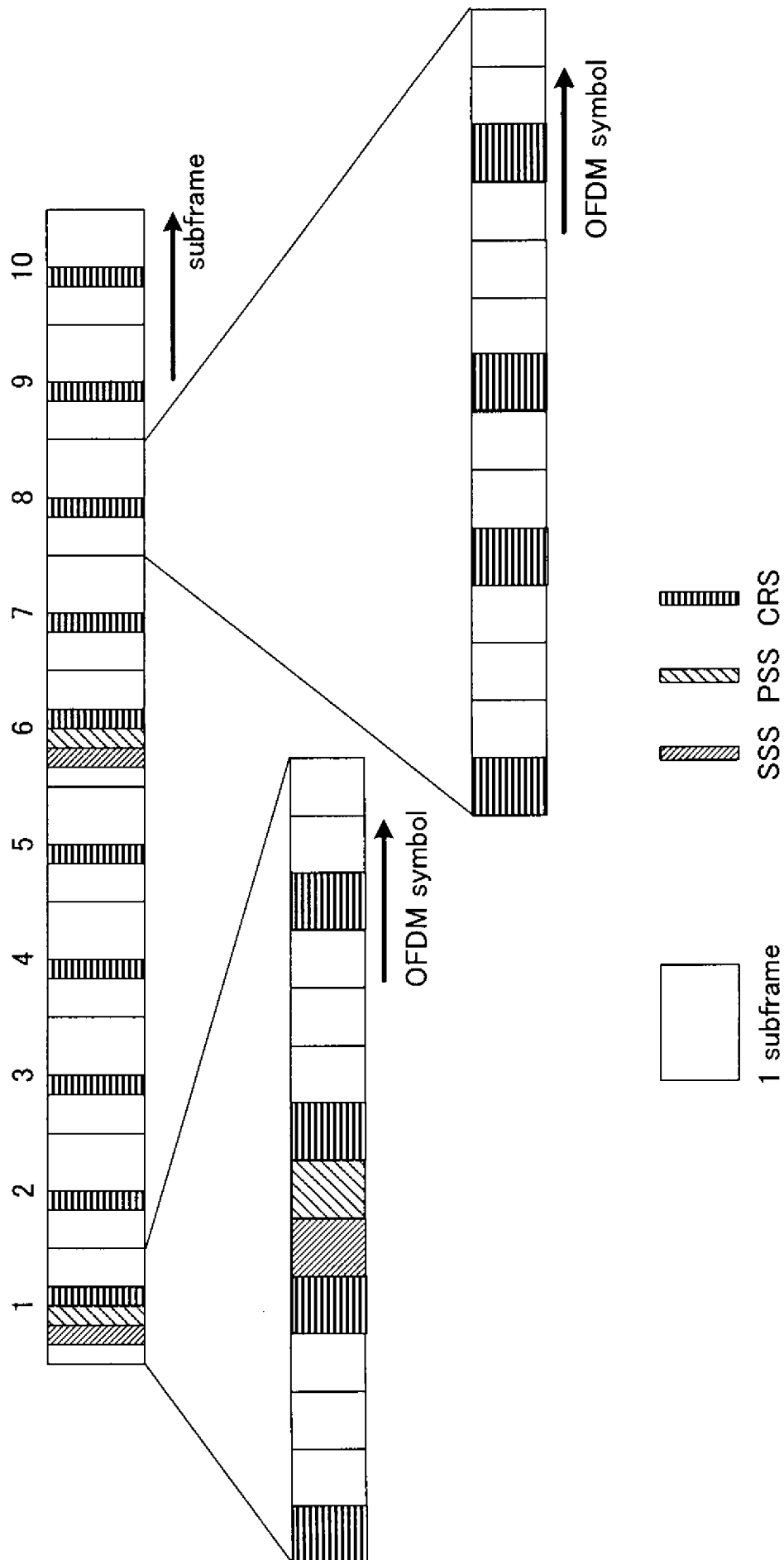
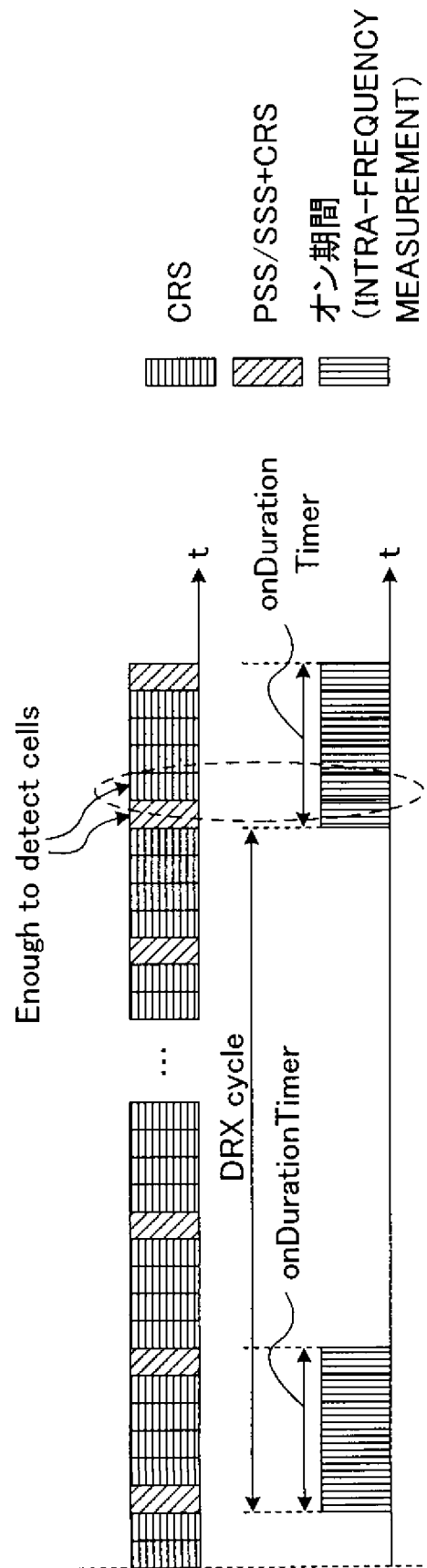


図2B

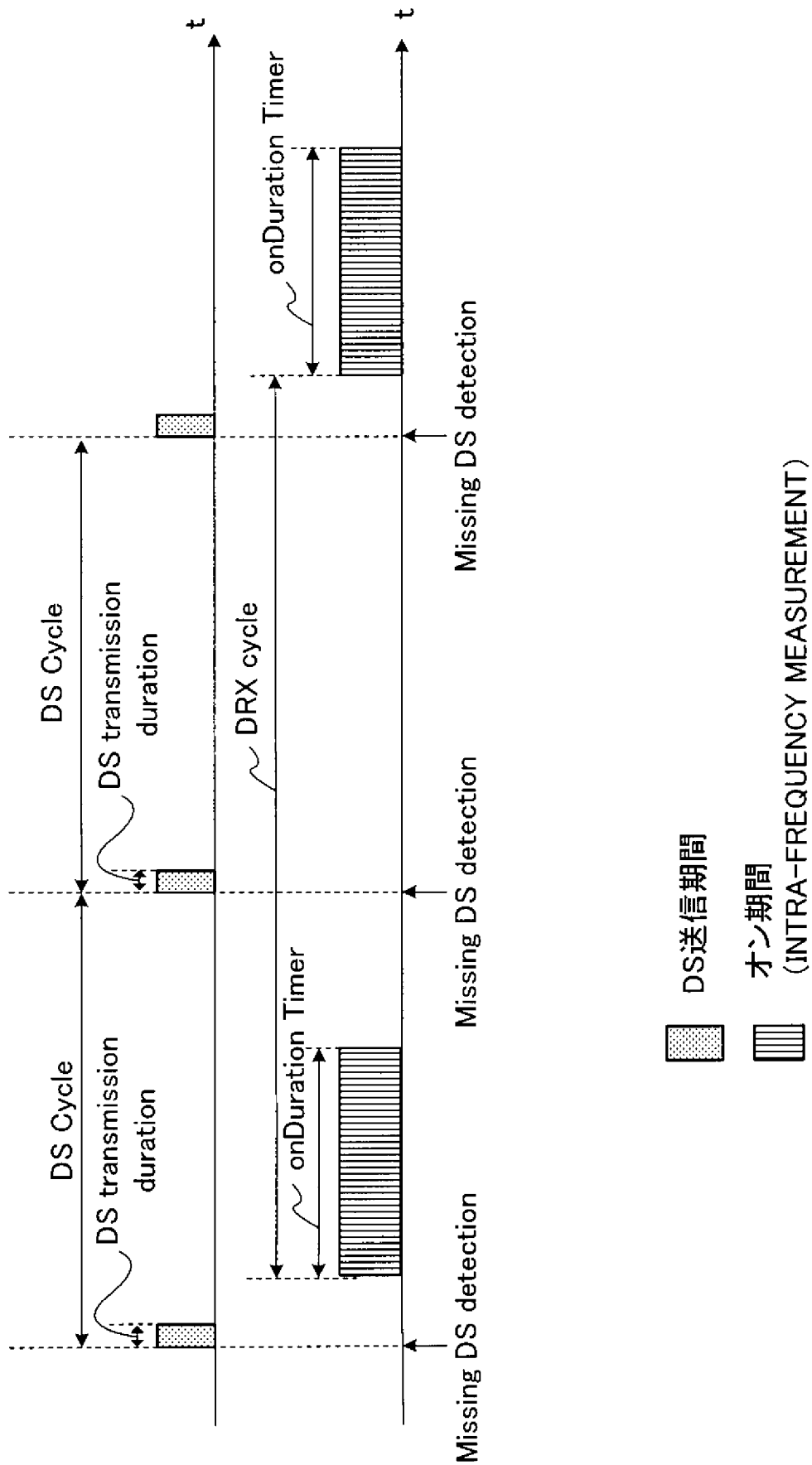
[図3]



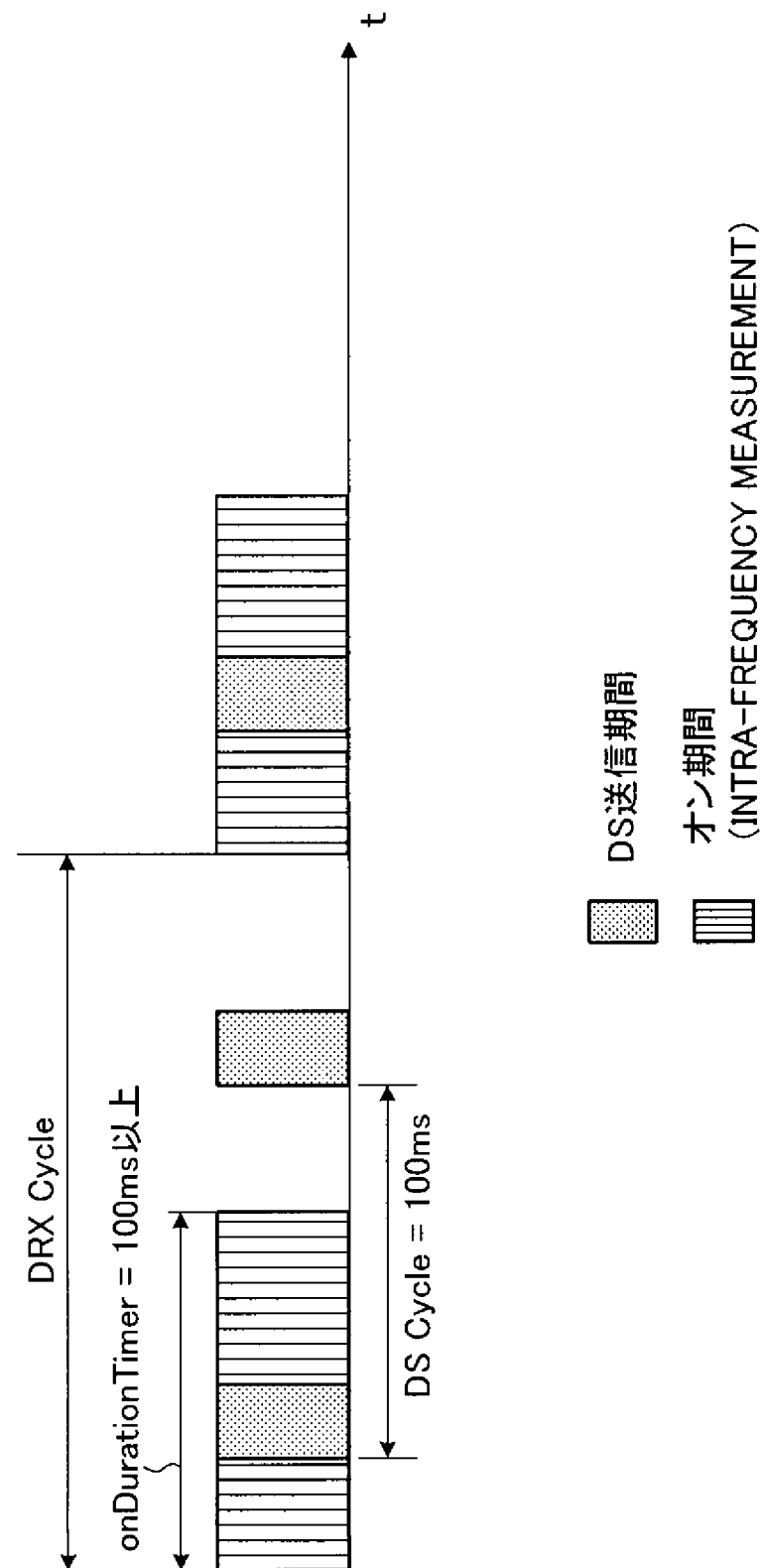
[図4]



[図5]



[図6]



[図7]

	DS Cycle	DS Start offset (subframe number)	k	DRX Cycle	When to start onDuration Timer (SFN, subframe number)
UE1	100ms	5	1	100ms	(10, 5); (20, 5); (30, 5); (40, 5)
UE2	100ms	5	2	200ms	(20, 5); (40, 5)
UE3	100ms	5	3	300ms	(30, 5)

図7A

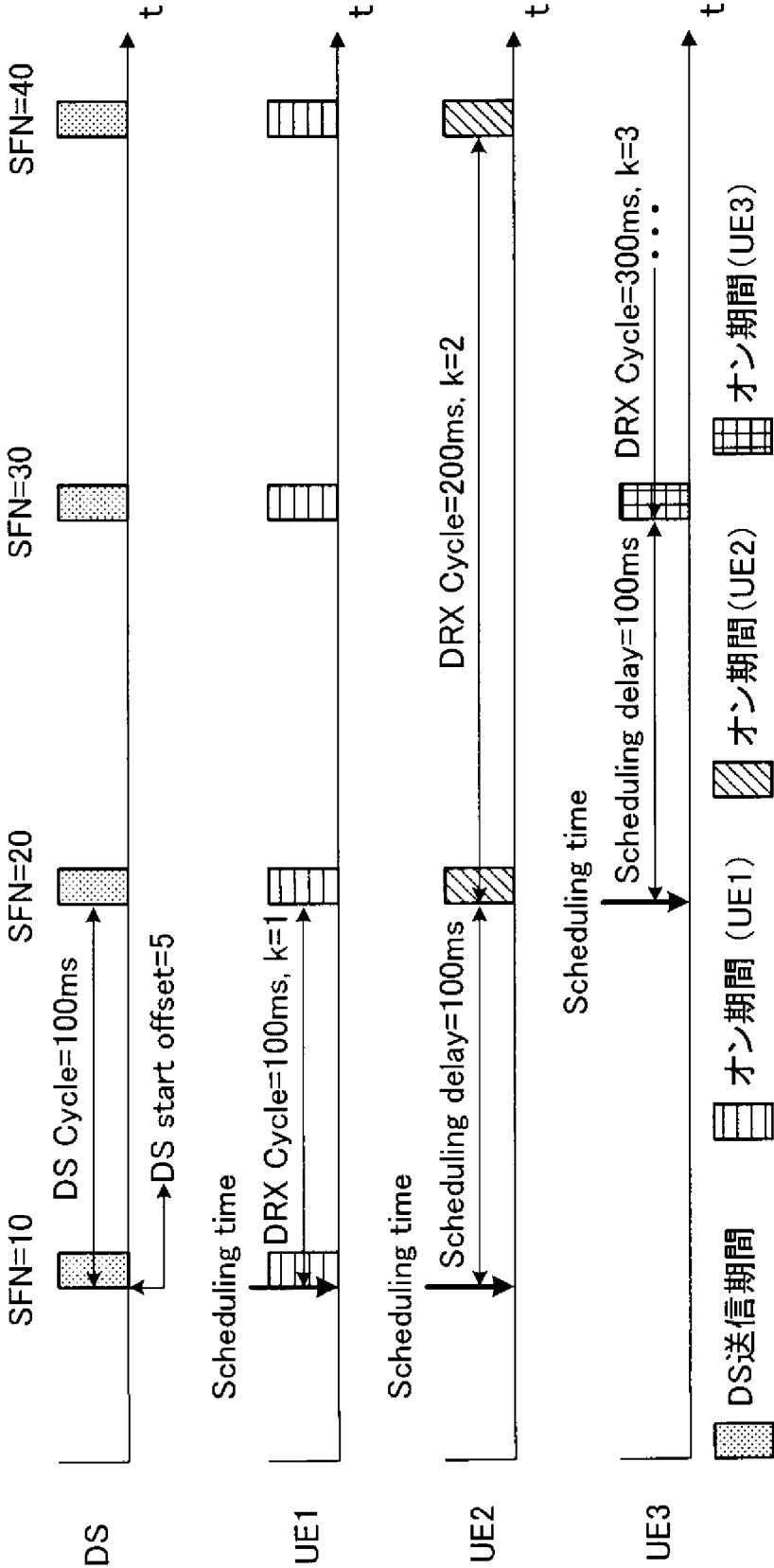
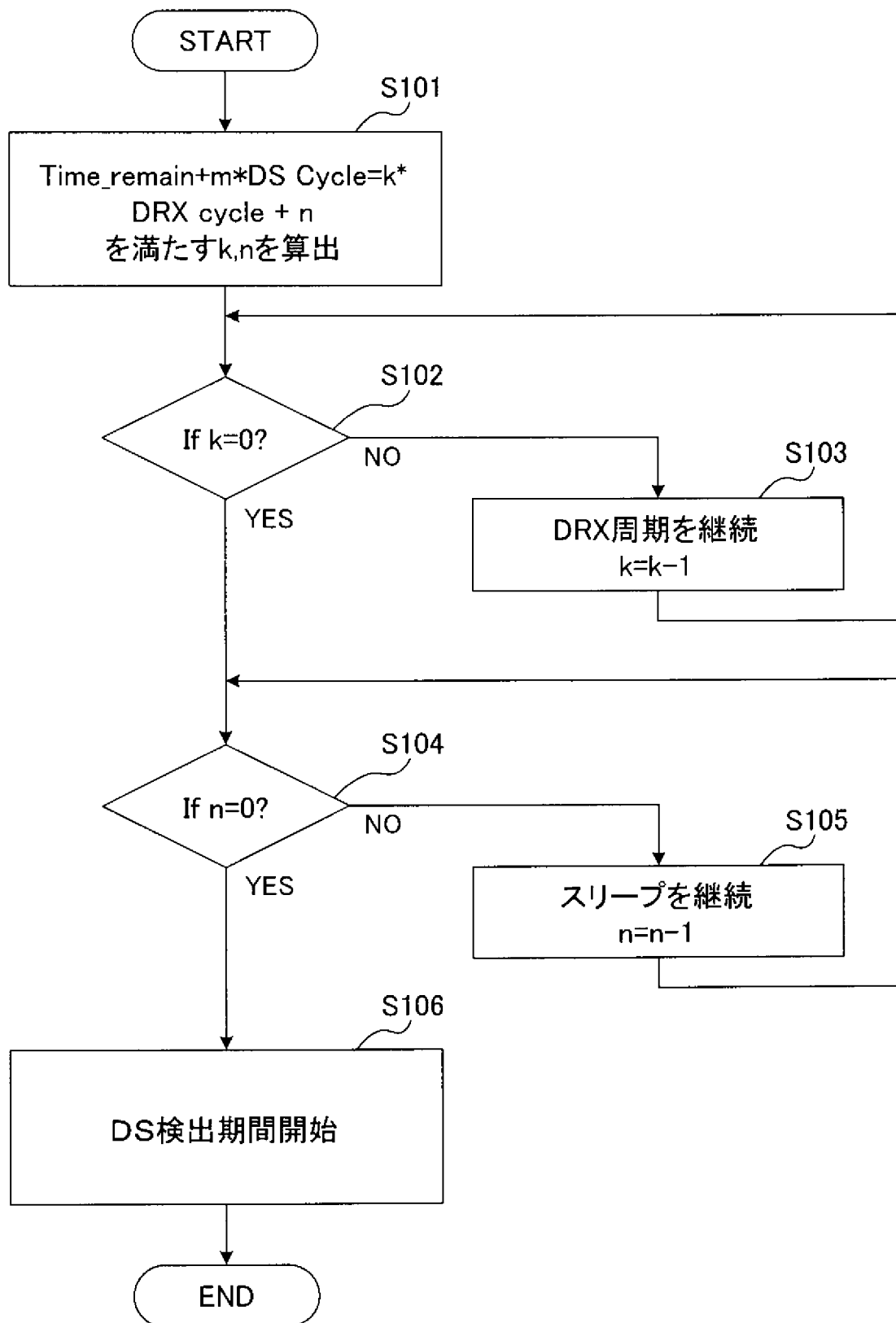


図7B

[図8]

80%≤P_remain≤100%	50%≤P_remain<80%	20%≤P_remain<50%	P_remain<20%
m=0	m=1	m=2	m=6

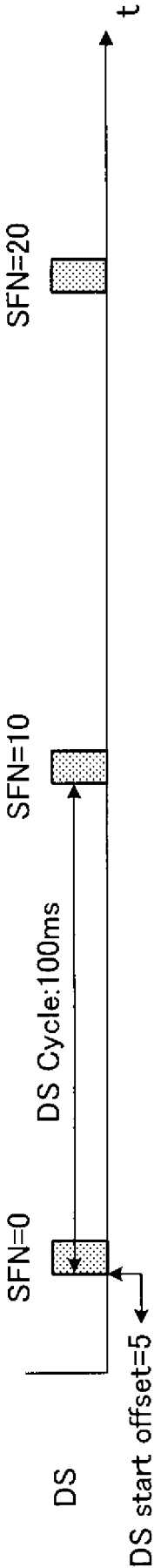
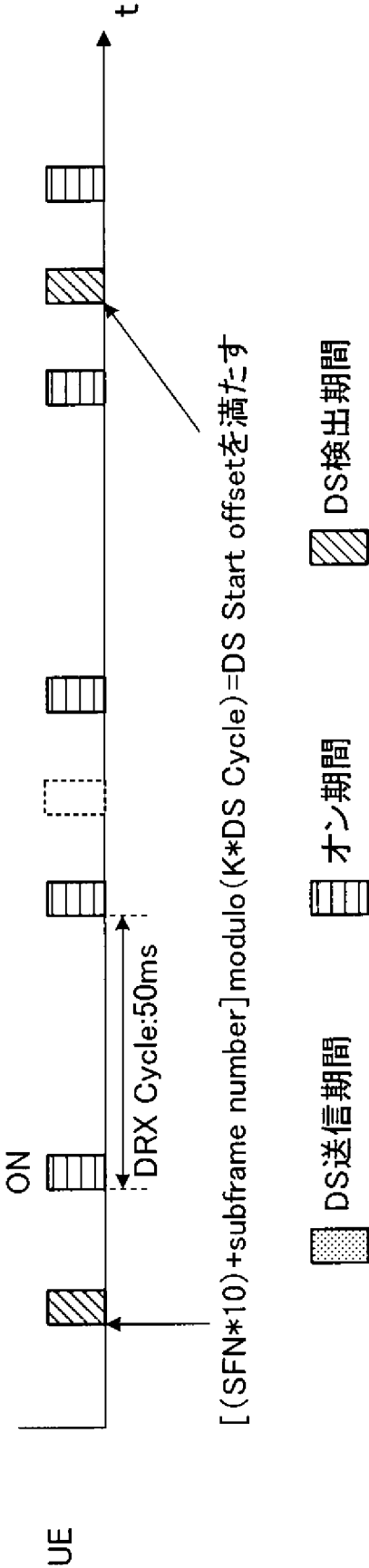
[図10]



[図11]

DS Cycle	DS Start offset	k	DRX Cycle
100ms	5	2	50ms

図11A



$$[(\text{SFN} \times 10) + \text{subframe number}] \bmod (K \times \text{DS Cycle}) = \text{DS Start offset}$$

図11B

[図12]

Option	Baseline	1	2 and 3
DRX Cycle	200ms	200ms	200ms
onDurationTimer	100ms	50ms	50ms
Option 2, m=1. Option 3, k=2.			

図12A

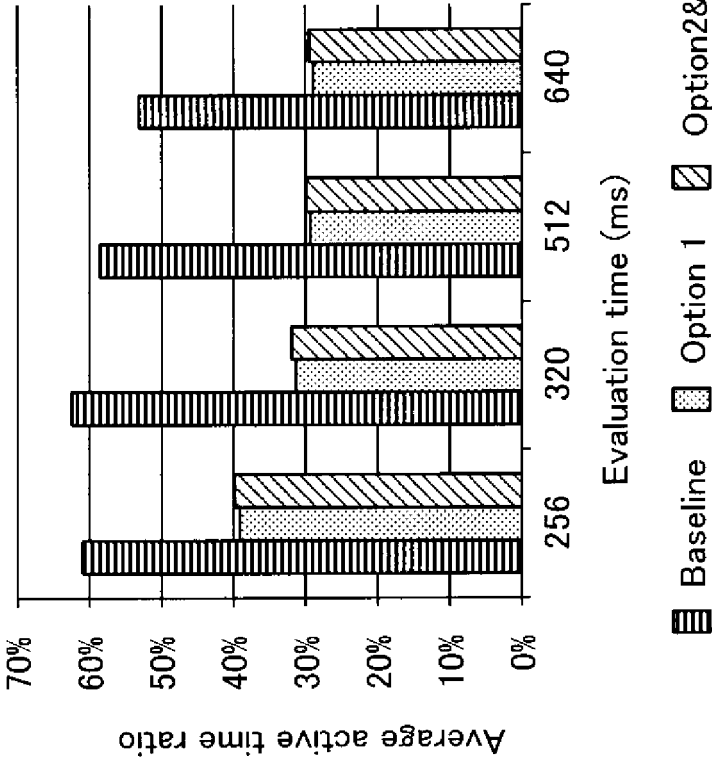


図12B

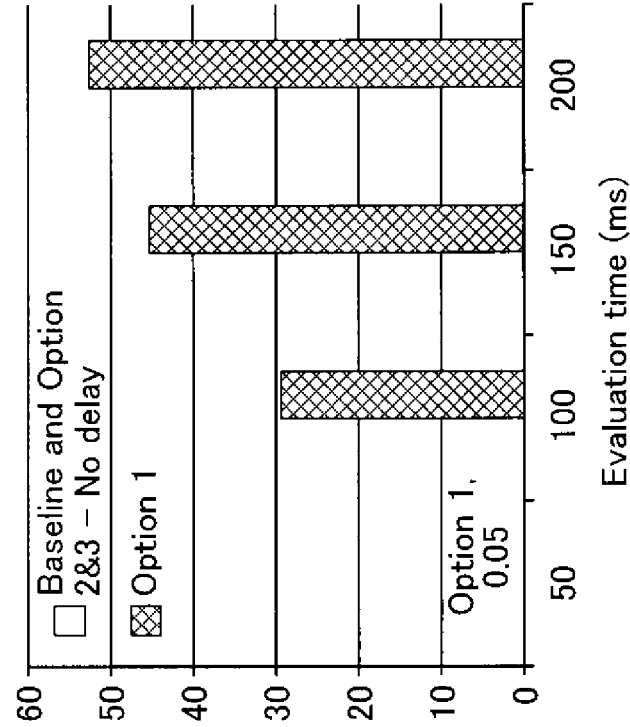


図12C

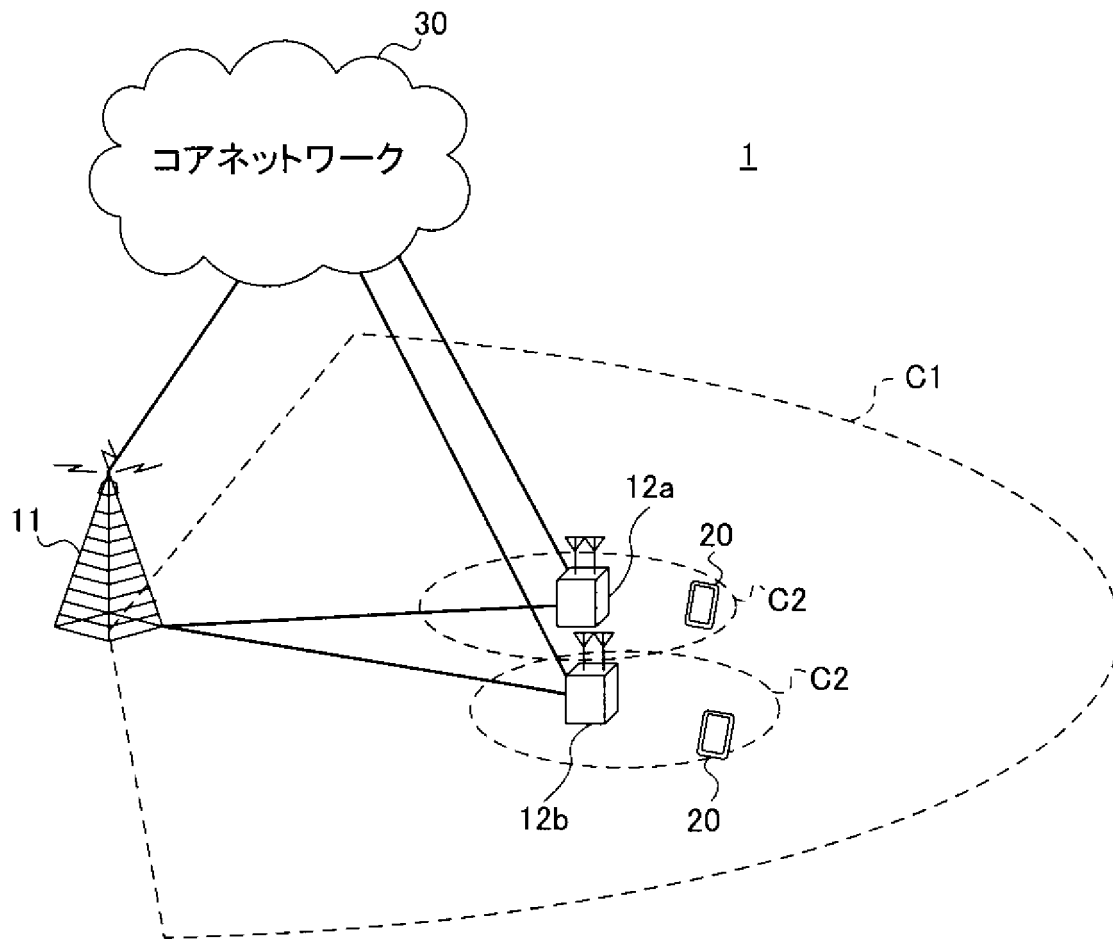
[図13]

	UE power saving	Chance to detect DS rate	UE behavior control	Scheduling delay
Baseline	Bad, except for actual onDuration Timer needed is 100ms	Uncertainty, <100% (NOTE 2)	Network	No delay
Option 1	Better compared to option 2&3	Uncertainty, <100%	Network	Large delay
Option 2	Good (NOTE 1)	Certainty, <100%	UE-self	No delay
Option 3			Network	No delay

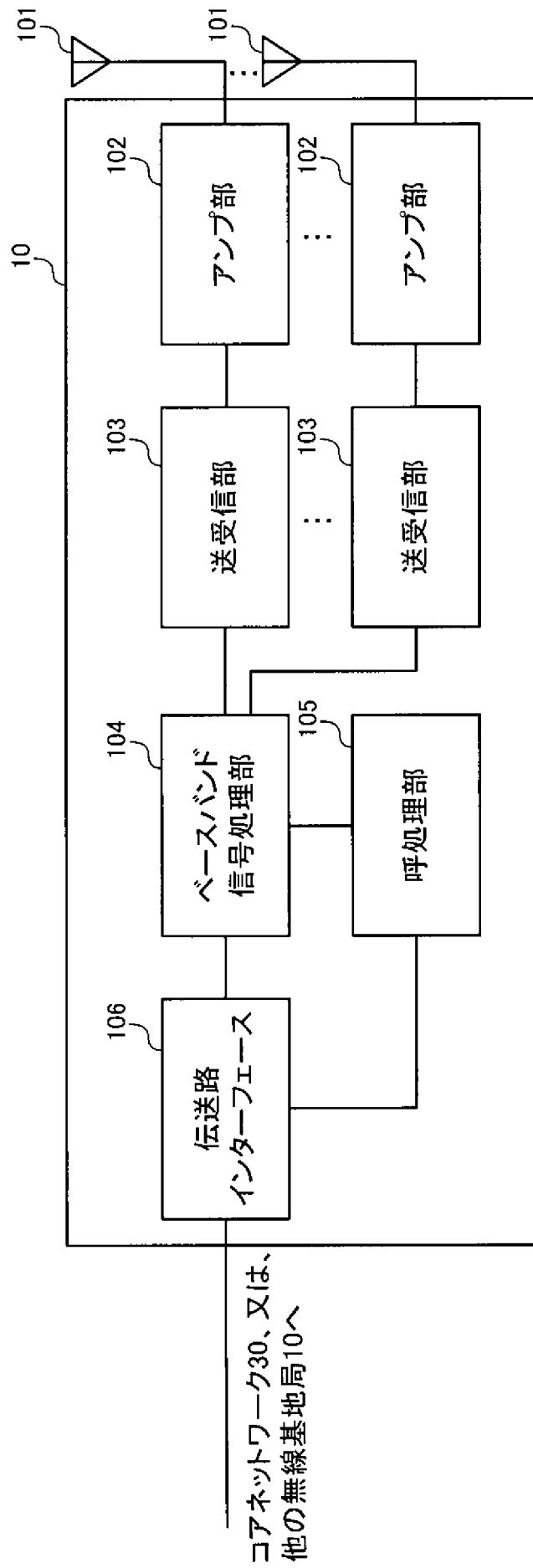
NOTE 1: UE power saving depends on active time of DRX

NOTE 2: The chance to detect DS depends on DRX configuration

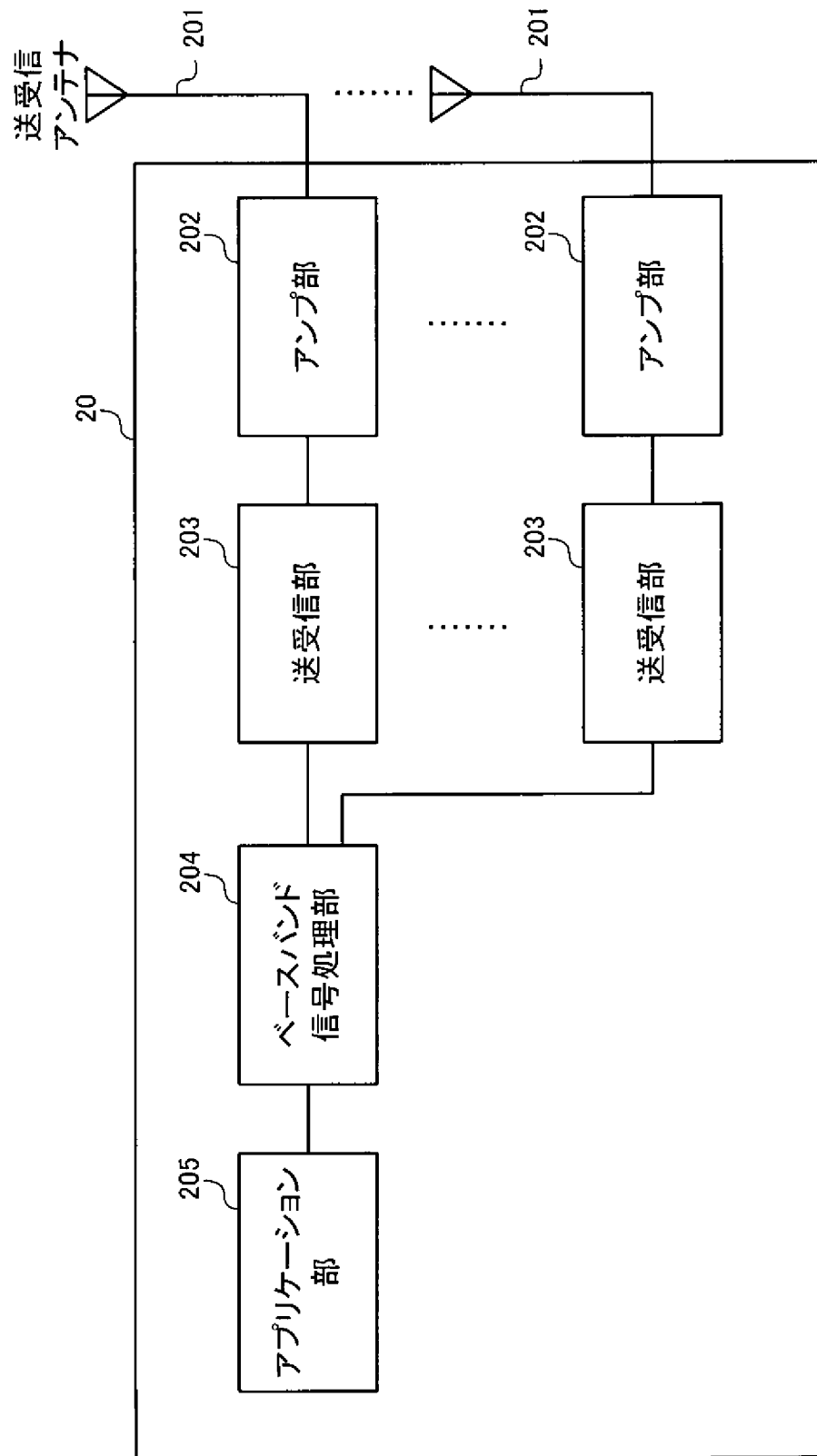
[図14]



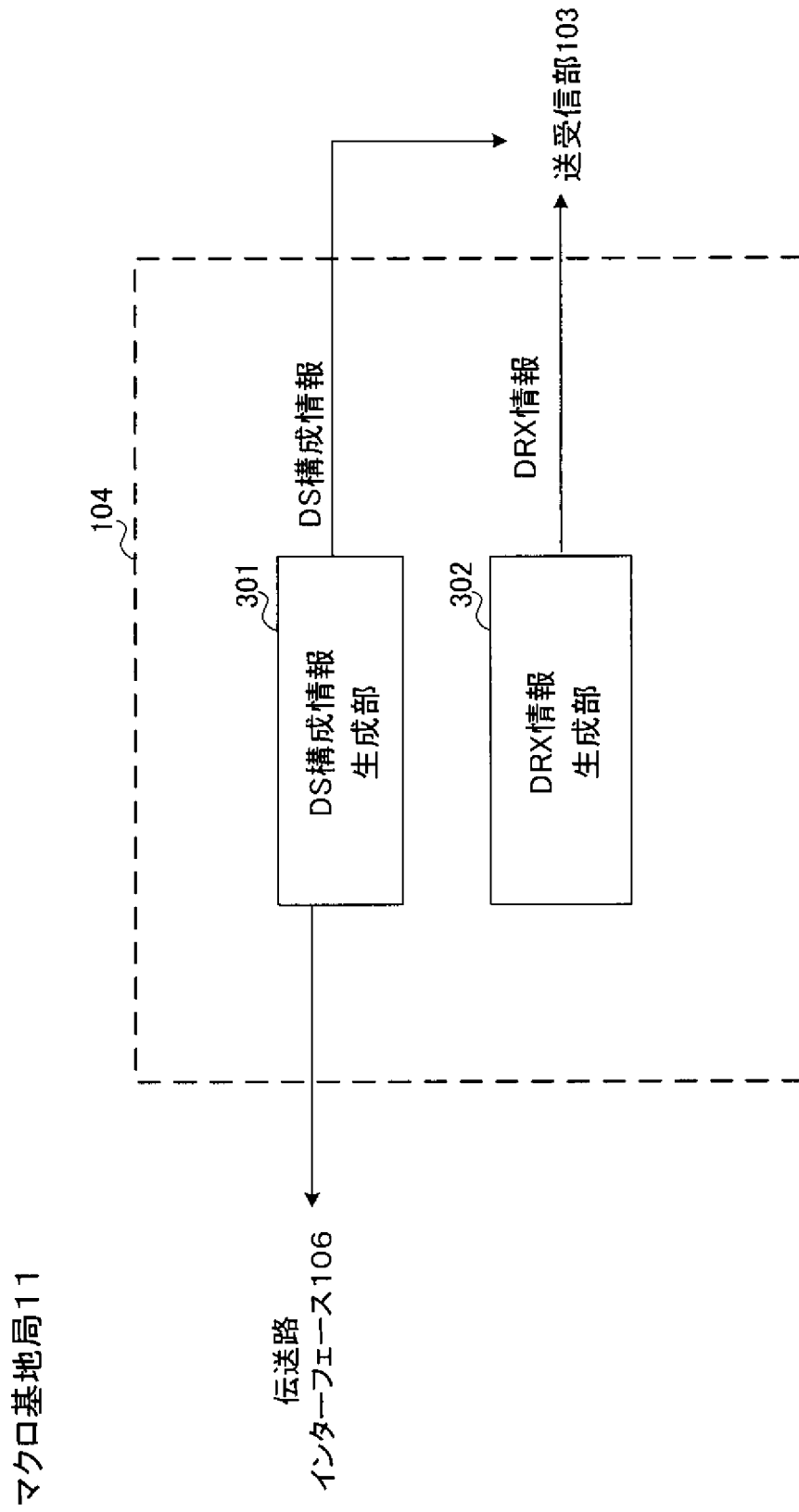
[図15]



[図16]



[図17]



[図18]

